

CYGNUS C

CPU : Intel Dothan533 / Yonah(TBD)
Chip Set : Intel Alviso & ICH6-M
Remarks : Mobility Platform

Model Name : CYGNUS C
PBA Name : MAIN
PCB Code : BA41-00451A
Dev. Step : MP
Revision : 1.0
T.R. Date : NOV 29 2004

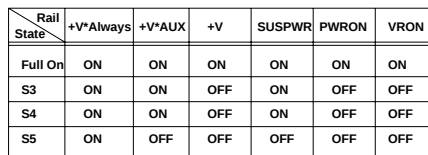
DRAW	CHECK	APPROVAL

Owner : SEC Mobile R & D Signature : X

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DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C	SAMSUNG
CHECK	BIN, KK	DEV. STEP	MP			ELECTRONICS
APPROVAL	KIM, DW	REV	1.0		COVER	PART NO. BA41-00451A
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COM-22C-015 (1996.6.5) REV. 3

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	0	A,B,C
	AD21	3	
MiniPCI SLOT1	AD23	2	D,E
USB	AD29(internal)	-	USB2.0 #0 : A USB2.0 #1 : D USB2.0 #2 : C
Hub to PCI	AD30(internal)	-	
LPC bridge/IDE/AC97/SMBUS	AD31(internal)	-	B
		-	A,B
Internal MAC	AD24(internal)	-	E
AC Link	-	-	B

Voltage Rails

VDC	Primary DC system power supply (7 to 21V)
VCC_CORE	Core voltage for DOTHAN (1.308~1.068V)
VTT	DOTHAN/ALVISO Processor System Bus(PSB) Termination (1.05V) MCH-M Core Voltage
P0.9V	0.9V switched power rail (off in S3-S5)
P1.2V	1.2V switched power rail (off in S3-S5)
P1.5V	1.5V switched power rail (off in S3-S5)
P1.5V_AUX	1.5V power rail (off in S4-S5)
P1.8V	1.8V switched power rail (off in S3-S5)
P1.8V_AUX	1.8V power rail(off in S4-S5)
P2.5V	2.5V switched power rail (off in S3-S5)
MICOM_P3V	3.3V always on power rail for MICOM
P3.3V	3.3V switched power rail (off in S3-S5)
P3.3V_AUX	3.3V power rail (off in S4-S5)
P5V	5.0V switched power rail (off in S3-S5)
P5V_AUX	5.0V power rail (off in S4-S5)

CPU Core Voltage Table

VID5	VID4	VID3	VID2	VID1	VID0	Voltage	VID5	VID4	VID3	VID2	VID1	VID0	Voltage
0	0	0	0	0	0	1.708 V	1	0	0	0	0	0	1.196 V
0	0	0	0	0	1	1.692 V	1	0	0	0	0	1	1.180 V
0	0	0	0	1	0	1.676 V	1	0	0	0	1	0	1.164 V
0	0	0	0	1	1	1.660 V	1	0	0	0	1	1	1.148 V
0	0	0	1	0	0	1.644 V	1	0	0	1	0	0	1.132 V
0	0	0	1	0	1	1.628 V	1	0	0	1	0	1	1.116 V
0	0	0	1	1	0	1.612 V	1	0	0	1	1	0	1.100 V
0	0	0	1	1	1	1.596 V	1	0	0	1	1	1	1.084 V
0	0	1	0	0	0	1.580 V	1	0	1	0	0	0	1.068 V
0	0	1	0	0	1	1.564 V	1	0	1	0	0	1	1.052 V
0	0	1	0	1	0	1.548 V	1	0	1	0	1	0	1.036 V
0	0	1	0	1	1	1.532 V	1	0	1	0	1	1	1.020 V
0	0	1	1	0	0	1.516 V	1	0	1	1	0	0	1.004 V
0	0	1	1	0	1	1.500 V	1	0	1	1	0	1	0.988 V
0	0	1	1	1	0	1.484 V	1	0	1	1	1	0	0.972 V
0	0	1	1	1	1	1.468 V	1	0	1	1	1	1	0.956 V
0	1	0	0	0	0	1.452 V	1	1	0	0	0	0	0.940 V
0	1	0	0	0	1	1.436 V	1	1	0	0	0	1	0.924 V
0	1	0	0	1	0	1.420 V	1	1	0	0	1	0	0.908 V
0	1	0	0	1	1	1.404 V	1	1	0	0	1	1	0.892 V
0	1	0	1	0	0	1.388 V	1	1	0	1	0	0	0.876 V
0	1	0	1	0	1	1.372 V	1	1	0	1	0	1	0.860 V
Highest Freq.	0	1	0	1	1	1.356 V	1	1	0	1	1	0	0.844 V
	0	1	0	1	1	1.340 V	1	1	0	1	1	1	0.828 V
0	1	1	0	0	0	1.324 V	1	1	1	0	0	0	0.812 V
0	1	1	0	0	1	1.308 V	1	1	1	0	0	1	0.796 V
0	1	1	0	1	0	1.292 V	1	1	1	0	1	0	0.780 V
0	1	1	0	1	1	1.276 V	1	1	1	0	1	1	0.764 V
0	1	1	1	0	0	1.260 V	1	1	1	1	0	0	0.748 V
0	1	1	1	0	1	1.244 V	1	1	1	1	0	1	0.732 V
0	1	1	1	1	0	1.228 V	1	1	1	1	1	0	0.716 V
0	1	1	1	1	1	1.212 V	1	1	1	1	1	1	0.700 V

IC / SMB Address

Devices	Address	Hex	Bus
ICH6	Master	-	SMBUS Master
EMC6N300(CPU Thermal Sensor)	1001 110X	9Ch	Thermal Sensor
SODIMM0	1010 0000	A0h	-
SODIMM1	1010 001X	A2h	-
CK-408 (Clock Generator)	1101 001X	D2h	Clock, Unused Clock Output Disable

USB PORT Assign

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1,2	SYSTEM PORT B
3	BLUETOOTH
4	FINGER PRINT
	OPTION
	OPTION

System Power States

- CHP3_SLPS1* S1, Powered-On-Suspend(POS) : In this state, all clocks(except the 32.768KHz clock) are stopped.
The system context is maintained in system DRAM. Power is maintained to PCI, the CPU, memory controller, memory, and all other critical subsystems.
Note that this state does not preclude power being removed from non-essential devices, such as disk drives. During this state, CPU can be selected for either Deep Sleep or Deeper Sleep.
In Deeper Sleep, CPU voltage reduced in this state to reduce the leakage power.
- CHP3_SLPS3* S3, Suspend-To-RAM(STR) : The system context is maintained in system DRAM, but power is shut off to non-critical circuits.
Memory is retained, and refreshes continue. All clocks stop except RTC clock.
- CHP3_SLP4S* S4, Suspend-To-Disk(STD) : The Context of the system is maintained on the disk. All power is then shut off to the system except for the logic required to resume.
Externally appears same as S5, but may have different wake events.
- CHP3_SLPS5* S5, Soft Off(SOFF) : System context is not maintained. All power is shut off except for the logic required to restart. A full boot is required when waking.

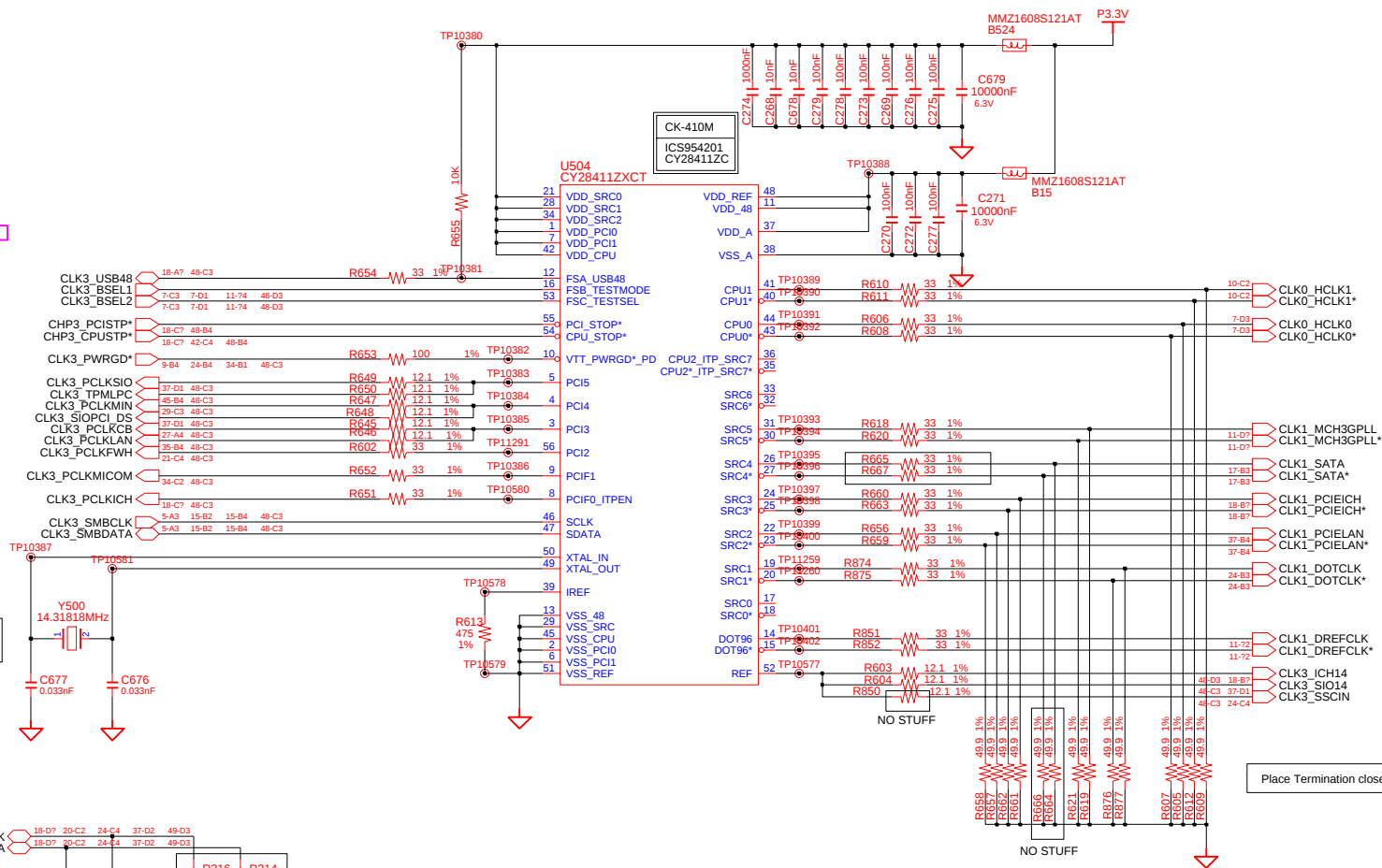
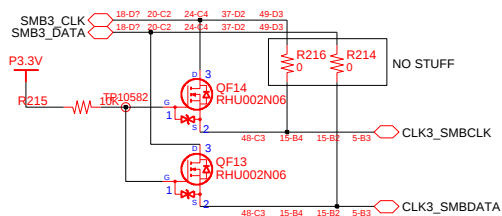
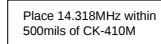
REVISION HISTORY

See rev notes in the changes file for more information.

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CHECK	BIN, KK	DEV. STEP	MP		MAIN	ELECTRONICS
APPROVAL	KIM, DW	REV	1.0	BOARD INFORMATION		PART NO. BA41-00451A
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	4	OF 49

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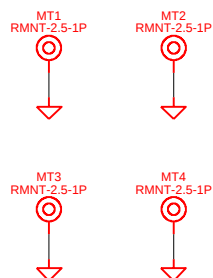
FSA BSEL2	FSB BSEL1	FSC BSEL0	HOST CLK
0	0	0	266 MHz
0	0	1	333 MHz
0	1	0	200 MHz
0	1	1	400 MHz
1	0	0	133 MHz
1	0	1	100 MHz
1	1	0	166 MHz
1	1	1	RSVD



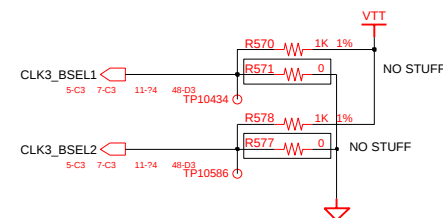
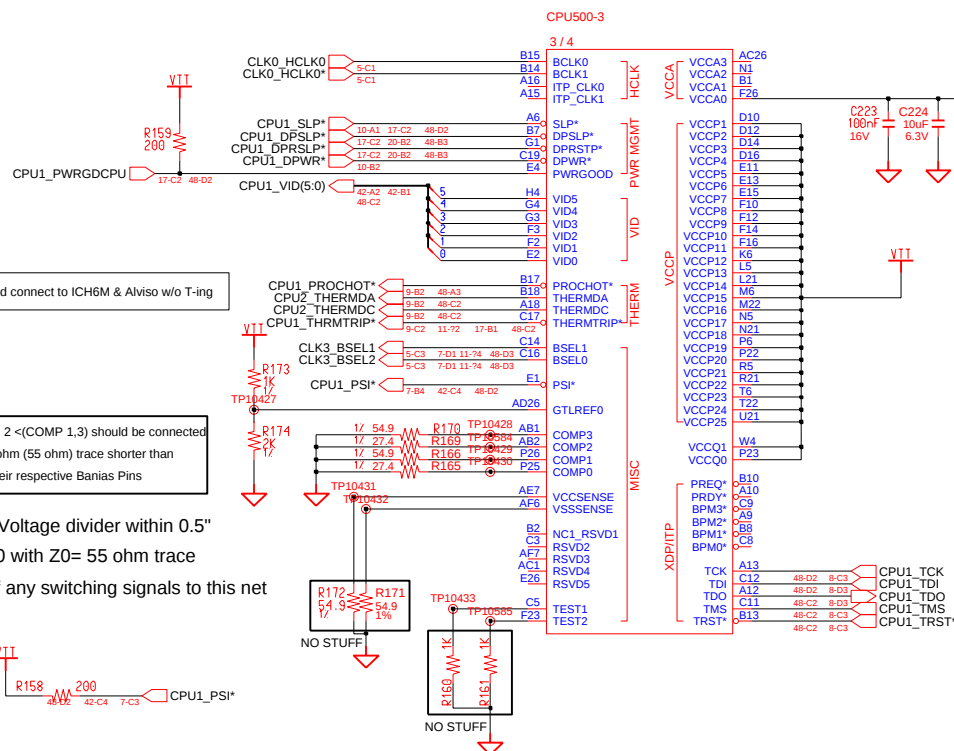
Place Termination close to CK-410M

DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C MAIN CLOCK GENERATOR	
CHECK	BIN, KK	DEV. STEP	MP			
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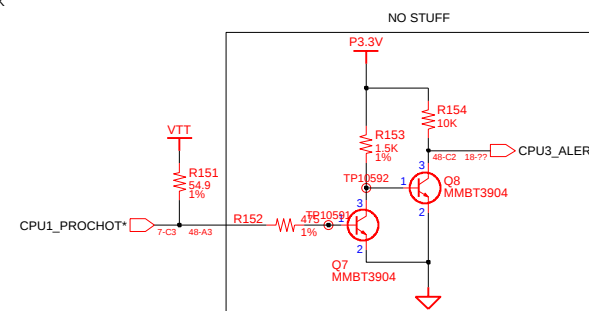


CPU Core Voltage Table

VID(5.0)				Voltage		VID(5.0)				Voltage	
0	0	0	0	0	1.708 V	1	0	0	0	0	1.196 V
0	0	0	0	0	1.692 V	1	0	0	0	0	1.180 V
0	0	0	0	1	1.676 V	1	0	0	0	1	1.164 V
0	0	0	0	1	1.660 V	1	0	0	0	1	1.148 V
0	0	0	0	1	1.644 V	1	0	0	1	0	1.132 V
0	0	0	1	0	1.628 V	1	0	0	1	0	1.116 V
0	0	0	1	0	1.612 V	1	0	0	1	0	1.100 V
0	0	0	1	1	1.596 V	1	0	0	1	1	1.084 V
0	0	1	0	0	1.580 V	1	0	1	0	0	1.068 V
0	0	1	0	0	1.564 V	1	0	1	0	0	1.052 V
0	0	1	0	1	1.548 V	1	0	1	0	0	1.036 V
0	0	1	0	1	1.532 V	1	0	1	0	1	1.020 V
0	0	1	1	0	1.516 V	1	0	1	1	0	1.004 V
0	0	1	1	0	1.500 V	1	0	1	1	0	0.988 V
0	0	1	1	1	1.484 V	1	0	1	1	0	0.972 V
0	0	1	1	1	1.468 V	1	0	1	1	1	0.956 V
0	1	0	0	0	1.452 V	1	1	0	0	0	0.940 V
0	1	0	0	0	1.436 V	1	1	0	0	0	0.924 V
0	1	0	0	1	1.420 V	1	1	0	0	0	0.908 V
0	1	0	0	1	1.404 V	1	1	0	0	1	0.892 V
0	1	0	1	0	1.388 V	1	1	0	1	0	0.876 V
0	1	0	1	0	1.372 V	1	1	0	1	0	0.860 V
0	1	0	1	1	1.356 V	1	1	0	1	0	0.844 V
0	1	0	1	1	1.340 V	1	1	0	1	1	0.828 V
0	1	1	0	0	1.324 V	1	1	1	0	0	0.812 V
0	1	1	0	0	1.308 V	1	1	1	0	0	0.796 V
0	1	1	0	1	1.292 V	1	1	1	0	0	0.780 V
0	1	1	0	1	1.276 V	1	1	1	0	1	0.764 V
0	1	1	1	0	1.260 V	1	1	1	1	0	0.748 V
0	1	1	1	0	1.244 V	1	1	1	1	0	0.732 V
0	1	1	1	1	1.228 V	1	1	1	1	0	0.716 V
0	1	1	1	1	1.212 V	1	1	1	1	1	0.700 V

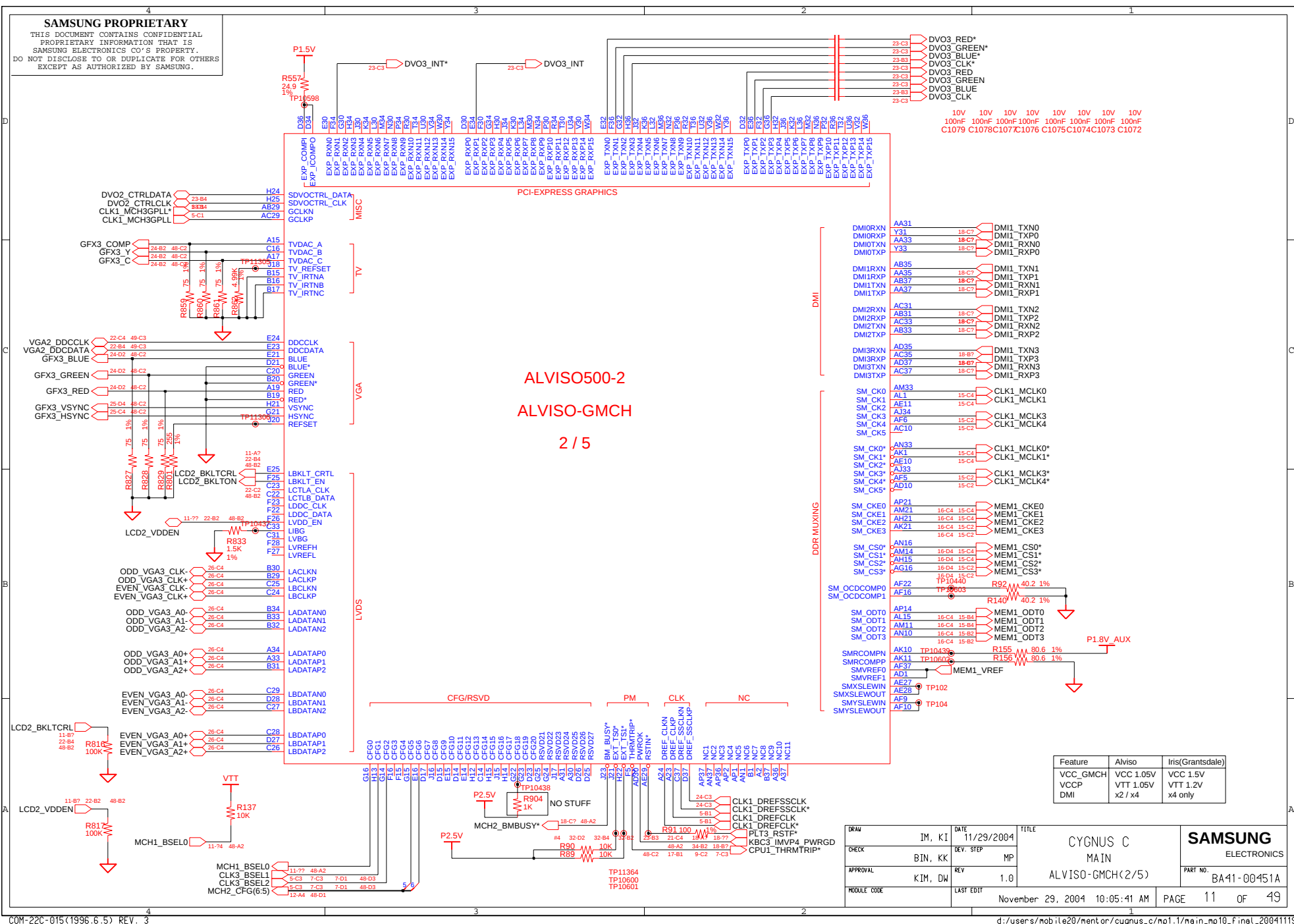
Highest Freq. of Dothan400(1.8GHz)
* Highest Freq. of Dothan533(?? GHz : TBD)

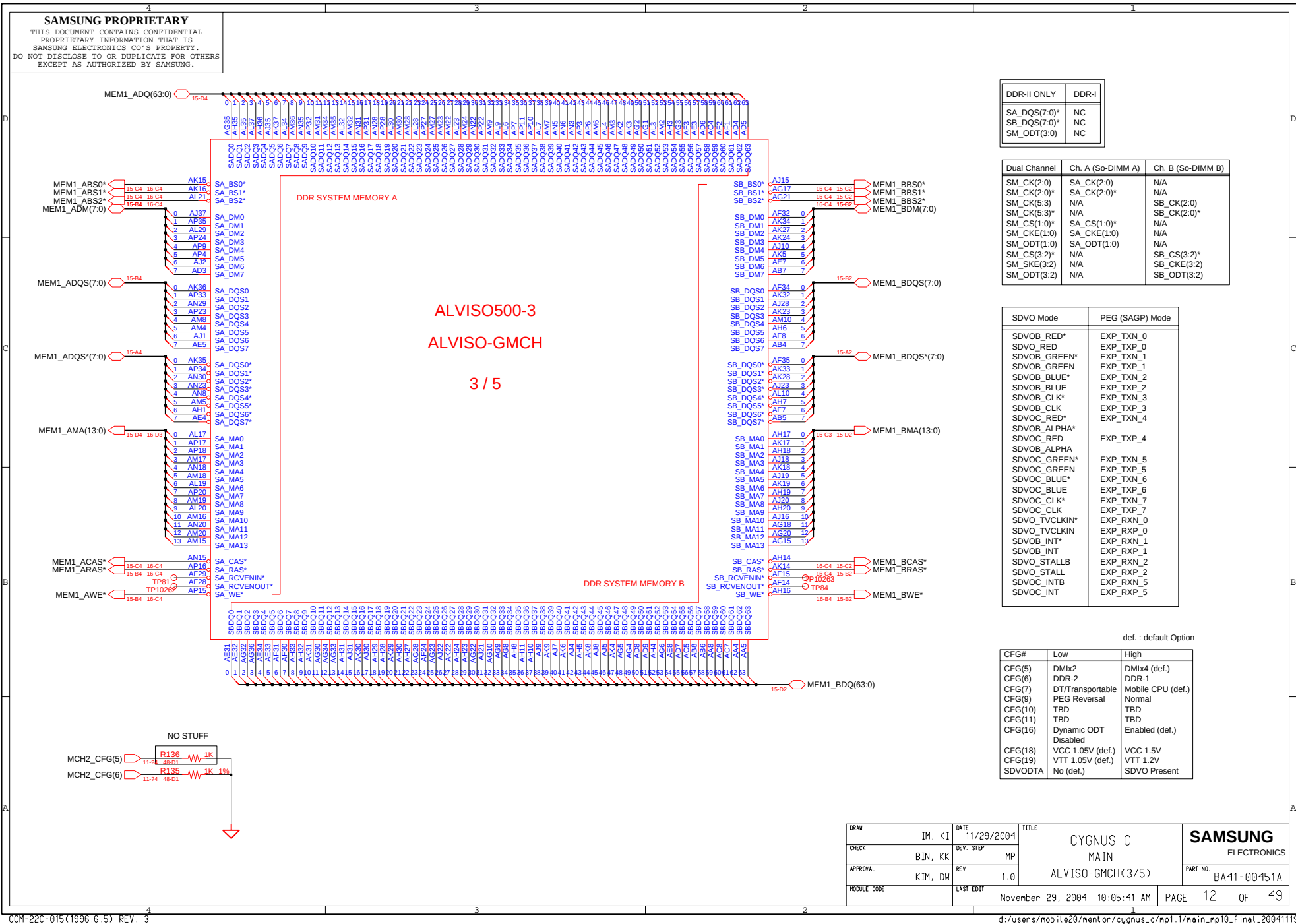
Refer To Thermal Sensor Layout Guidelines.



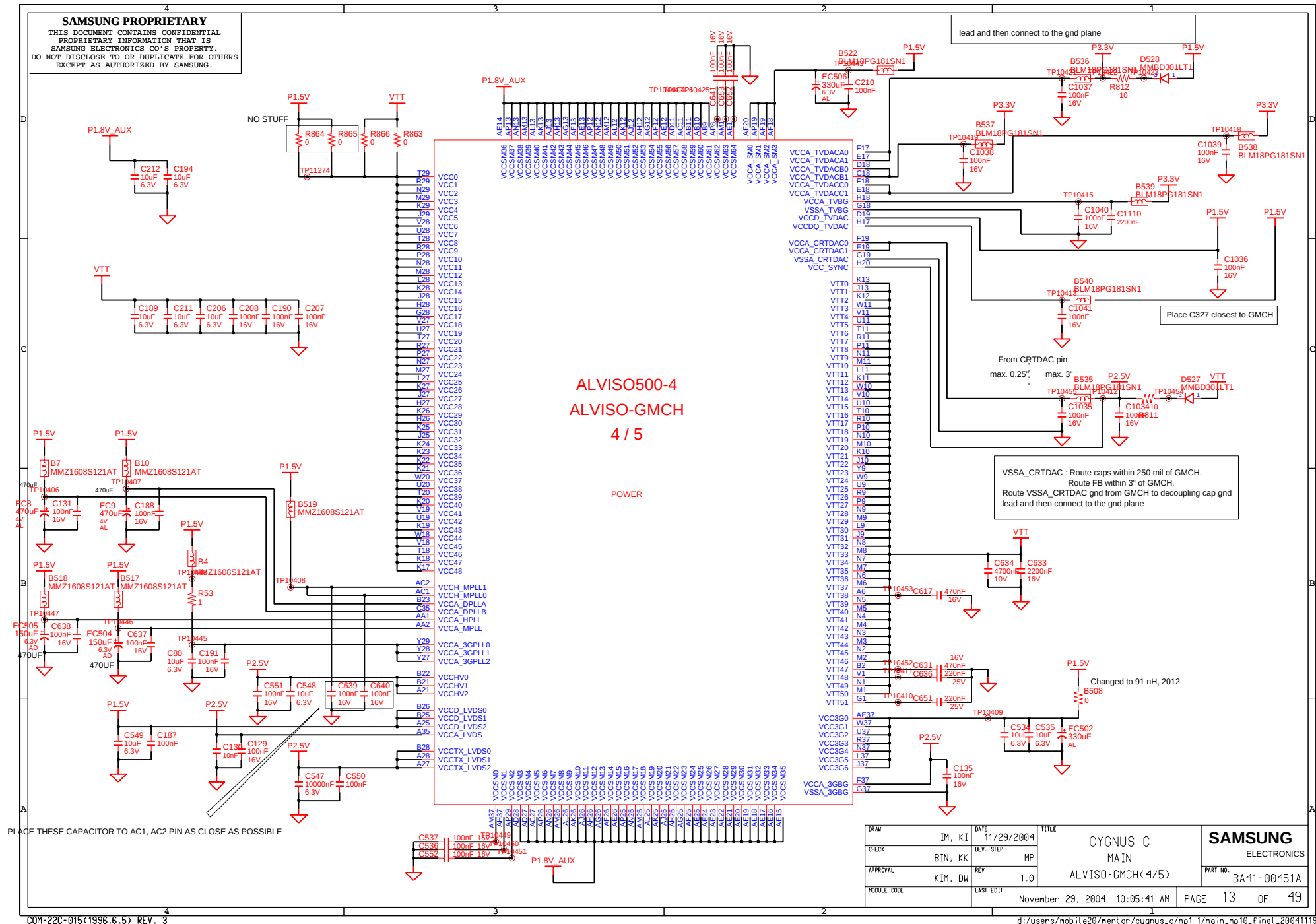
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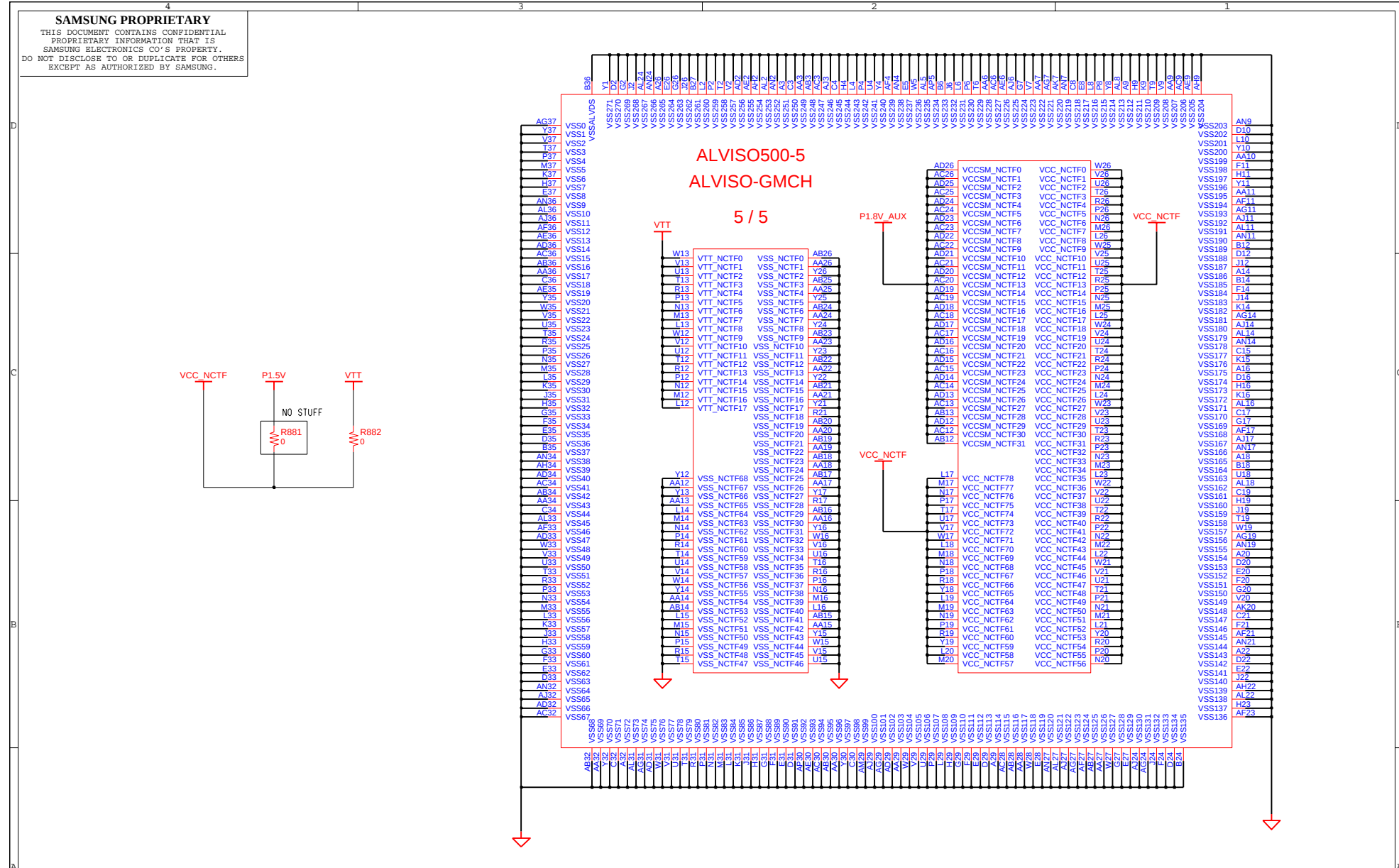




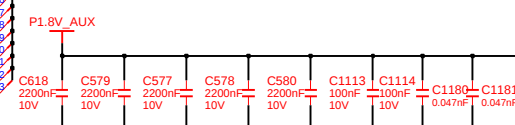
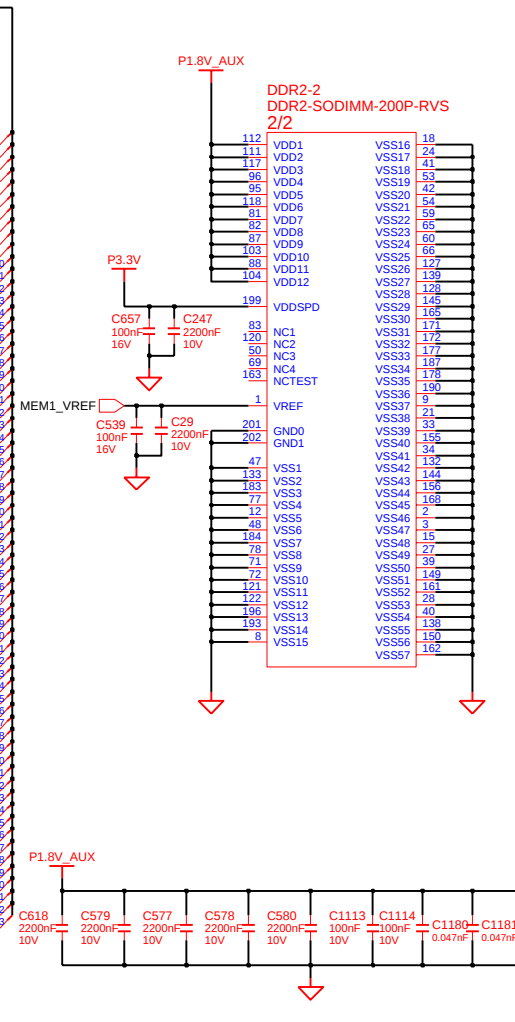
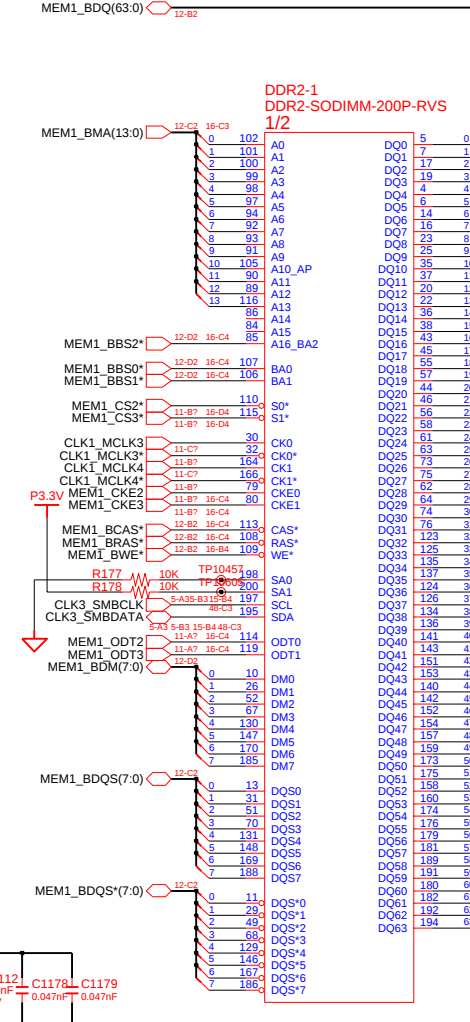
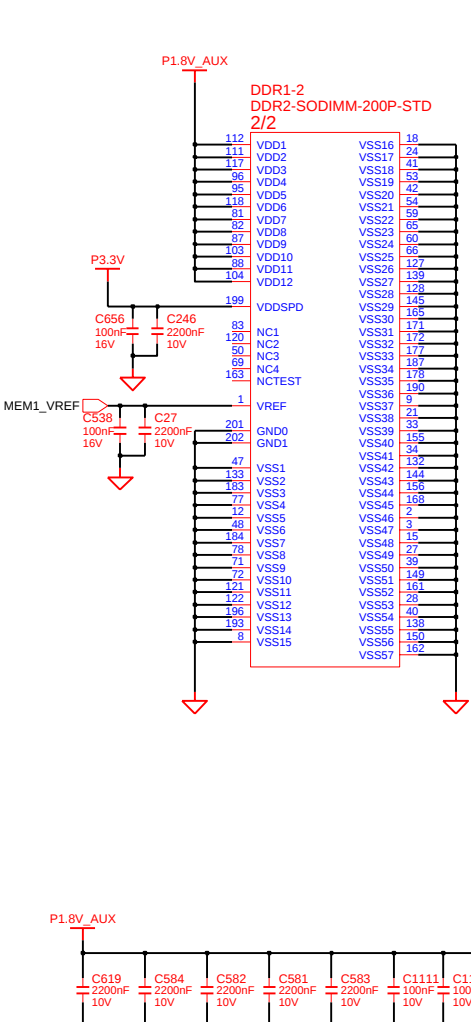
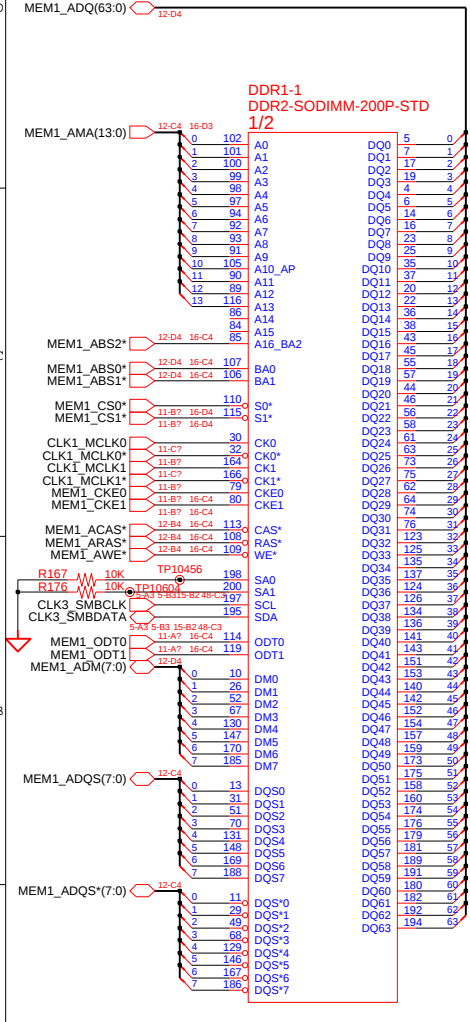
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DRAM	IM, K1	DATE	11/29/2004	CYGNUS C MAIN ALVISO-GMCH(5/5)	SAMSUNG ELECTRONICS	
CHECK	BIN, KK	DEV. STEP	MP			
APPROVAL	KIM, DW	REV	1.0		PART NO.	BA41-00451A
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CHECK	BIN, KK	DEV. STEP	MP	MAIN	MAIN	ELECTRONICS
APPROVAL	KIM, DW	REV	1.0	DDR - SODIMM	PART NO.	BA41-00451A
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Memory Topology
(Dual channel for DDR-II)

VCCSM VCCA_SM P1.8V_AUX P3.3V

Channel A Alviso-GM Channel B

DDR-II :: P1.8V_AUX P1.5V

Address CS CKE BS CAS/RAS/WE DQ(63:0) CLK

DDR-II Standard Connector Memory Channel A

DDR-II Reverse Connector Memory Channel B

P0.9V

MEM1_AMA(13:0) 12-C4 15-D4

MEM1_BMA(13:0) 12-C2 15-D2

MEM1_CS0* MEM1_CS1* MEM1_CS2* MEM1_CS3*

MEM1_CKE0 MEM1_CKE1 MEM1_CKE2 MEM1_CKE3

MEM1_ODT0 MEM1_ODT1 MEM1_ODT2 MEM1_ODT3

MEM1_ABS0* MEM1_ABS1* MEM1_ABS2*

MEM1_ACAS* MEM1_ARAS* MEM1_AWE*

MEM1_BBS0* MEM1_BBS1* MEM1_BBS2*

MEM1_BCAS* MEM1_BRAS* MEM1_BWE*

C213 C192 C193 C209 C1130 C1131 C1165 C1166 C1167 C553 C621 C620 C554 C1132 C1133 C1134 C1135 C1168 C1169 C1170 C622 C555 C556 C623 C1136 C1137 C1138 C1139 C1171 C1172 C1173

C597 C598 C599 C602 C603 C592 C588 C590 C580 C592 C601 C604 C609 C592 C587 C588 C600 C608 C607 C589 C599 C603 C594 C599 C610 C604 C1158 C1159 C1160 C1161 C1174 C1175 C1176 C1177

Place near GMCH

Place near SO-DIMM0

Place near SO-DIMM1

Place one cap close to every 2 pull-up resistors terminated to P0.9V

DRAW IM, KI DATE 11/29/2004 TITLE CYGNUS C MAIN DDR - TERMINATION

CHECK BIN, KK DEV. STEP MP

APPROVAL KIM, DW REV 1.0

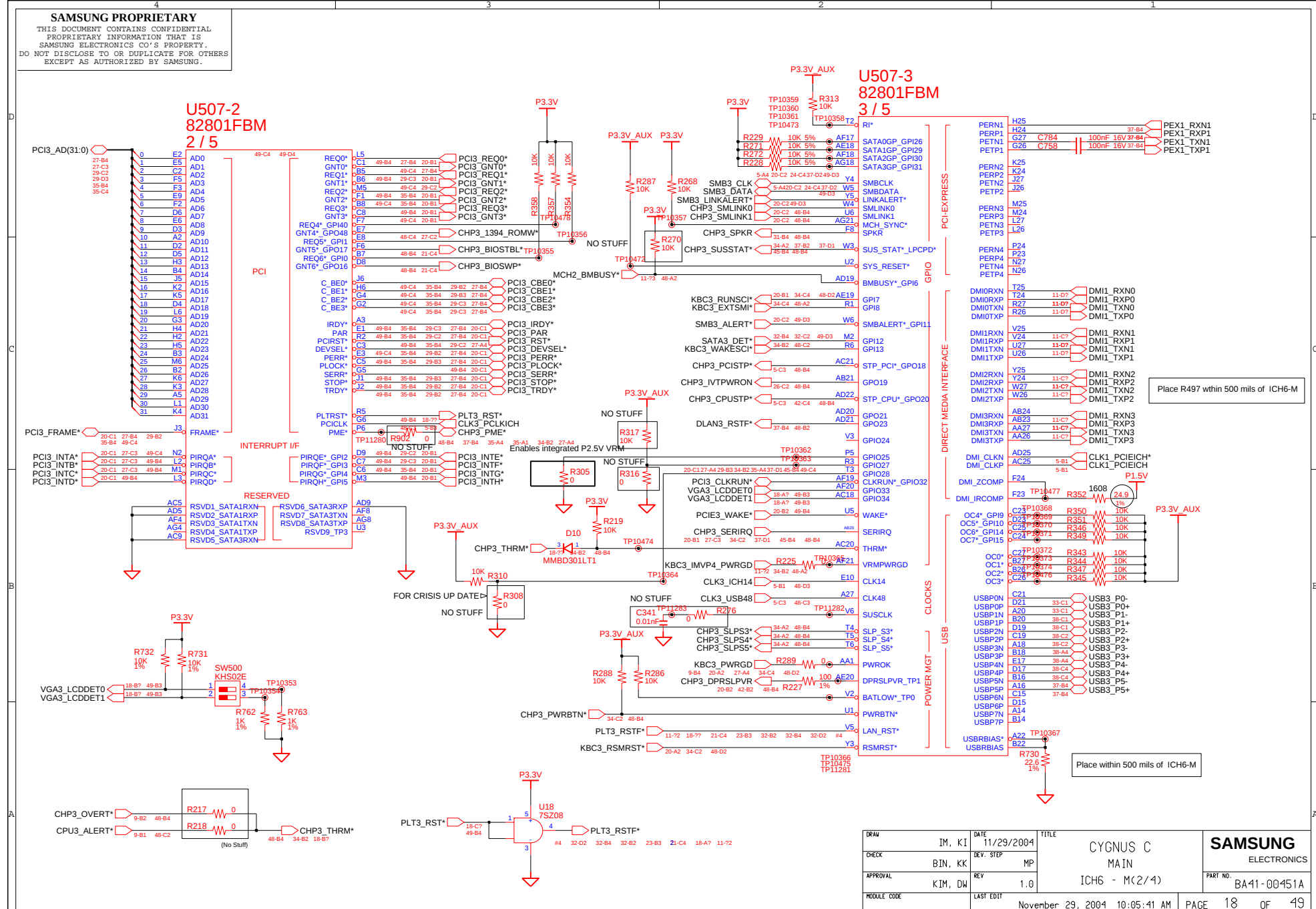
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SAMSUNG ELECTRONICS PART NO. BA41-00451A

x1 / 2 Docking			
ACZ_SYNC	PORT X Line	PU	PD
1	1x2 / 2x1	Stuff	No Stuff
0	4x1	No Stuff	Stuff

CPUSLP*
Dothan A stepping : Stuff resistor on SLP* of ICH6 Dothan B stepping ~ : Stuff resistor on SLP* of Alviso
DPRSLP*
Dothan A stepping : No Stuff Dothan B stepping ~ : Stuff

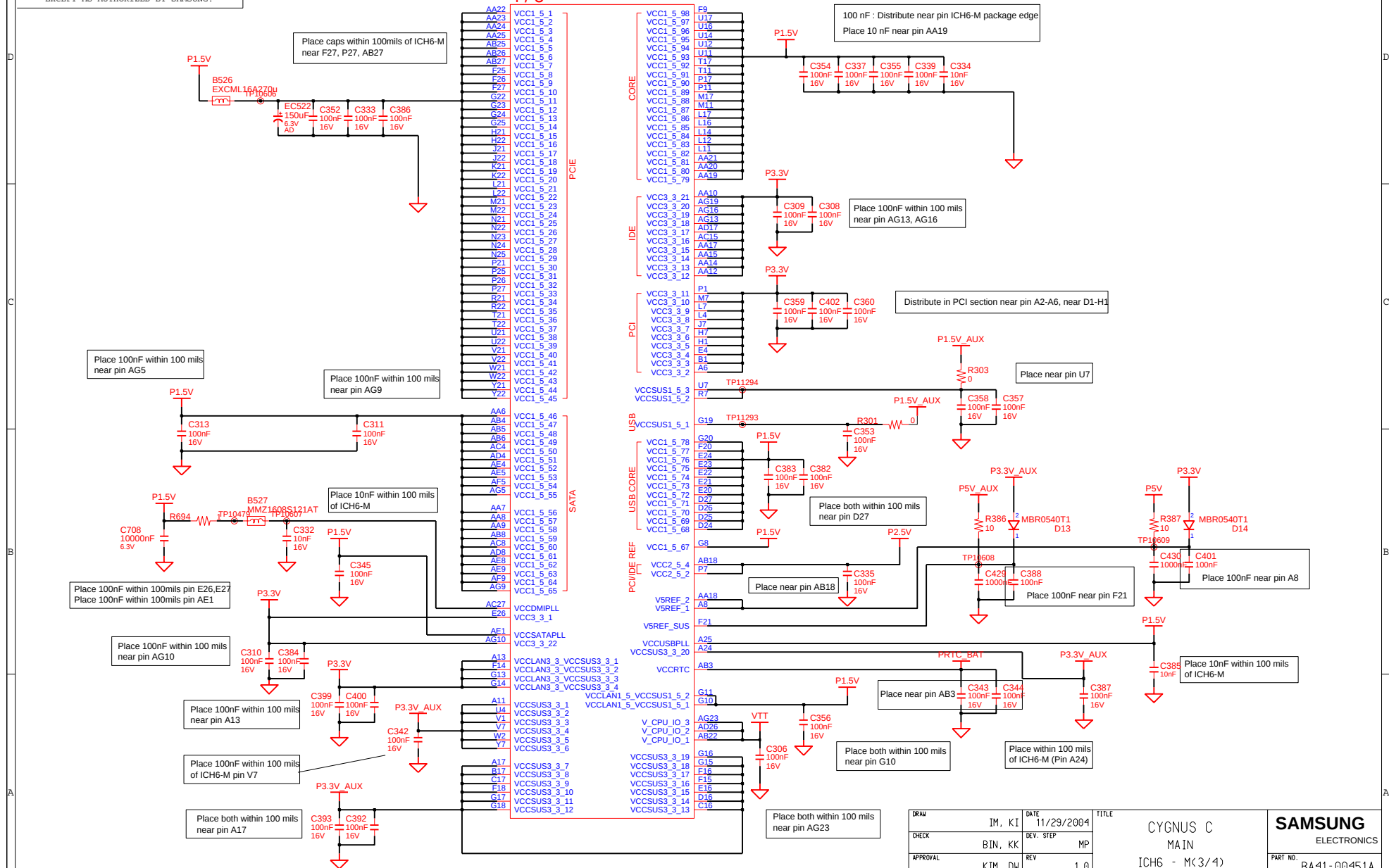
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APPROVAL	KIM, DW	REV	1.0				
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U507-4
82801FBM
4 / 5

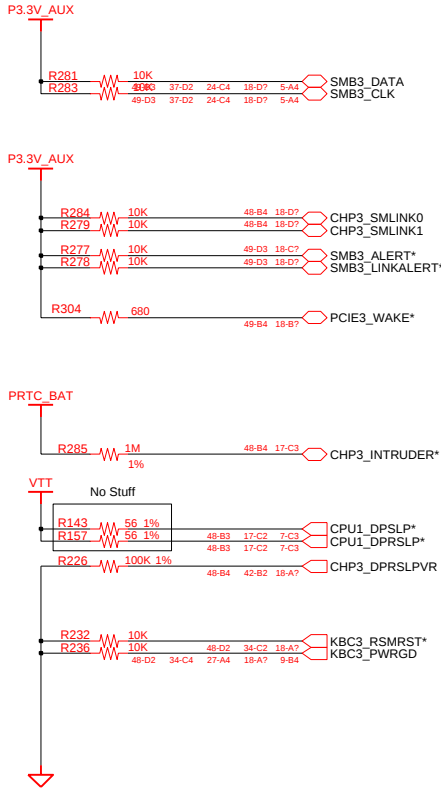


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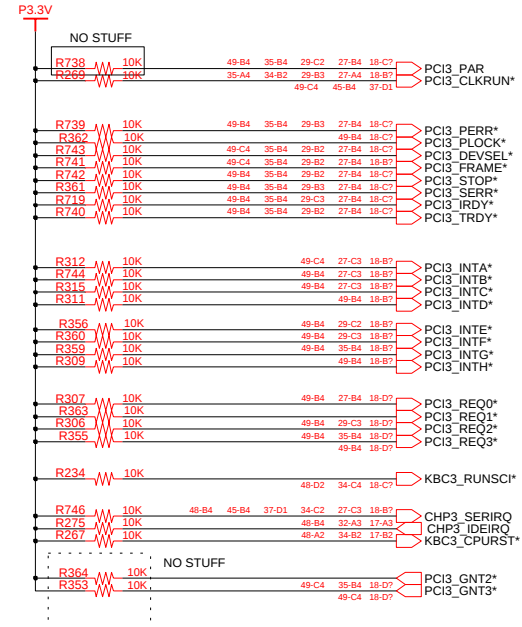
U507-5
82801FBM
5 / 5

AG22	VSS_172	VSS_86	P22
AG20	VSS_171	VSS_85	P16
AG17	VSS_170	VSS_84	P15
AG14	VSS_169	VSS_83	P14
AG12	VSS_168	VSS_82	P13
AG7	VSS_167	VSS_81	P12
AG3	VSS_166	VSS_80	N17
AG1	VSS_165	VSS_79	N16
AF26	VSS_164	VSS_78	N15
AF12	VSS_163	VSS_77	N14
AF10	VSS_162	VSS_76	N13
AF7	VSS_161	VSS_75	N12
AF3	VSS_160	VSS_74	N11
AF1	VSS_159	VSS_73	N7
AE25	VSS_158	VSS_72	N1
AE21	VSS_157	VSS_71	M27
AE12	VSS_156	VSS_70	M26
AE11	VSS_155	VSS_69	M25
AE10	VSS_154	VSS_68	M15
AE7	VSS_153	VSS_67	M14
AE6	VSS_152	VSS_66	M13
AE2	VSS_151	VSS_65	M12
AD24	VSS_150	VSS_64	M4
AD18	VSS_149	VSS_63	L25
AD15	VSS_148	VSS_62	L24
AD10	VSS_147	VSS_61	L23
AD6	VSS_146	VSS_60	L15
AD2	VSS_145	VSS_59	L13
AD1	VSS_144	VSS_58	K26
AC24	VSS_143	VSS_57	K27
AC23	VSS_142	VSS_56	K28
AC22	VSS_141	VSS_55	K7
AC12	VSS_140	VSS_54	K1
AC10	VSS_139	VSS_53	J25
AC6	VSS_138	VSS_52	J24
AC3	VSS_137	VSS_51	J23
AB19	VSS_136	VSS_50	J4
AB10	VSS_135	VSS_49	H27
AB9	VSS_134	VSS_48	H23
AB7	VSS_133	VSS_47	H25
AB2	VSS_132	VSS_46	G21
AB1	VSS_131	VSS_45	G12
AA16	VSS_129	VSS_43	G9
AA13	VSS_128	VSS_42	G1
AA11	VSS_127	VSS_41	F22
AA4	VSS_126	VSS_40	F19
Y27	VSS_125	VSS_39	F17
Y26	VSS_124	VSS_38	F4
Y23	VSS_123	VSS_37	E27
Y6	VSS_122	VSS_36	E25
W25	VSS_121	VSS_35	E13
W24	VSS_120	VSS_34	E14
W23	VSS_119	VSS_33	E15
W1	VSS_118	VSS_32	E14
W7	VSS_117	VSS_31	D22
V27	VSS_116	VSS_30	D23
V26	VSS_115	VSS_29	D19
V23	VSS_114	VSS_28	D14
V4	VSS_113	VSS_27	D13
U25	VSS_112	VSS_26	D10
U24	VSS_111	VSS_25	D7
U23	VSS_110	VSS_24	D1
U15	VSS_109	VSS_23	C22
U13	VSS_108	VSS_22	C20
T27	VSS_107	VSS_21	C12
T26	VSS_106	VSS_20	C14
T23	VSS_105	VSS_19	C4
T16	VSS_104	VSS_18	B25
T15	VSS_103	VSS_17	B24
T14	VSS_102	VSS_16	B23
T13	VSS_101	VSS_15	B21
T12	VSS_100	VSS_14	B19
T7	VSS_99	VSS_13	B15
T1	VSS_98	VSS_12	B14
R25	VSS_97	VSS_11	B13
R24	VSS_96	VSS_10	A26
R23	VSS_95	VSS_9	A23
R17	VSS_94	VSS_8	A21
R16	VSS_93	VSS_7	A19
R15	VSS_92	VSS_6	A15
R14	VSS_91	VSS_5	A12
R13	VSS_90	VSS_4	A9
R12	VSS_89	VSS_3	A7
R11	VSS_88	VSS_2	A4
R4	VSS_87	VSS_1	A1

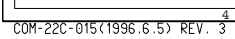


ICH6-m Strapping Options

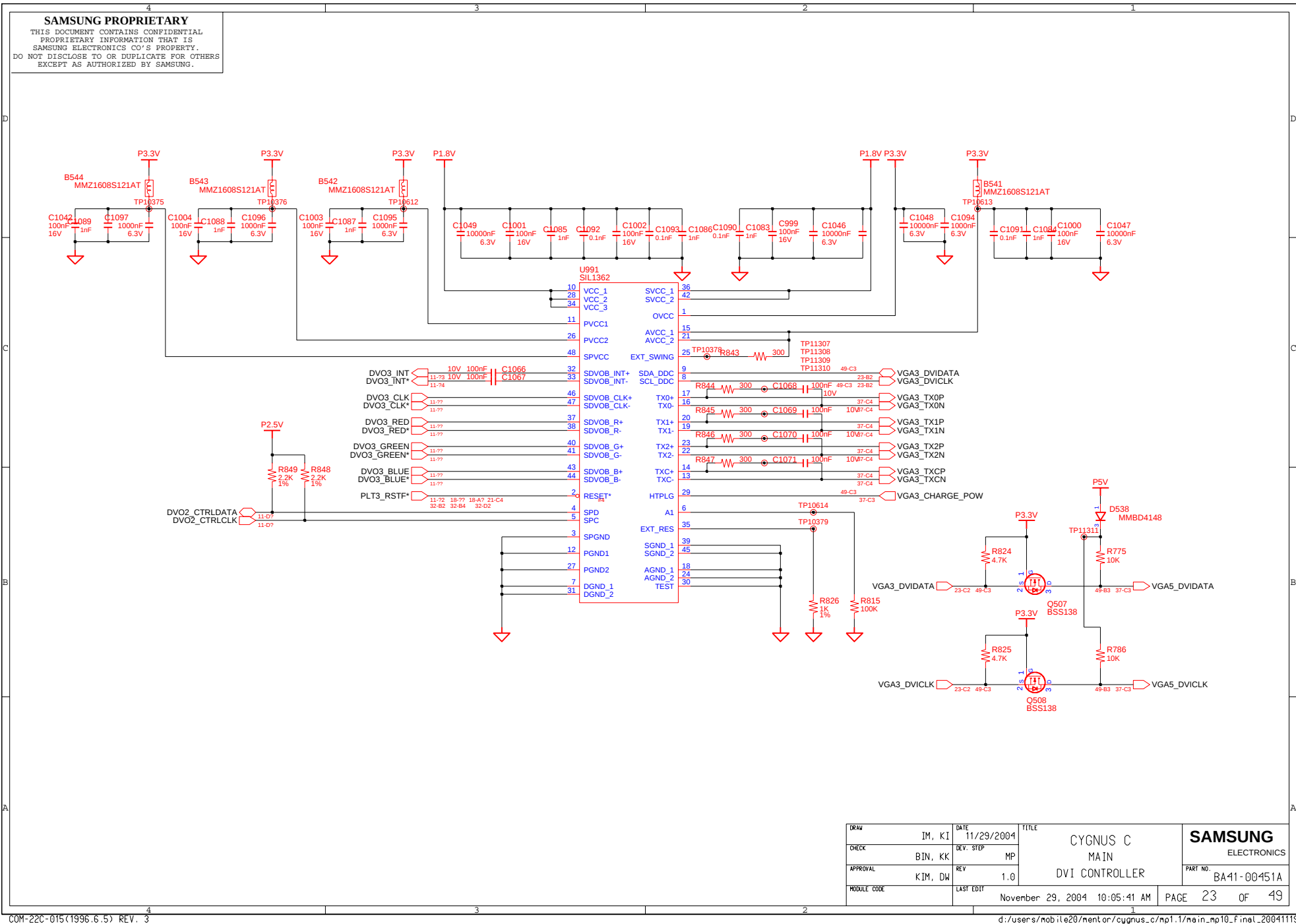
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CHP3_SPKR	No Reboot	No Stuff
CHP3_BIOSWP*	Boot BIOS	No Stuff
PC/PCI GNTA*	A16 swap override	No Stuff
AC97_SDOUT	Safe Mode	TBD
EEP_DOUT		TBD



DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C	SAMSUNG
CHECK	BIN, KK	DEV. STEP	MP		MAIN	ELECTRONICS
APPROVAL	KIM, DW	REV	1.0		ICH6 - M(4/4)	PART NO. BA41-00451A
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	20	OF 49

2

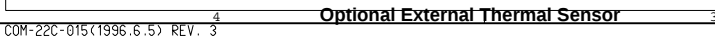
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1

COM-22C-015(1996.6.5) REV. 3

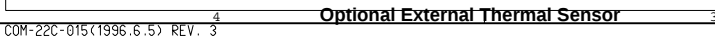


COM-22C-015(1996.6.5) REV. 3

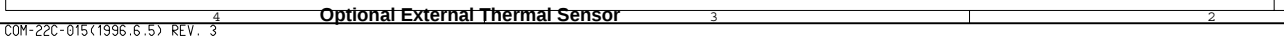
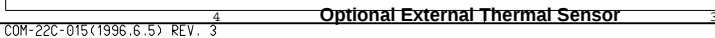
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COM-22C-015(1996.6.5) REV. 3

COM-22C-015(1996.6.5) REV. 3



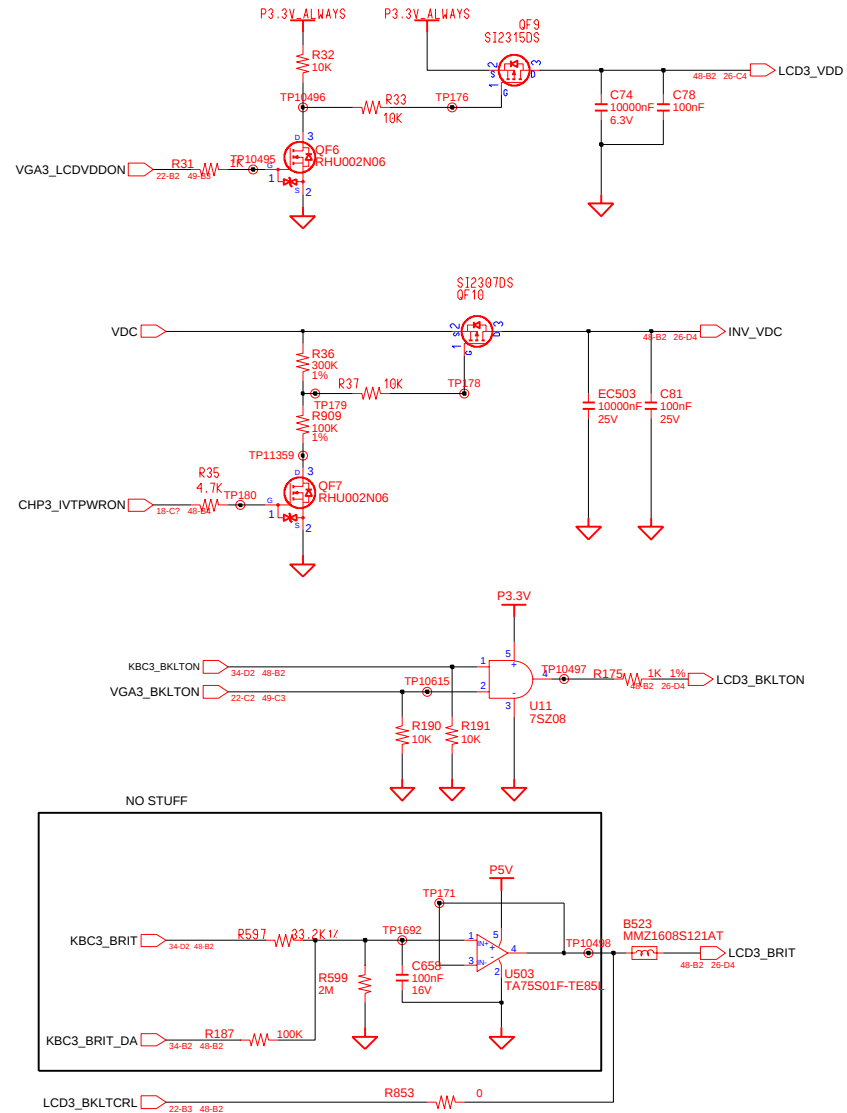
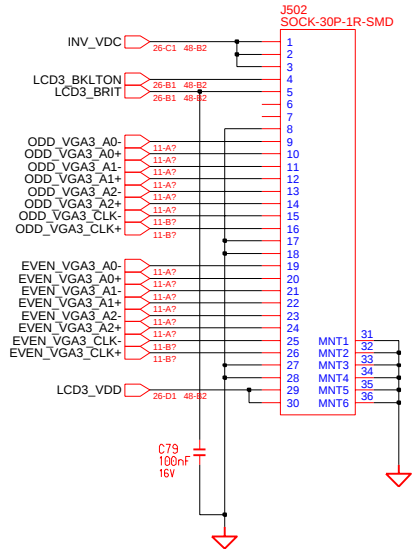
COM-22C-015(1996.6.5) REV. 3



COM-22C-015(1996.6.5) REV. 3

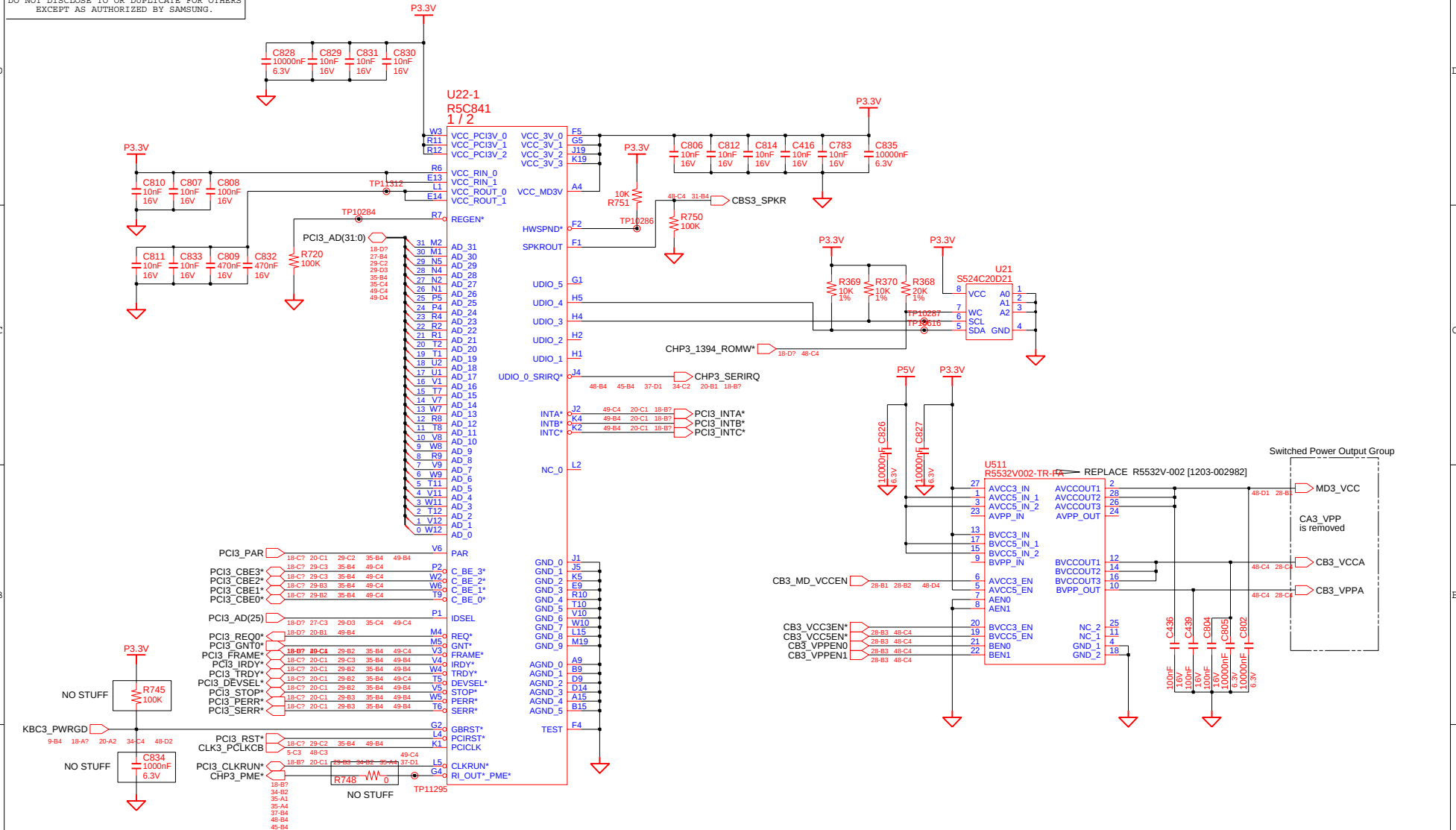
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LCD CONNECTOR



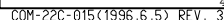
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CHECK	BIN, KK	DEV. STEP	MP			PART NO. BA41-00451A
APPROVAL	KIM, DW	REV	1.0			
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	26	OF 49

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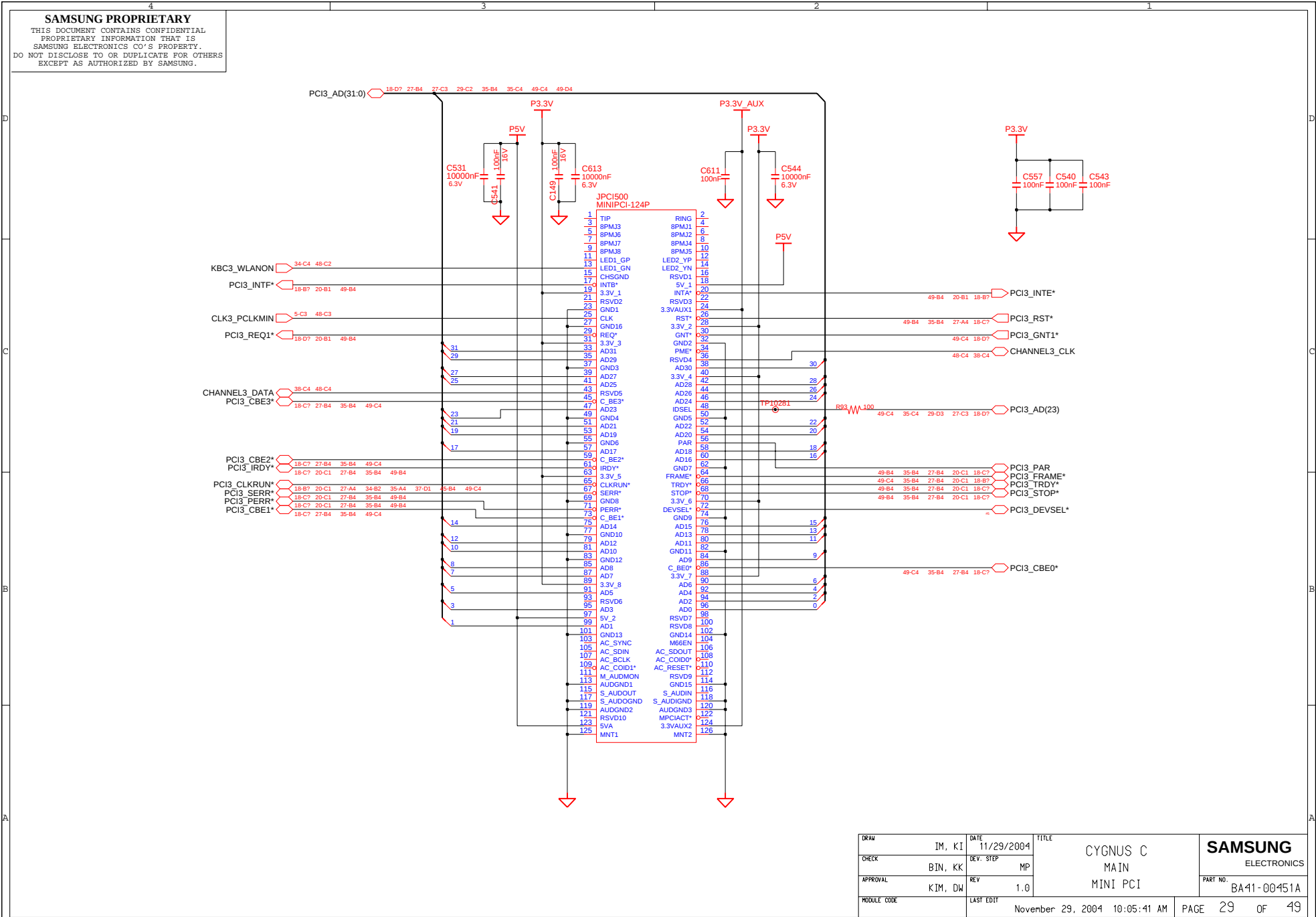


DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C MAIN CARDBUS / 1394	SAMSUNG ELECTRONICS PART NO. BA41-00451A
CHECK	BIN, KK	DEV. STEP	MP			
APPROVAL	KIM, DW	REV	1.0			
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	27 OF 49	

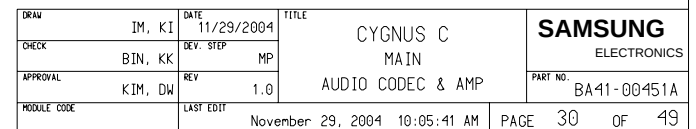
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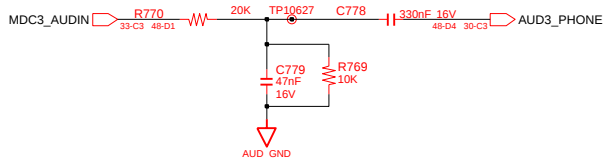
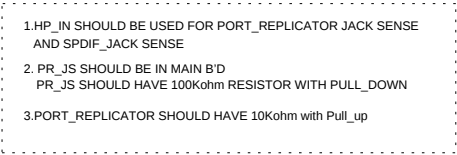


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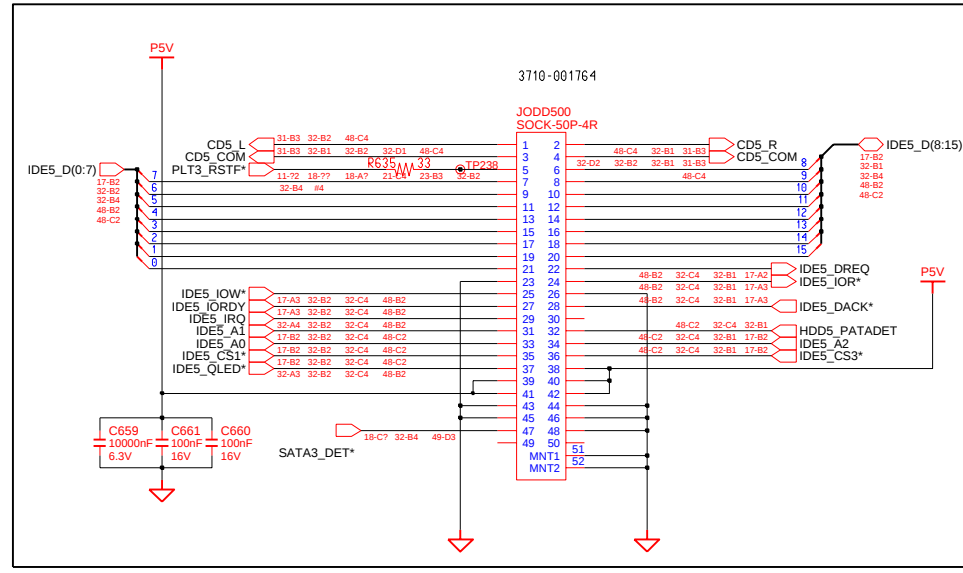


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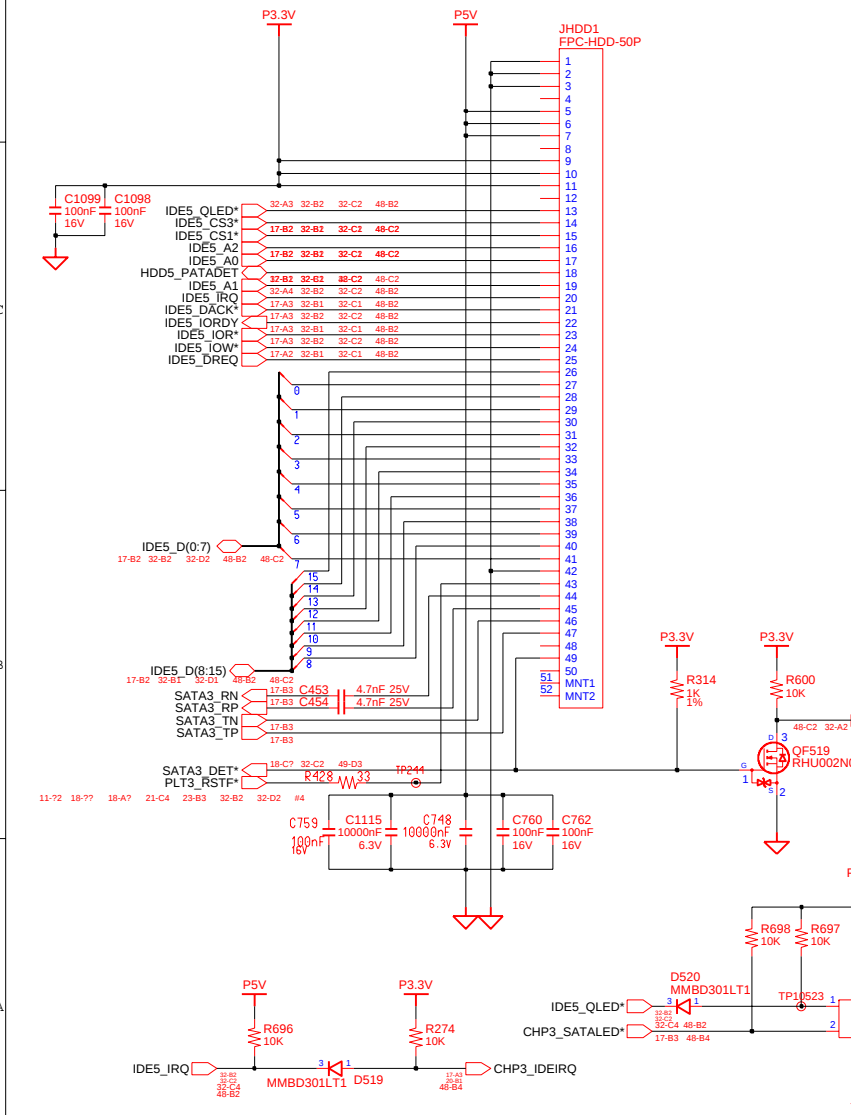
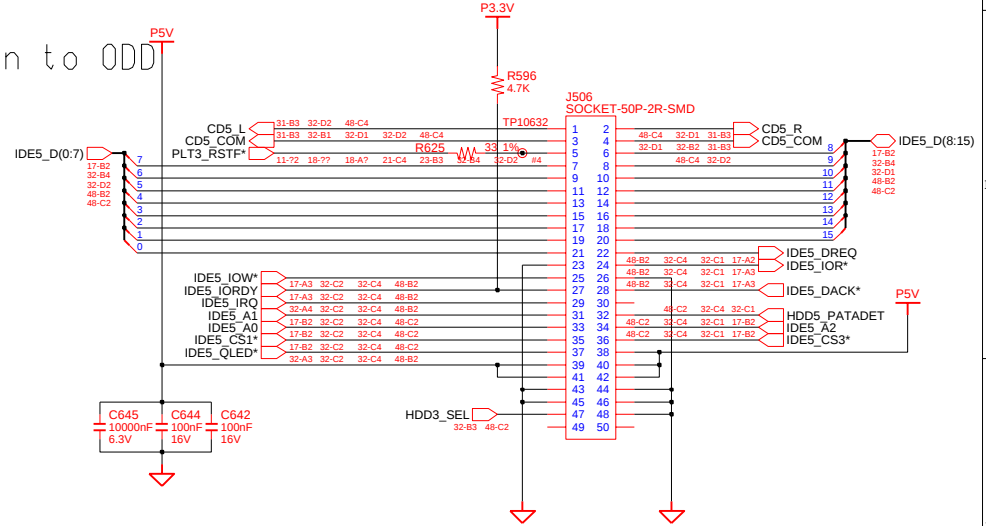
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Main to HDD

NO STUFF



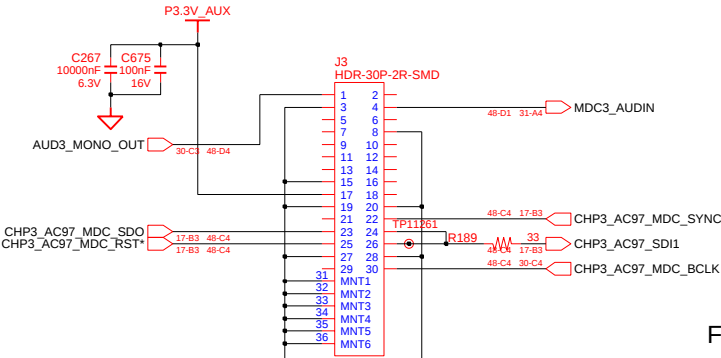
Main to ODD



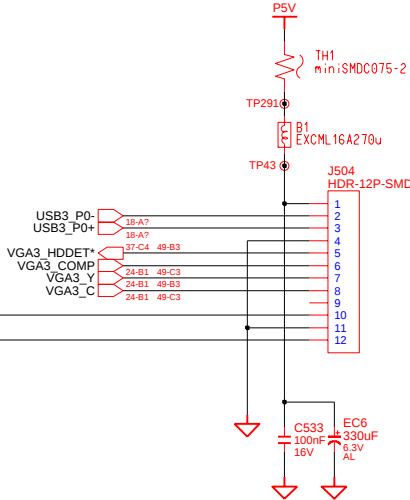
DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C	SAMSUNG ELECTRONICS
CHECK	BIN, KK	DEV. STEP	MP	MAIN		
APPROVAL	KIM, DW	REV	1.0	HDD & ODD CONNECTOR	PART NO.	BA41-00451A
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	32	OF 49

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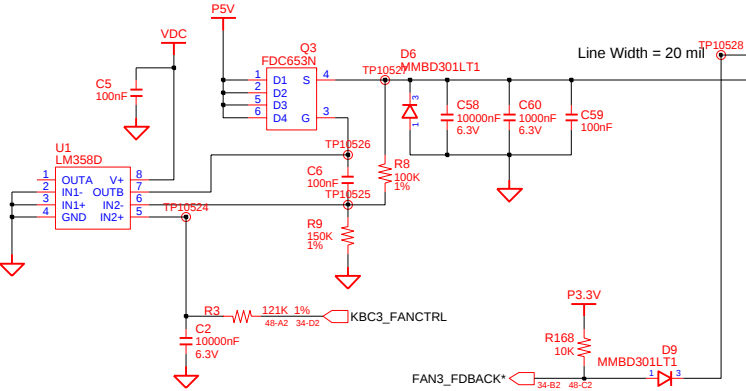
MDC Connector



TV-OUT(S-VHS)

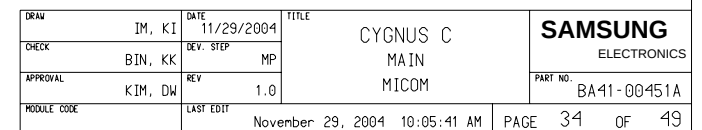


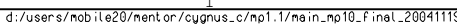
FAN Control Logic



DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C	SAMSUNG ELECTRONICS
CHECK	BIN, KK	DEV. STEP	MP	MAIN	MAIN	
APPROVAL	KIM, DW	REV	1.0	MDC MODEM / TV-OUT(SVHS)	PART NO.	BA41-00451A
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	33	OF 49

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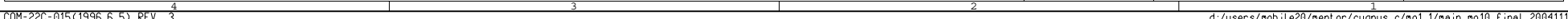




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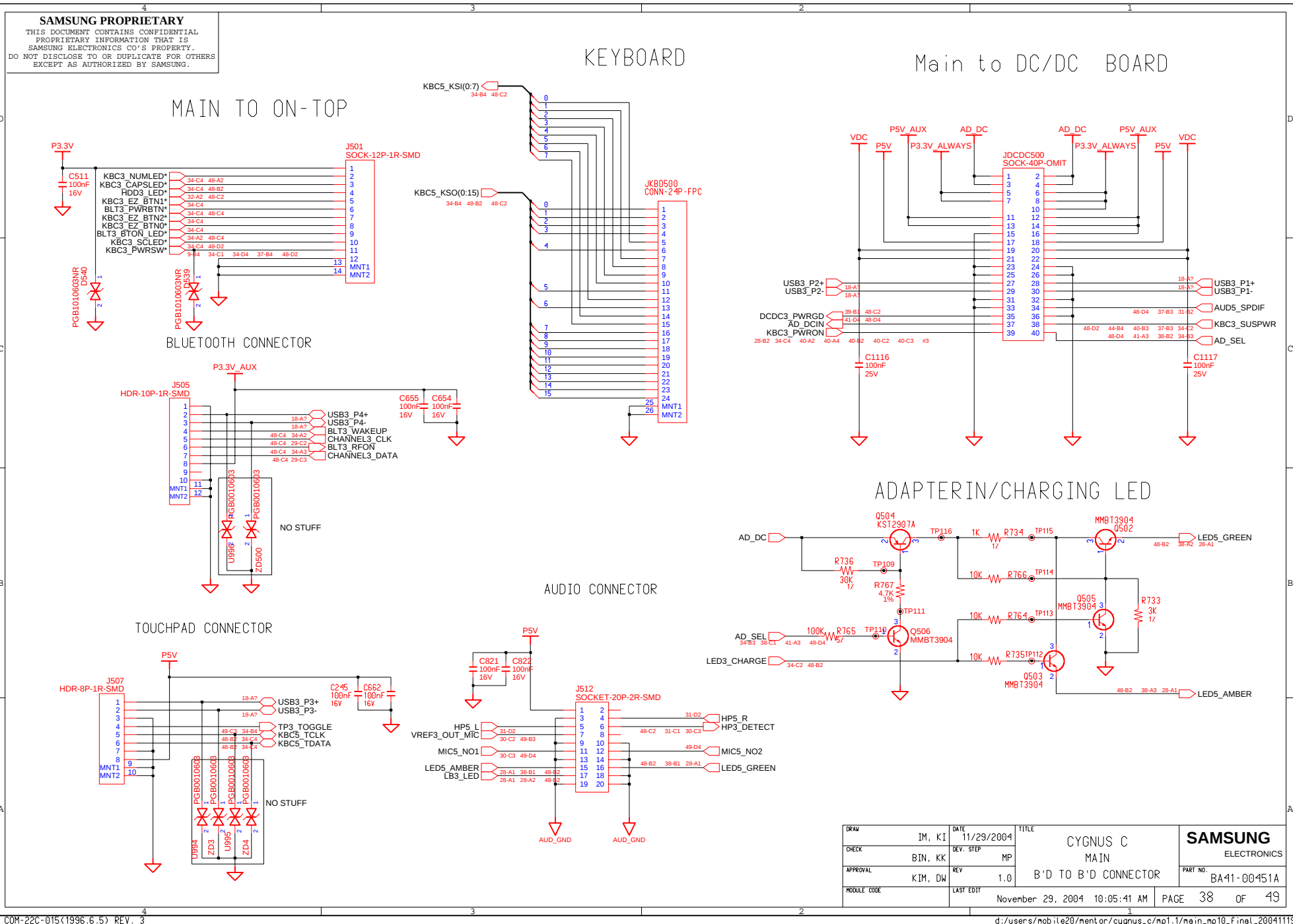
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 Docking Placement			
PWR1	1	129	PWR4
Top View			
PWR3	2	130	PWR2

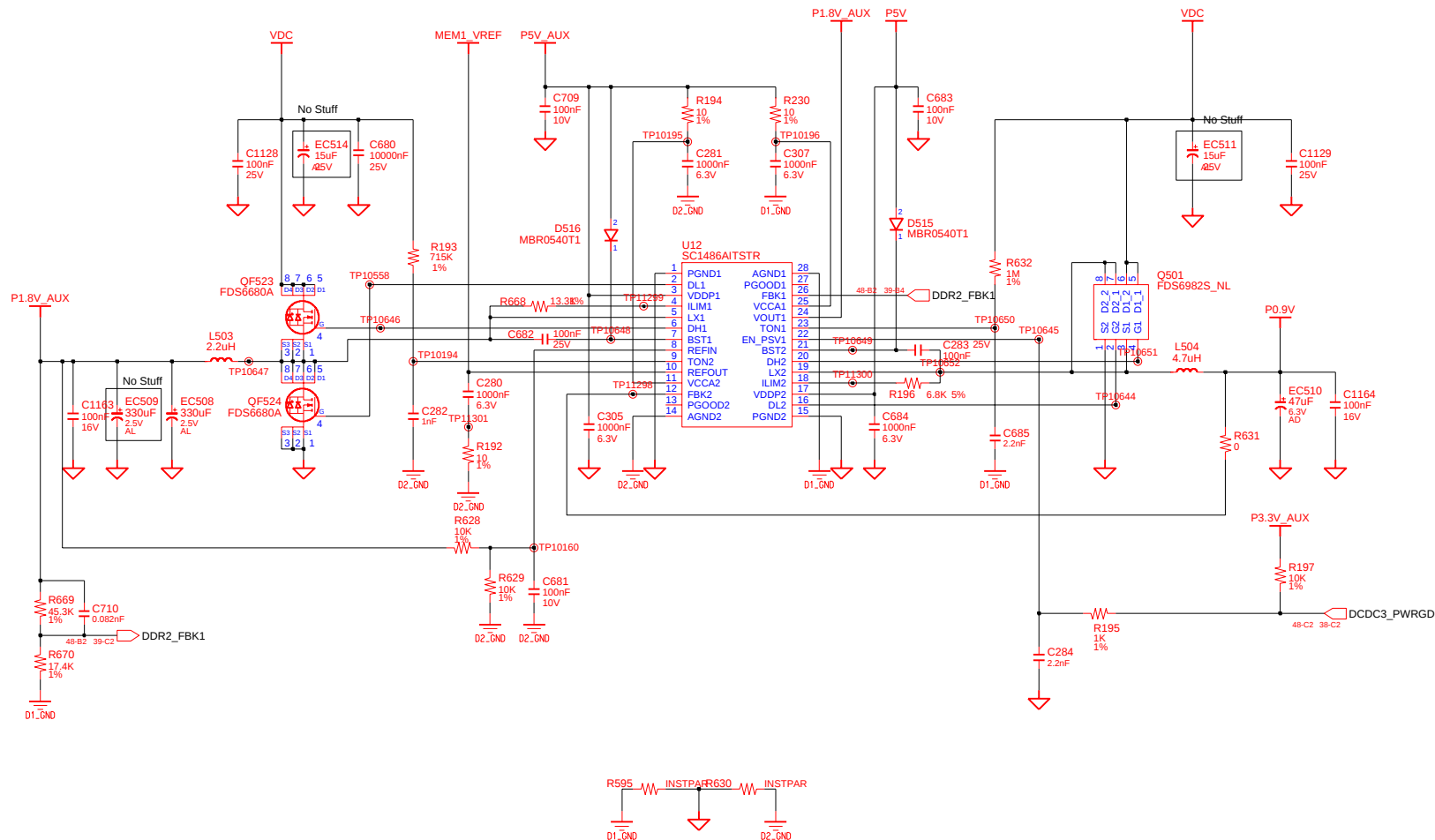


DRAM	IM, K1	DATE	11/29/2004	TITLE CYGNUS C MAIN DOCKING CONNECTOR		SAMSUNG ELECTRONICS	
CHECK	BIN, KK	DEV. STEP	MP				
APPROVAL	KIM, DW	REV	1.0				
MODULE CODE	LAST EDIT		November 29, 2004 10:05:41 AM				
						PART NO.	BA41-00451A

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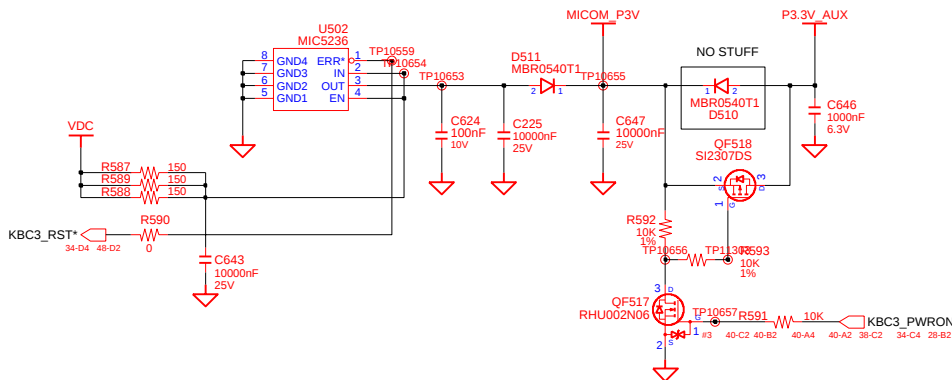
DDR Power



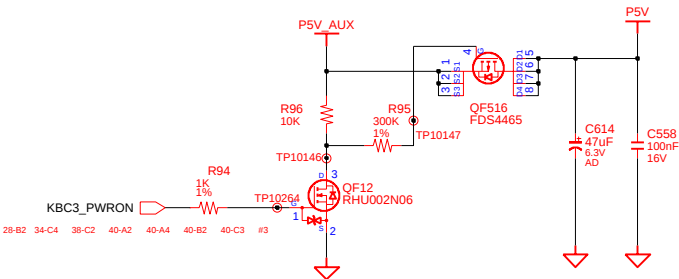
DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C MAIN DDR POWER	SAMSUNG ELECTRONICS
CHECK	BIN, KK	DEV. STEP	MP			PART NO. BA41-00451A
APPROVAL	KIM, DW	REV	1.0			
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	39	OF 49

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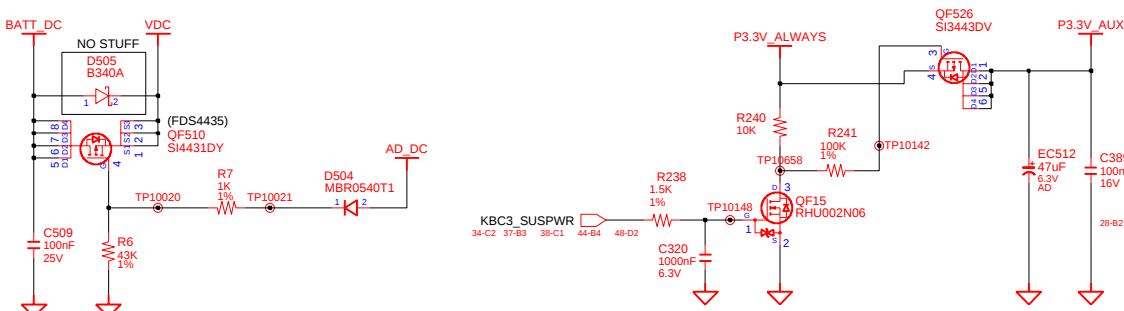
MICOM_P3V Power



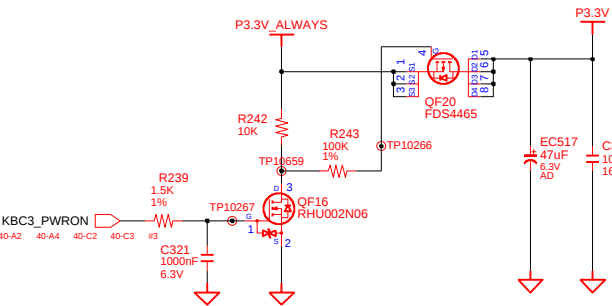
Switched Power On (P5V)



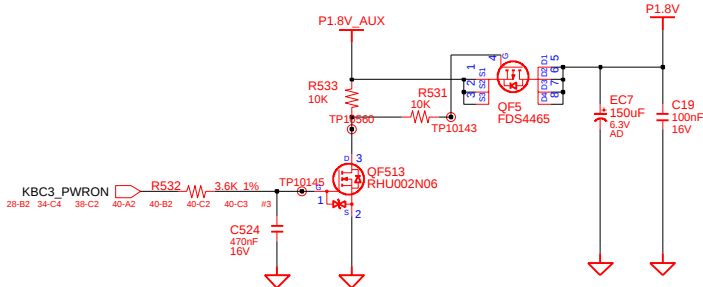
Switched Power On (P3.3V_AUX)



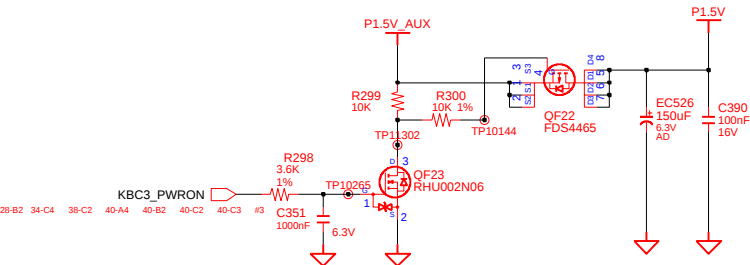
Switched Power On (P3.3V)



Switched Power On (P1.8V)

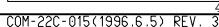


Switched Power On (P1.5V)

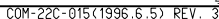


DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C	SAMSUNG
CHECK	BIN, KK	DEV. STEP	MP		MAIN	ELECTRONICS
APPROVAL	KIM, DW	REV	1.0		MICOM & SWITCHED POWER	PART NO.
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM			BA1A-00451A
					PAGE	40 OF 49

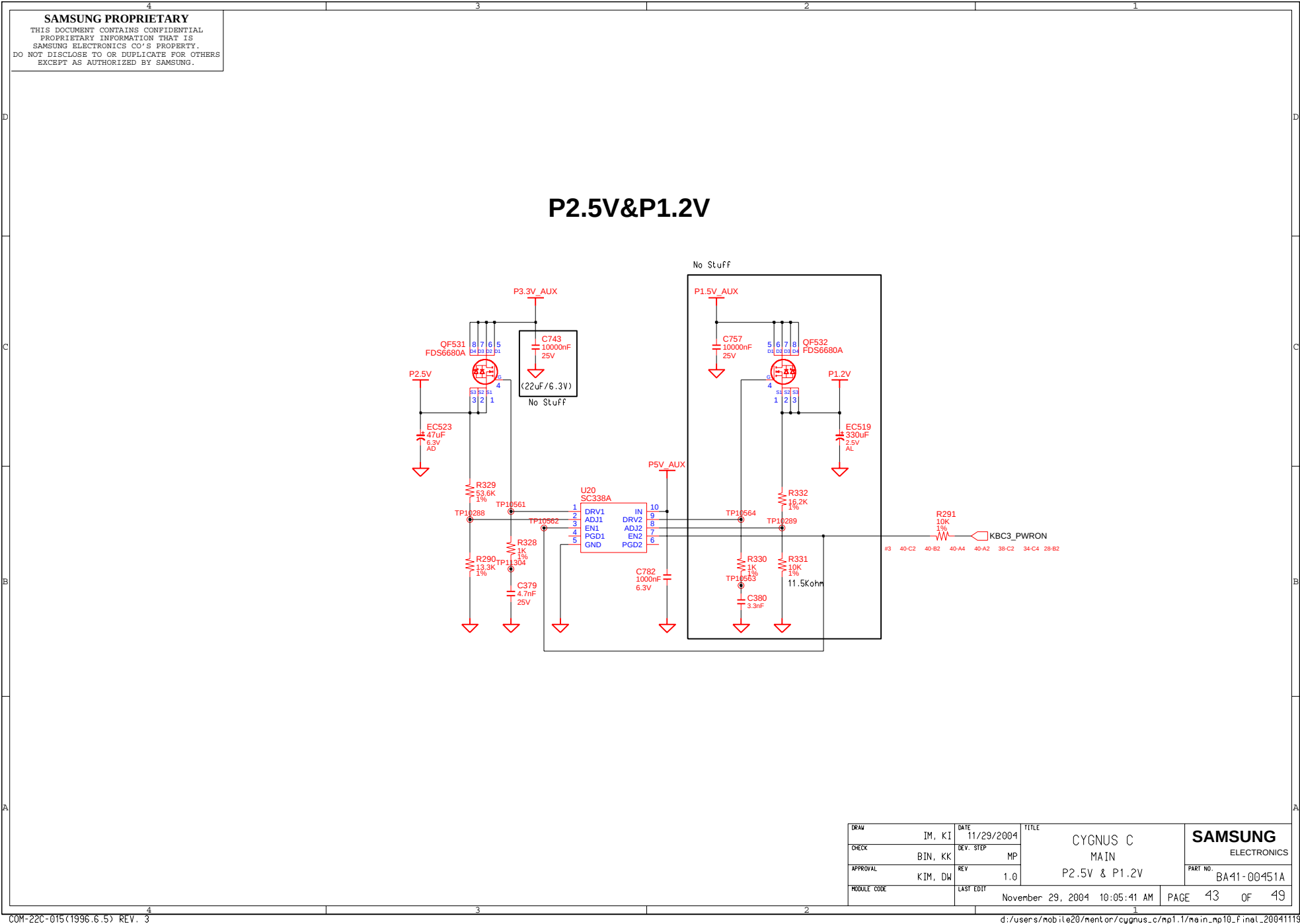
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DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C MAIN CPU POWER	SAMSUNG ELECTRONICS	
CHECK	BIN, KK	DEV. STEP	MP				
APPROVAL	KIM, DW	REV	1.0			PART NO.	BA41-00451A
MODULE CODE	LAST EDIT		November 29, 2004 10:05:41 AM			PAGE	42 OF 49



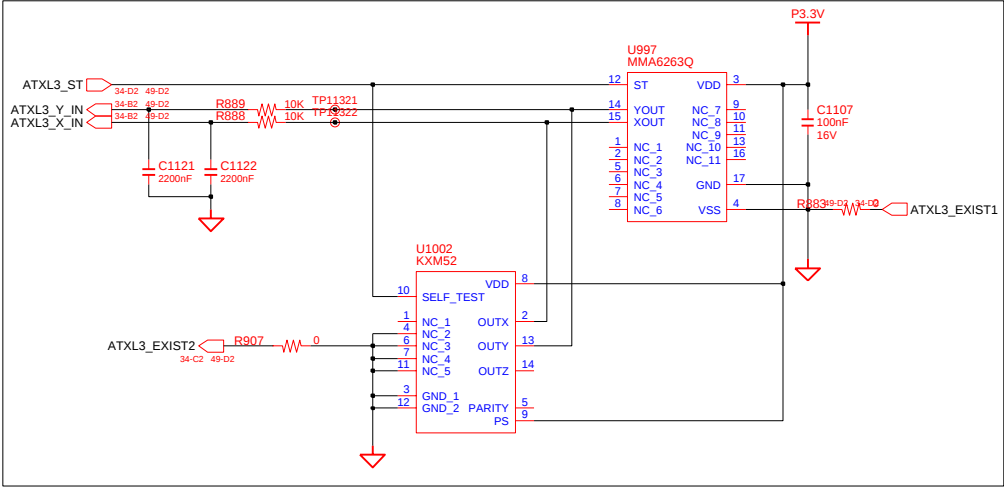
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COMPONENT LIST:

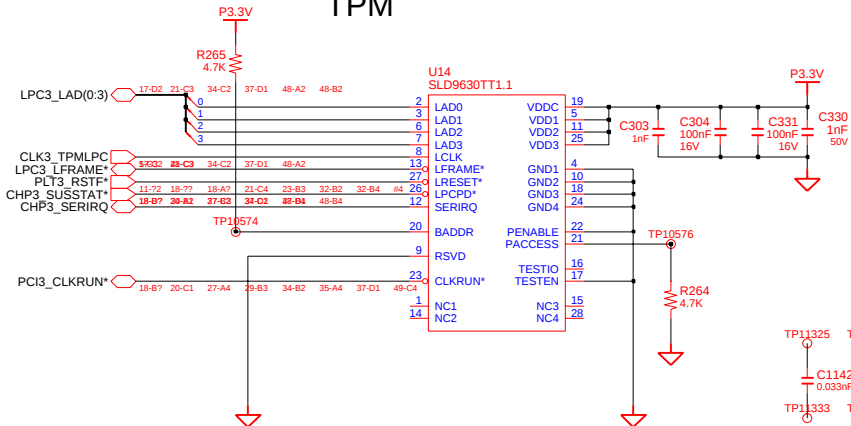
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QF18	RHU002N06	QF22	FDS6680A	QF521	FDS6680A	QF522	FDS6680A
QF19	RHU002N06	QF523	FDS6680A	QF524	FDS6680A	QF525	FDS6680A
QF20	RHU002N06	QF526	FDS6680A	QF527	FDS6680A	QF528	FDS6680A
QF21	RHU002N06	QF529	FDS6680A	QF530	FDS6680A	QF531	FDS6680A
QF22	RHU002N06	QF532	FDS6680A	QF533	FDS6680A	QF534	FDS6680A
QF23	RHU002N06	QF535	FDS6680A	QF536	FDS6680A	QF537	FDS6680A
QF24	RHU002N06	QF538	FDS6680A	QF539	FDS6680A	QF540	FDS6680A
QF25	RHU002N06	QF541	FDS6680A	QF542	FDS6680A	QF543	FDS6680A
QF26	RHU002N06	QF544	FDS6680A	QF545	FDS6680A	QF546	FDS6680A
QF27	RHU002N06	QF547	FDS6680A	QF548	FDS6680A	QF549	FDS6680A
QF28	RHU002N06	QF550	FDS6680A	QF551	FDS6680A	QF552	FDS6680A
QF29	RHU002N06	QF553	FDS6680A	QF554	FDS6680A	QF555	FDS6680A
QF30	RHU002N06	QF556	FDS6680A	QF557	FDS6680A	QF558	FDS6680A
QF31	RHU002N06	QF559	FDS6680A	QF560	FDS6680A	QF561	FDS6680A
QF32	RHU002N06	QF562	FDS6680A	QF563	FDS6680A	QF564	FDS6680A
QF33	RHU002N06	QF565	FDS6680A	QF566	FDS6680A	QF567	FDS6680A
QF34	RHU002N06	QF568	FDS6680A	QF569	FDS6680A	QF570	FDS6680A
QF35	RHU002N06	QF571	FDS6680A	QF572	FDS6680A	QF573	FDS6680A
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QF38	RHU002N06	QF580	FDS6680A	QF581	FDS6680A	QF582	FDS6680A
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QF40	RHU002N06	QF586	FDS6680A	QF587	FDS6680A	QF588	FDS6680A
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QF45	RHU002N06	QF601	FDS6680A	QF602	FDS6680A	QF603	FDS6680A
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QF53	RHU002N06	QF625	FDS6680A	QF626	FDS6680A	QF627	FDS6680A
QF54	RHU002N06	QF628	FDS6680A	QF629	FDS6680A	QF630	FDS6680A
QF55	RHU002N06	QF631	FDS6680A	QF632	FDS6680A	QF633	FDS6680A
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QF59	RHU002N06	QF643	FDS6680A</				

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AIRBAG

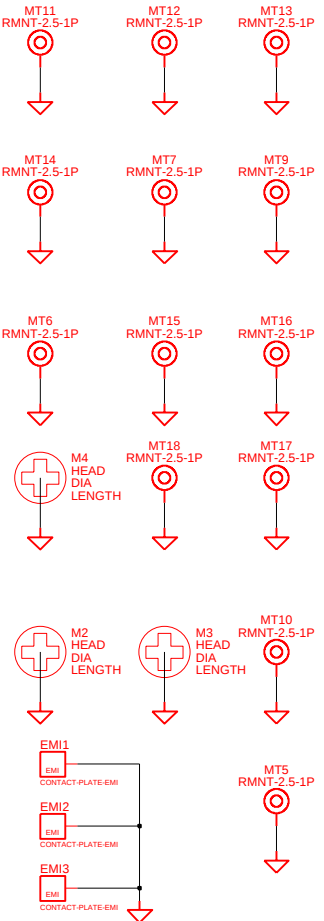
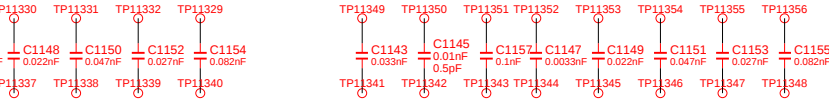


TPM



PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/M/DD)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

REV1
1
2 0 3

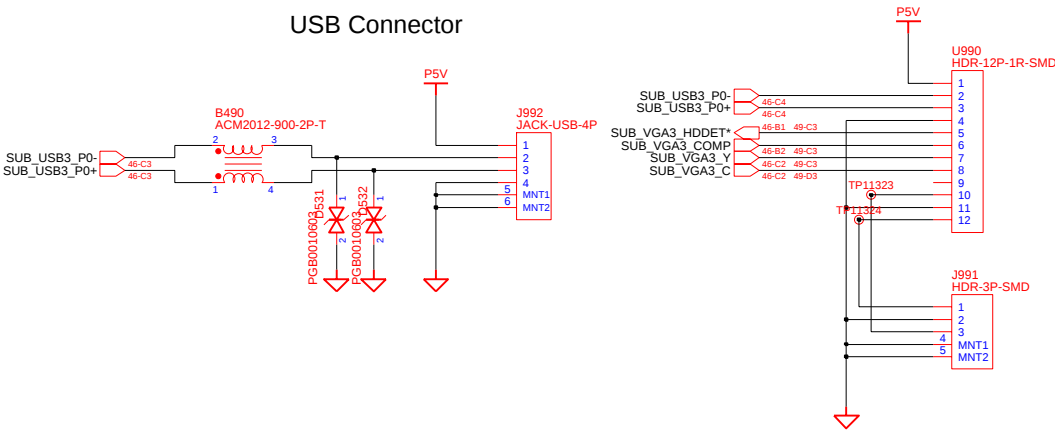


DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C MAINBD AIRBAG & TPM	SAMSUNG ELECTRONICS
CHECK	BIN, KK	DEV. STEP	MP			PART NO. BA41-00451A
APPROVAL	KIM, DW	REV	1.0			
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	45	OF 49

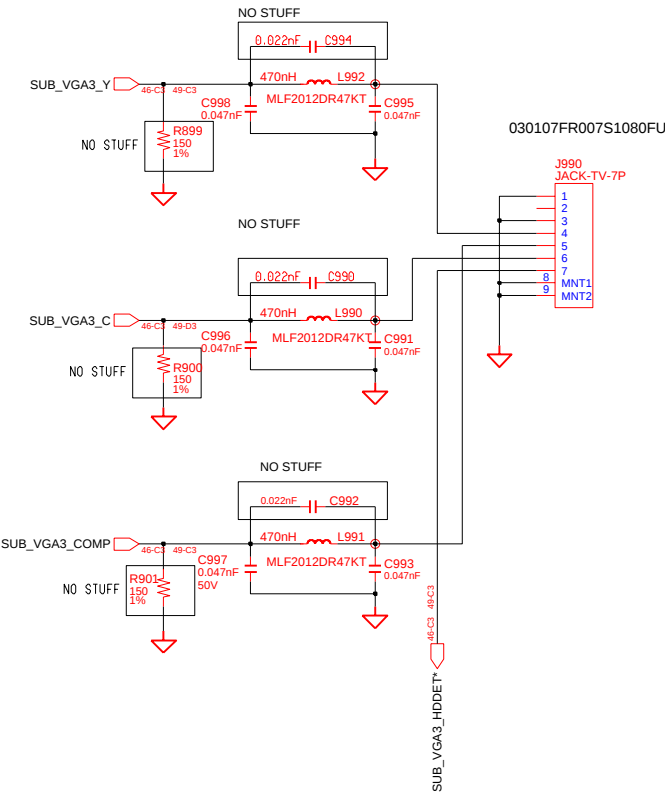
< USB BOARD >

Mother Board I/F

USB Connector



TV OUT JACK



DRAW	IM, KI	DATE	11/29/2004	TITLE	CYGNUS C MAIN MOUNT HOLE	SAMSUNG ELECTRONICS
CHECK	BIN, KK	DEV. STEP	MP			PART NO. BA41-00451A
APPROVAL	KIM, DW	REV	1.0			
MODULE CODE		LAST EDIT	November 29, 2004 10:05:41 AM	PAGE	46	OF 49

A	B	C	D
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Revision History

Page No.	Cause And Actions (Revised Items)
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TP10954 OC83_CVS1
TP10955 OC83_CVS2
TP10956 OC83_CVS3_CLK
TP10957 OC83_CD_DATA0_MS_SDIO
TP10958 OC83_CD_DATA1*
TP10959 OC83_CD_DATA2
TP10960 OC83_CD_DATA3
TP10961 OC83_CD_DATA4_XD
TP10962 OC83_CD_DATA5_XD
TP10963 OC83_CD_DATA6_XD
TP10964 OC83_CD_DATA7_XD
TP10965 OC83_CD_DATA8_XD
TP10966 OC83_CD_XD_ALE
TP10967 OC83_CD_XD_CE*
TP10968 OC83_CD_XD_CLE
TP10969 OC83_CD_XD_CSN
TP10970 OC83_MS_BS_SD_CMD
TP10971 OC83_MS_INS_XD_CD*
TP10972 OC83_CD_CD_XD_CD*
TP10973 OC83_CD_CD_XD_R_B*
TP10974 OC83_VCCSEN*
TP10975 OC83_VCCSEN*
TP10976 OC83_VCCA
TP10977 OC83_VCCB
TP10978 OC83_VCCD
TP10979 OC83_VCCEN
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TP1090SOHCP3.THRM\$TRIP	

TP10866 CPU1_CPURST*

TP10859 CPU1_IGNNE

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TP10672CLK3_POLCKFW
TP10673CLK3_POLCKIOH
TP10674CLK3_POLCKLAN
TP10675CLK3_POLCKMICOM
TP10676CLK3_POLCKMIN
TP10677CLK3_POLCKSIO
TP10678CLK3_PWDG*
TP10679CLK3_S1014
TP10680CLK3_S10PC1_DS
TP10681CLK3_SMBCLK
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TP106876CLK3_PCLKMIN
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TP106878CLK3_PWRGD0
TP106879CLK3_SI014
TP106880CLK3_SIOPCI_DS
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TP106884CLK3_TPMPLPC
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TP10866 CPU1_CPURST*

TP10859 CPU1_IGNNE

[illegible][illegible]

TP10802 AMP5_PWRDOWN

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 TP10773 CFAN3.INIT1+
 TP10774 CFK3.BLUE
 TP10775 CFK3.BLUE+
 TP10776 CFK3.COMP
 TP10777 CFK3.DCCLCK
 TP10778 CFK3.DCDDATA
 TP10779 CFK3.GREEN
 TP10780 CFK3.LINC
 TP10781 CFK3.RED
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 TP10783 CFK3.VSYNC+
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 TP10786 CHDS.SEL
 TP10787 CHP3.DTECT
 TP10788 CHP3.DTECT+
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 TP10996 CHS.DC(194)
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TP10772 CFAN3.FDBACK+
 TP10773 CFAN3.INIT1+
 TP10774 CFK3.BLUE
 TP10775 CFK3.CMP
 TP10776 CFK3.COMP
 TP10777 CFK3.DCCLCK
 TP10778 CFK3.DCDDATA
 TP10779 CFK3.GREEN
 TP10780 CFK3.LINC
 TP10781 CFK3.RED
 TP10782 CFK3.VSYNC
 TP10783 CFK3.VSYNCR
 TP10784 CFK3.VSYNCR+
 TP10785 CHDS.PADEF.T
 TP10786 CHDS.SEL
 TP10787 CHP3.DTECT
 TP10788 CHP3.DTECT+
 TP10789 CHS.A1
 TP10790 CHS.A2
 TP10791 CHS.CS1+
 TP10792 CHS.CS2+
 TP10793 CHS.CS3+
 TP10794 CHS.DC(1)
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 TP1

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TP17570>IDES12_IOW
TP17572>IDES12_IOW
TP17573>IDES12_OLED*
TP17574>IMVP4_PWRGD
TP17575>IMV_VDC
TP17576>KBC3_A20G

TP17578>KBC3_BKLTON
TP17579>KBC3_BRIT
TP17600>KBC3_BRIT_DA
TP17610>KBC3_CASPLED*
TP17620>KBC3_CHGEN
TP17630>KBC3_CPUKST*
TP17640>KBC3_CKTON
TP17650>KBC3_DOWN
TP17660>KBC3_EJECT*
TP17670>KBC3_EXSTMI*
TP17680>KBC3_FANCTRL
TP17690>KBC3_IMVP4_PWRGD
TP17700>KBC3_LBLED*
TP17710>KBC3_NUMLED*

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TP10763 OKBC3_CPURST*

TP10766 ○ KBC3_EJECT*

TP10769 ○ KBC3_IMVP4_PWRGD
TP10770 ○ KBC3_LBLED*
TP10771 ○ KBC3_NUMLED*

TP10708CKBC3_PWRGD
TP10709CKBC3_PWRON
TP10710CKBC3_PWRSW*
TP10711CKBC3_QS*
TP10712CKBC3_RSMRST*
TP10713CKBC3_RST*
TP10714CKBC3_RUNSCI*
TP10715CKBC3_SCLEL*
TP10716CKBC3_SMCLK*
TP10717CKBC3_SMDATA*

TP170118 CBXC3 SPMUKTE
 TP170120 CBXC3 SUSWPR
 TP170120 CBXC3 THERM_ALERT*
 TP170120 CBXC3 THERM_ALERT*
 TP170122 CBXC3 THERM_SMDATA
 TP170123 CBXC3 VAKON
 TP170124 CBXC3 WAKESC1*
 TP170125 CBXC3 WILANON
 TP170126 CBXC3 WILANON
 TP170127 CBXC3 AVDD_ON
 TP170128 CBXC3 KS1(0)
 TP170129 CBXC3 KS1(1)
 TP170130 CBXC3 KS1(2)
 TP170131 CBXC3 KS1(3)
 TP170132 CBXC3 KS1(4)
 TP170133 CBXC3 KS1(5)
 TP170134 CBXC3 KS1(6)
 TP170135 CBXC3 KS1(7)
 TP170136 CBXC3 KS0(0)
 TP170137 CBXC3 KS0(1)
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 TP1693 CBXC3 KS0(7)
 TP1694 CBXC3 KS0(8)
 TP1695 CBXC3 KS0(9)
 TP1696 CBXC3 TCLK
 TP1697 CBXC3 DATA
 TP1698 LAN3_EDATA
 TP1699 LAN3_EDATA

TP170118 CBXC3 SPMUKTE
 TP170120 CBXC3 SUSWPR
 TP170120 CBXC3 THERM_ALERT*
 TP170120 CBXC3 THERM_ALERT*
 TP170122 CBXC3 THERM_SMDATA
 TP170123 CBXC3 VAKON
 TP170124 CBXC3 WAKESC1*
 TP170125 CBXC3 WILANON
 TP170126 CBXC3 WILANON
 TP170127 CBXC3 AVDD_OND
 TP170128 CBXC5 KS1(0)
 TP170129 CBXC5 KS1(1)
 TP170130 CBXC5 KS1(2)
 TP170131 CBXC5 KS1(3)
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 TP16089 CBXC5 KS0(3)
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 TP16091 CBXC5 KS0(5)
 TP16092 CBXC5 KS0(6)
 TP16093 CBXC5 KS0(7)
 TP16094 CBXC5 KS0(8)
 TP16095 CBXC5 KS0(9)
 TP16096 CBXC5 TCLK
 TP16097 CBXC5 DATA
 TP16098 LAN3_EDATA
 TP16099 LAN3_EDATA

TP10700 LAN3_LOW_PWR
TP10701 LB3_LED
TP10702 LCD2_BKLTCTRL
TP10703 LCD2_BKLTON
TP10704 LCD2_VDDEN
TP10705 LCD3_BKLTCTRL
TP10706 LCD3_BKLTON
TP10707 LCD3_BRIT
TP10664 LCD3_VDD

TP10664 O LCB3_YDD
TP10665 O LED3_CHARGE
TP10666 O LED5_AMBER
TP10667 O LED5_GREEN
TP10668 O LID3_SWITCH*
TP10669 O LPC3_LAD(0)
TP10670 O LPC3_LAD(1)
TP10671 O LPC3_LAD(2)
TP10672 O LPC3_LAD(3)
TP10673 O LPC3_LFRAME*
TP10674 O MCH1_BSEL0

TP106740 MCAT-BSELO

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TP10677○MCH1_HXSWING
TP10678○MCH1_HYSWING
TP10679○MCH2_BMBUSY*
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TP10680 ○ MCH2_CFG(5)
TP10681 ○ MCH2_CFG(6)
TP10682 ○ MDC3_AUDIN
TP10683 ○ MD3_VCC

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TP11030MIC5_N01
TP11031MIC5_N02

TP10663OP1.2V_LAN

TP11032OPIC3_AD(0)
TP11033OPIC3_AD(1)
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TP11064OPIC3_CBE0*
TP11065OPIC3_CBE1*
TP11066OPIC3_CBE2*
TP11067OPIC3_CBE3*
TP11068OPIC3_CLKRUN*
TP11069OPIC3_DEVSSEL*
TP11070OPIC3_FRAME*
TP11071OPIC3_GNT0*
TP11072OPIC3_GNT1*
TP11073OPIC3_GNT2*
TP11074OPIC3_GNT3*
TP11075OPIC3_INTA*
TP11076OPIC3_INTB*
TP11077OPIC3_INTC*
TP11078OPIC3_INTD*
TP11079OPIC3_INTE*
TP11080OPIC3_INTF*
TP11081OPIC3_INTG*
TP11082OPIC3_INTH*
TP11083OPIC3_TRDY*
TP11084OPIC3_PAR*
TP11085OPIC3_PERR*
TP11086OPIC3_PL0CK*
TP11087OPIC3_RE00*
TP11088OPIC3_RE01*
TP11089OPIC3_RE02*
TP11090OPIC3_RE03*
TP11091OPIC3_RS1*
TP11092OPIC3_SERR*
TP11093OPIC3_STOP*
TP11094OPIC3_TRDY*
TP11096OPICIE3_WAKE*
TP11097OPLT3_RST*
TP11098OPLT3_RSTF*
TP11099OPS25_CLK*
TP11100OPS25_DATA*
TP11101OPS25_NCLK*
TP11102OPS25_MDATA*
TP11103QUICK3_BOOT*
TP11104OS1+
TP11105OS1-
TP11106OS2+
TP11107OS2-

TP11108OSATA3_DET*
TP11109OSMB3_ALERT*
TP11110OSMB3_CLK*
TP11111OSMB3_DATA*
TP11112OSMB3_LINKALERT*
TP11113OSPDIF5_ON*
TP11114OSPDIF5_OUT*
TP11115OSPROM3_DIN*
TP11116OSPROM3_DOUT*
TP11117OSUB_VGA3_C*
TP11118OSUB_VGA3_COMP*
TP11119OSUB_VGA3_HDET*
TP11120OSUB_VGA3_Y*
TP11121OSYSTEM_SENSE+*
TP11122OSYSTEM_SENSE-*
TP11123OTP3_TOGGLE*
TP11124OVGA2_DDCCLK*
TP11125OVGA2_DDCDATA*
TP11126OVGA3_BKLTON*
TP11127OVGA3_BLUE*
TP11128OVGA3_GREEN*
TP11129OVGA3_CHARGE_POW*
TP11130OVGA3_COMP*
TP11131OVGA3_DVCLK*
TP11132OVGA3_DVDATA*
TP11133OVGA3_GREEN*
TP11134OVGA3_HDET*
TP11135OVGA3_LCDDT0*
TP11136OVGA3_LCDDT1*
TP11137OVGA3_CDDVDON*
TP11138OVGA3_RED*
TP11139OVGA3_Y*
TP11140OVGA5_DDCCLK*
TP11141OVGA5_DDCDATA*
TP11142OVGA5_DVCLK*
TP11143OVGA5_DVDATA*
TP11144OVGA5_HSYNC*
TP11145OVGA5_VSYNC*
TP11146OVDS*
TP11147OVREF3_OUT_MIC*
TP11148OVTTPWGDO*
TP11149OAD_DC*
TP11150OAD_DC*
TP11151OAD_DC*
TP11152OAD_DC*
TP11153OAD_DC*
TP11154OAMP_VDD*
TP11155OAMP_VDD*
TP11156OAUD_GND*
TP11157OAUD_GND*
TP11158OAUD_GND

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TP11160OAUD_GND*
TP11161OAUD_GND*
TP11162OAVDD*
TP11163OBATT_DC*
TP11164OBATT_DC*
TP11165OCHSS_GND*
TP11166OCHSS_GND*
TP11167OCRT_GND*
TP11168OCRT_GND*
TP11169OC_AGND*
TP11170OC_AGND*
TP11171OD1_GND*
TP11172OD1_GND*
TP11173OD2_GND*
TP11174OD2_GND*
TP11175OMEM1_VREF*
TP11176OP0_9V*
TP11177OP0_9V*
TP11178OP0_9V*
TP11179OP0_9V*
TP11180OP0_9V*
TP11181OP1_2V*
TP11182OP1_2V*
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TP11184OP1_2V*
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TP11210OP2_5V_LAN*
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TP11212OP3_3V_AUX*
TP11213OP3_3V_AUX*

TP11214OP3_3V_AUX*
TP11215OP3_3V_AUX*
TP11216OP5V*
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TP11225OP5V_AUX*
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TP11237OV_GND*

TP11238OGROUND*
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TP11244OGROUND*

TP11245OP3_3V_ALWAYS*
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TP11248OP3_3V_ALWAYS*
TP11249OP3_3V_ALWAYS*

TP11262OATXL3_EXIST1*
TP11358OATXL3_EXIST2*
TP11263OATXL3_ST*
TP11264OATXL3_X_IN*
TP11265OATXL3_Y_IN*
TP11271ODCK3_PWRONS*
TP11272ODPRSLP*
TP11273OSIO3_DRST*

TP11266OVCC_NCTF*
TP11267OVCC_NCTF*
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TP11270OVCC_NCTF*