

1

2

3

4

5

6

7

8

THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF INVENTEC CORPORATION AND SHALL NOT BE REPRODUCED, COPIED, OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION, INVENTEC CORPORATION 2010 ALL RIGHT RESERVED

HSF Property : **Halogen-Free**

A

B

C

D

E

F

SS11 AMD Muxless

PIXIES 15"

Llano FS1/Hudson M3

DB1 (X02)

2010.10.15

</

Table Of Contents

Page

5- DC & Battery Charger
6- Select & Battery CONN
7- System Power (+V3A/+V5A)
8- System Power (+V1.1A/+V2.5S)
9- DDR3 Power (+V1.5_+V1.35/+V0.75S)
10- APU Power (VDD/VDDNB)
11- SYSTEM POWER VDDP,VDDR (+V1.2)
12- Power (Sleep: +V5S/+V3S/+V1.1S)
13- OCP
14- Power (Sequence)

Page

15- APU LLANO-1
16- APU LLANO-2
17- APU LLANO-3 & HDT
18- APU LLANO-4
19- DDR3-DIMM0 (8.0)
20- DDR3-DIMM1 (4.0)
21- Thermal & FAN
22- Travis (DP to LVDS)
23- LCM
24- HDMI
25- VGA CONN
26- FCH-Hudson M2-1
27- FCH-Hudson M2-2
28- FCH-Hudson M2-3
29- FCH-Hudson M2-4
30- FCH-Hudson M2-5
31- SPI Flash
32- KBC
33- Keyboard, Touch Pad
34- SATA HDD, ODD & LED.
35- Audio CODEC
36- MODEM
37- GLAN Controller

Page

38- RJ45 CONN, Transformer
39- WLAN (Mini Card)
40- G sensor (Accelerometer)
41- PWR&FP&Launch&WEB/DMIC CONN
42- USB3.0 Controller
43- USB CONNs
44- Express Card
45- Screw
46- DB~3in1 Card Reader & Audio jack-1
47- DB~3in1 Card Reader & Audio jack-2
48- DB~USB CONNs (right side)
49- DB~Power Button Board
50- DB~LID & Quick Launch, WLAN SW & LED
51- DB~Pick Button
52- DB~ODD DB Board
53- GPU-Seymour XT (S3 pakage)-1
54- GPU-Seymour XT (S3 pakage)-2
55- GPU-Seymour XT (S3 pakage)-3
56- GPU-Seymour XT (S3 pakage)-4
57- VRAM-1
58- VRAM-2
59- VGA Power
60- WWAN, SIM Card

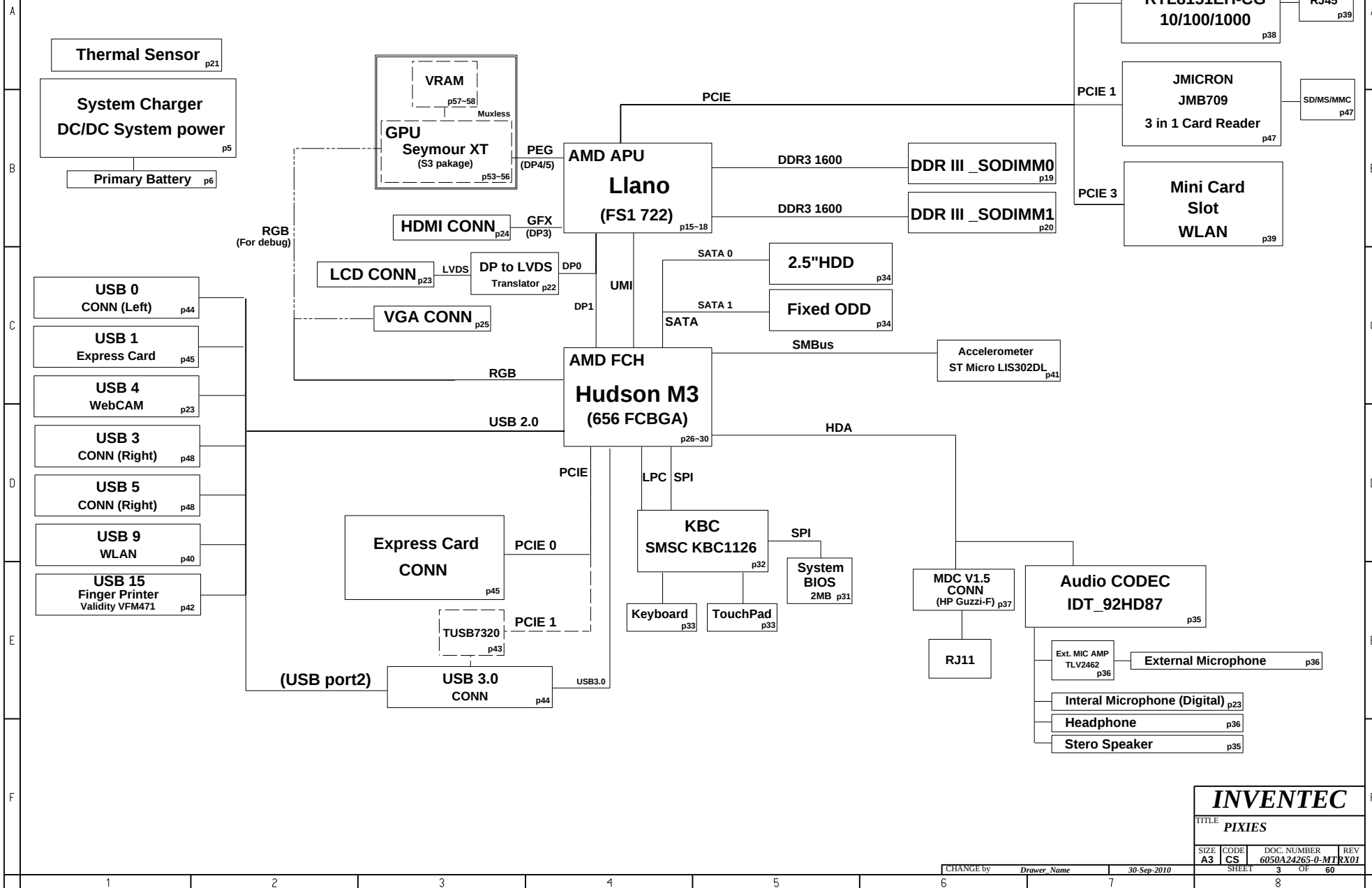
INVENTEC

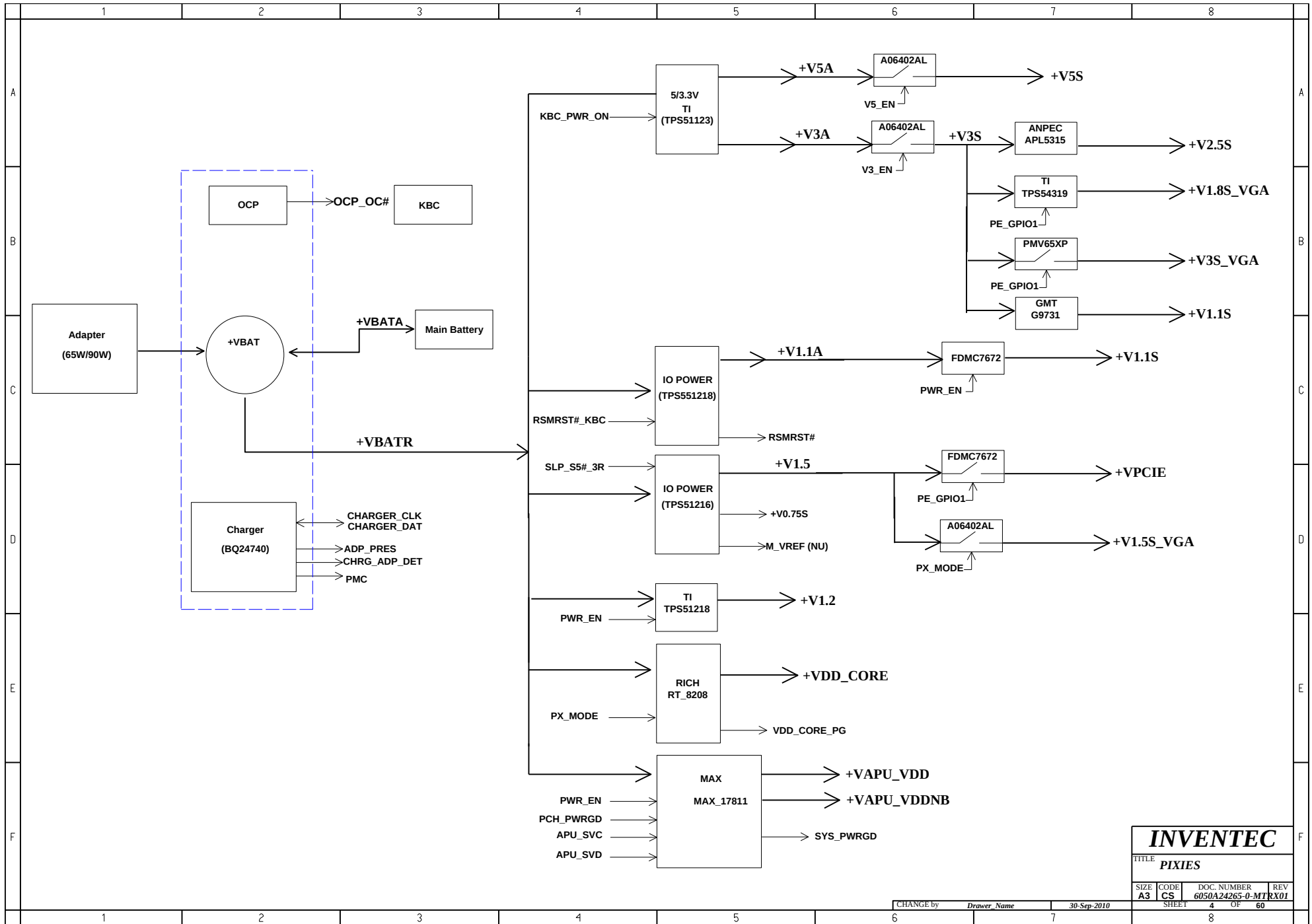
TITLE			
Contents			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	X01
SHEET		2	OF 60

CHANGE by *Draver Name* 30-Sep-2010

Inventec Confidential

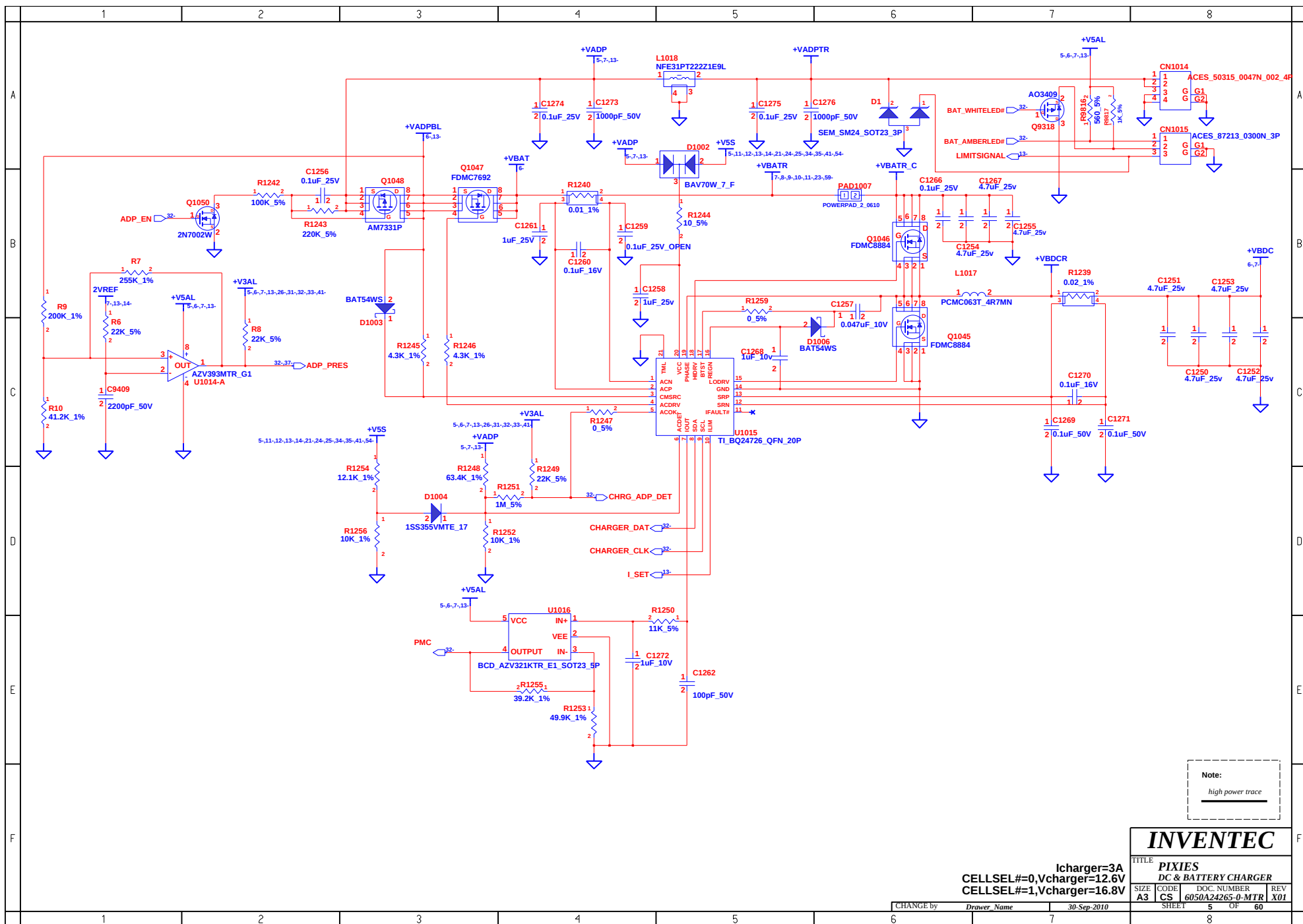
Hudson M2 (B SERIES : 6019B0775601)
218-0755002

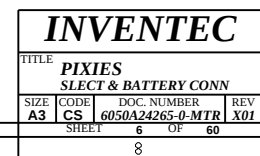


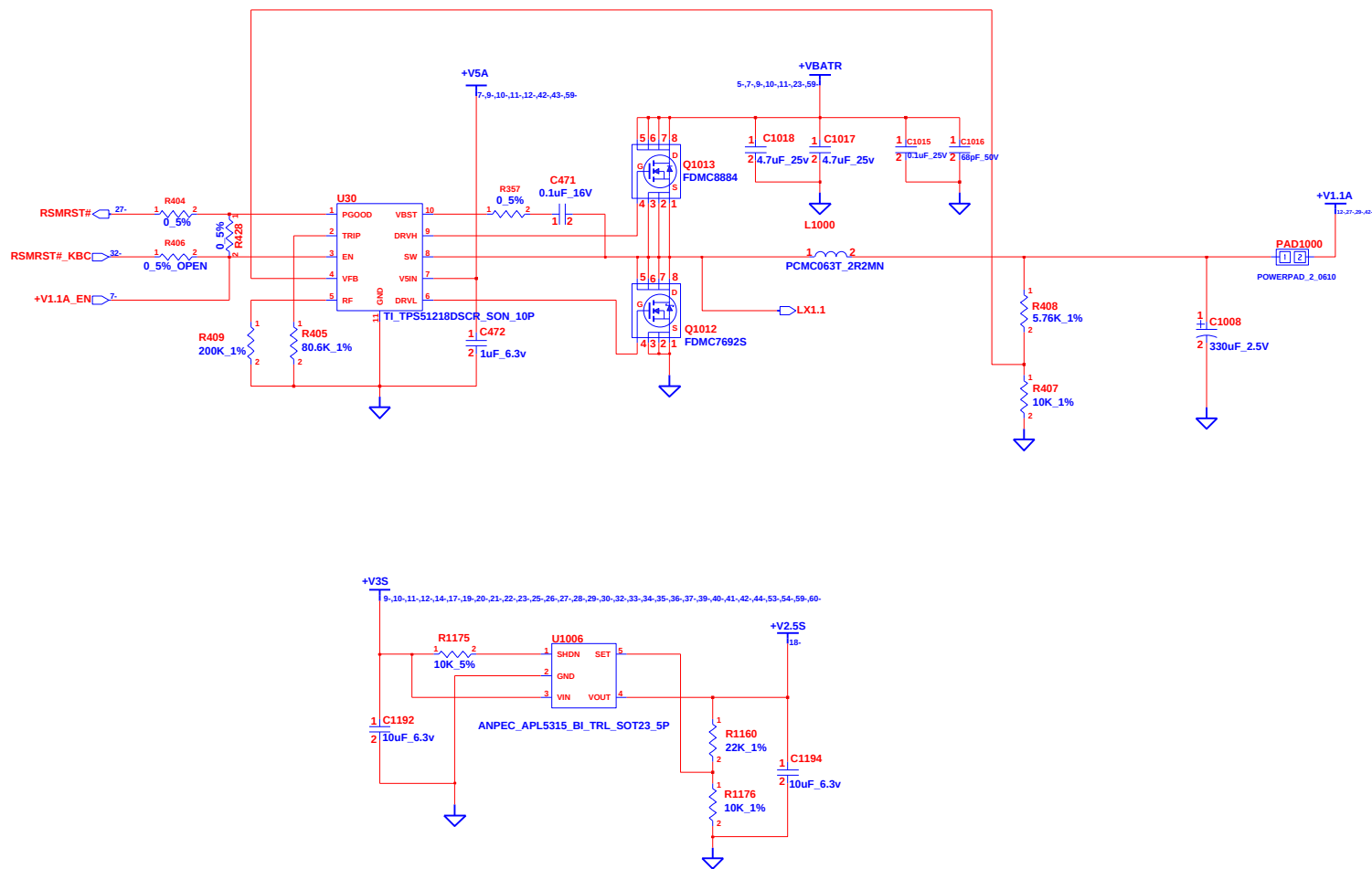


INVENTEC			
TITLE PIXIES			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	01
SHEET		4	OF 60

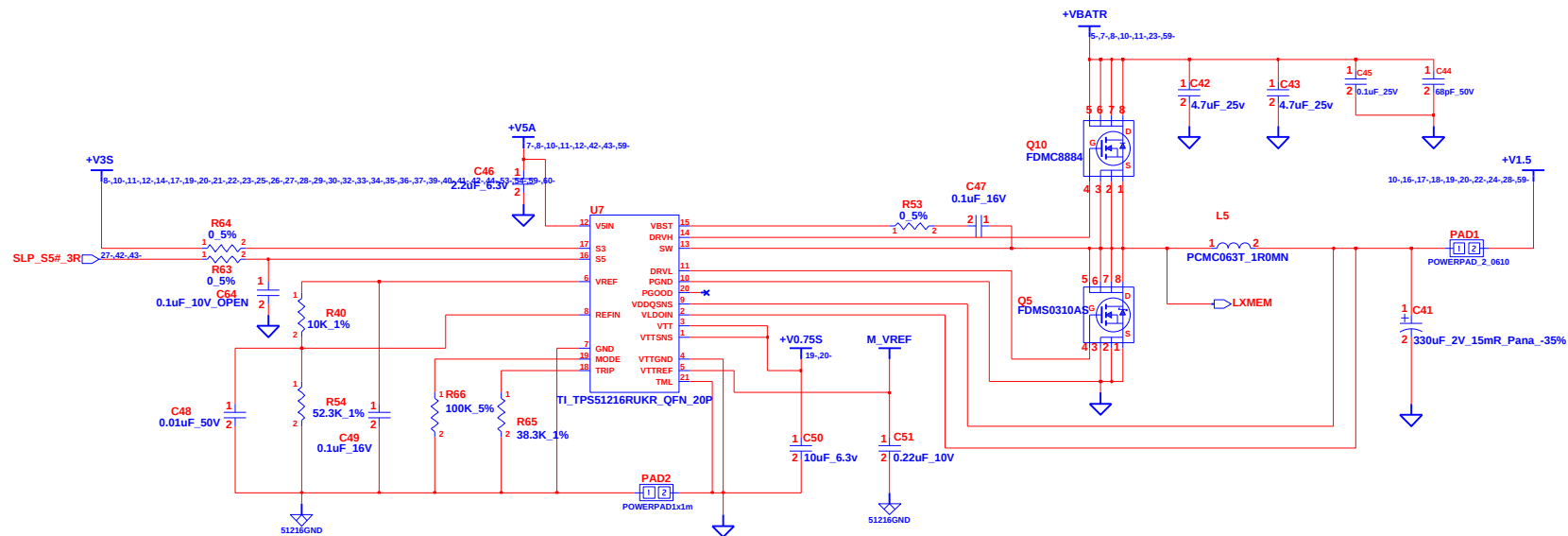
CHANGE by *Drawn Name* 30-Sep-2010





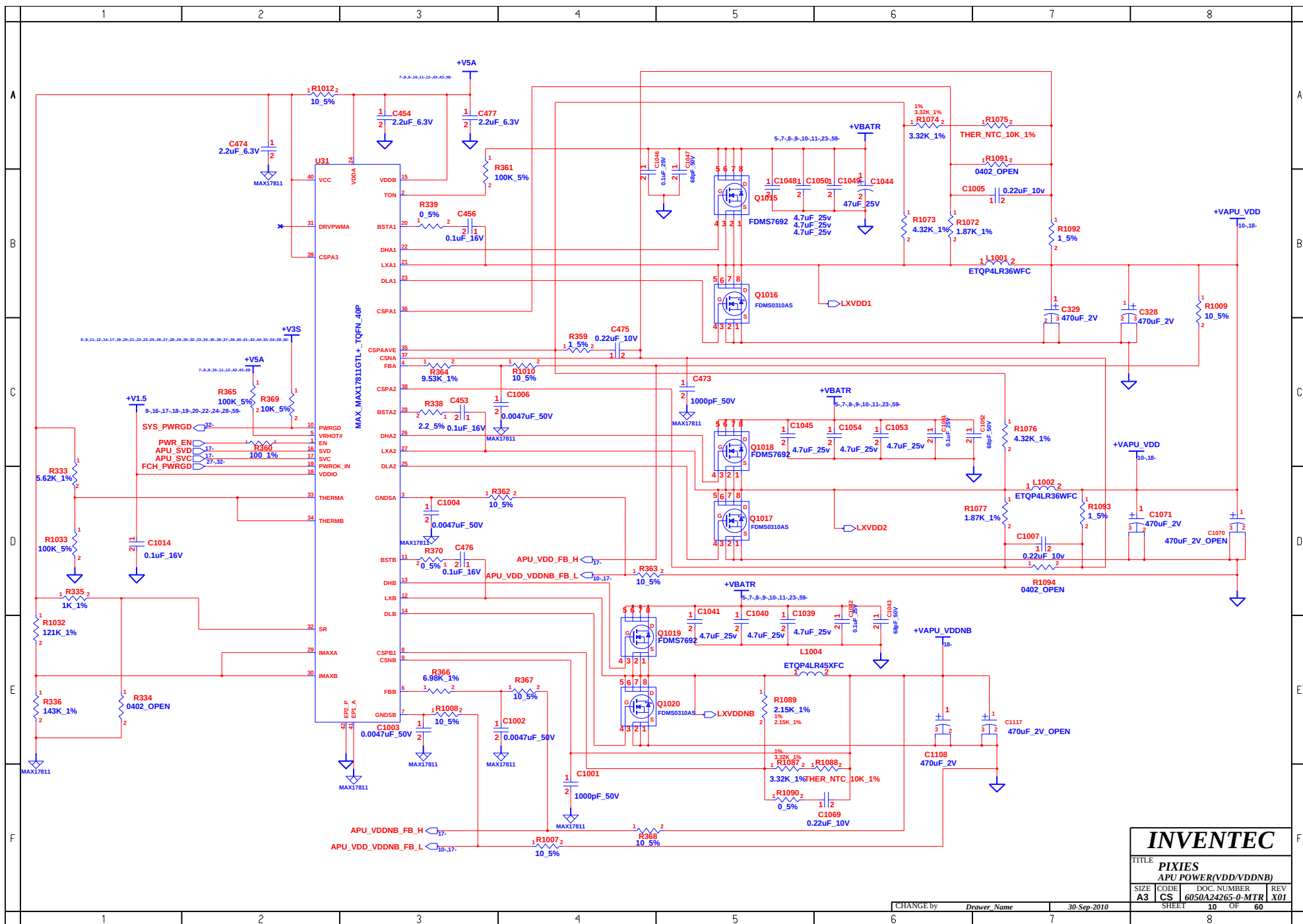


INVENTEC			
TITLE PIXIES SYSTEM POWER(+V1.1A/+V2.5S)			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
CHANGE by Drawer Name		30-Sep-2010	
SHEET 8		OF 60	



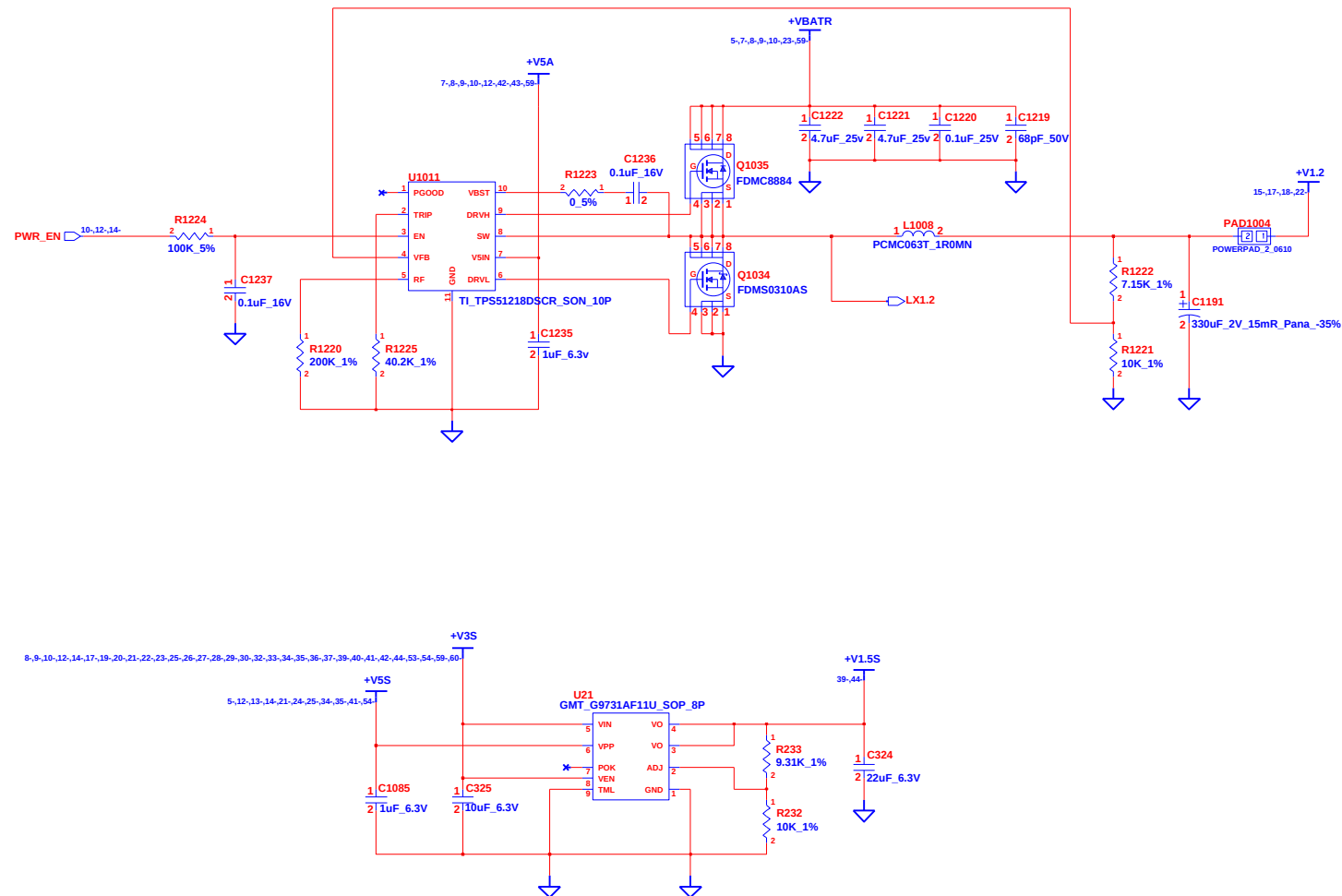
NOTE: DDR3 REGULATOR

INVENTEC			
TITLE			
PIXIES			
DDR3 POWER (+V1.5/+V0.75S)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	X01
CHANGE by		Draver Name	30-Sep-2010
SHEET		9	OF 60



INVENTEC

TITLE				PIXIES			
				APU POWER(VDD/VDDNB)			
SIZE	CODE	DOC. NUMBER		REV.		DATE	
A3	CS	6050A24265-0-MTR		1		10/10/2008	
SHEET		10		OF		60	

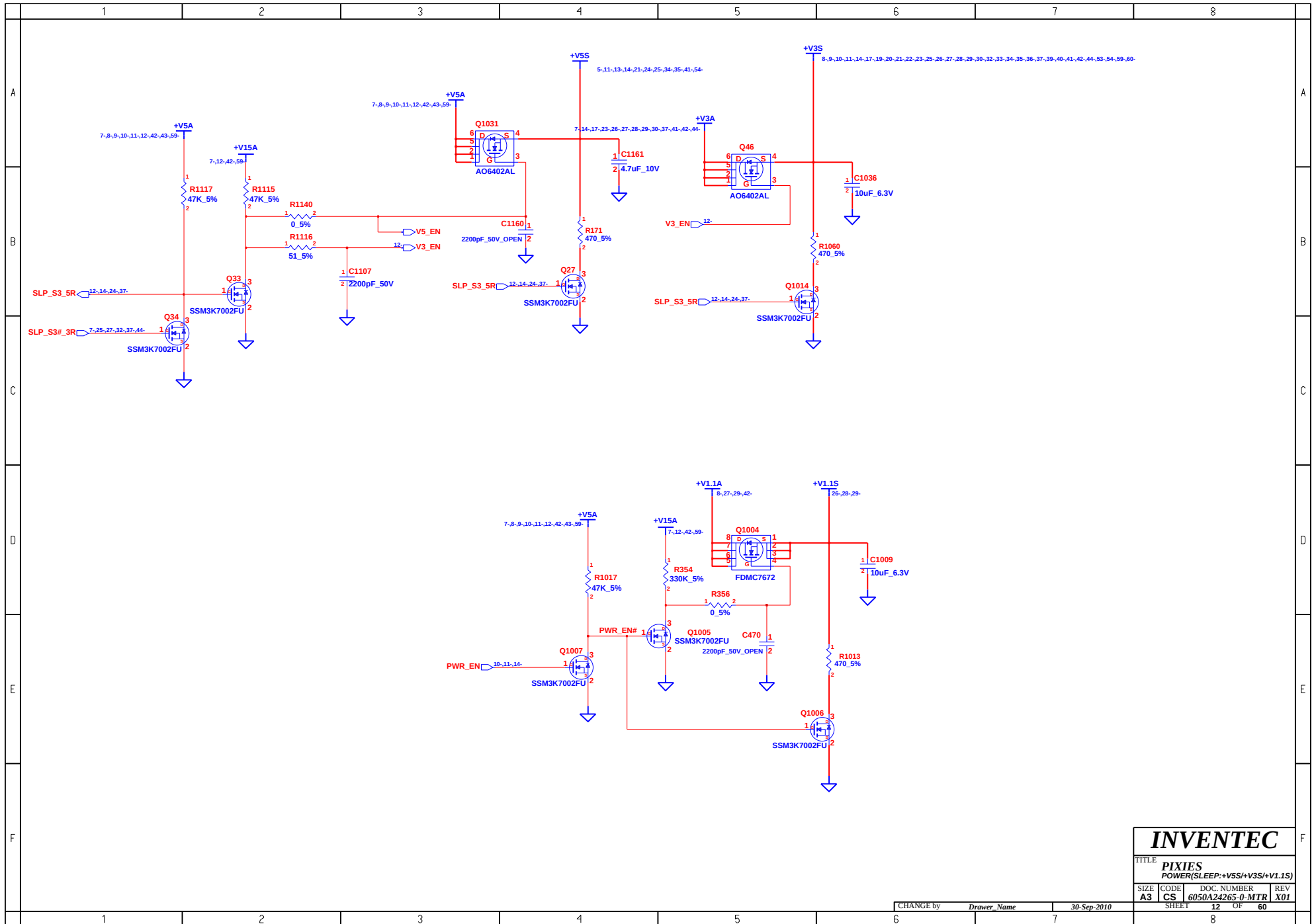


INVENTEC

TITLE			
PIXIES			
SYSTEM POWER VDDP,VDDR(+V1.2)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	X01

CHANGE by *Drawn Name* 30-Sep-2010

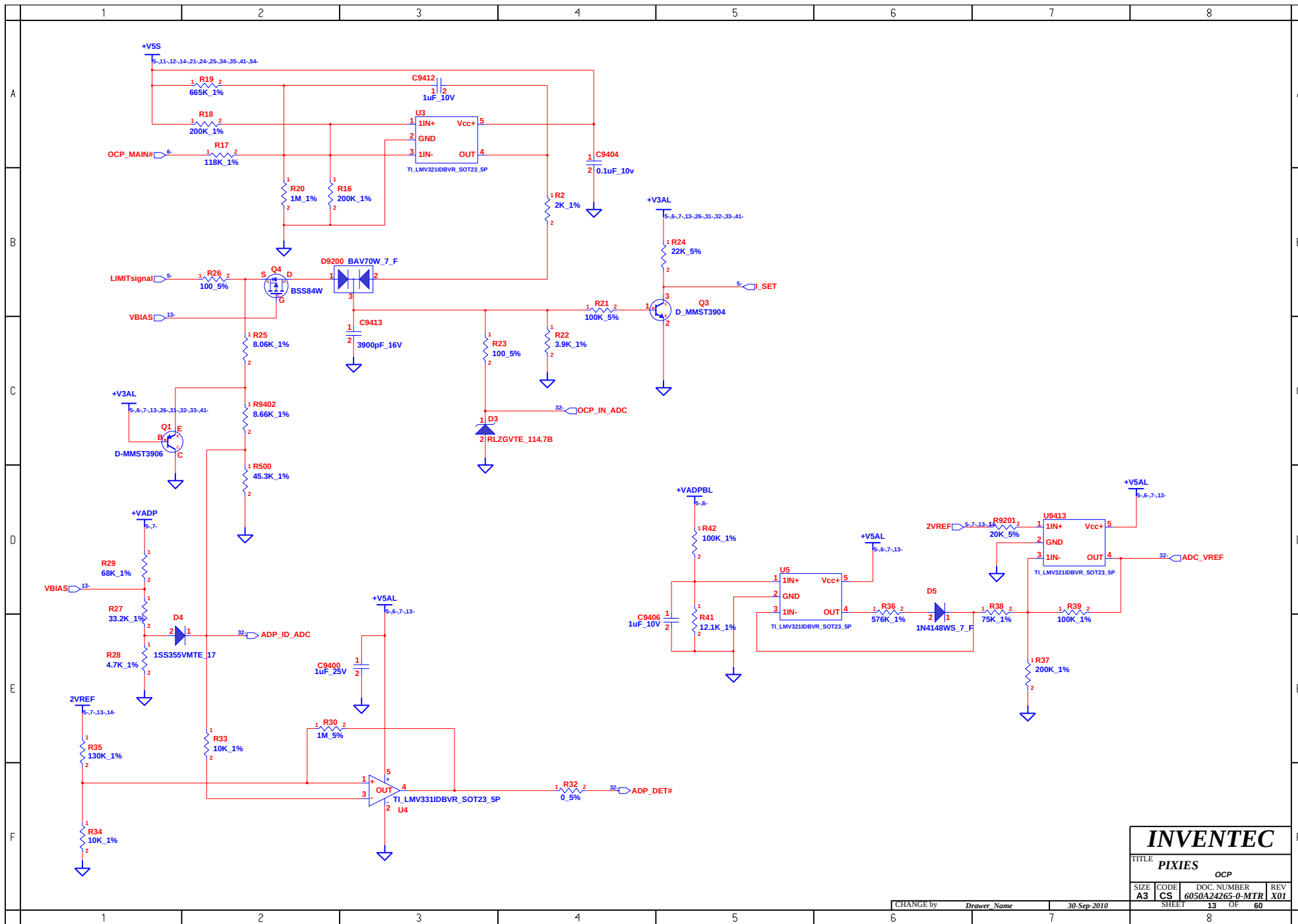
SHEET 11 OF 60

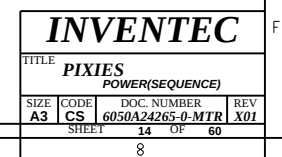


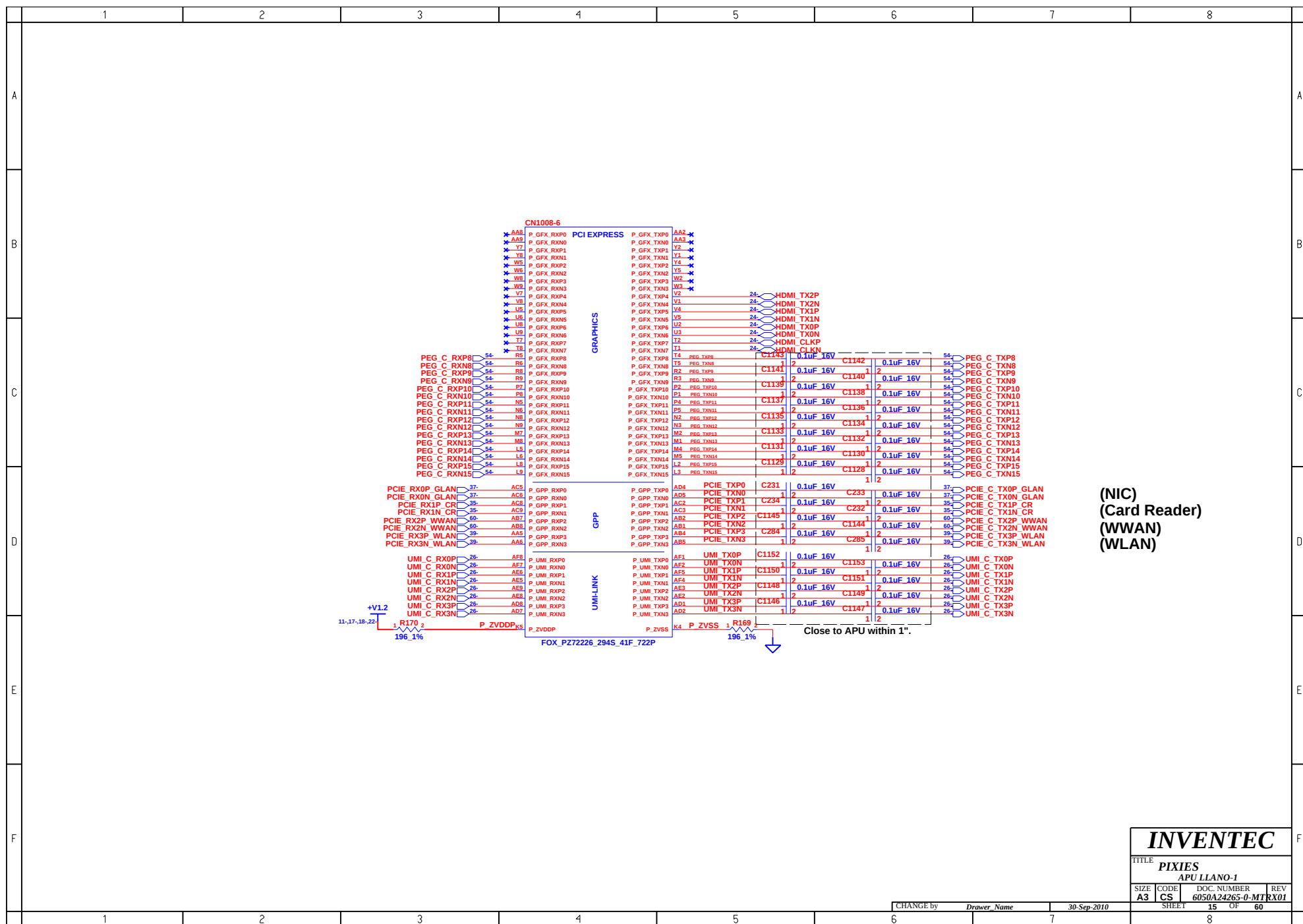
INVENTEC

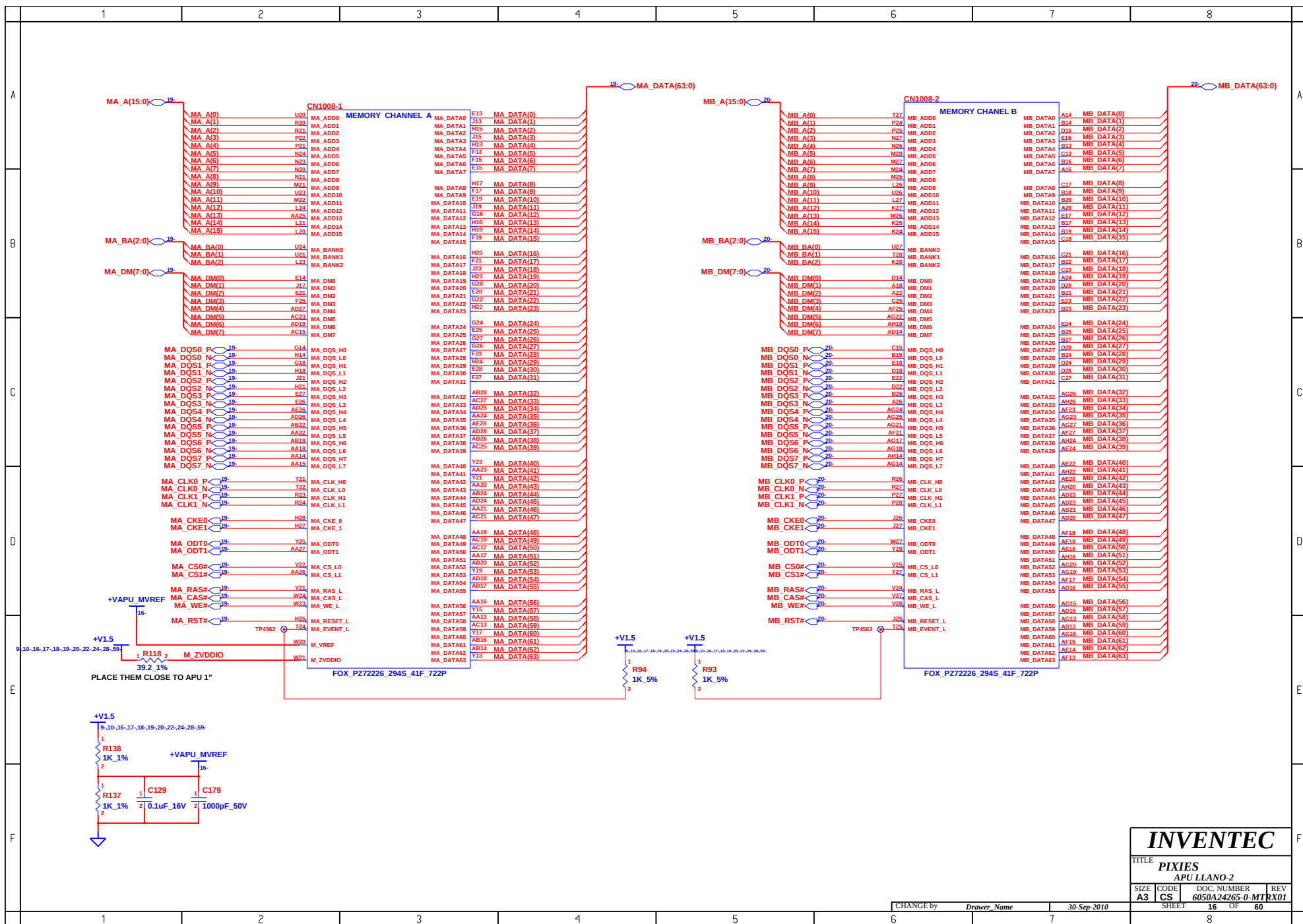
TITLE PIXIES POWER(SLEEP:+V5S/+V3S/+V1.1S)			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET 12		OF 60	

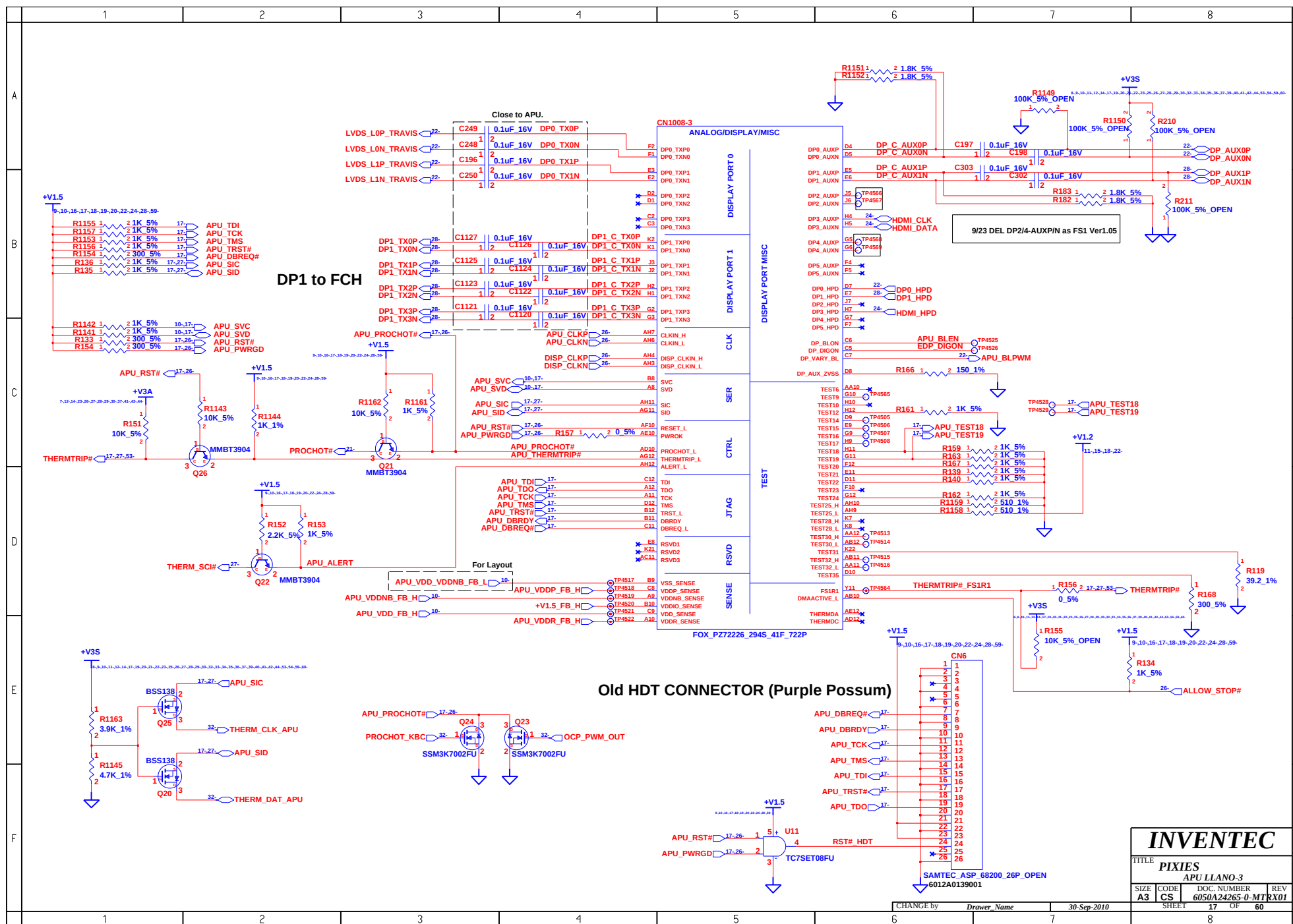
CHANGE by *Drawer Name* 30-Sep-2010

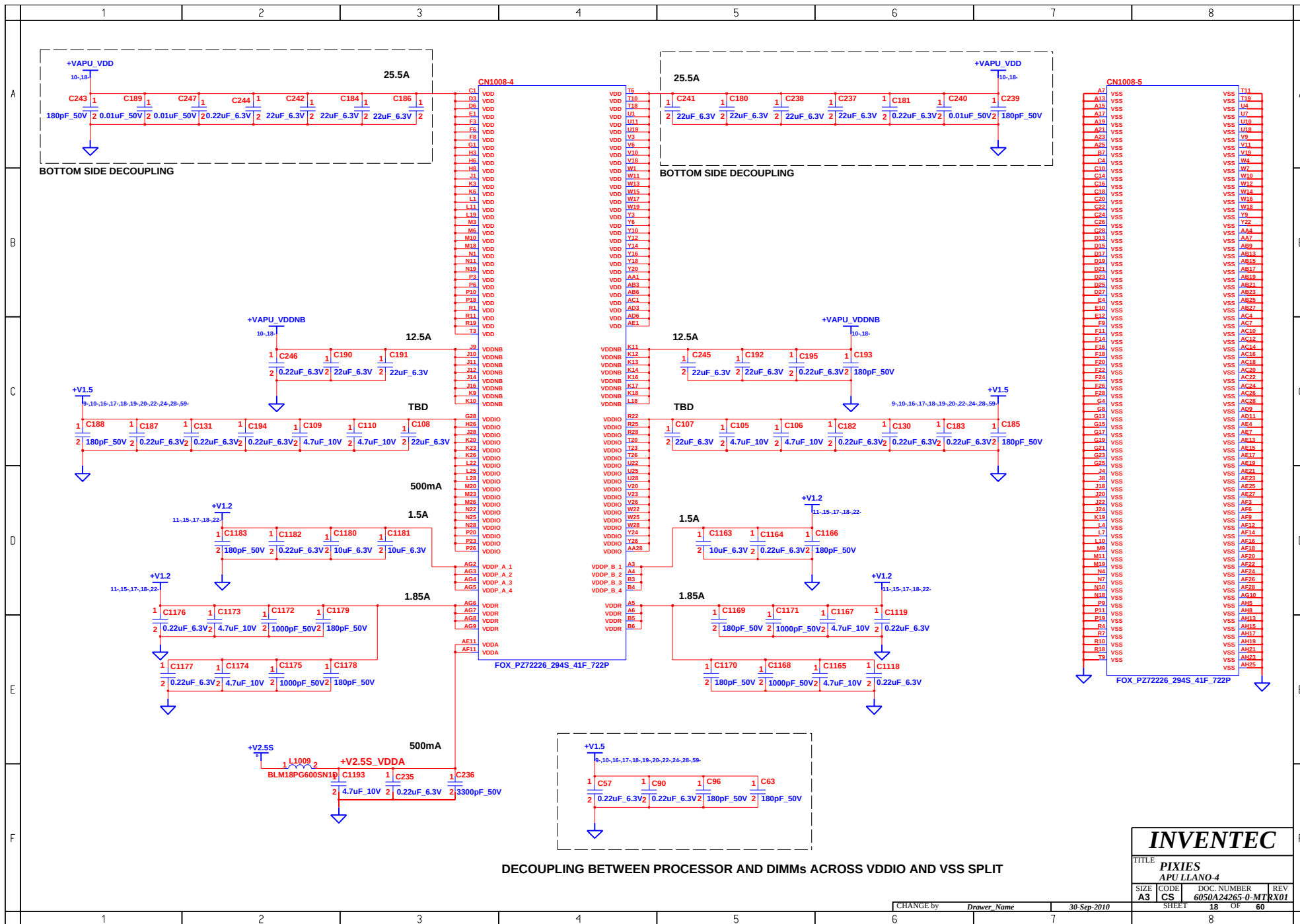


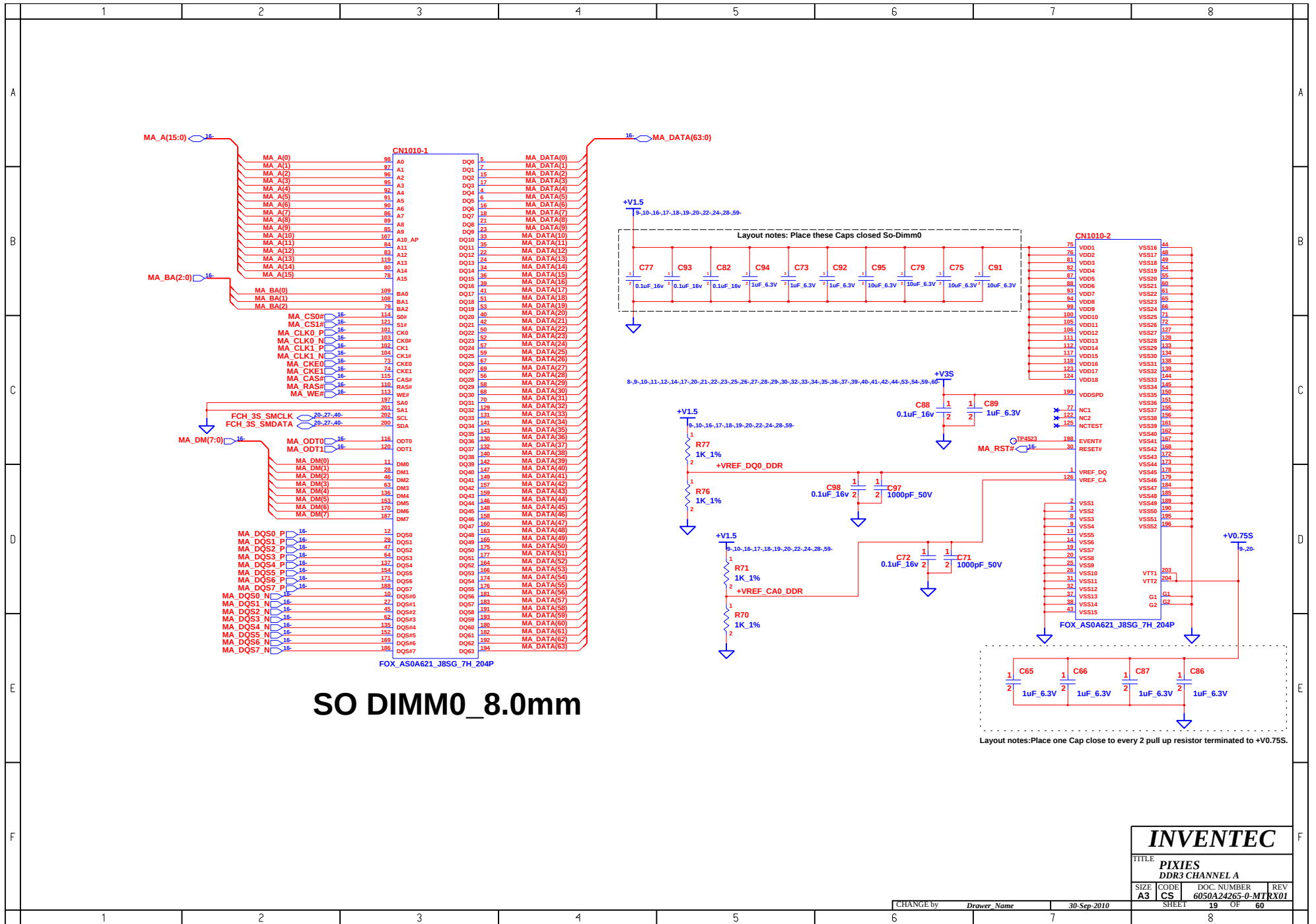


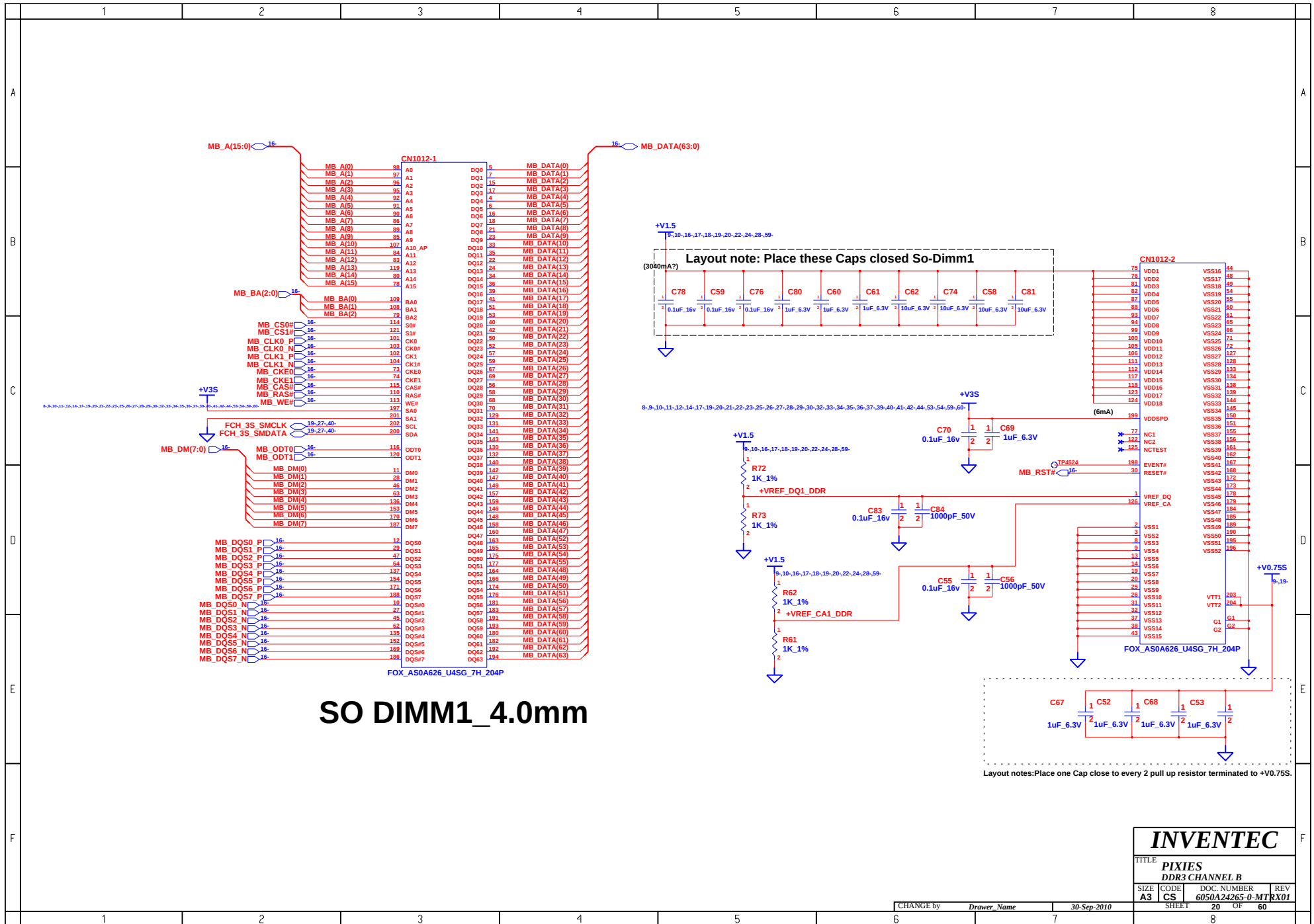












INVENTEC

TITLE			
PIXIES			
DDR3 CHANNEL B			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTRX01	
CHANGE by		Drawer Name	30-Sep-2010
SHEET		20	OF 60

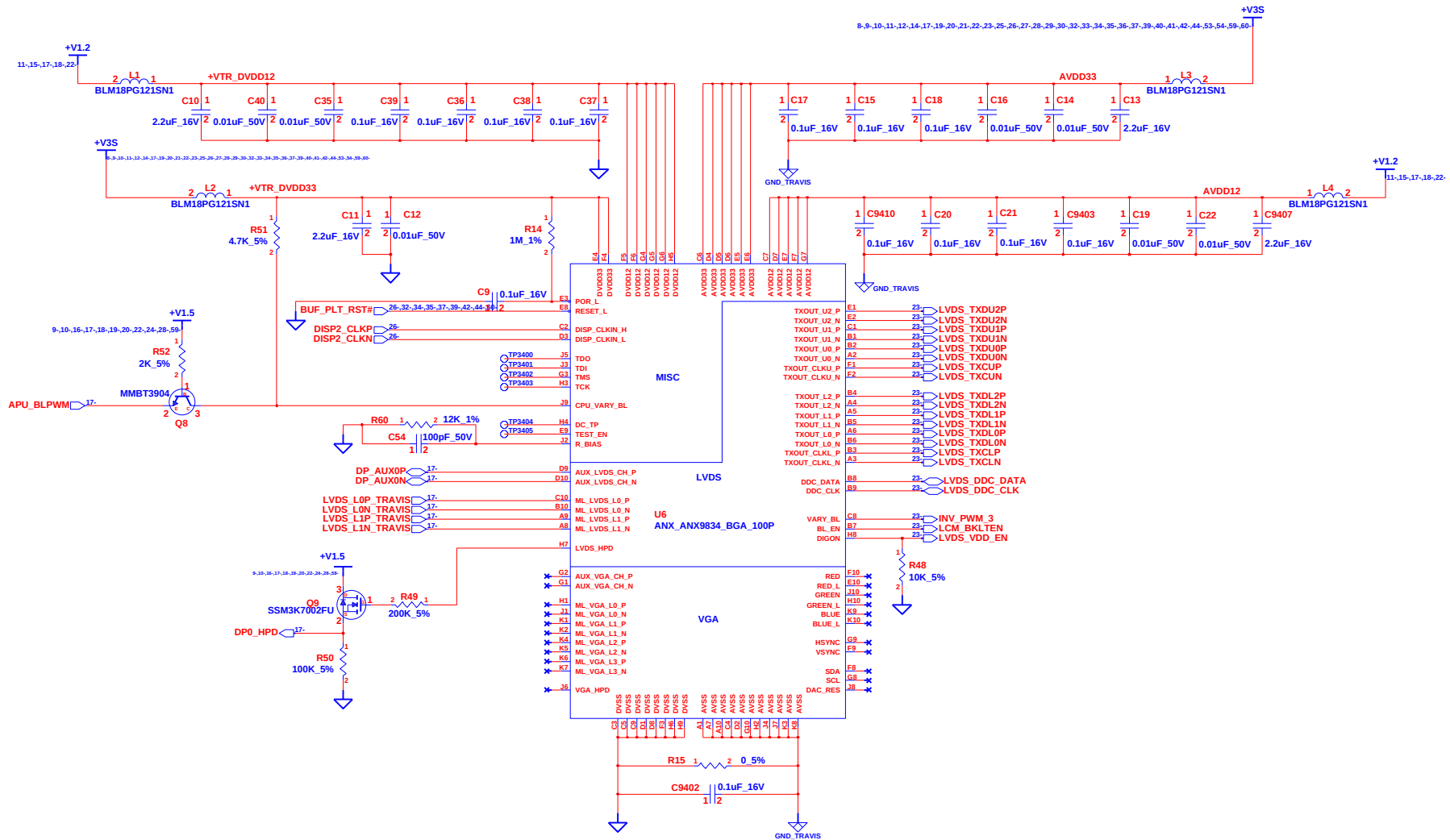
Travis (DP to LVDS)

INVENTEC

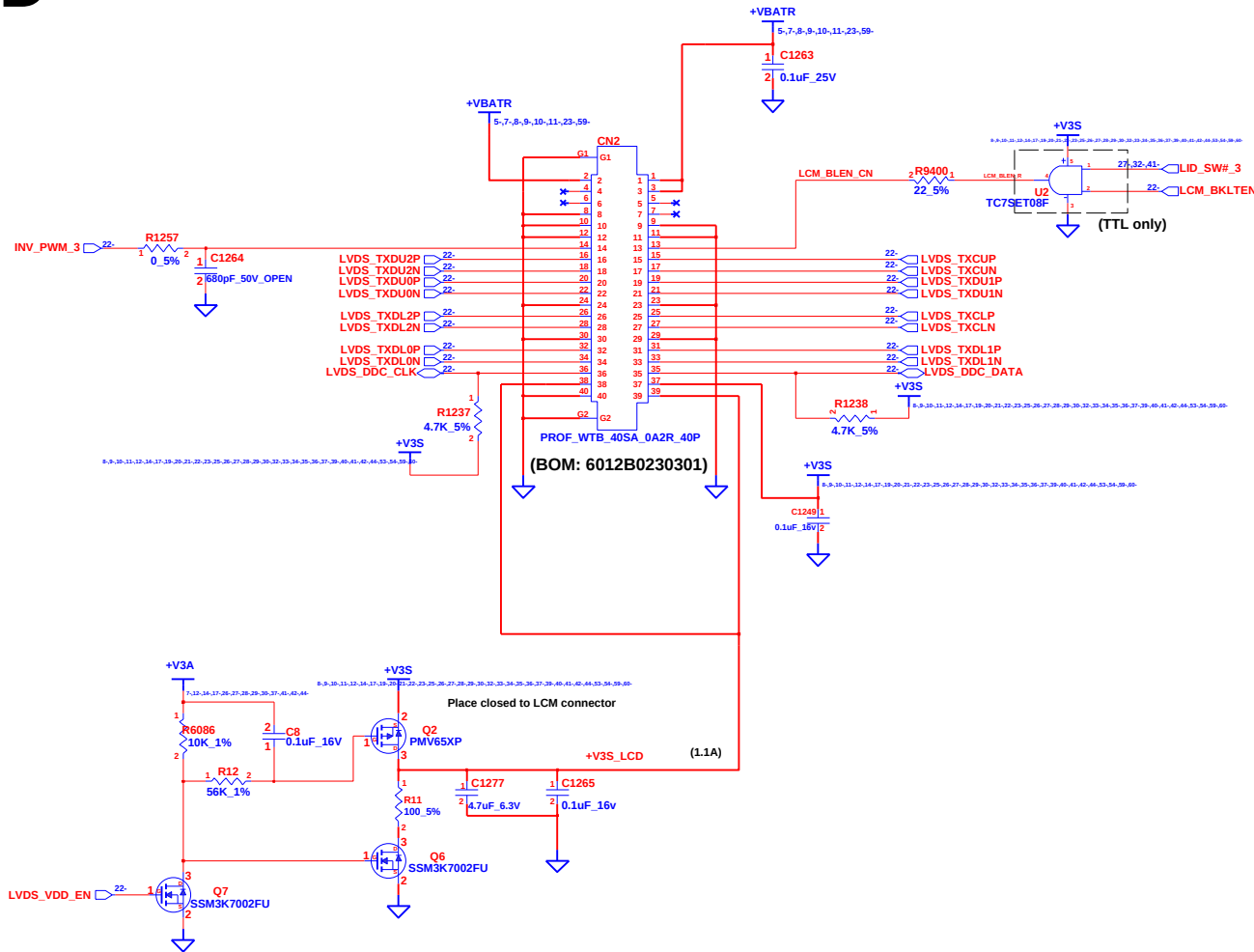
TITLE			
PIXIES			
Travis (DP to LVDS)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTRX01	

CHANGE by *Drawer Name* 30-Sep-2010

SHEET 22 OF 60



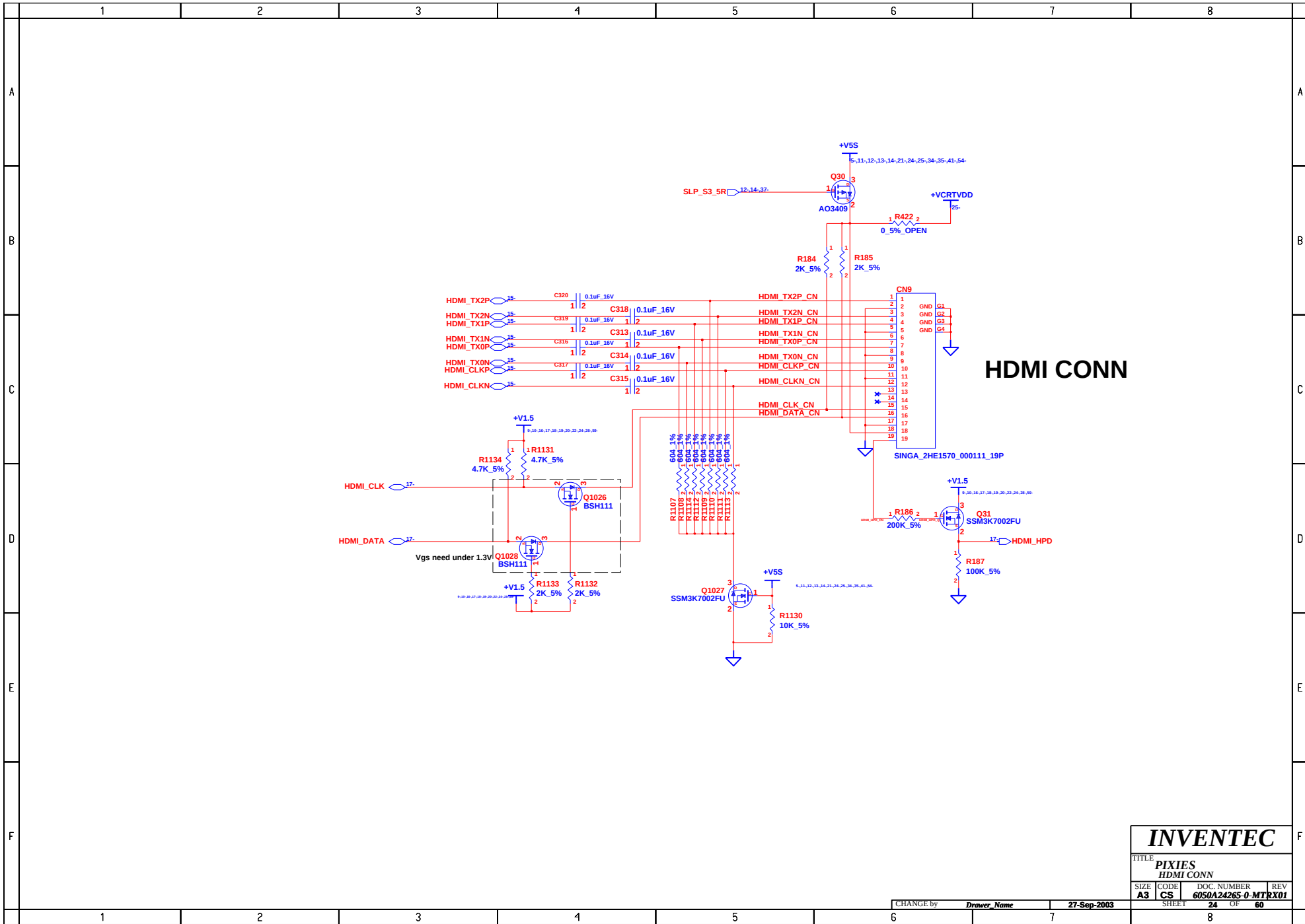
LCD



INVENTEC

TITLE			
PIXIES			
LVDS			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTRX01	
SHEET		23	OF 60

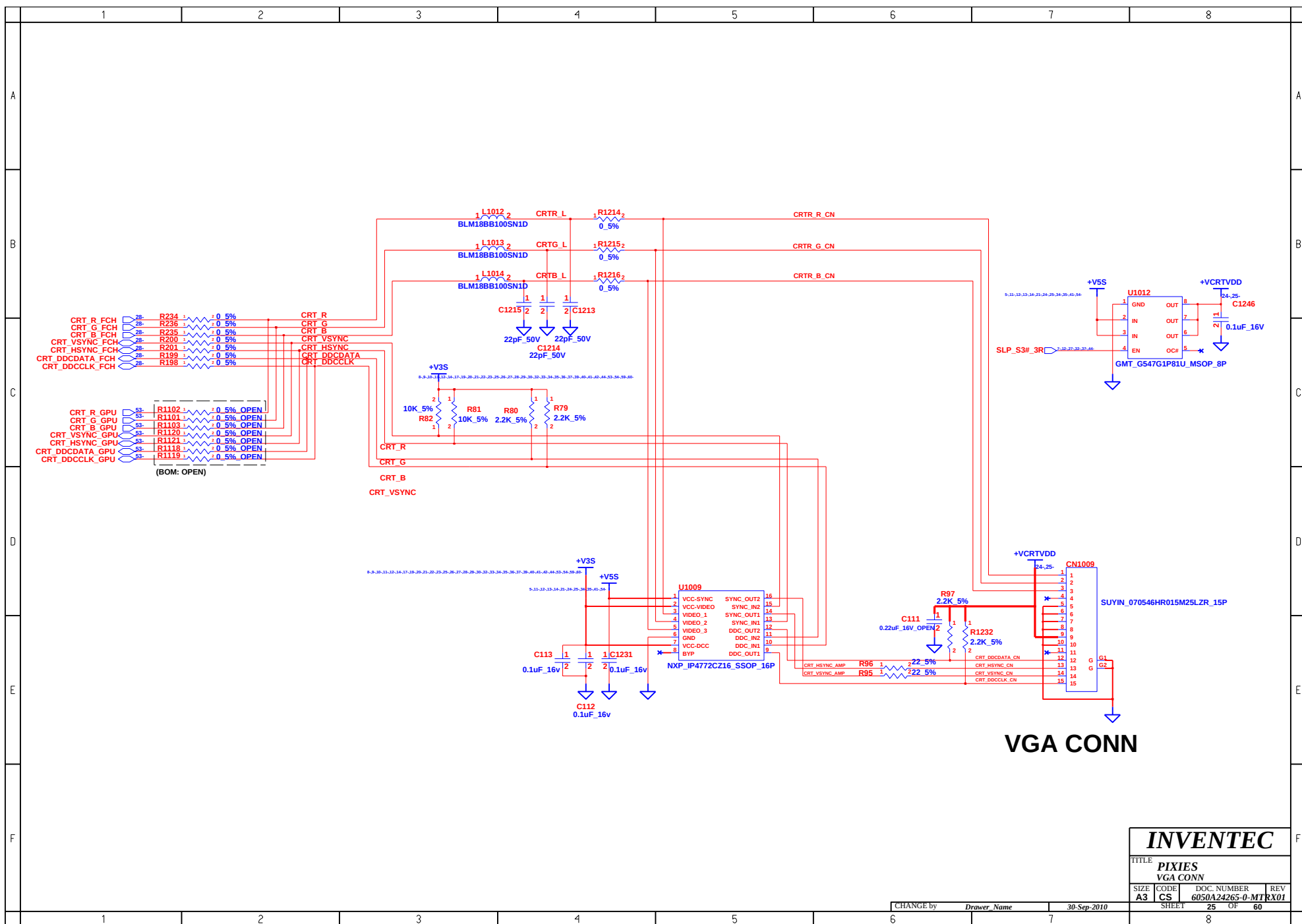
CHANGE by *Drawn Name* 30-Sep-2010

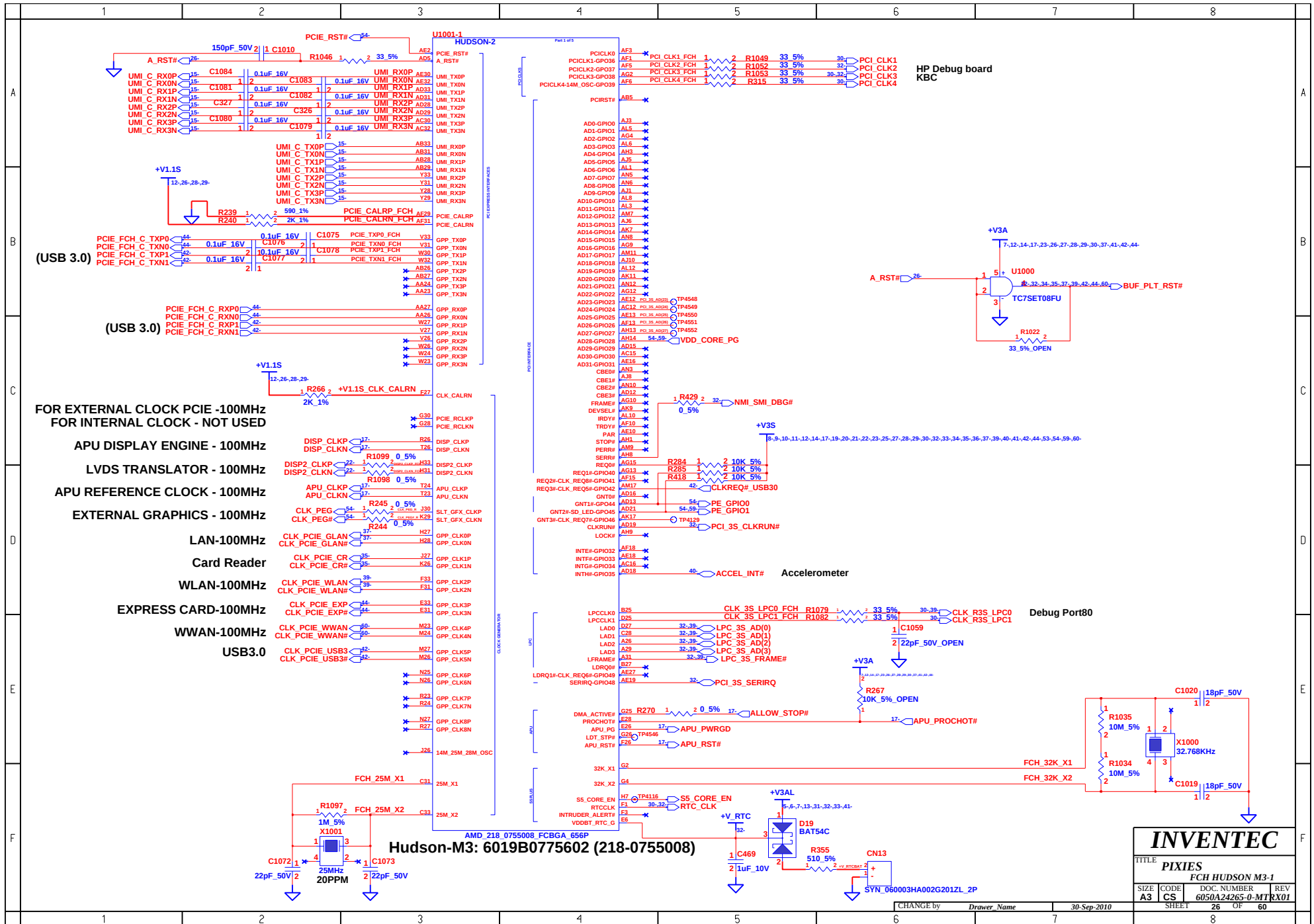


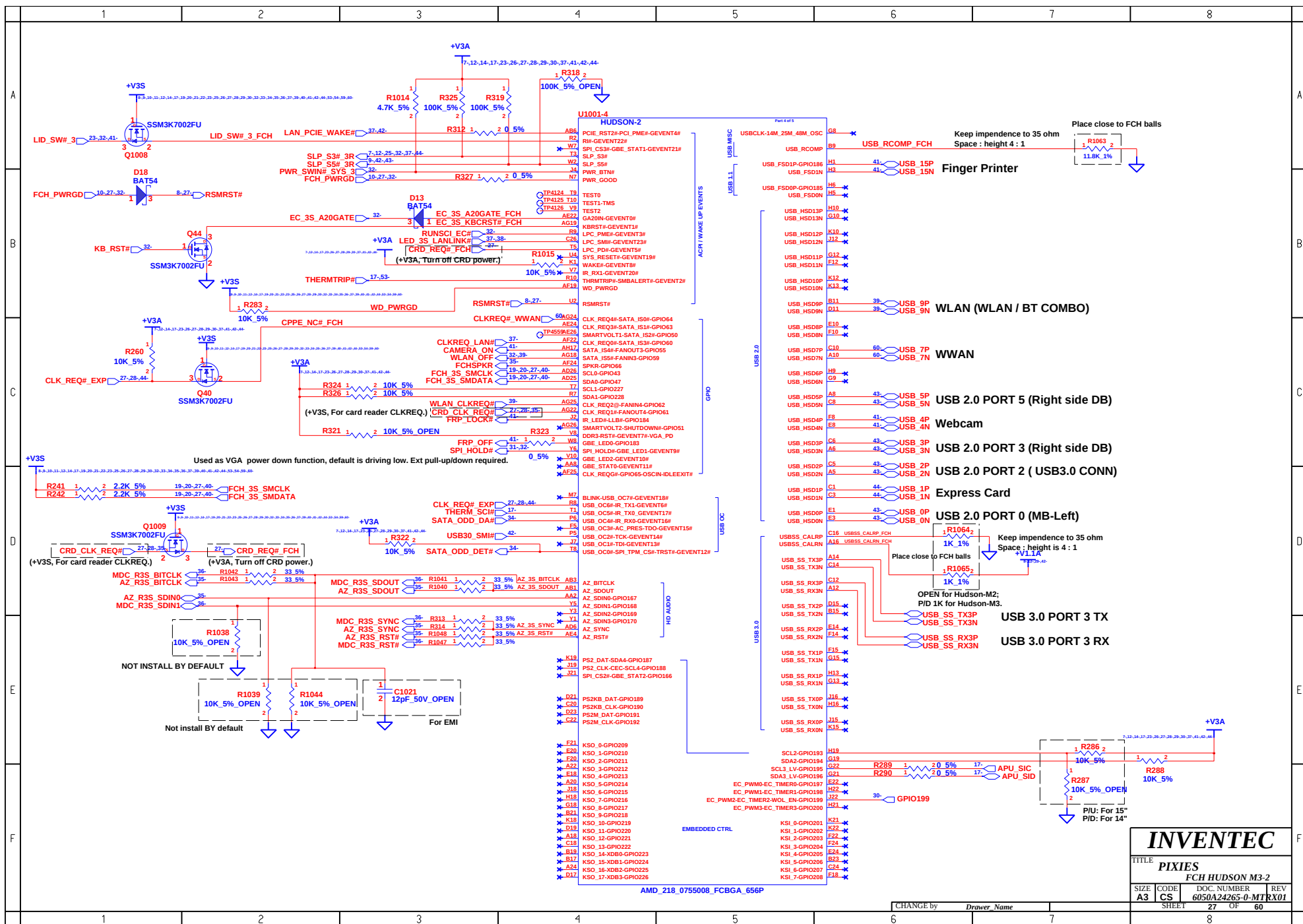
INVENTEC

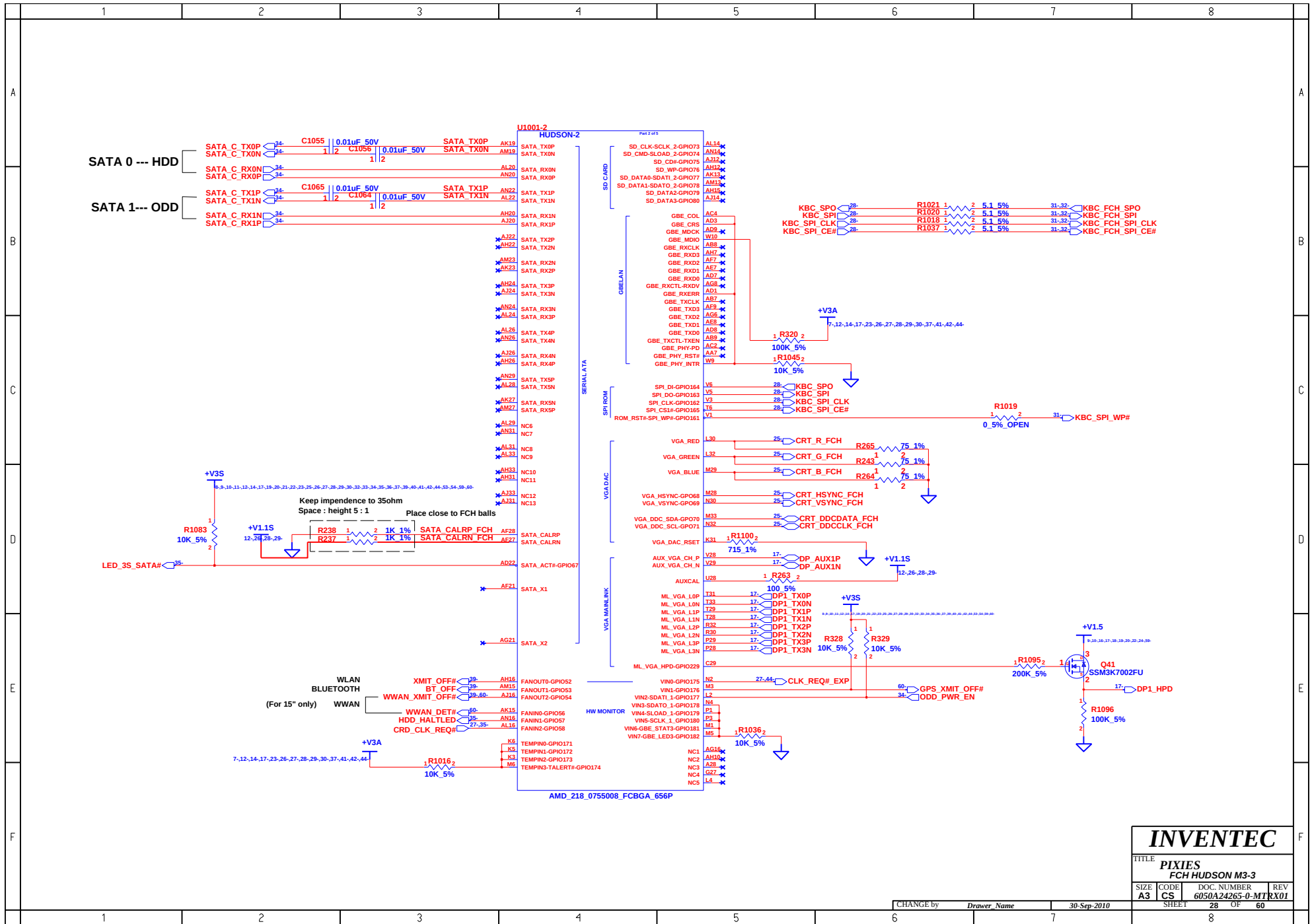
TITLE
**PIXIES
HDMI CONN**

SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV
SHEET 24		OF 60	





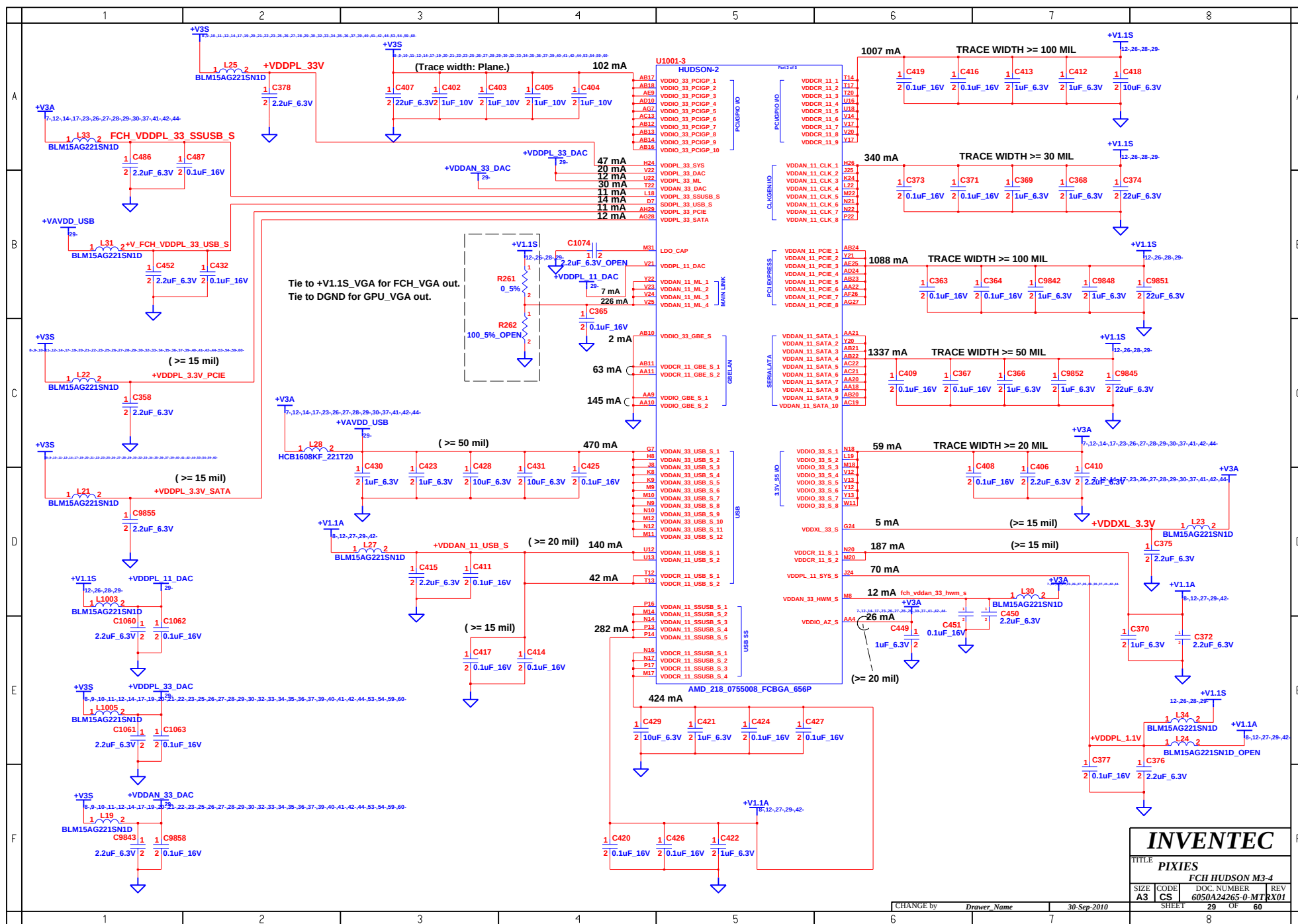


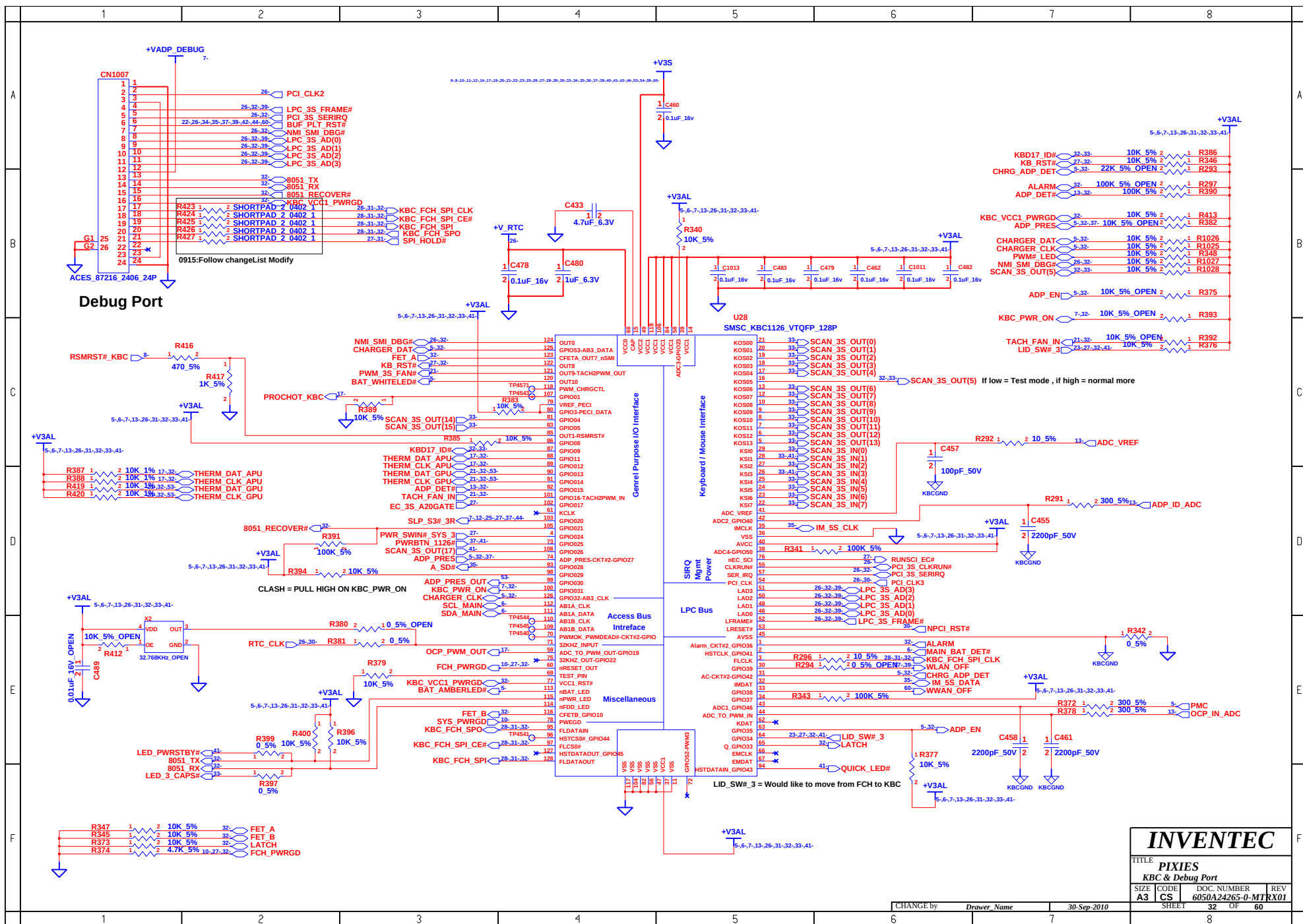


INVENTEC

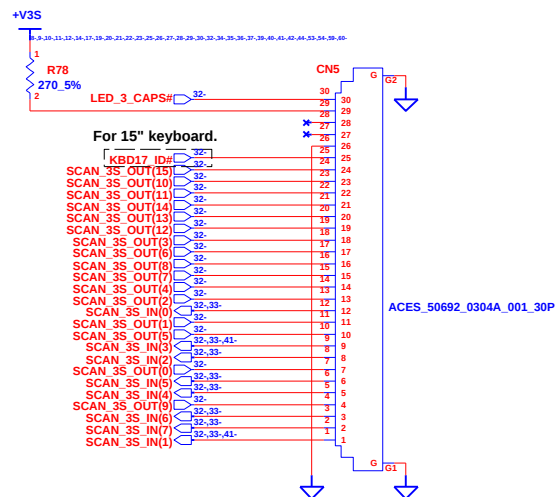
TITLE PIXIES FCH HUDSON M3-3			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01
SHEET 28		OF 60	

CHANGE by Drawer Name 30-Sep-2010

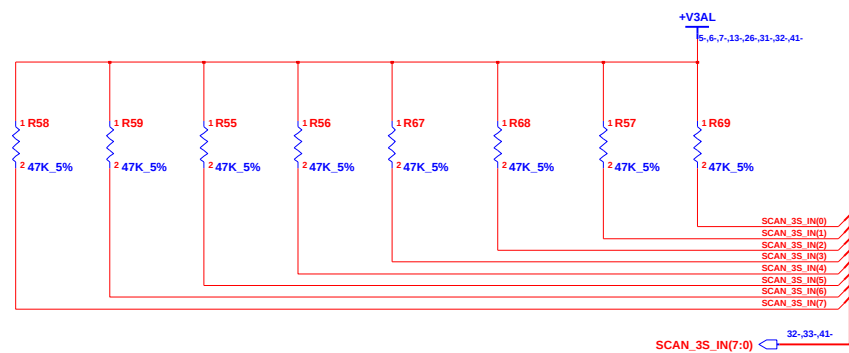




Keyboard CONN



9/9 : Removed T/P CONN CN1000



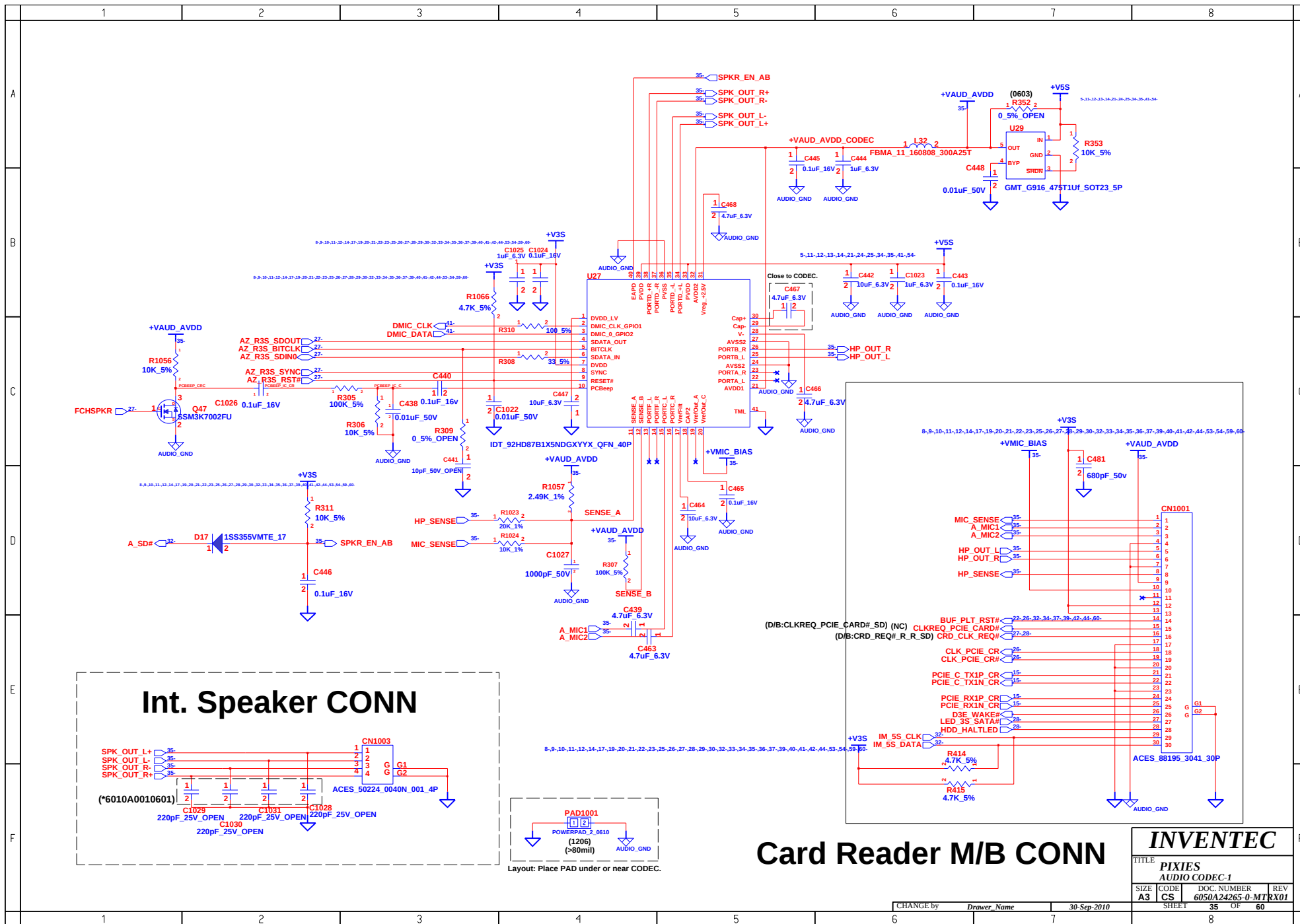
INVENTEC			
TITLE			
PIXIES			
Keyboard & TouchPad CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	01
CHANGE by		Draver Name	30-Sep-2010
SHEET		33	OF 60

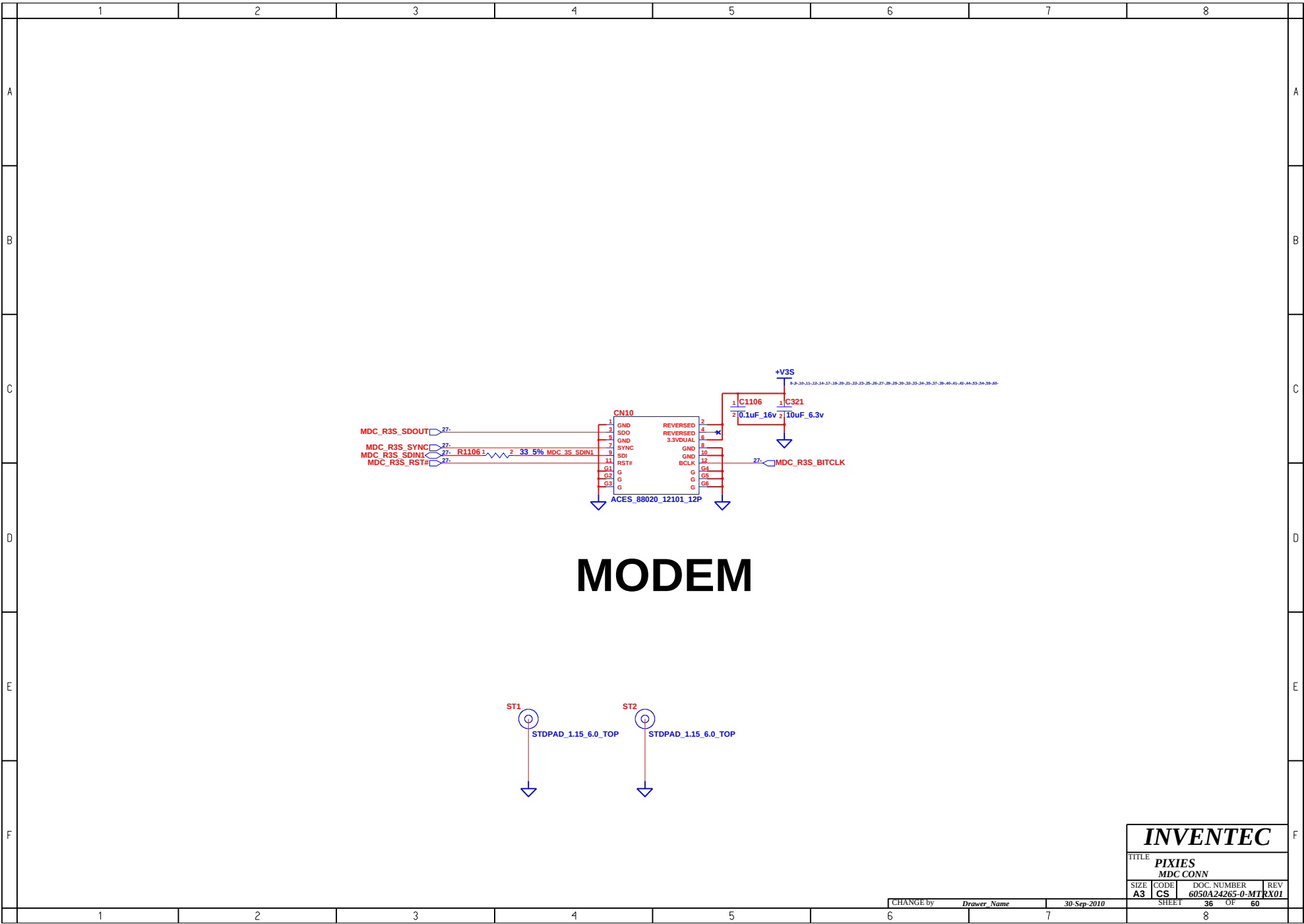


TITLE			
PIXIES			
SATA HDD & ODD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTRX01	
SHEET		34	OF 60

CHANGE by	<i>Drawer_Name</i>	30-Sep-2010
-----------	--------------------	-------------

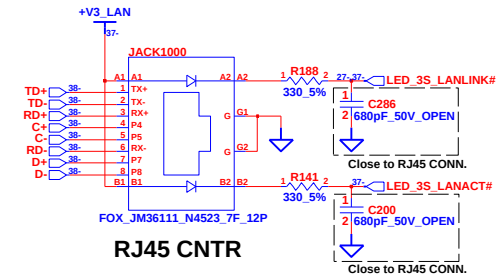
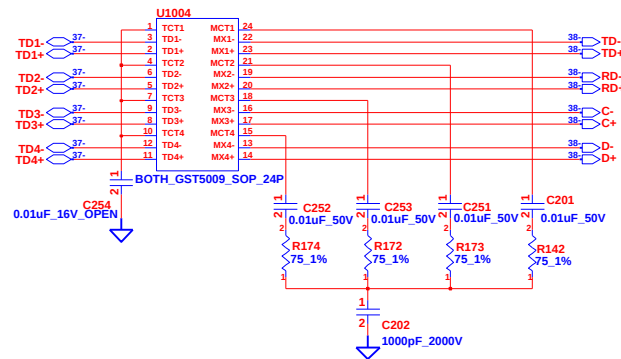
CS	6050A24265-0-MTR
SHEET	34 OF 60





LAYOUT NOTE:

To place one 0.1uF at each pin 1 , 4 , 7 , 10
and place the 1uF in the spot that
is as close as possible to all 4 pins



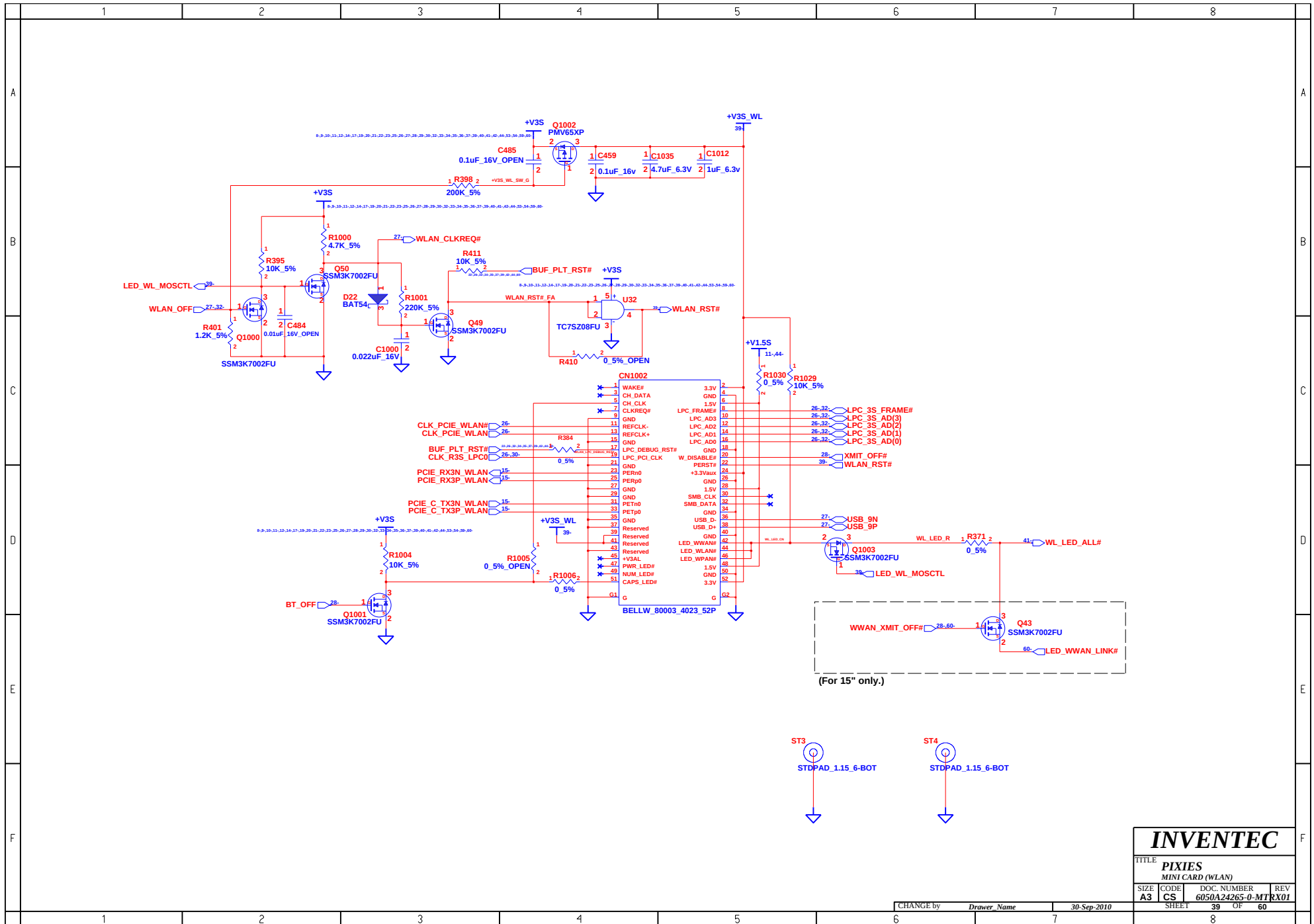
INVENTEC

TITLE **PIXIES**
Transformer & RJ45 CONN

SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01
-------------------	-------------------	---	------------------

CHANGE by *Drawn Name* 30-Sep-2010

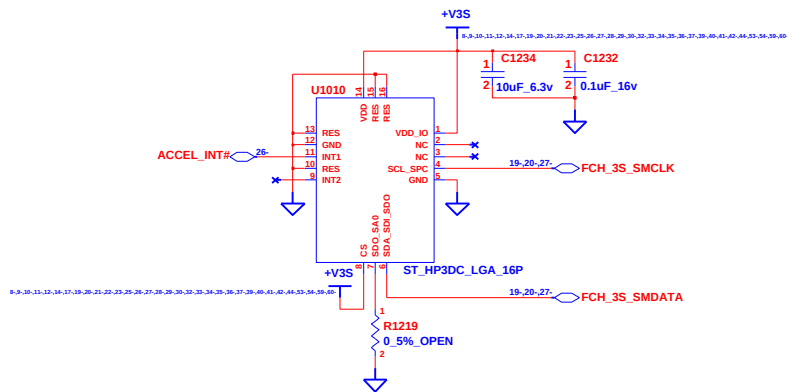
SHEET 38 OF 60



INVENTEC

TITLE		
PIXIES		
MINI CARD (WLAN)		
SIZE	CODE	DOC. NUMBER
A3	CS	6050A24265-0-MTRX01
SHEET		REV
39		60

CHANGE by *Drawn Name* 30-Sep-2010



Accelerometer

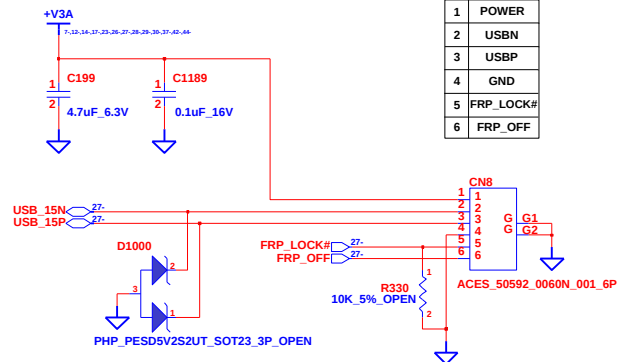
INVENTEC

TITLE
**PIXIES
ACCELEROMETER**

SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
-------------------	-------------------	---	-----------------

CHANGE by *Drawn Name* 30-Sep-2010

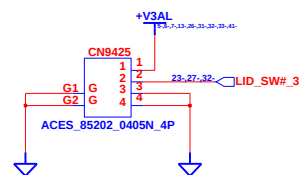
SHEET 40 OF 60



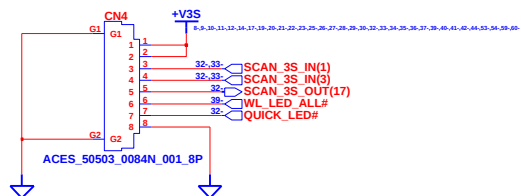
Intel connector define

1	POWER
2	USBN
3	USBP
4	GND
5	FRP_LOCK#
6	FRP_OFF

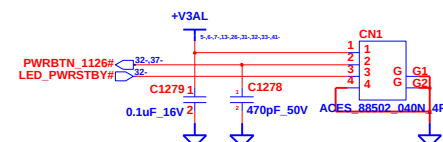
Finger Printer CONN



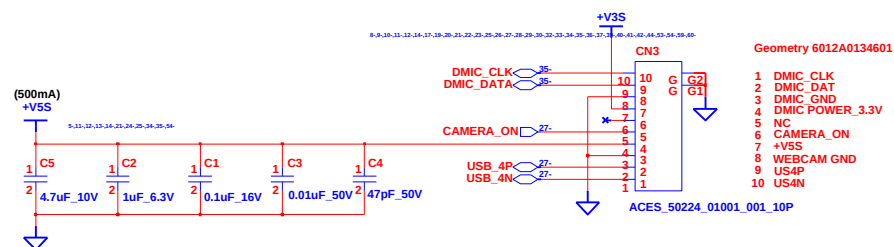
LID SWITCH B/B CONN



Quick Launch B/B CONN



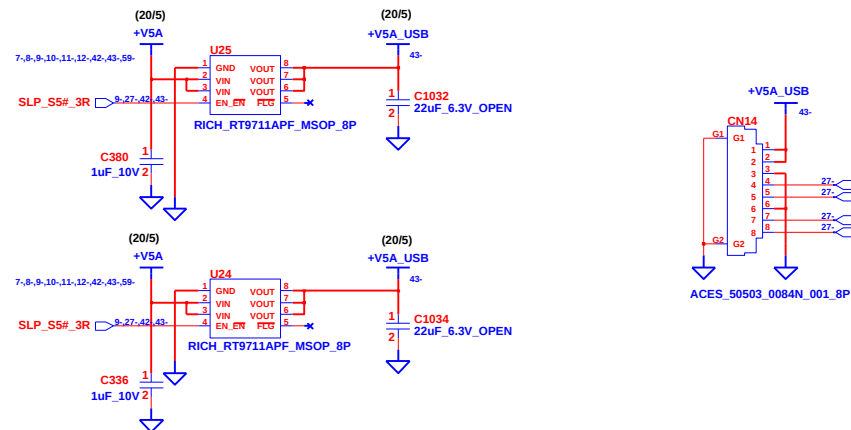
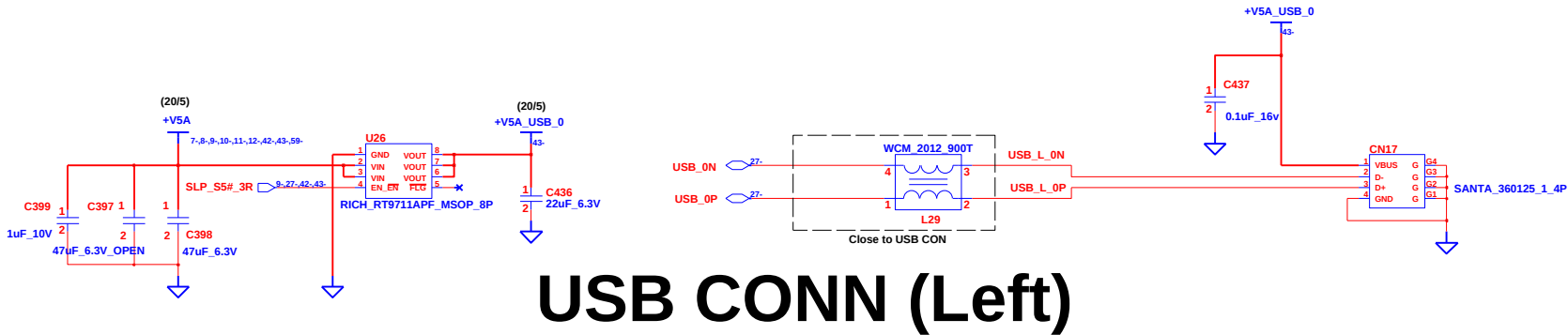
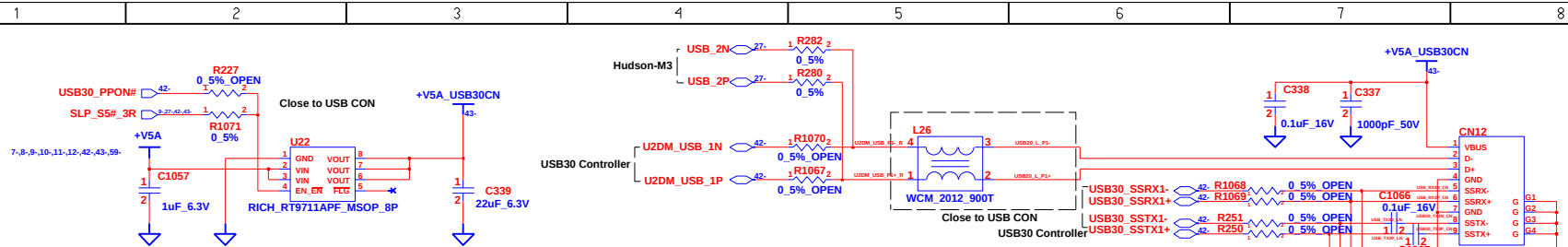
Power Button B/B CONN



DMIC / WebCAM

INVENTEC			
TITLE PIXIES			
PWR&FP&Launch&Web/DMIC CONN			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV
SHEET 41		OF 60	

CHANGE by Drawer Name 30-Sep-2010



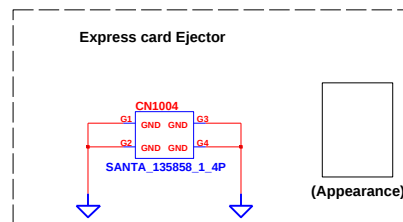
USB B/B CONN (MB Right side)

INVENTEC

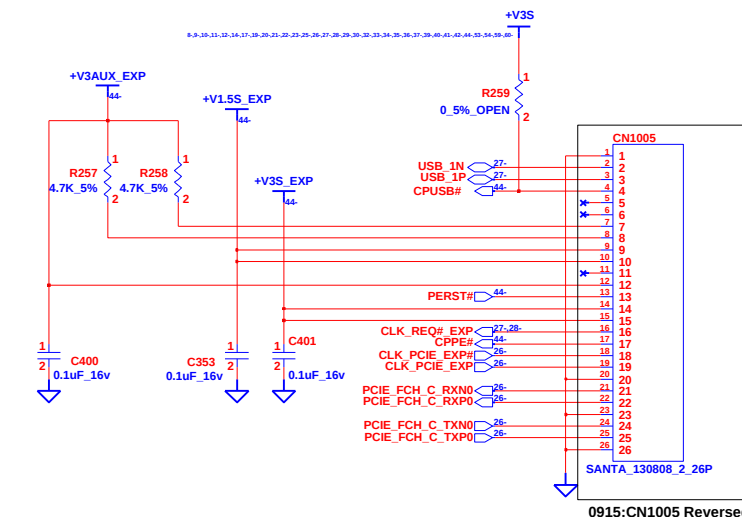
TITLE			
PIXIES			
USB3.0 & USB2.0 CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MT	01

CHANGE by *Drawer Name* 30-Sep-2010

SHEET 43 OF 60



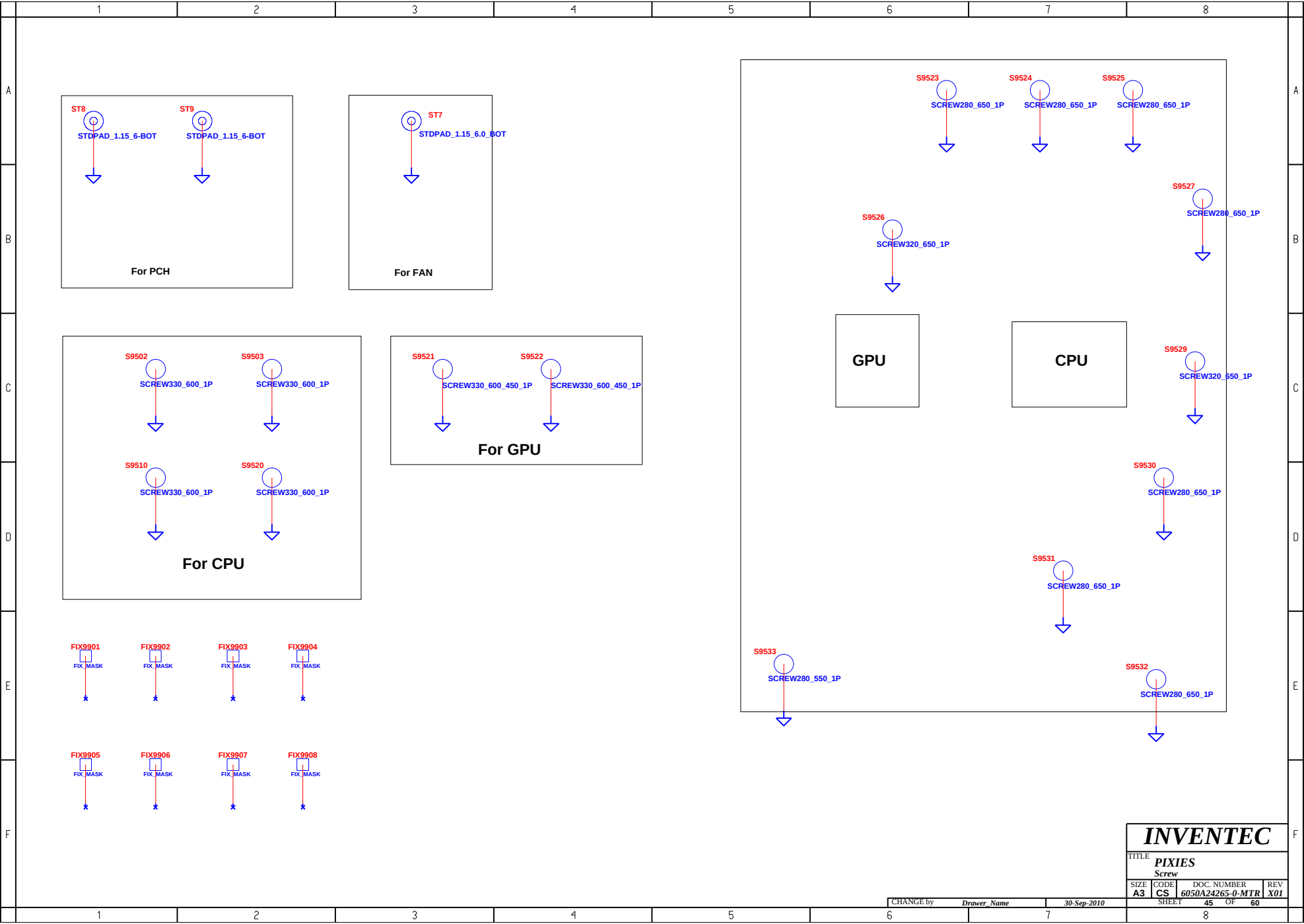
Express card

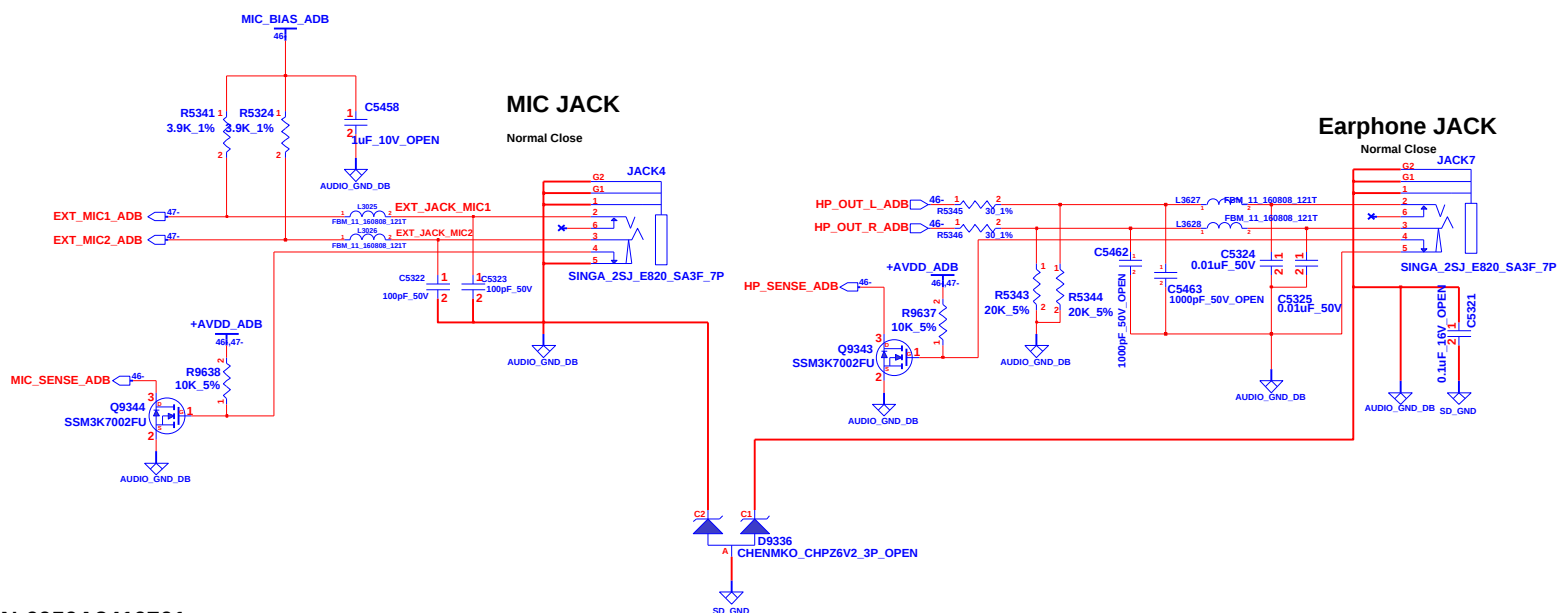
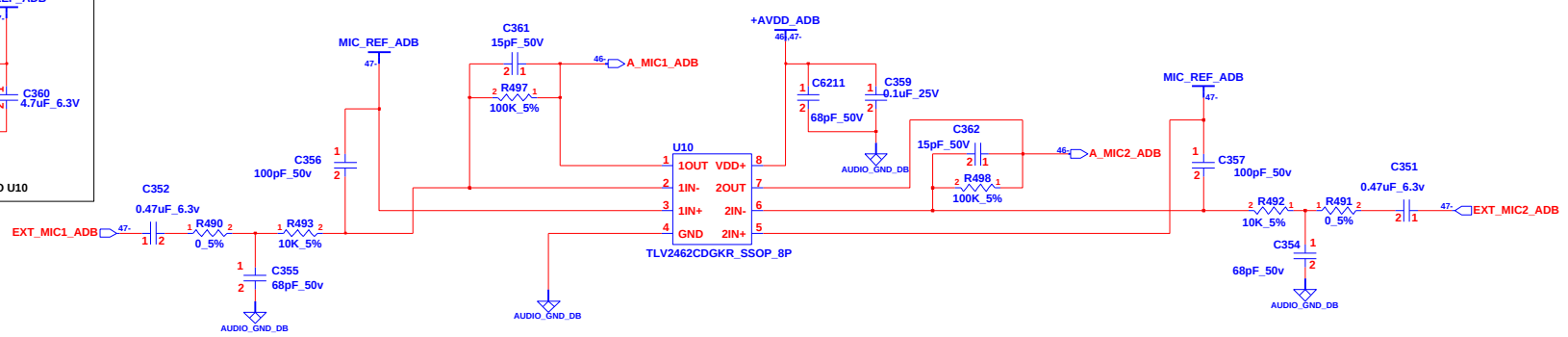
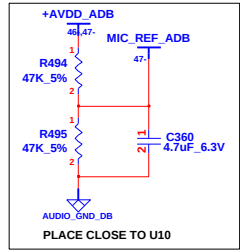


INVENTEC

TITLE PIXIES Express Card			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01
SHEET 44 OF 60			

CHANGE by *Drawer Name* 30-Sep-2010





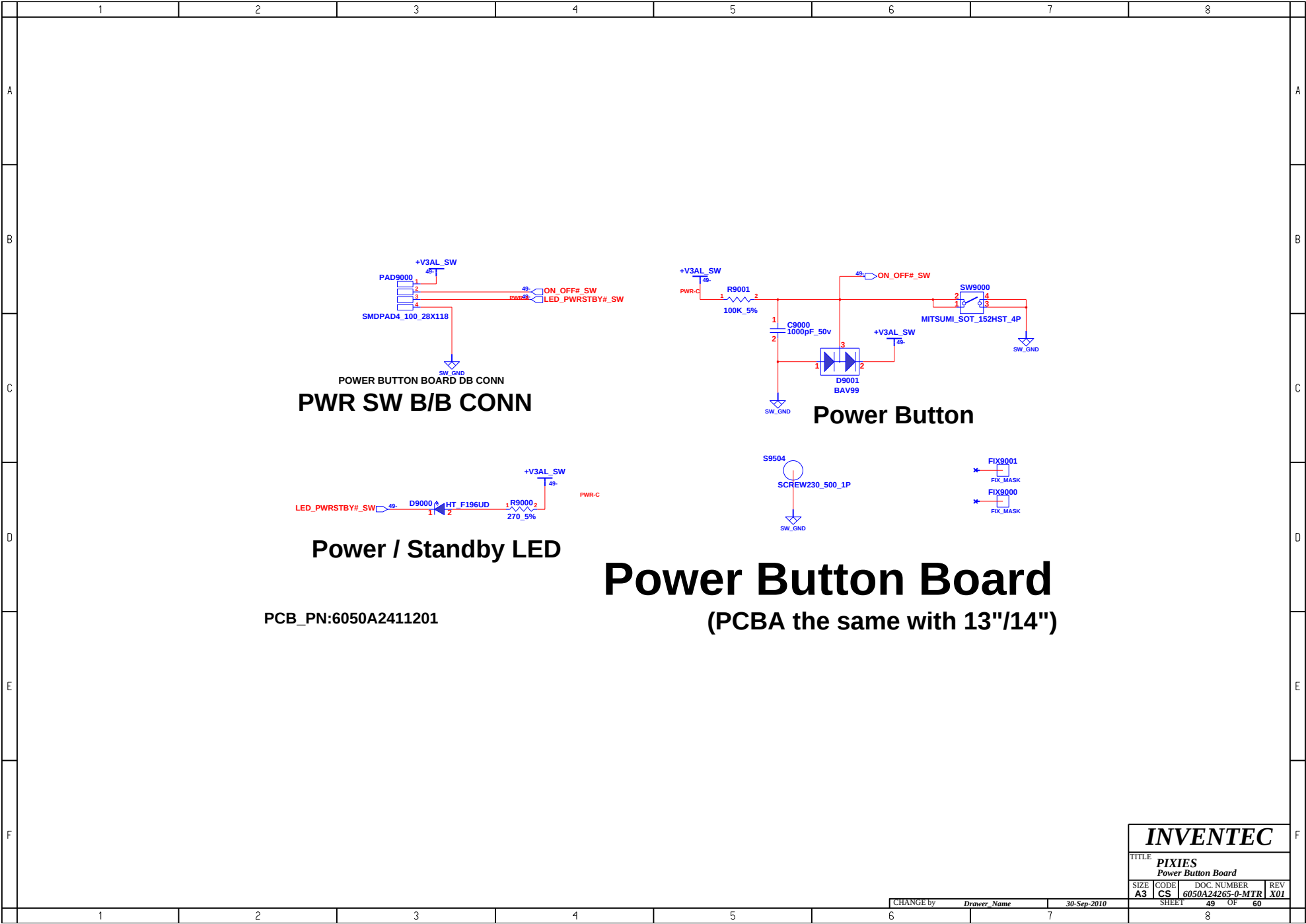
PCB_PN:6050A2410701

Card READER & AUDIO BOARD 2/2

INVENTEC			
TITLE PIXIES			
3 in 1 Card Reader daughter board-2			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV REV

CHANGE by *Drawer Name* 30-Sep-2010

SHEET 47 OF 60



PWR SW B/B CONN

Power Button

Power / Standby LED

Power Button Board

(PCBA the same with 13"/14")

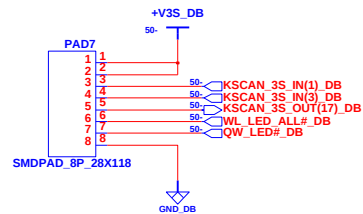
PCB_PN:6050A2411201

INVENTEC

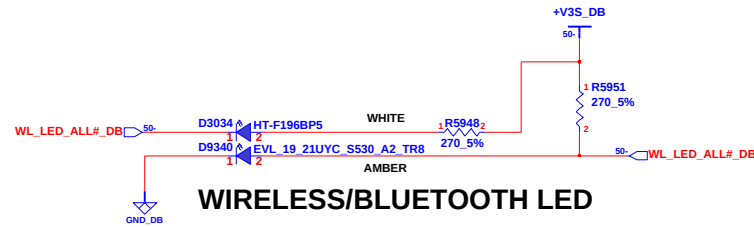
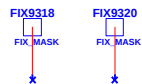
TITLE PIXIES Power Button Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
CHANGE by Drawer Name		30-Sep-2010	
SHEET		49 OF 60	

Quick Launch Board

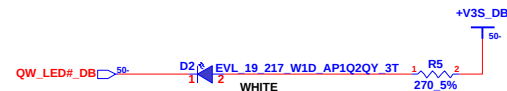
PCB_PN:6050A2410501



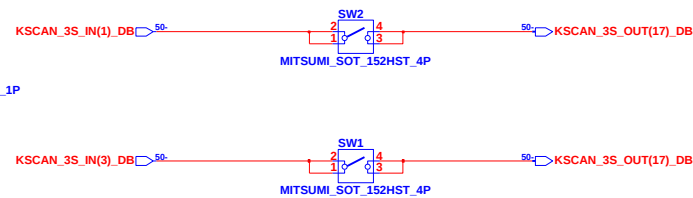
QUICK LAUNCH BOARD DB CONN



WIRELESS/BLUETOOTH LED

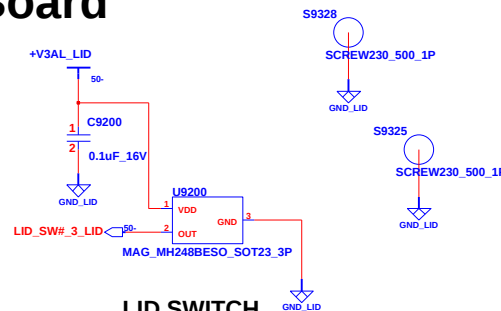
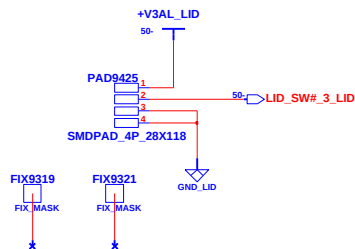


QUICK Lanch LED



LID SWITCH BOARD Board

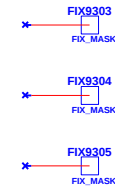
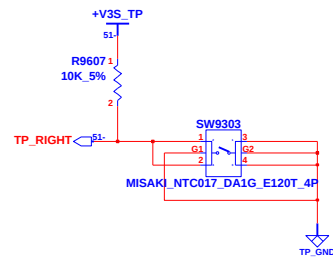
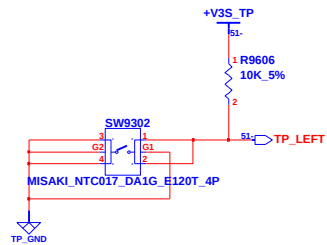
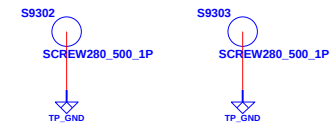
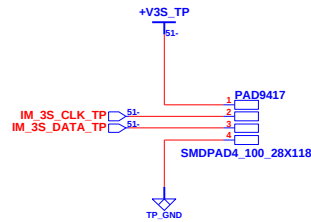
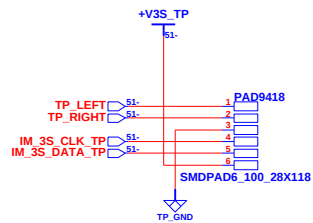
PCB_PN:6050A2414801



LID SWITCH
Referense Number 9200-9299

INVENTEC			
TITLE PIXIES			
LID & Quick Launch & WLAN SW Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET 50		OF 60	

CHANGE by Drawer Name 30-Sep-2010



LEFT BUTTON

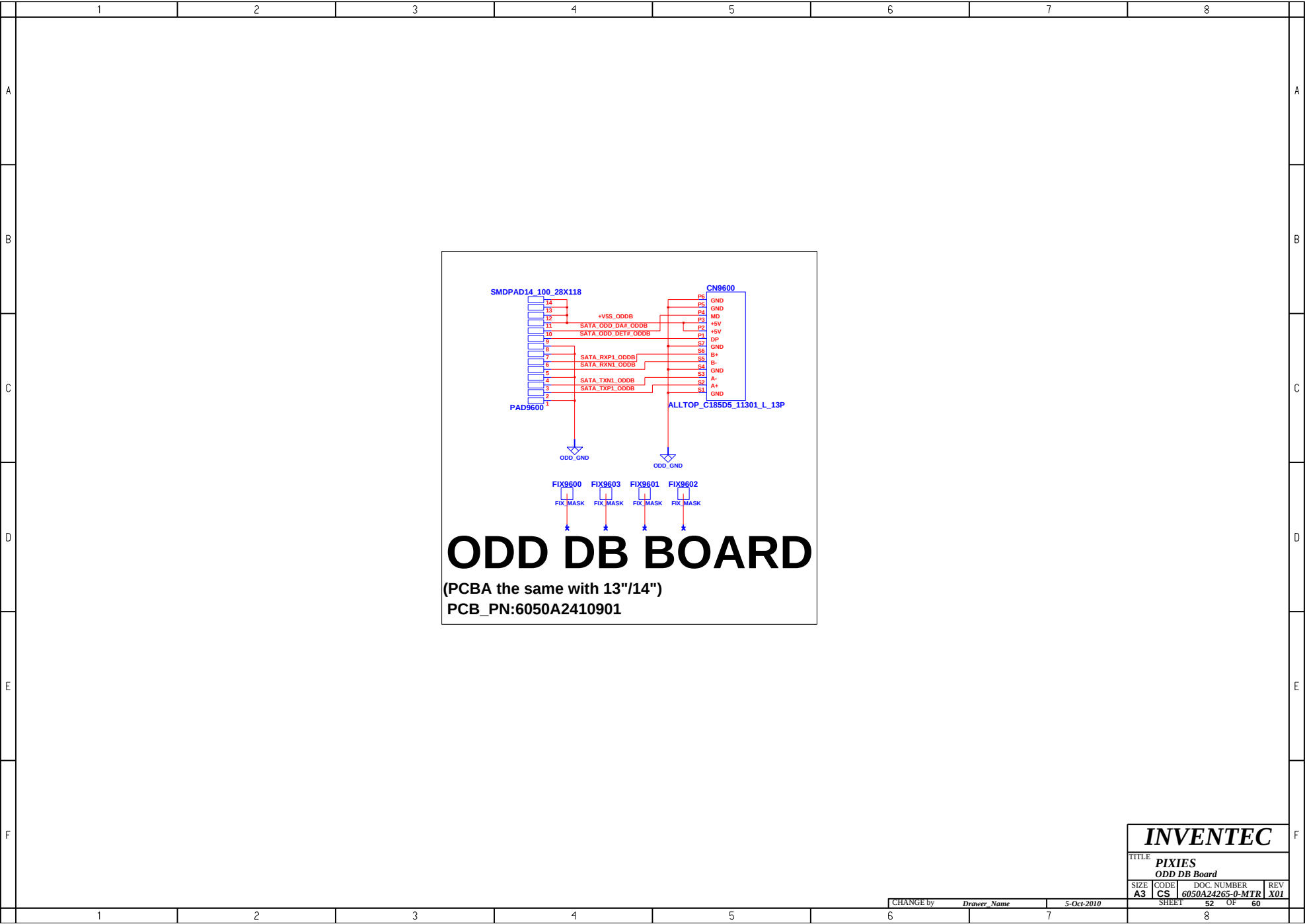
RIGHT BUTTON

PICK Button Board

PCB_PN:6050A2410601

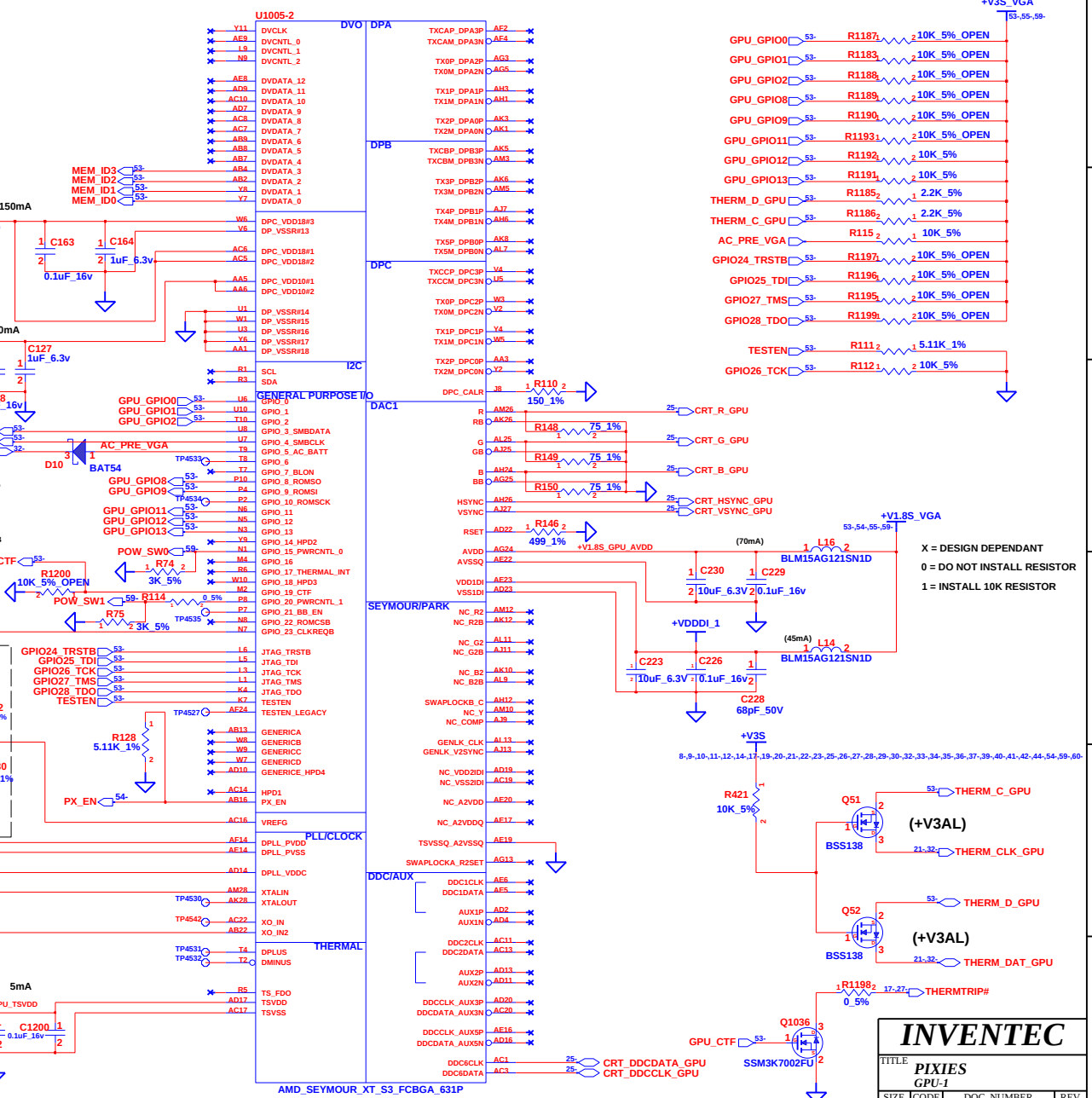
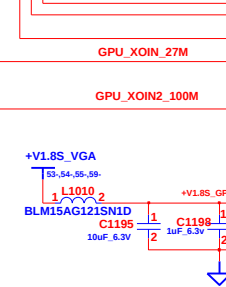
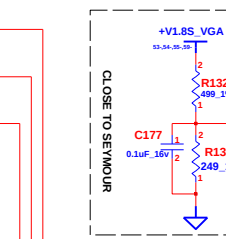
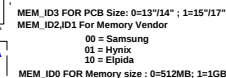
INVENTEC			
TITLE PIXIES Pick Button Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET 51		OF 60	

CHANGE by *Drawn Name* 30-Sep-2010

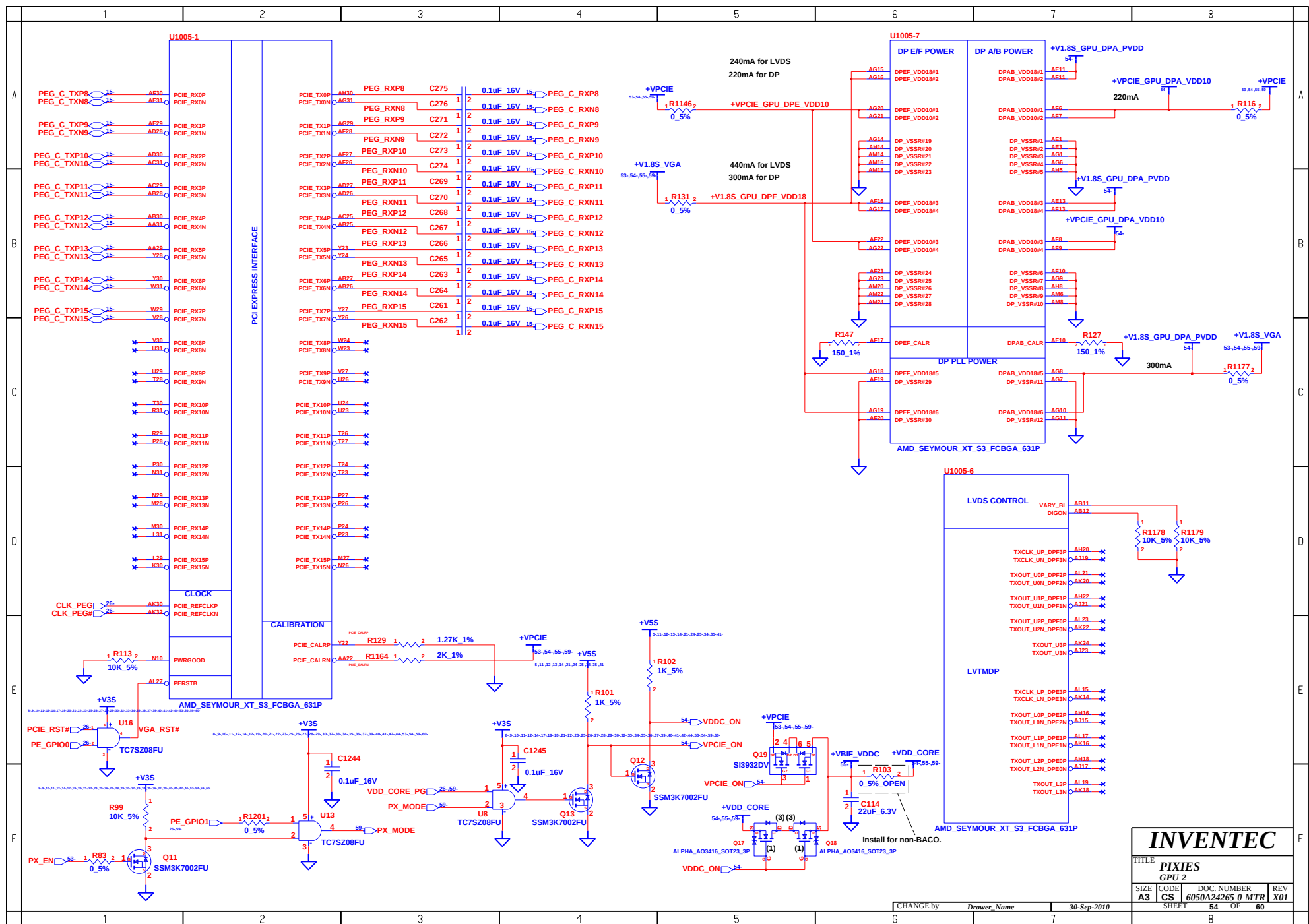


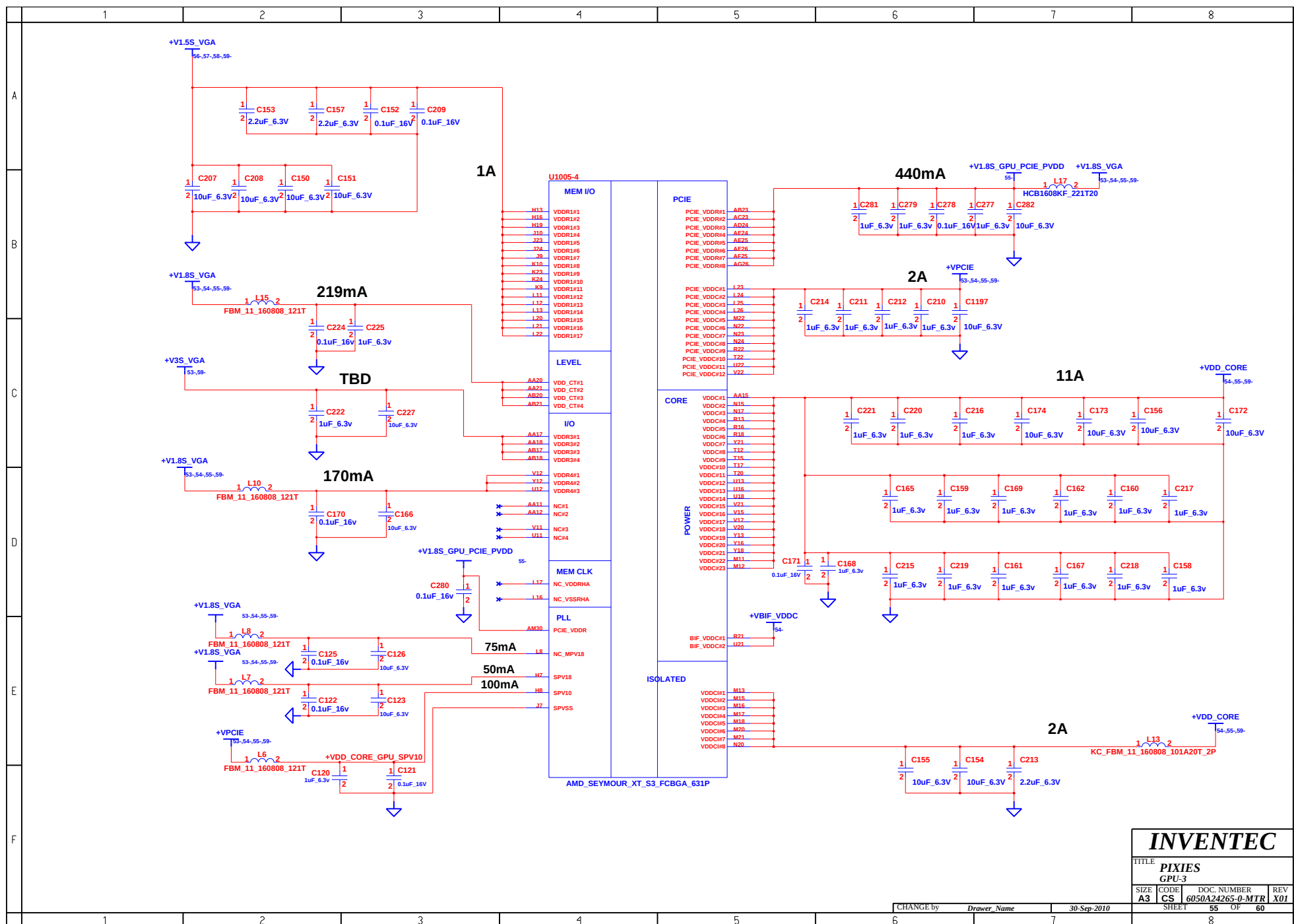
INVENTEC			
TITLE PIXIES ODD DB Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET		52	OF 60

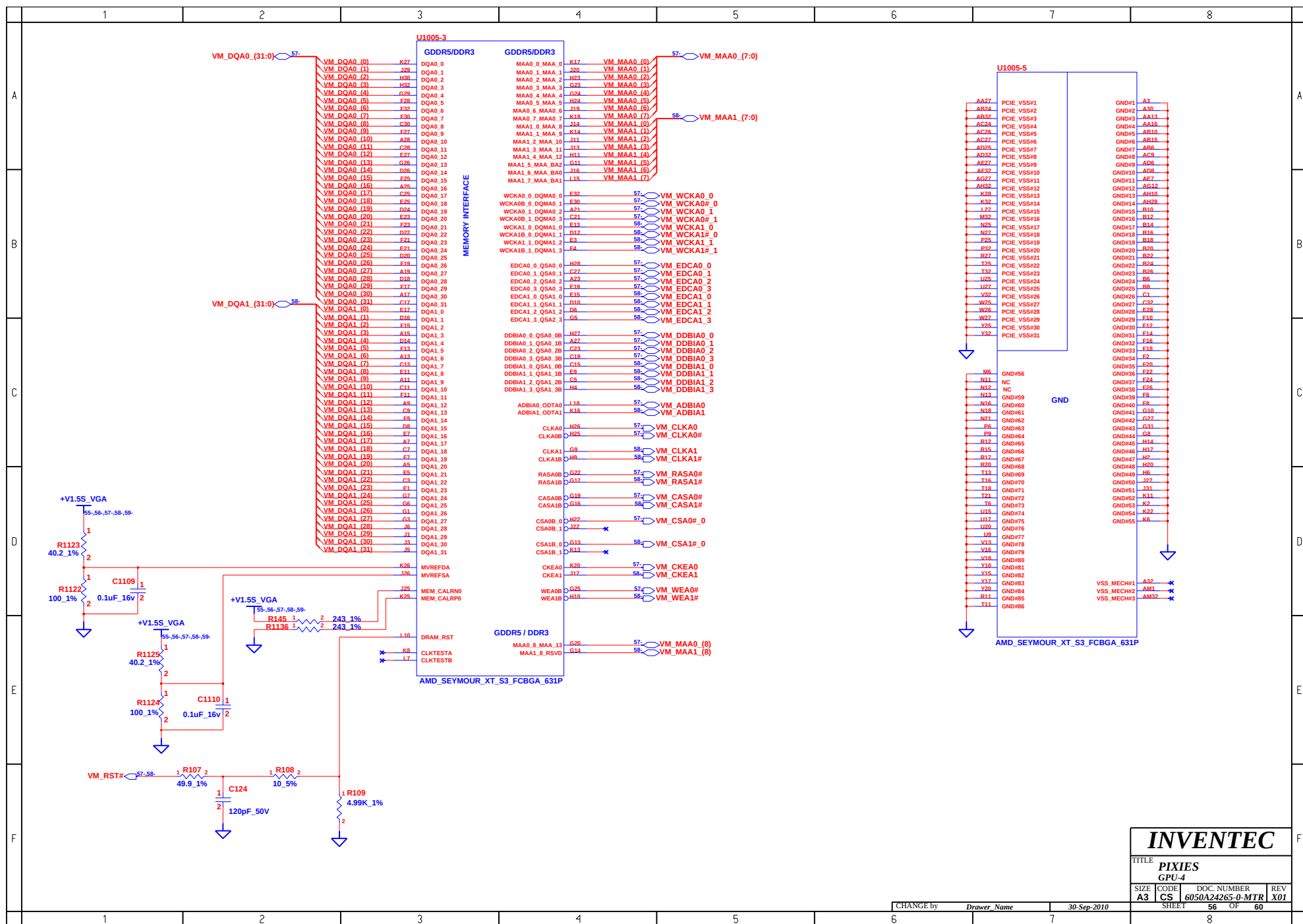
MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	Vendor	(900Mhz)
Ra	Rb	Rc	Rd		
0	0	0	0	Samsung(32MX32)	K4G10325FE-HC04
0	0	0	1	Samsung(64MX32)	K4G20325FC-HC04
0	0	0	1	Hynix(32MX32)	H5GQ1H24A4FR-72C
0	0	0	1	Hynix(64MX32)	H5GQ2H24MFR-72C
0	1	0	0	Elpida(32MX32)	EDW1032BABG-50-F
0	1	0	1	Elpida(64MX32)	EDW2032BABG-50-F
1	0	0	0	Samsung(32MX32)	K4G10325FE-HC04
1	0	0	1	Samsung(64MX32)	K4G20325FC-HC04
1	0	1	0	Hynix(32MX32)	H5GQ1H24A4FR-72C
1	0	1	1	Hynix(64MX32)	H5GQ2H24MFR-72C
1	1	0	0	Elpida(32MX32)	EDW1032BABG-50-F
1	1	0	1	Elpida(64MX32)	EDW2032BABG-50-F

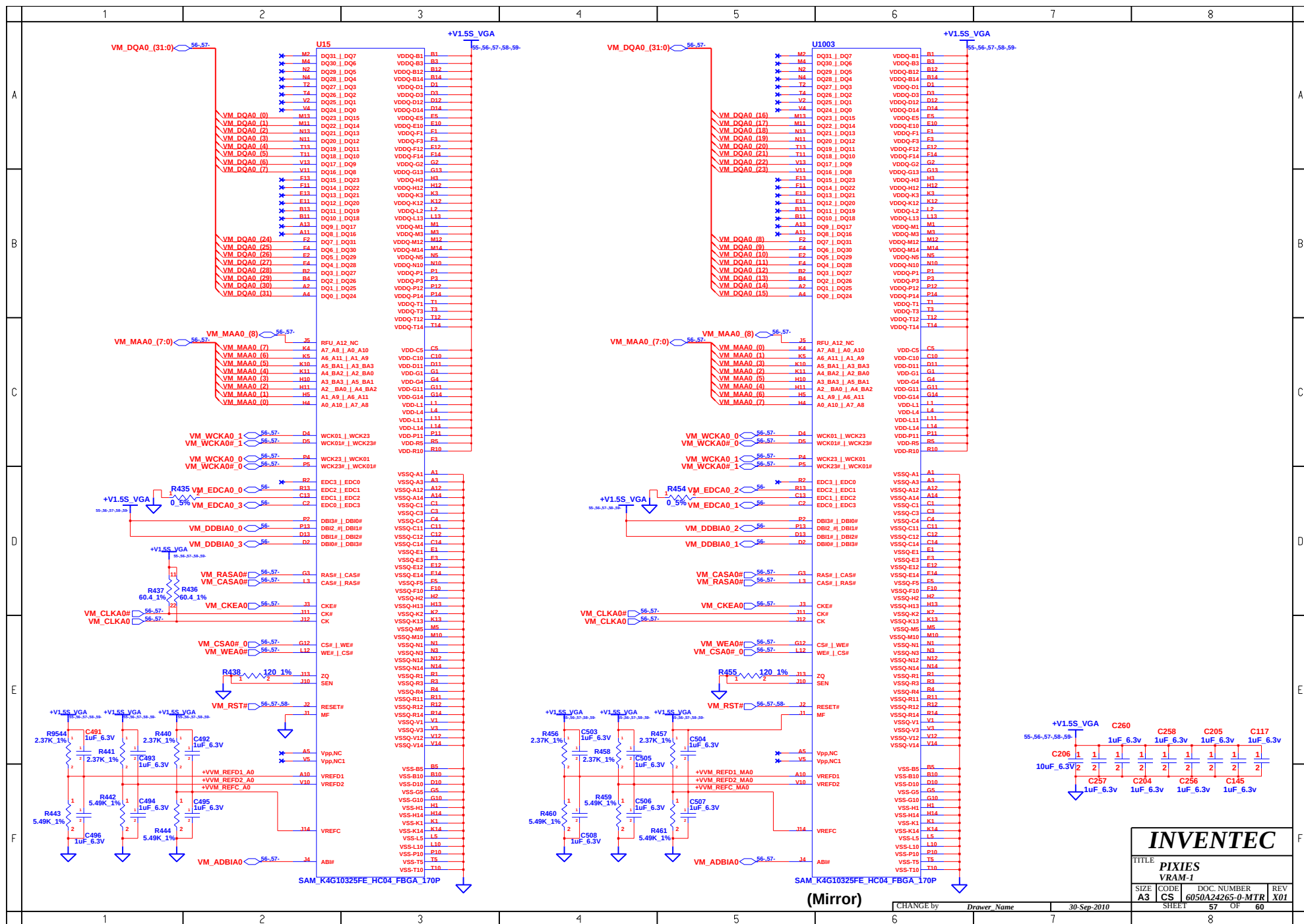


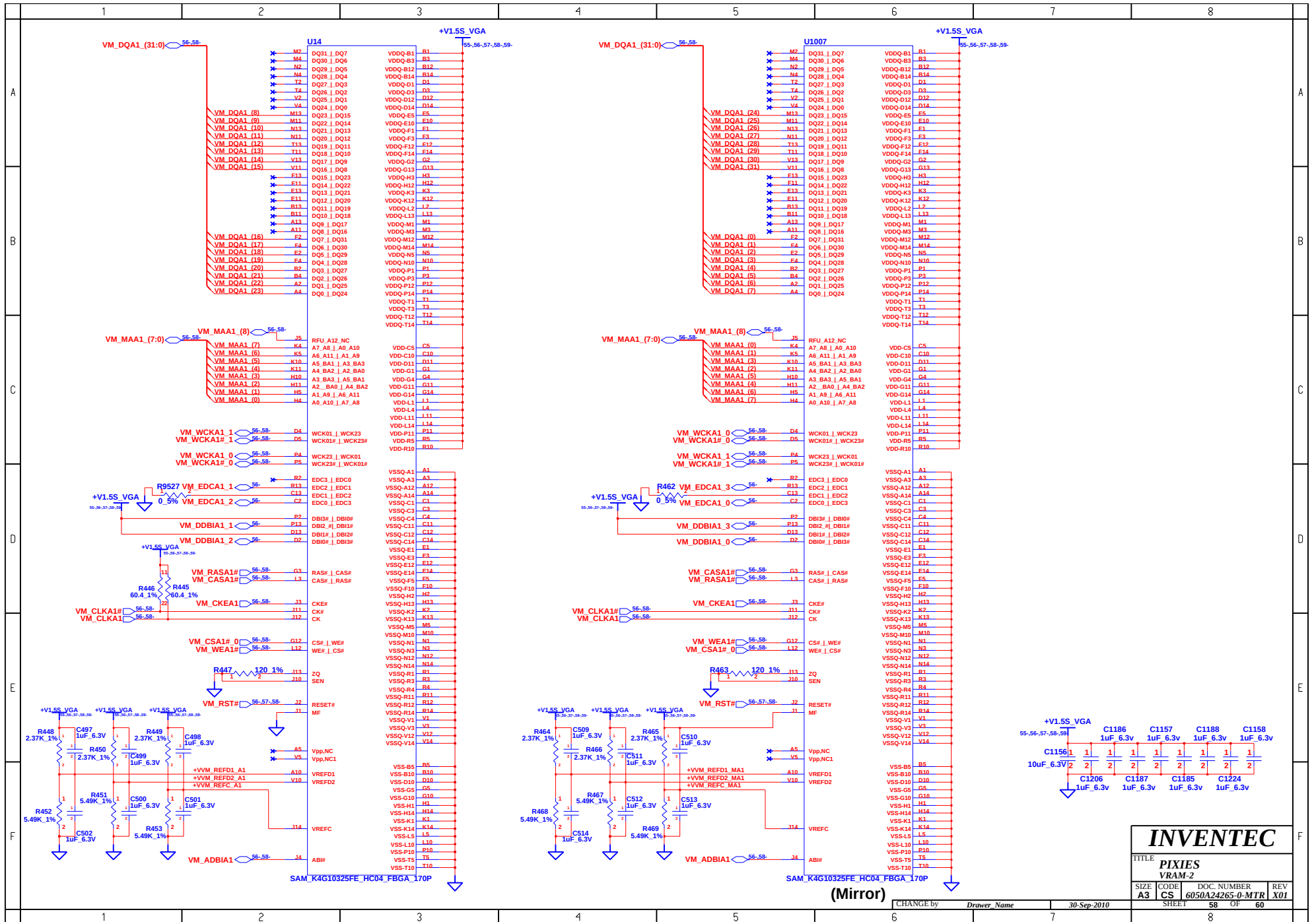
INVENTEC			
TITLE PIXIES GPU-1			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET		53	OF 60











INVENTEC

TITLE: **PXIES VRAM-2**

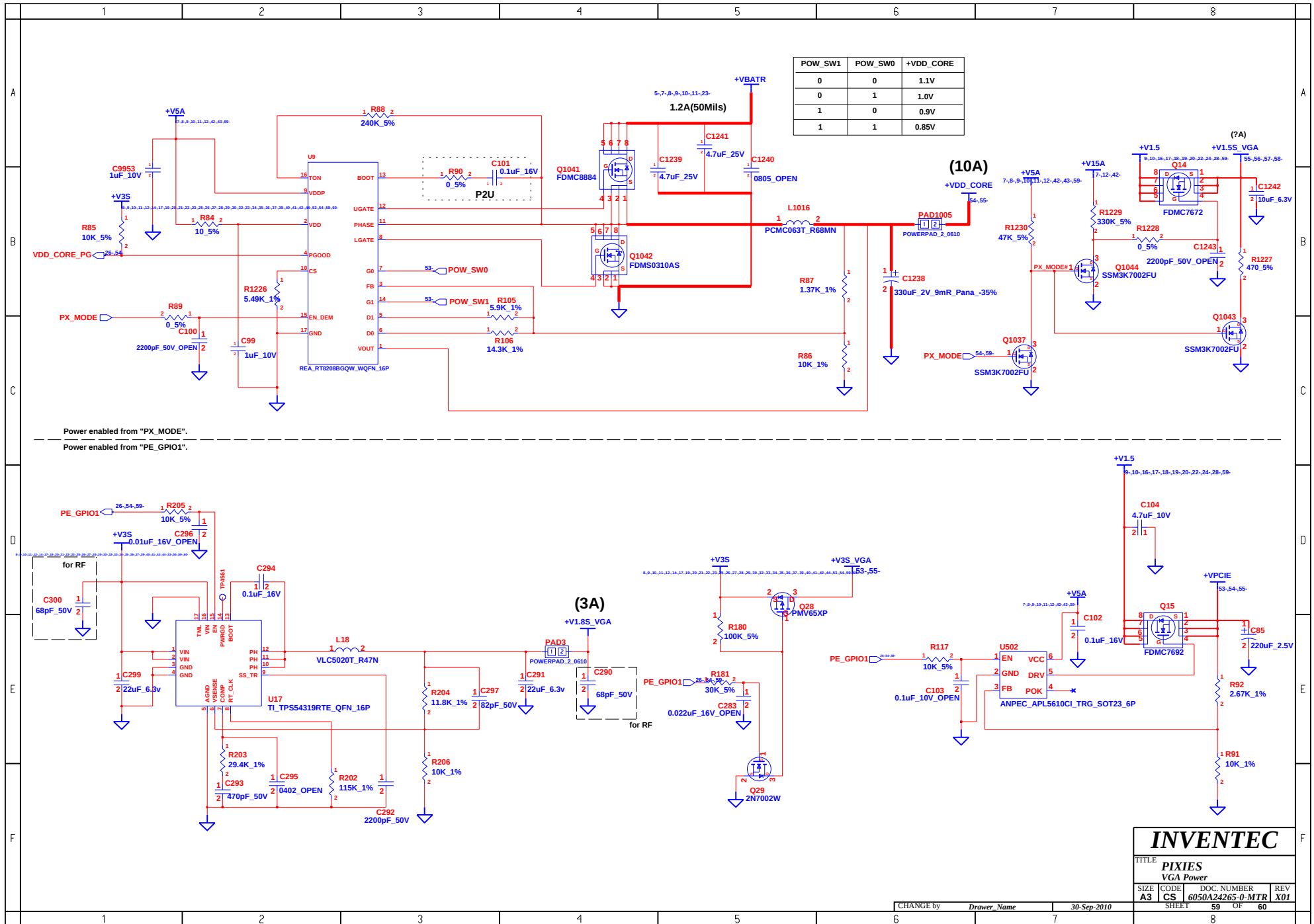
SIZE: **A3** CODE: **CS** DOC. NUMBER: **6050A24265-0-MTR** REV: **X01**

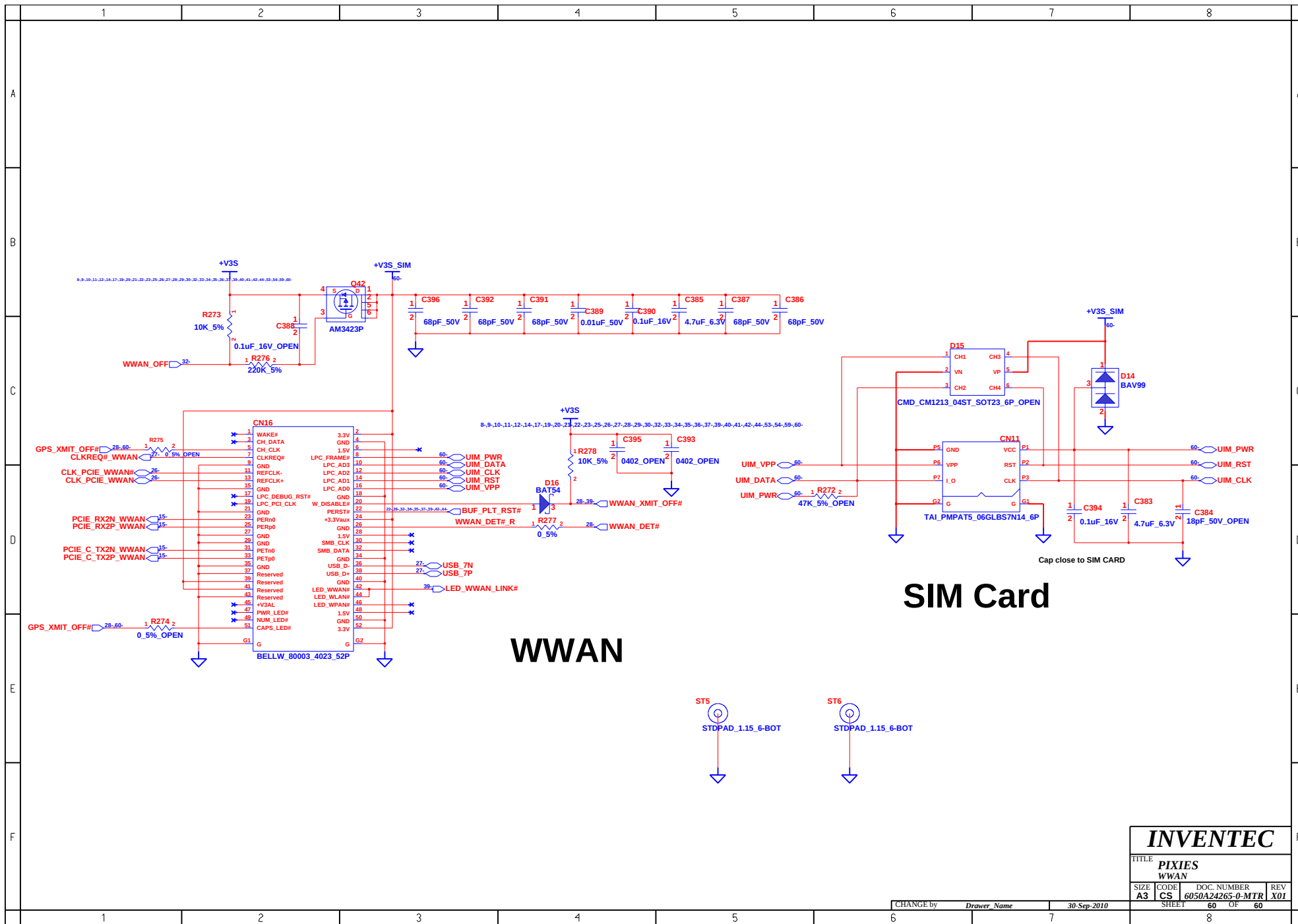
SHEET: **58** OF: **60**

(Mirror)

CHANGE by: **Dravner Name** 30-Sep-2010

8





INVENTEC			
TITLE PIXIES WWAN			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET 60		OF 60	

CHANGE by *Drawn Name* 30-Sep-2010