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SHEET		1	OF 60																																																																										

Index Of Content

PAGE

01- Project Name
 02- Index
 03- Power Procedure
 04- Power Sequence
 05- Block Diagram
 06- DC & BATTERY CHARGER
 07- BATTERY CONN
 08- +V5A & +V3A
 09- +V1.1A & +VDDA & +V2.5S
 10- +VCC_CORE & +VDDIO_SUS&+V0.75S
 11- +APU_VDD&+VDDNB
 12- +VDDP&+V1.8S
 13- +V5S&+V3S&+V1.5S
 14- +GPU_VDDC&+VPCIE
 15- Reserve
 16- CPU-1
 17- CPU-2
 18- CPU-3
 19- CPU-4
 20- DDR3 DIMM0
 21- DDR3 DIMM1
 22- FAN & THERMAL IC
 23- FCH-1
 24- FCH-2
 25- FCH-3
 26- FCH-4
 27- FCH-5
 28- FCH-6

PAGE

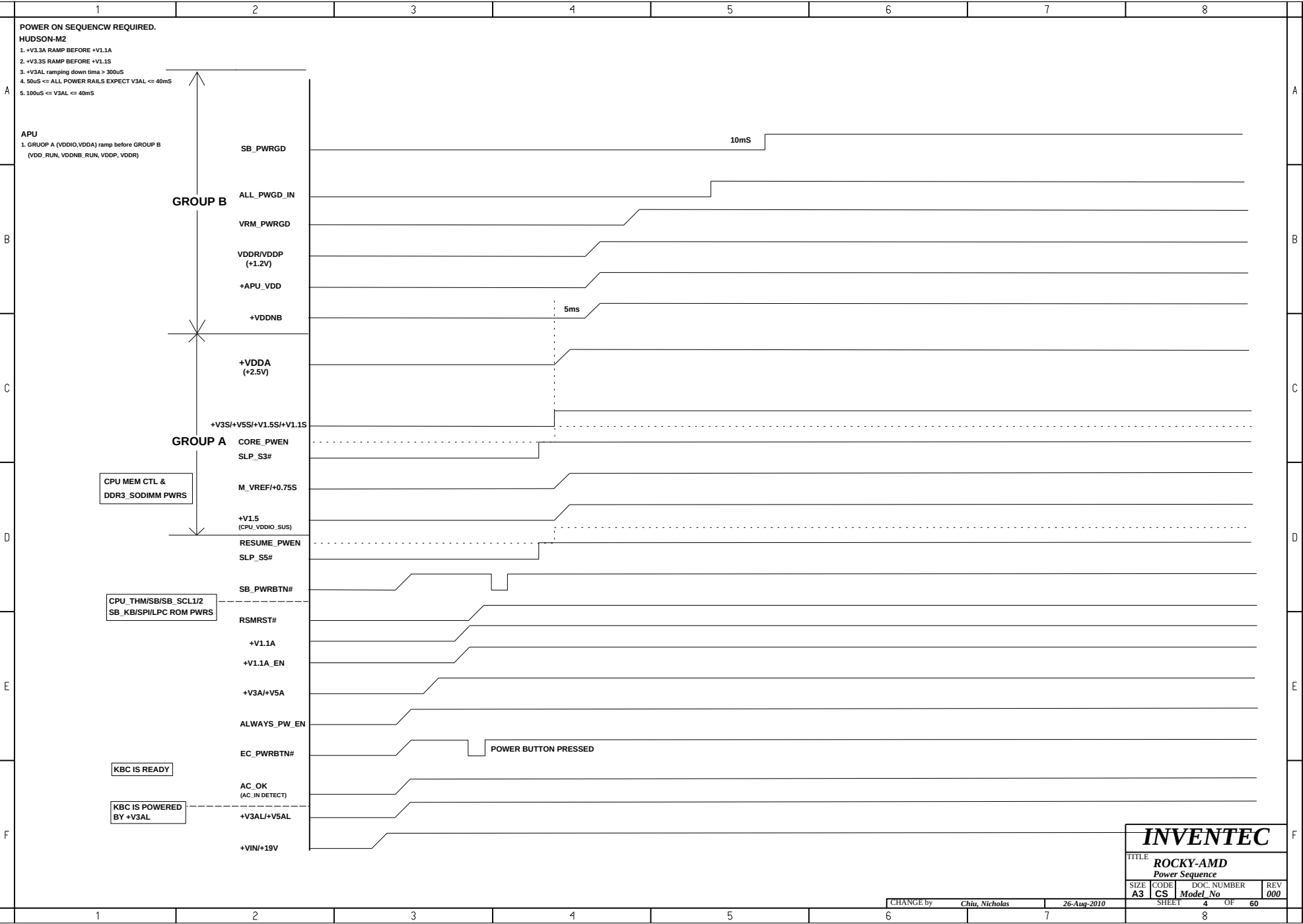
29- KBC
 30- HDMI
 31- CRT
 32- LCM
 33- Keyboard CONN & Indicate LED
 34- Card reader-RTS5219
 35- SATA HDD & SATA ODD CONN
 36- USB CONN
 37- LAN
 38- RJ45 CONN
 39- AUDIO CODEC & SPEAKER CONN
 40- HP JACK & MIC JACK
 41- SATA LED & PWR LED
 42- SCREW
 43- WLAN & BT
 44- BLANK
 45- BLANK
 46- BLANK
 47- TRAVIS
 48- STRAP
 49- GPU-1
 50- GPU-2
 51- GPU-3
 52- GPU-4
 53- GPU-5
 54- GPU-6
 55- VRAM-1
 56- VRAM-2

PAGE

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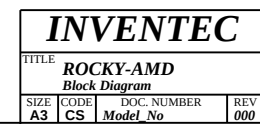
TITLE ROCKY-AMD Index			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
SHEET 2		OF 60	

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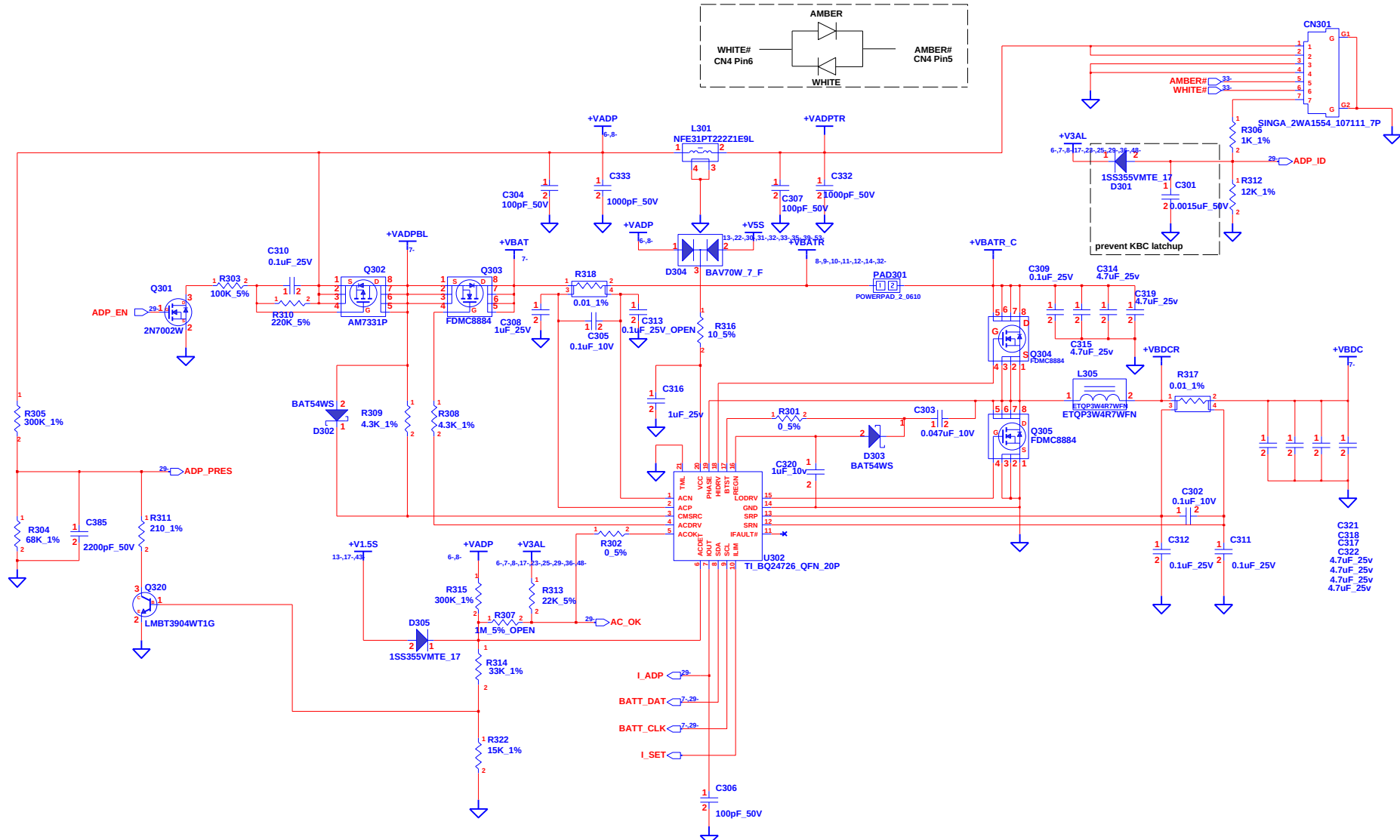
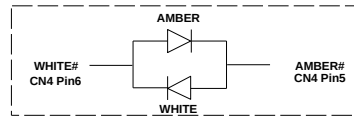


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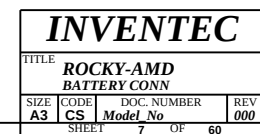


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TITLE			
ROCKY-AMD DC & BATTERY CHARGER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

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SHEET 6 OF 60



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11-1

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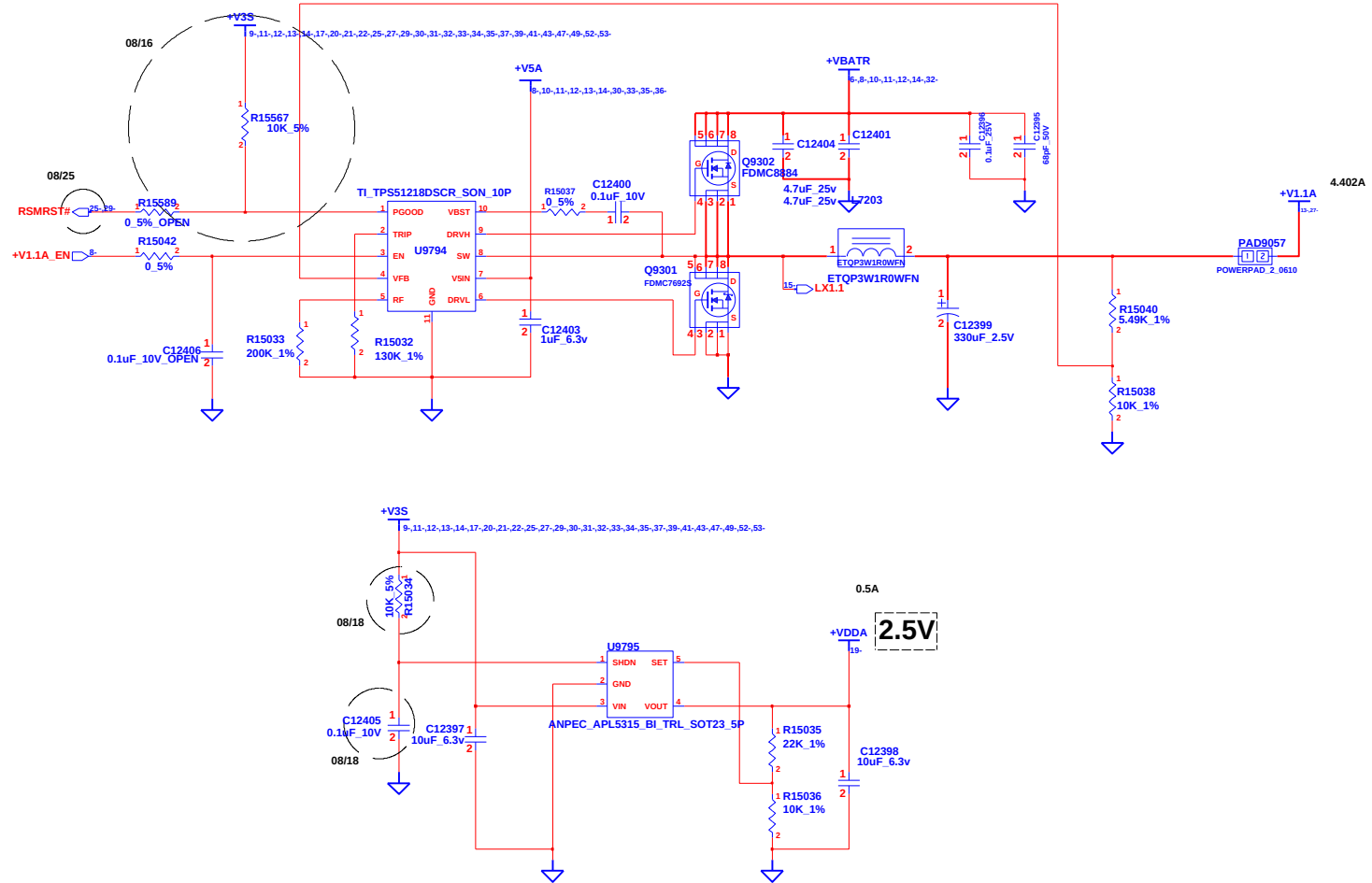
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TITLE			
ROCKY-AMD +V3A & +V5A			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>000</i>
SHEET		8	OF 60
		8	

CHANGE by	<i>Chiu, Nicholas</i>	<i>26-Aug-2010</i>
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+V1.1A & +VDDA & +V2.5S



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TITLE			
ROCKY-AMD			
+V1.1A & +VDDA & +V2.5S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

CHANGE by Chiu, Nicholas 26-Aug-2010

SHEET 9 OF 60

+VCC_CORE & VDDIO_SUS & +V0.75S

NOTE: DDR3 REGULATOR

I MAX = 14A

INVENTEC			
TITLE: ROCKY-AMD			
+VCC_CORE & +VDDIO_SUS & +V0.75S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000
CHANGE by		Chiu, Nicholas	26-Aug-2010
SHEET		10	OF 60

+VCC_CORE & VDDIO_SUS & +V0.75S

The schematic illustrates the power distribution network for three critical voltage rails: +VCC_CORE, VDDIO_SUS, and +V0.75S. The circuit is centered around the U305 regulator, which manages the +V0.75S rail from a +V5A input. This rail then branches off to supply the other two rails through various resistors and capacitors. A secondary path for +V0.75S involves a MOSFET driver stage (Q314) and a memory module (LXMEN). The VDDIO_SUS rail is derived from +V0.75S via a series resistor and capacitor network. The +VCC_CORE rail is also supplied by +V0.75S through a similar network. Various passive components like capacitors (C388, C396, C397, C389, C393, C394, C391, C373, C381, C390, C372, C380, C392) and resistors (R395, R396, R393, R392, R391) are used for decoupling, filtering, and current limiting. Power pads (PAD308, PAD309) are shown for thermal management. A note specifies the DDR3 REGULATOR.

NOTE: DDR3 REGULATOR

INVENTEC			
TITLE +VCC_CORE & +VDDIO_SUS & +V0.75S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000

CHANGE by	Chiu, Nicholas	26-Aug-2010
SHEET	10	OF 60

+VCC_CORE & VDDIO_SUS & +V0.75S

NOTE: DDR3 REGULATOR

I MAX = 14A

INVENTEC			
TITLE: ROCKY-AMD			
+VCC_CORE & +VDDIO_SUS & +V0.75S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000
CHANGE by		Chiu, Nicholas	26-Aug-2010
SHEET		10	OF 60

+VCC_CORE & VDDIO_SUS & +V0.75S

NOTE: DDR3 REGULATOR

I MAX = 14A

INVENTEC			
TITLE: ROCKY-AMD			
+VCC_CORE & +VDDIO_SUS & +V0.75S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000
CHANGE by		Chiu, Nicholas	26-Aug-2010
SHEET		10	OF 60

+VCC_CORE & VDDIO_SUS & +V0.75S

NOTE: DDR3 REGULATOR

I MAX = 14A

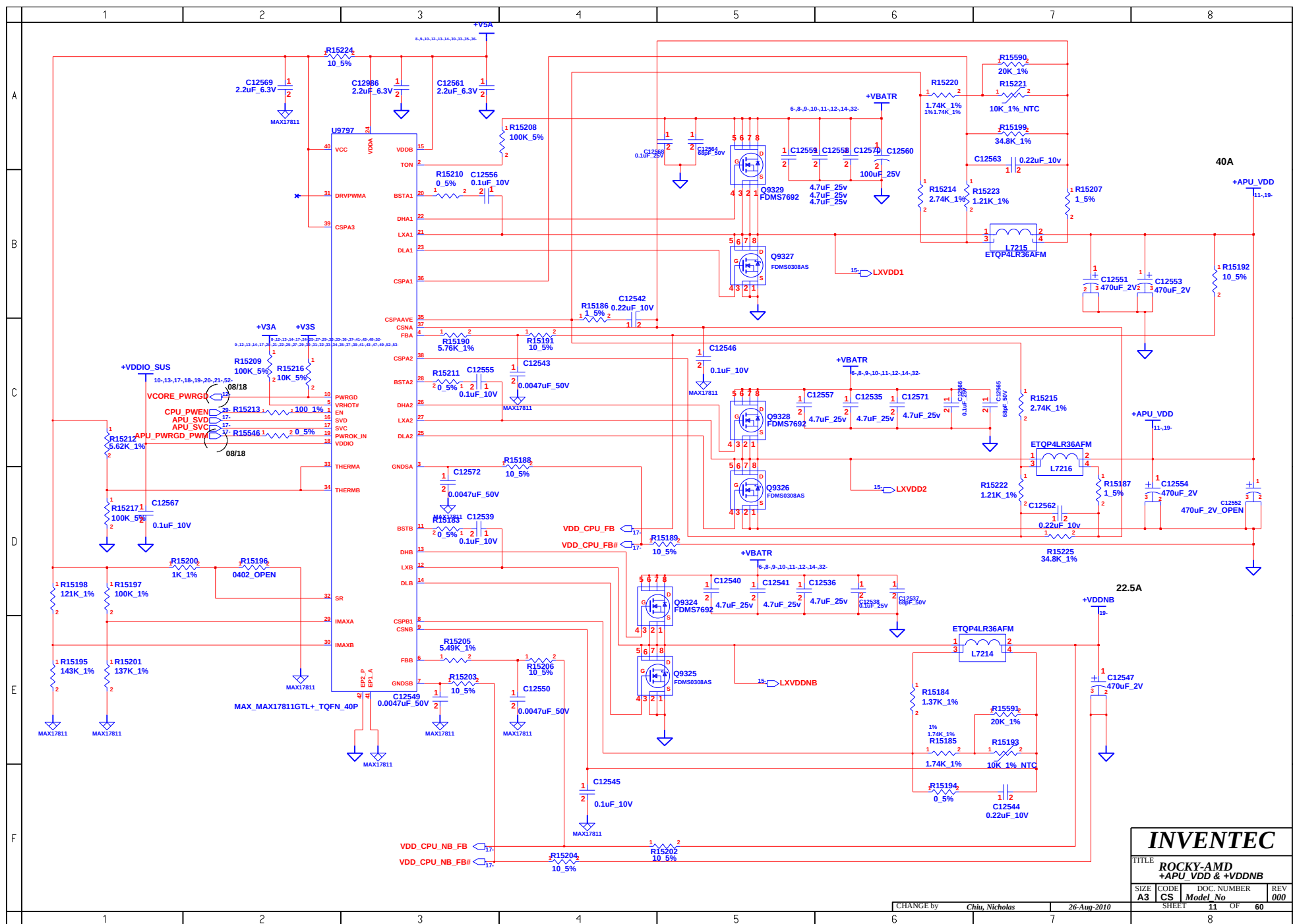
INVENTEC			
TITLE: ROCKY-AMD			
+VCC_CORE & +VDDIO_SUS & +V0.75S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000
CHANGE by: Chiu, Nicholas		26-Aug-2010	SHEET 10 OF 60

+VCC_CORE & VDDIO_SUS & +V0.75S

NOTE: DDR3 REGULATOR

I MAX = 14A

INVENTEC			
TITLE: ROCKY-AMD			
+VCC_CORE & +VDDIO_SUS & +V0.75S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000
CHANGE by		Chiu, Nicholas	26-Aug-2010
SHEET		10	OF 60



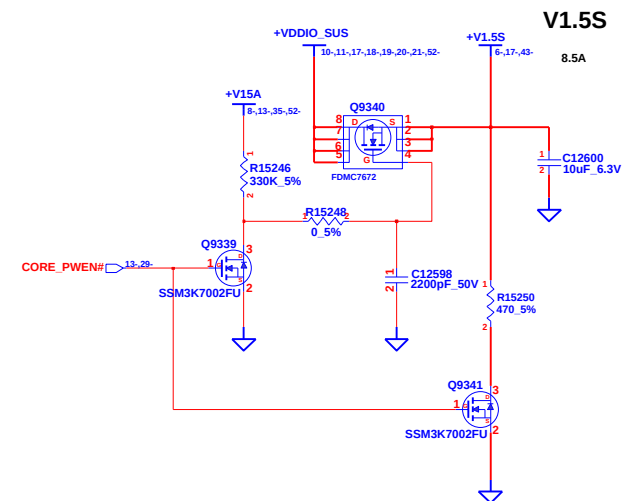
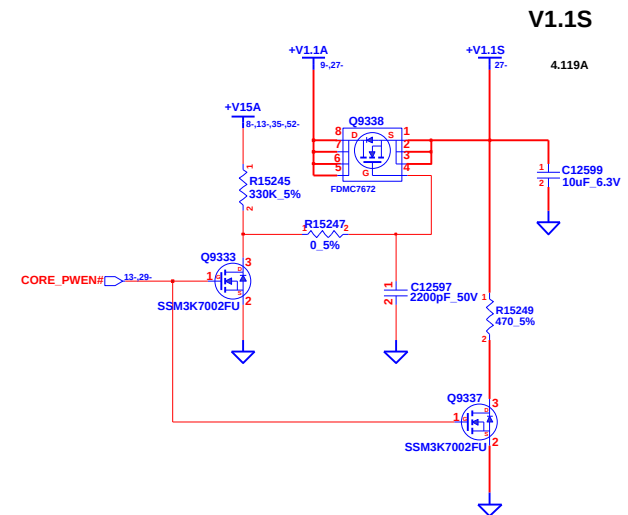
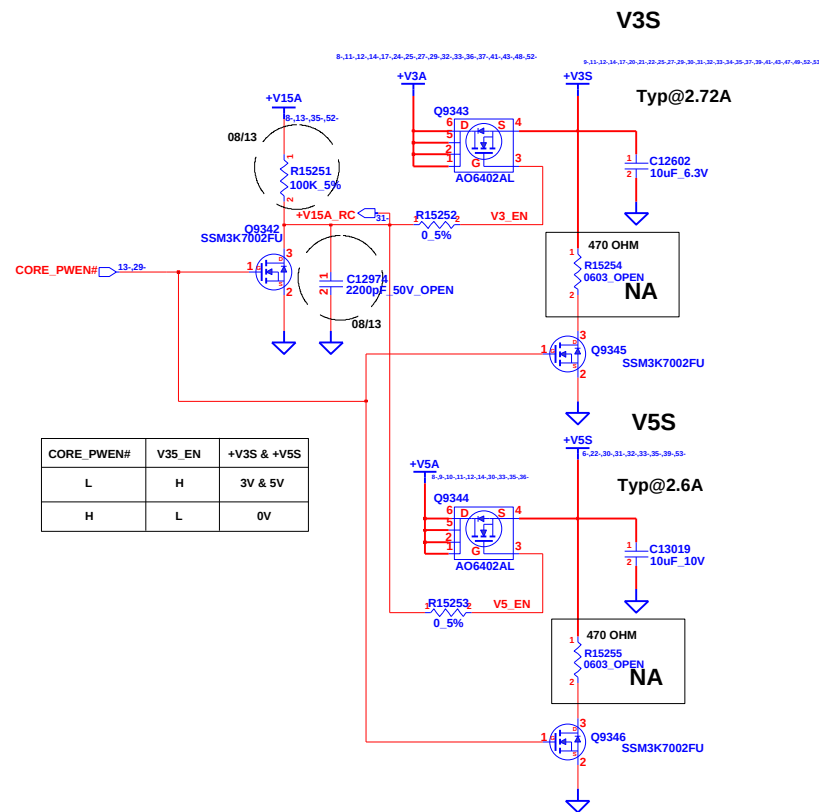
[illegible]

TITLE			
ROCKY-AMD +VDDP & +V1.8S			
SIZE	CODE	DOC. NUMBER	REV.
A3	CS	<i>Model No</i>	<i>000</i>

A3	CS	Model No	000
SHEET		12	OF 60

10

+V5S & +V3S & +V1.5S & +V1.1S



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TITLE			
ROCKY-AMD			
+V5S & +V3S & +V1.5S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		13	OF 60

CHANGE by Chiu, Nicholas 26-Aug-2010

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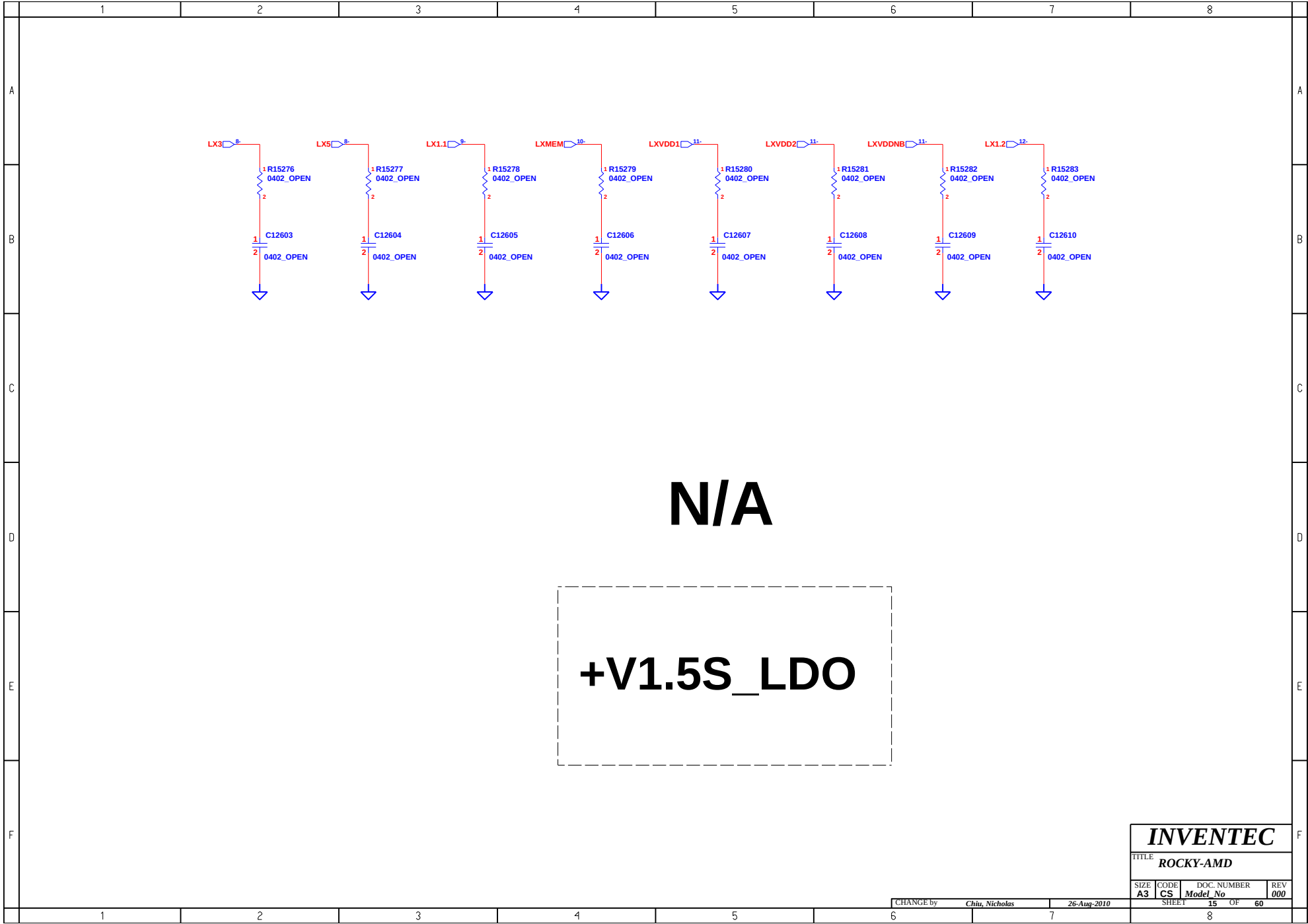
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CPU

U9804-6

PCI EXPRESS

GRAPHICS

PEG

HDMI

GPP

UMLINK

FOX_PZ72226_294S_41F_722P

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ROCKY-AMD

CPU-1

SIZE A3

CODE CS

DOC. NUMBER

Model No

REV 000

CHANGE by

Chiu, Nicholas

26-Aug-2010

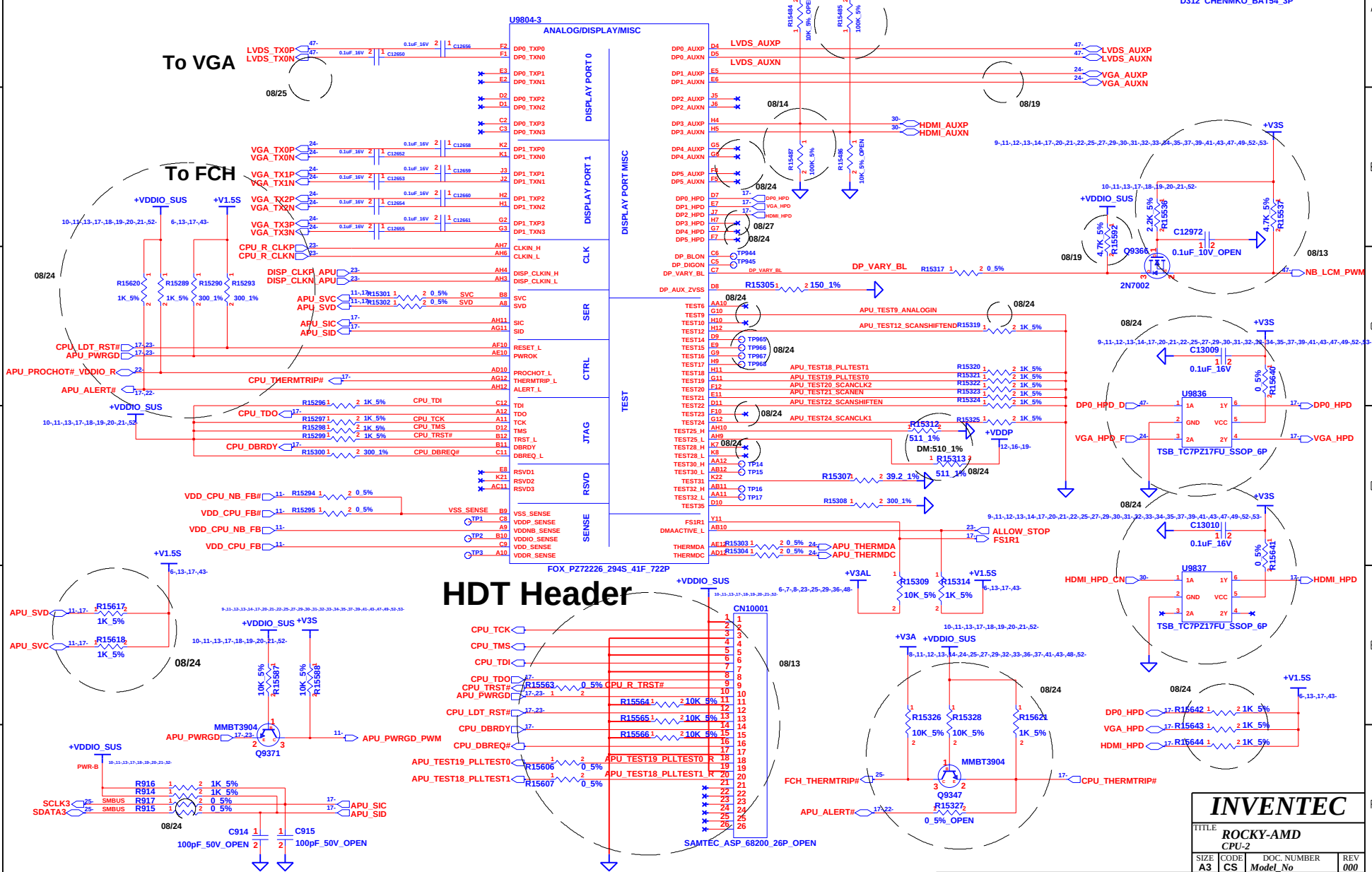
SHEET 16 OF 60

TITLE			
ROCKY-AMD CPU-1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		16	OF 60

CHANGE by	<i>Chiu, Nicholas</i>	<i>26-Aug-2010</i>
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CS Model No
SHEET 16 OF 60

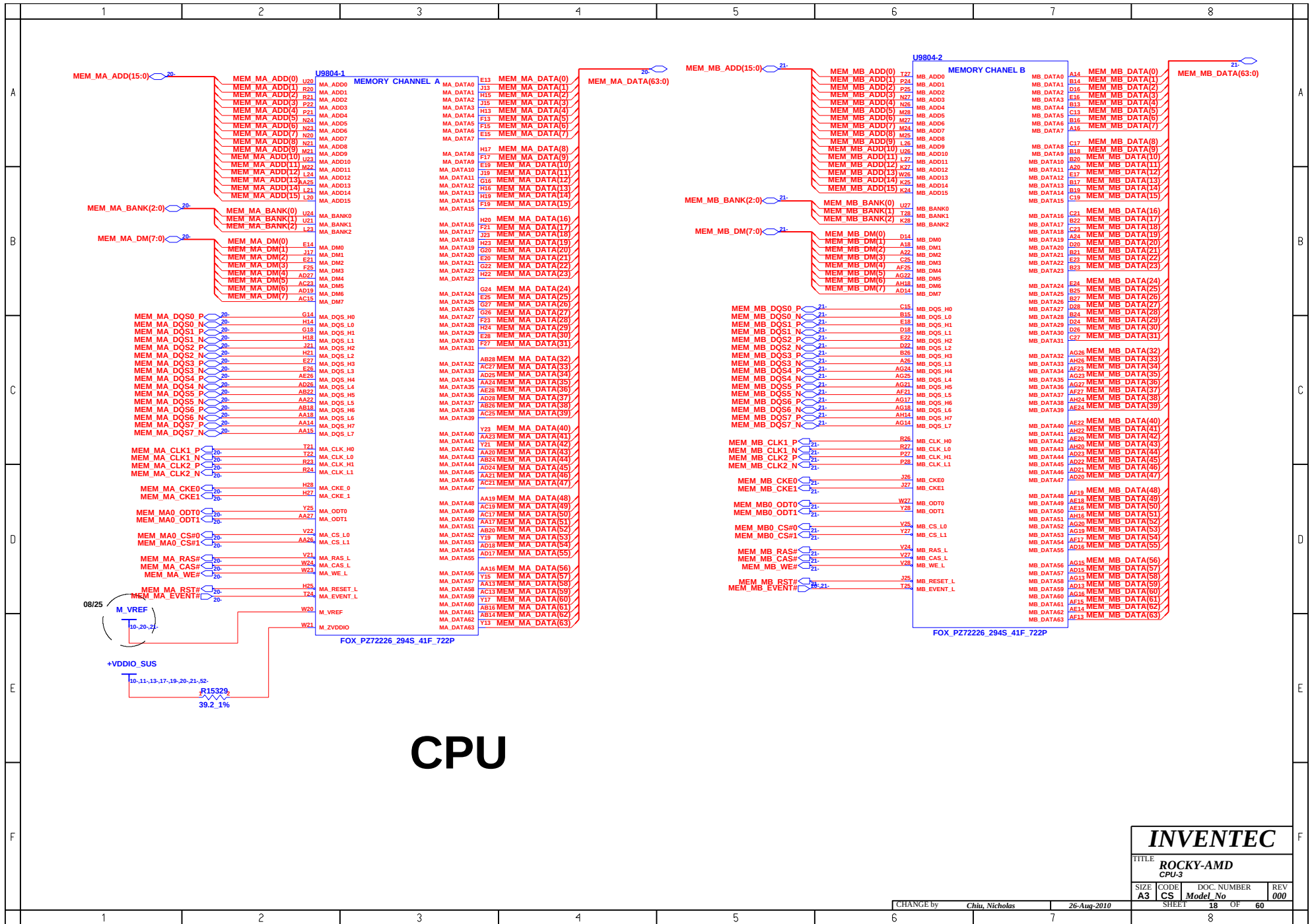
CPU



INVENTEC

TITLE ROCKY-AMD CPU-2			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
SHEET 17		OF 60	

CHANGE by Chiu, Nicholas 26-Aug-2010



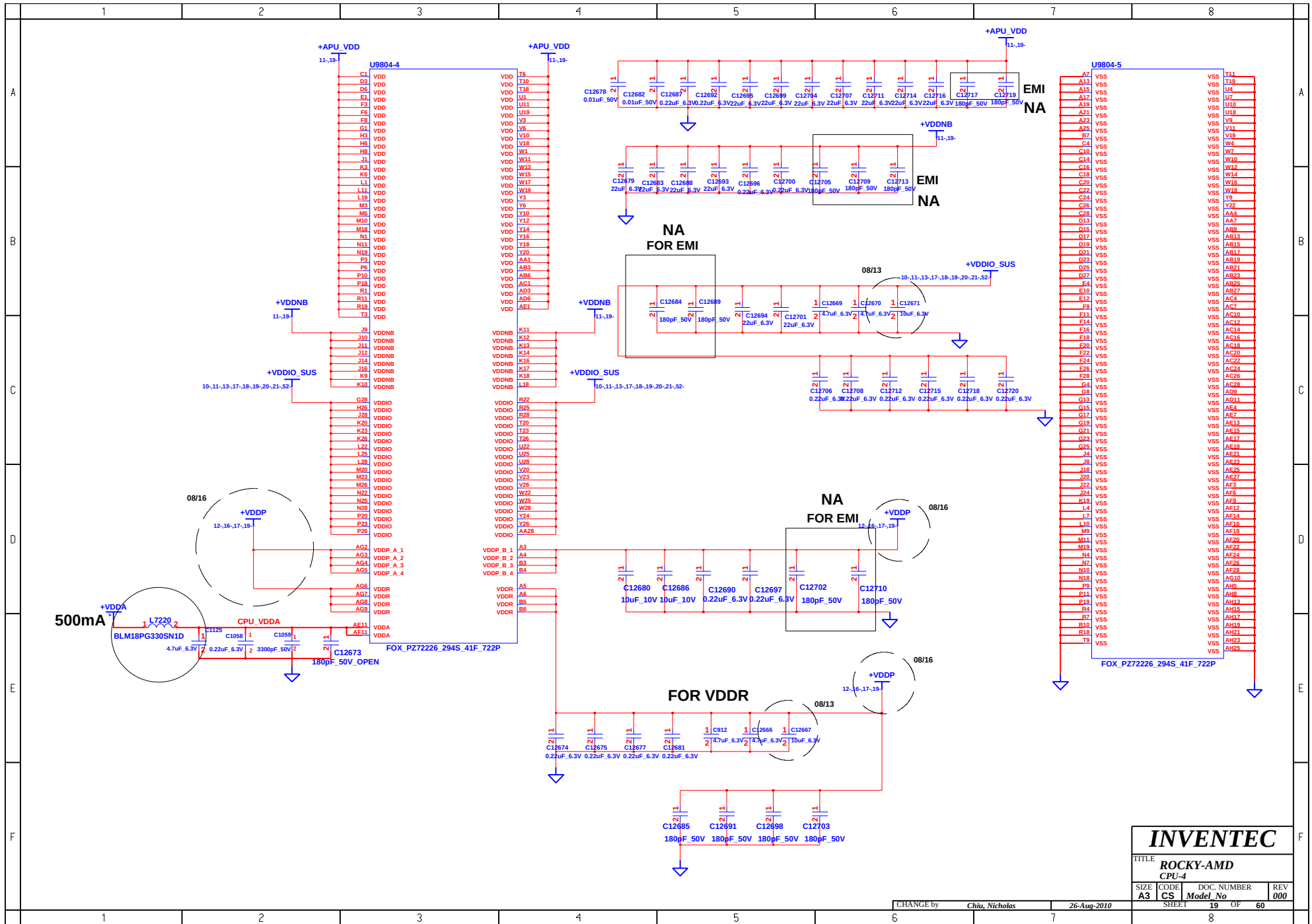
INVENTEC

TITLE
ROCKY-AMD
CPU-3

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

SHEET	18	OF	60
8			

CHANGE by Chiu, Nicholas 26-Aug-2010



SO-DIMM0

Pin Connections:

- MEM_MA_ADD(15:0):** 18-pin bus to U9805-1.
- MEM_MA_DATA(63:0):** 64-pin bus to U9805-1.
- MEM_MA_BANK(2:0):** 3-pin bus to U9805-1.
- MEM_MA_DM(7:0):** 8-pin bus to U9805-1.
- MEM_MA_DQS(0:15):** 16-pin bus to U9805-1.
- MEM_MA_DQS(16:31):** 16-pin bus to U9805-1.
- MEM_MA_DQS(32:47):** 16-pin bus to U9805-1.
- MEM_MA_DQS(48:63):** 16-pin bus to U9805-1.
- MEM_MA_DQS(64:79):** 16-pin bus to U9805-1.
- MEM_MA_DQS(80:95):** 16-pin bus to U9805-1.
- MEM_MA_DQS(96:111):** 16-pin bus to U9805-1.
- MEM_MA_DQS(112:127):** 16-pin bus to U9805-1.
- MEM_MA_DQS(128:143):** 16-pin bus to U9805-1.
- MEM_MA_DQS(144:159):** 16-pin bus to U9805-1.
- MEM_MA_DQS(160:175):** 16-pin bus to U9805-1.
- MEM_MA_DQS(176:191):** 16-pin bus to U9805-1.
- MEM_MA_DQS(192:207):** 16-pin bus to U9805-1.
- MEM_MA_DQS(208:223):** 16-pin bus to U9805-1.
- MEM_MA_DQS(224:239):** 16-pin bus to U9805-1.
- MEM_MA_DQS(240:255):** 16-pin bus to U9805-1.
- MEM_MA_DQS(256:271):** 16-pin bus to U9805-1.
- MEM_MA_DQS(272:287):** 16-pin bus to U9805-1.
- MEM_MA_DQS(288:303):** 16-pin bus to U9805-1.
- MEM_MA_DQS(304:319):** 16-pin bus to U9805-1.
- MEM_MA_DQS(320:335):** 16-pin bus to U9805-1.
- MEM_MA_DQS(336:351):** 16-pin bus to U9805-1.
- MEM_MA_DQS(352:367):** 16-pin bus to U9805-1.
- MEM_MA_DQS(368:383):** 16-pin bus to U9805-1.
- MEM_MA_DQS(384:399):** 16-pin bus to U9805-1.
- MEM_MA_DQS(400:415):** 16-pin bus to U9805-1.
- MEM_MA_DQS(416:431):** 16-pin bus to U9805-1.
- MEM_MA_DQS(432:447):** 16-pin bus to U9805-1.
- MEM_MA_DQS(448:463):** 16-pin bus to U9805-1.
- MEM_MA_DQS(464:479):** 16-pin bus to U9805-1.
- MEM_MA_DQS(480:495):** 16-pin bus to U9805-1.
- MEM_MA_DQS(496:511):** 16-pin bus to U9805-1.
- MEM_MA_DQS(512:527):** 16-pin bus to U9805-1.
- MEM_MA_DQS(528:543):** 16-pin bus to U9805-1.
- MEM_MA_DQS(544:559):** 16-pin bus to U9805-1.
- MEM_MA_DQS(560:575):** 16-pin bus to U9805-1.
- MEM_MA_DQS(576:591):** 16-pin bus to U9805-1.
- MEM_MA_DQS(592:607):** 16-pin bus to U9805-1.
- MEM_MA_DQS(608:623):** 16-pin bus to U9805-1.
- MEM_MA_DQS(624:639):** 16-pin bus to U9805-1.
- MEM_MA_DQS(640:655):** 16-pin bus to U9805-1.
- MEM_MA_DQS(656:671):** 16-pin bus to U9805-1.
- MEM_MA_DQS(672:687):** 16-pin bus to U9805-1.
- MEM_MA_DQS(688:703):** 16-pin bus to U9805-1.
- MEM_MA_DQS(704:719):** 16-pin bus to U9805-1.
- MEM_MA_DQS(720:735):** 16-pin bus to U9805-1.
- MEM_MA_DQS(736:751):** 16-pin bus to U9805-1.
- MEM_MA_DQS(752:767):** 16-pin bus to U9805-1.
- MEM_MA_DQS(768:783):** 16-pin bus to U9805-1.
- MEM_MA_DQS(784:799):** 16-pin bus to U9805-1.
- MEM_MA_DQS(800:815):** 16-pin bus to U9805-1.
- MEM_MA_DQS(816:831):** 16-pin bus to U9805-1.
- MEM_MA_DQS(832:847):** 16-pin bus to U9805-1.
- MEM_MA_DQS(848:863):** 16-pin bus to U9805-1.
- MEM_MA_DQS(864:879):** 16-pin bus to U9805-1.
- MEM_MA_DQS(880:895):** 16-pin bus to U9805-1.
- MEM_MA_DQS(896:911):** 16-pin bus to U9805-1.
- MEM_MA_DQS(912:927):** 16-pin bus to U9805-1.
- MEM_MA_DQS(928:943):** 16-pin bus to U9805-1.
- MEM_MA_DQS(944:959):** 16-pin bus to U9805-1.
- MEM_MA_DQS(960:975):** 16-pin bus to U9805-1.
- MEM_MA_DQS(976:991):** 16-pin bus to U9805-1.
- MEM_MA_DQS(992:1007):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1008:1023):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1024:1039):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1040:1055):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1056:1071):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1072:1087):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1088:1103):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1104:1119):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1120:1135):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1136:1151):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1152:1167):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1168:1183):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1184:1199):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1200:1215):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1216:1231):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1232:1247):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1248:1263):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1264:1279):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1280:1295):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1296:1311):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1312:1327):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1328:1343):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1344:1359):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1360:1375):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1376:1391):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1392:1407):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1408:1423):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1424:1439):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1440:1455):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1456:1471):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1472:1487):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1488:1503):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1504:1519):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1520:1535):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1536:1551):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1552:1567):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1568:1583):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1584:1599):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1600:1615):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1616:1631):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1632:1647):** 16-pin bus to U9805-1.
- MEM_MA_DQS(1648:1663):** 16-pin bus to U9805

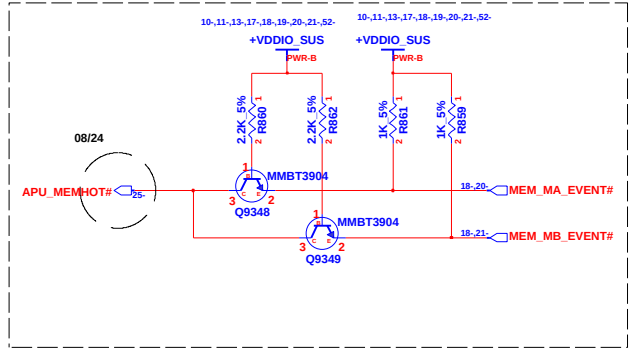
Note: Place C4100 on common path for both DIMM's

Place these caps
close to VTT1 and
VTT2

NOTE:

IF SA0_DIM0=0 , SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0xA0
SO-DIMMA TS ADDRESS IS 0x30

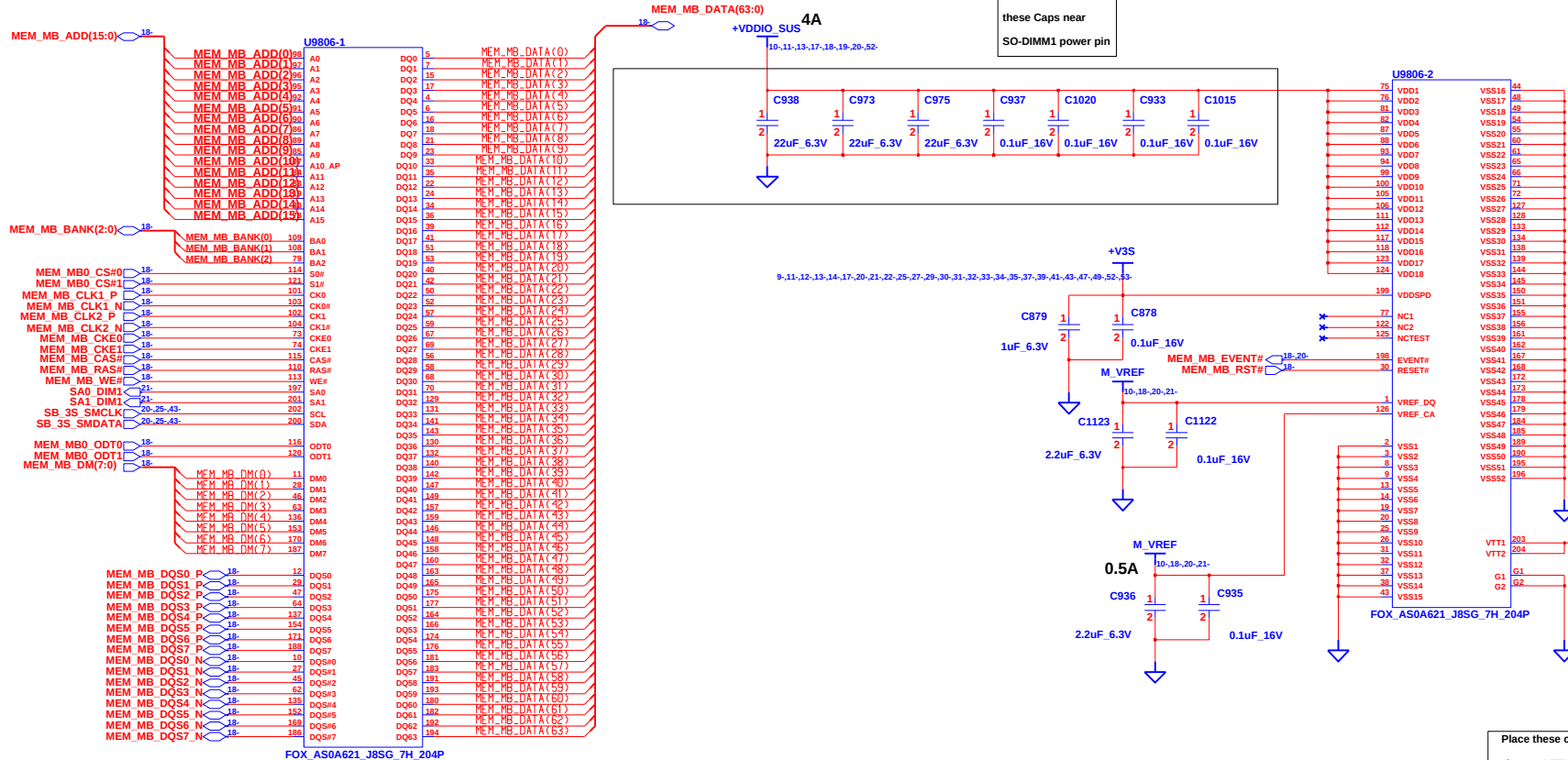
IF SA0_DIM0=1 , SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0xA2
SO-DIMMA TS ADDRESS IS 0x32



INVENTEC			
TITLE ROCKY-AMD SO-DIMMO			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>000</i>
SHEET 20		OF 60	

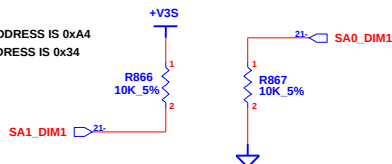
SO-DIMM1

Layout Note: Place
these Caps near
SO-DIMM1 power pin



NOTE:

SO-DIMMB SPD ADDRESS IS 0xA4
SO-DIMMB TS ADDRESS IS 0x34



INVENTEC

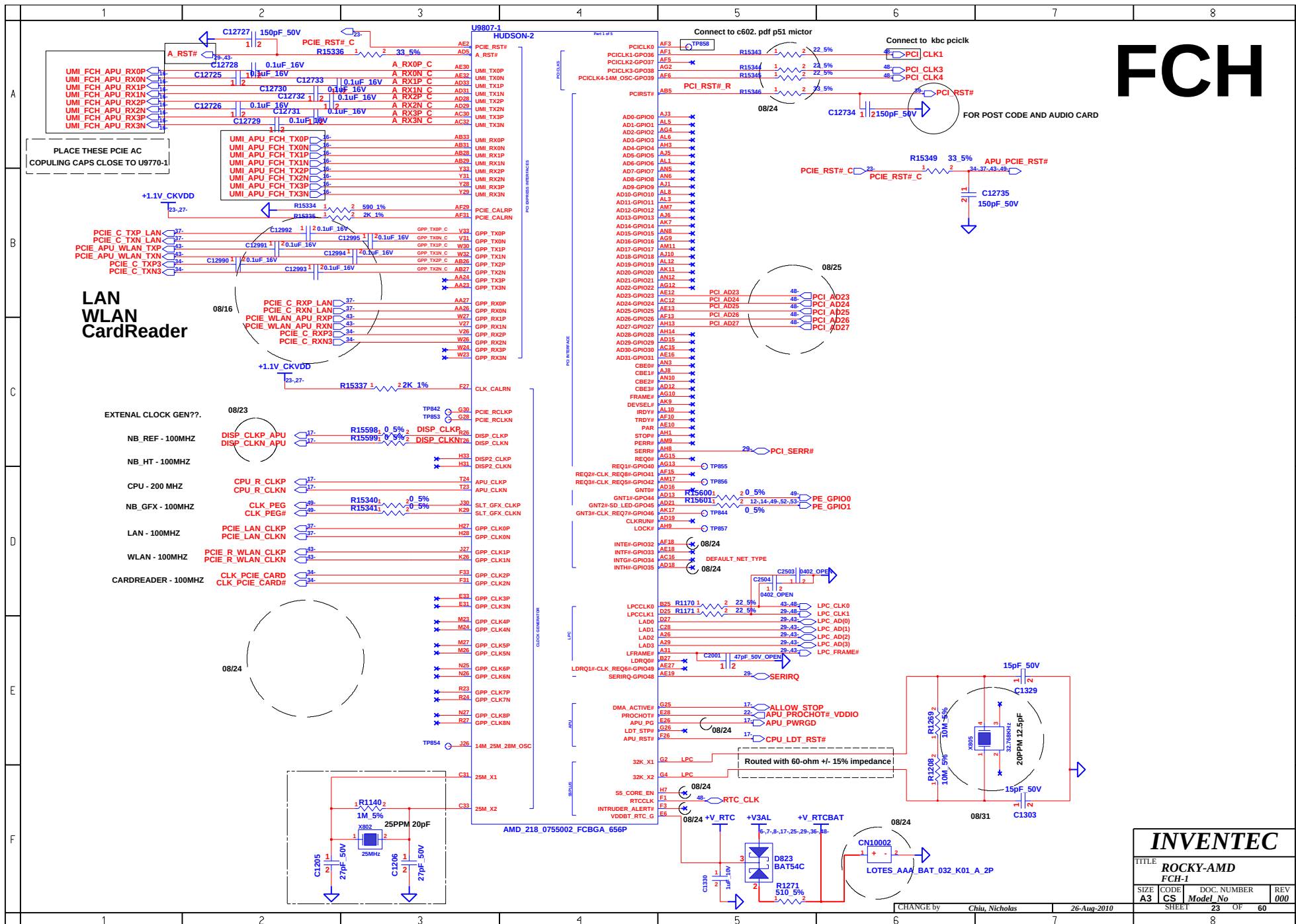
TITLE
ROCKY-AMD
SO-DIMM1

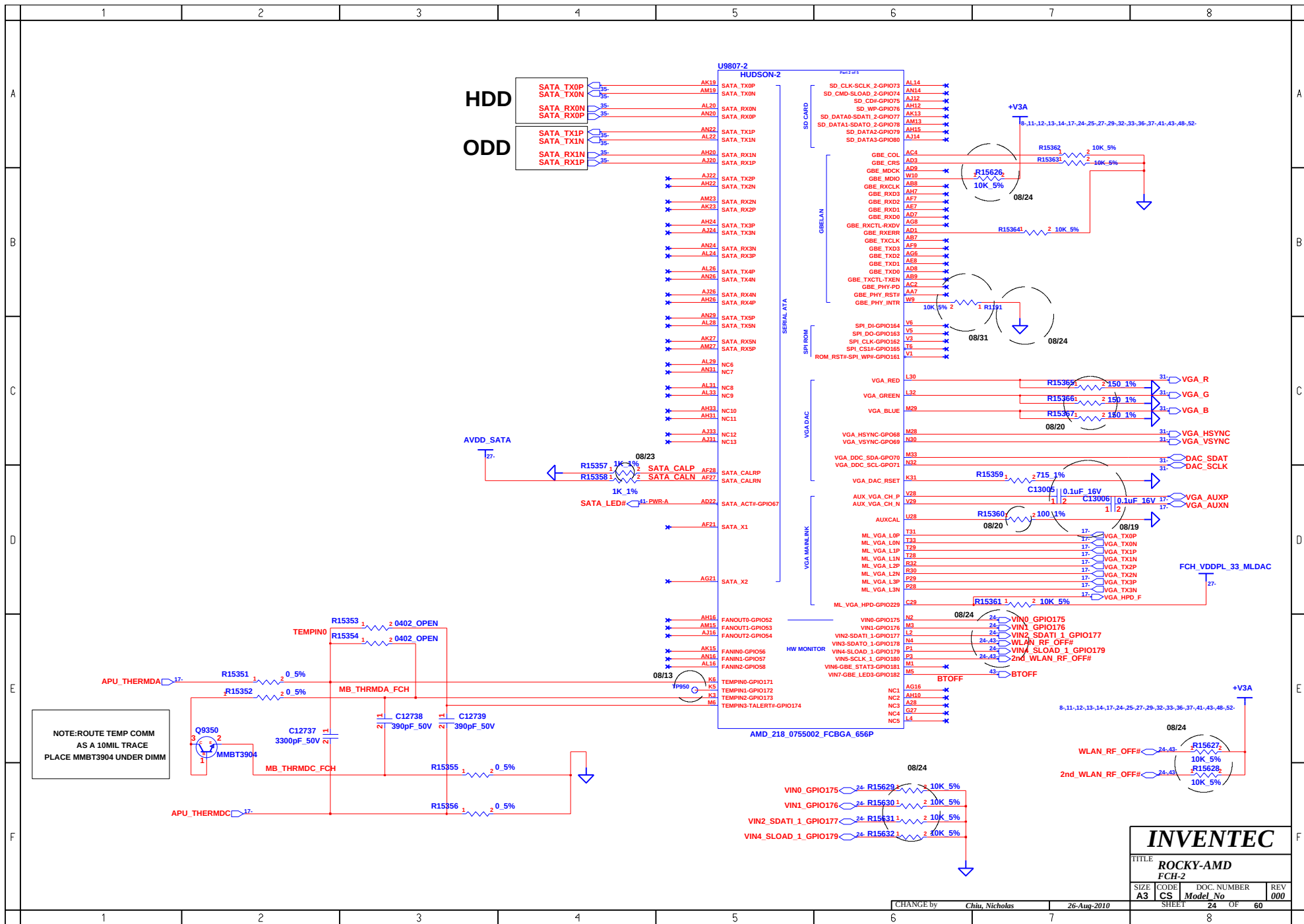
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

CHANGE by Chiu, Nicholas 26-Aug-2010

SHEET 21 OF 60

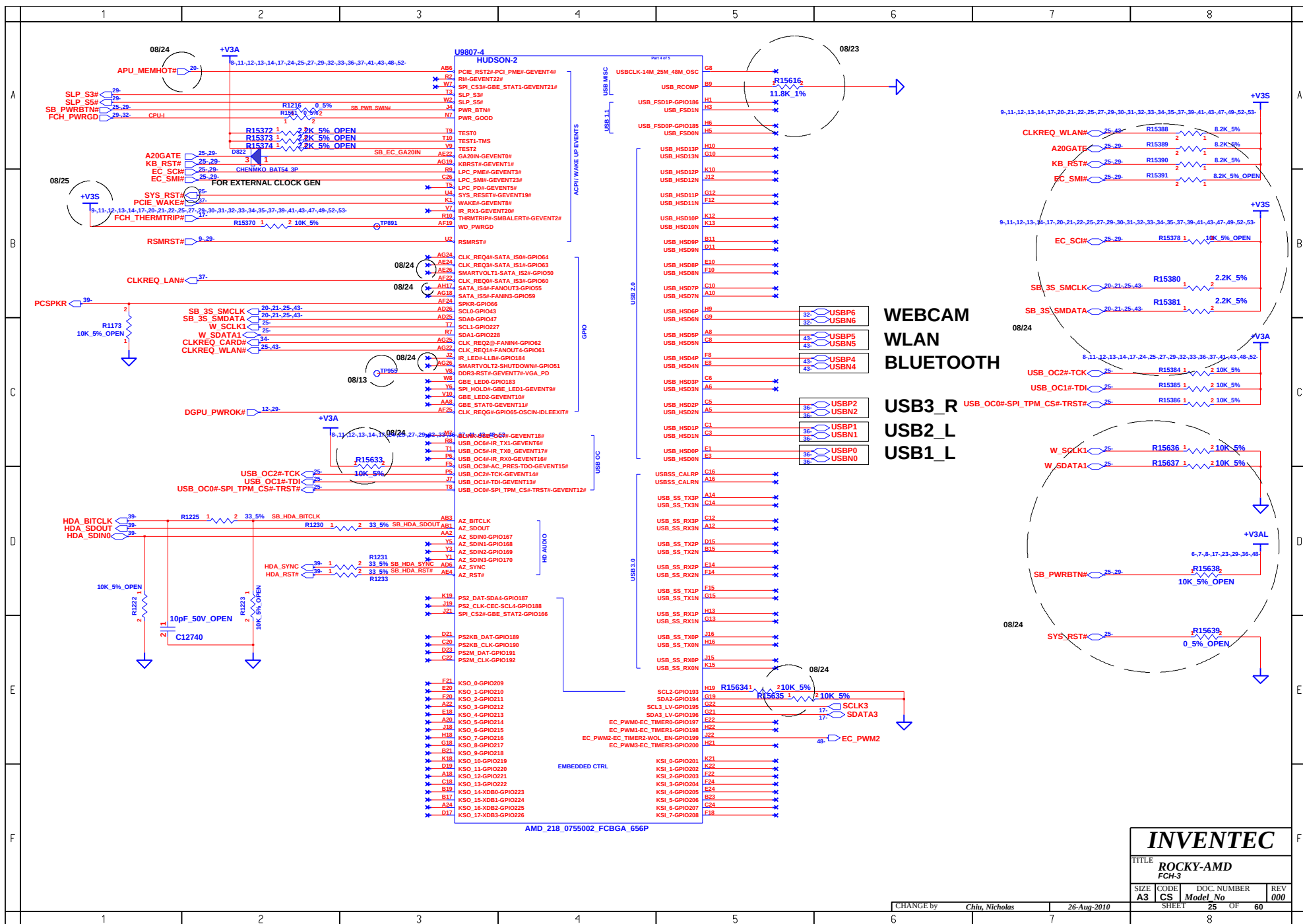
FCH





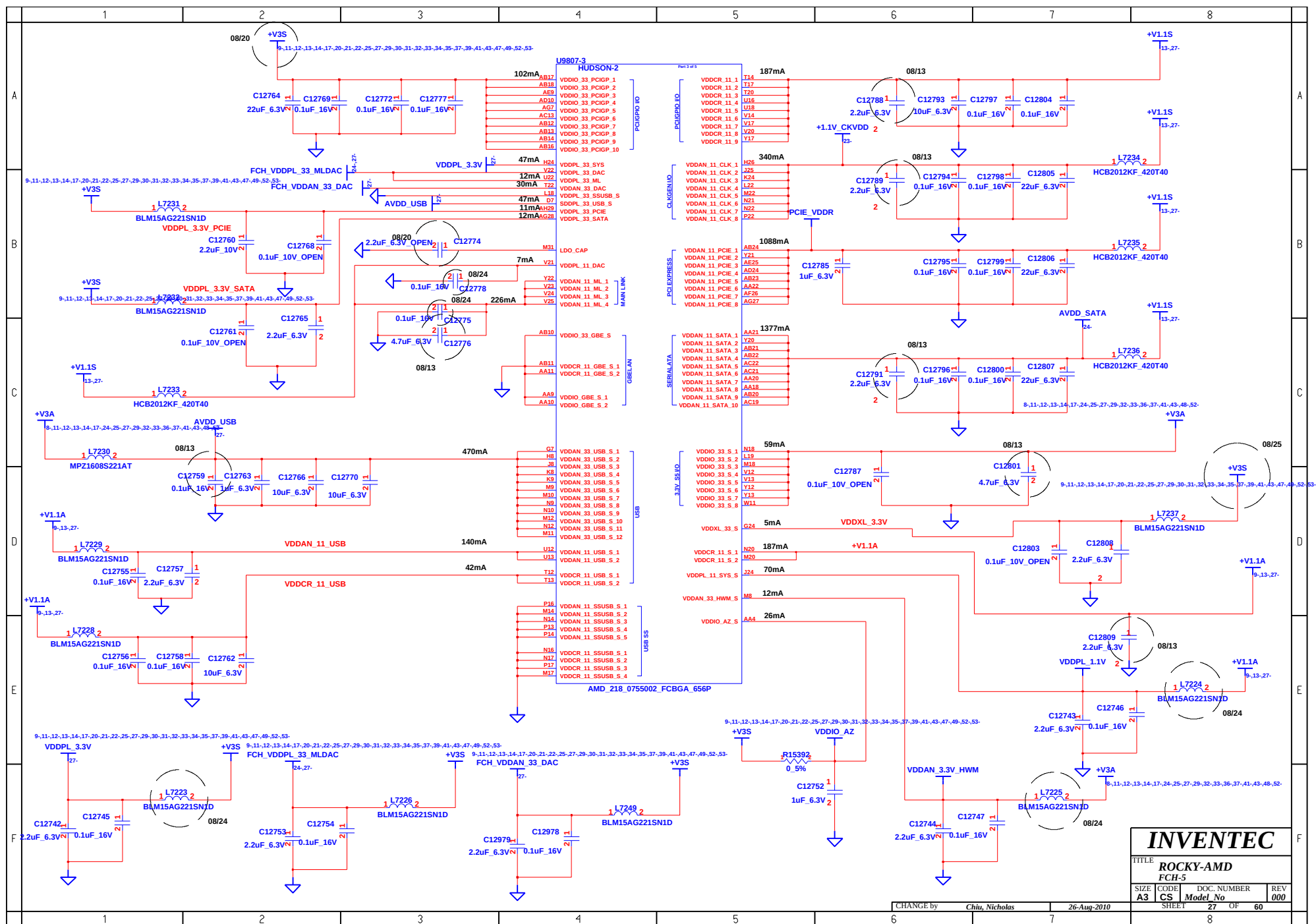
INVENTEC

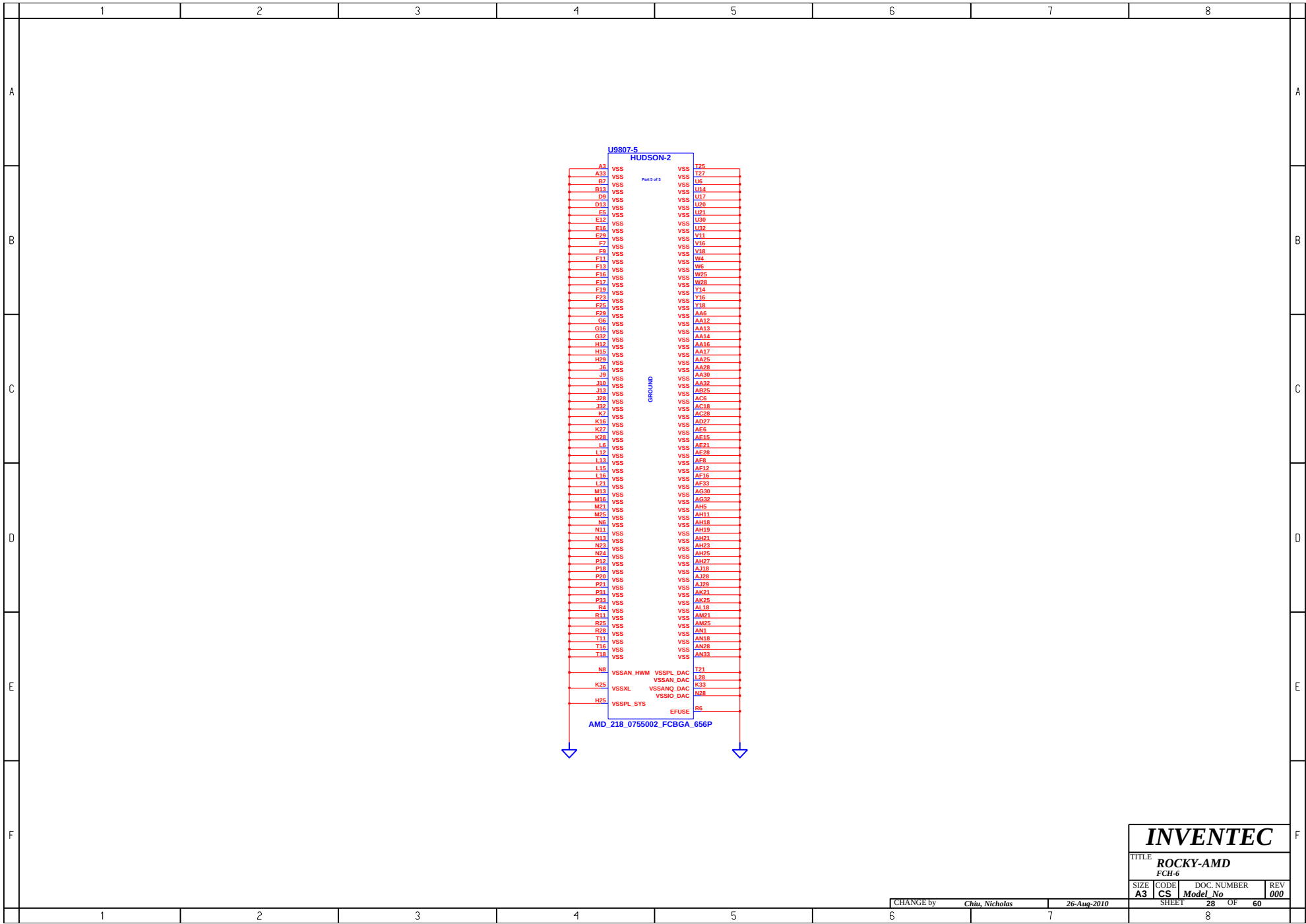
TITLE ROCKY-AMD FCH-2			
SIZE A3	CODE CS	DOC. NUMBER Model_No	REV 000
SHEET 24		OF 60	



	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>Chiu, Nicholas</i>		26-Aug-2010	
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE ROCKY-AMD FCH-4			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
SHEET		26	OF 60





INVENTEC

TITLE
ROCKY-AMD
FCH-6

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
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CHANGE by *Chiu, Nicholas* 26-Aug-2010

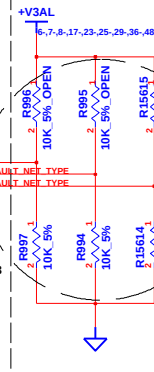
SHEET 28 OF 60

KBC

U805_ 6019B0724201

U805
ITE_I78502E-KX_L_LQFP_128P

BOARD ID



UMA	DIS
MB_ID2	LOW
MB_ID1	HI

INVENTEC

TITLE
ROCKY-AMD
KBC

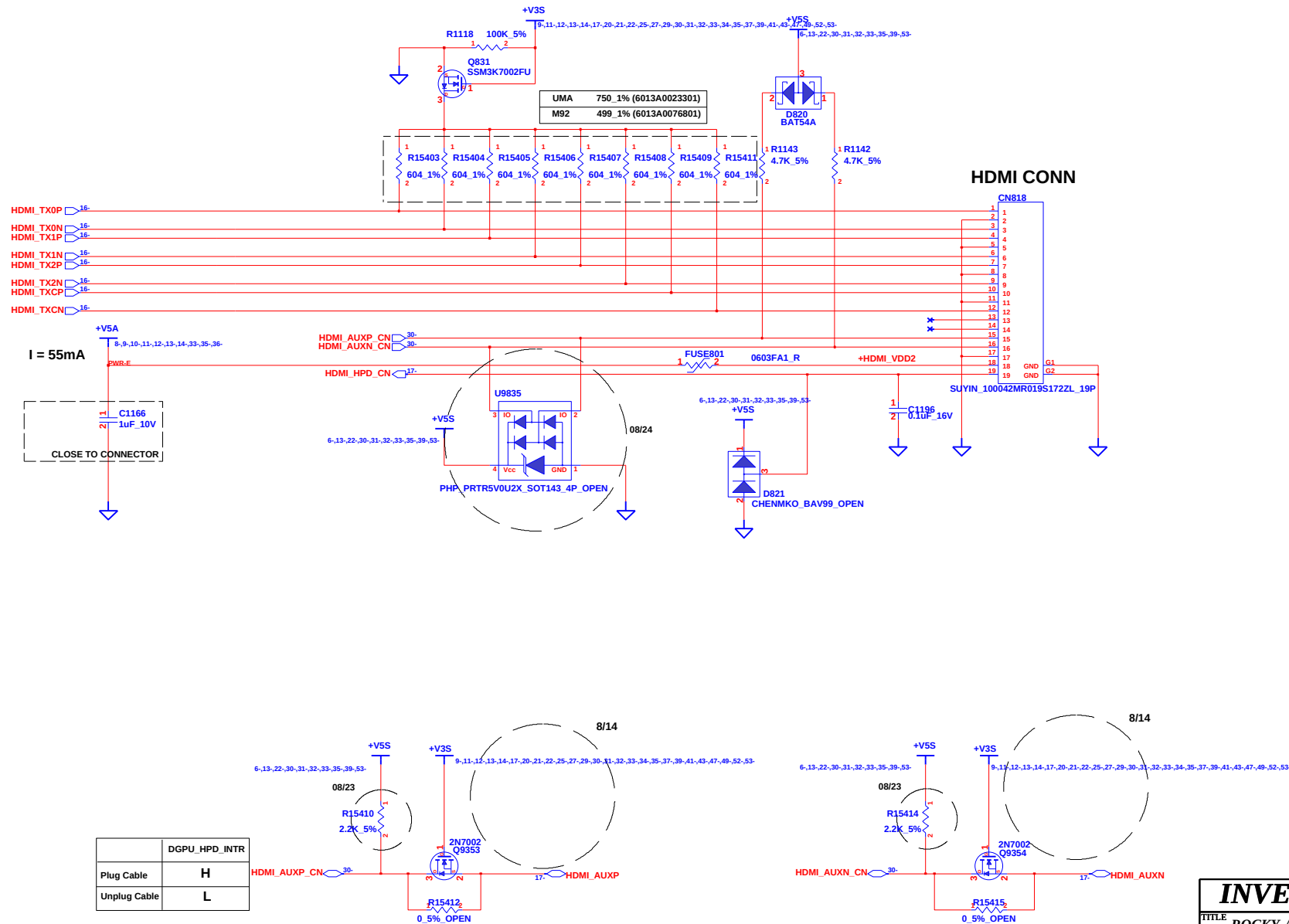
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	000

SHEET	29	OF	60
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CHANGE by Chiu, Nicholas 26-Aug-2010

8

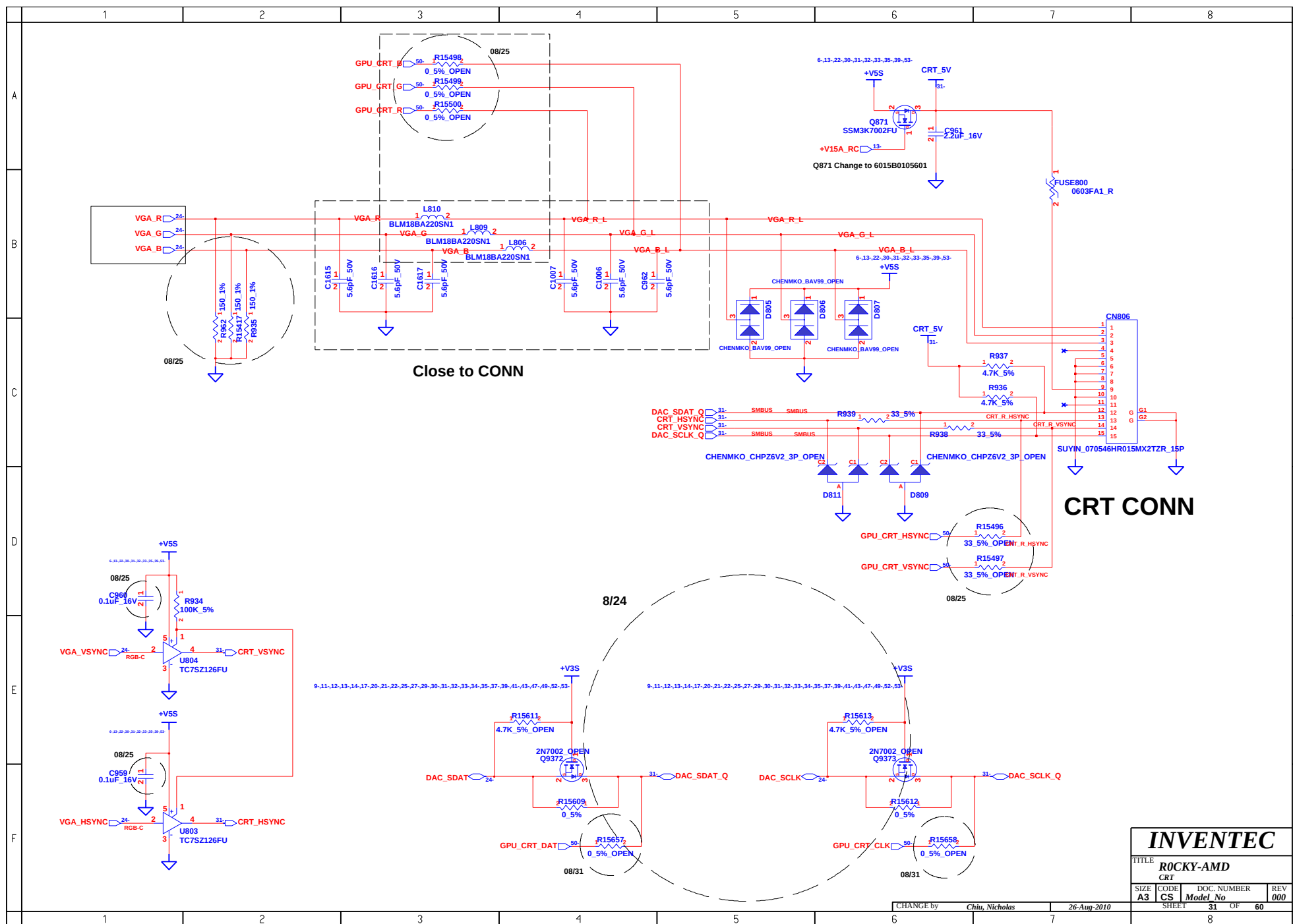
HDMI



INVENTEC

TITLE			
ROCKY-AMD			
HDMI			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		OF	60
30		OF	60

CHANGE by Chiu, Nicholas 26-Aug-2010



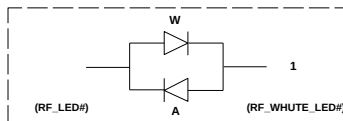
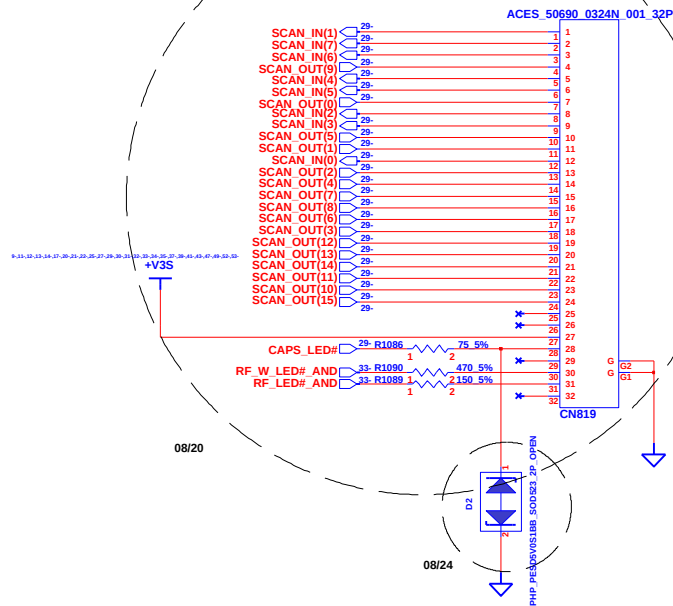
[illegible]

TITLE			
ROCKY-AMD LVDS			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		32	OF 60

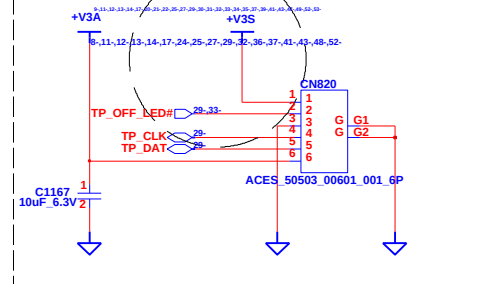
CHANGE by	<i>Chiu, Nicholas</i>	26-Aug-2010
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CS Model No
SHEET 32 OF 60

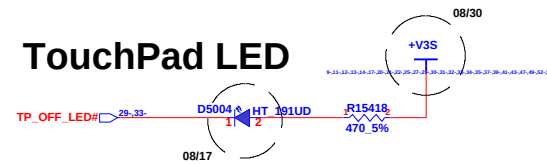
KeyBoard CONN (32 pin)



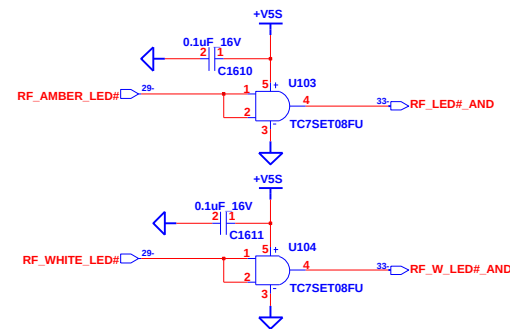
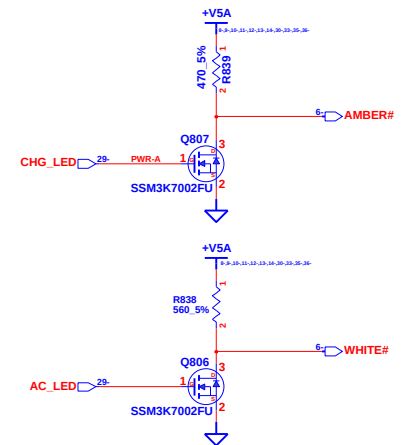
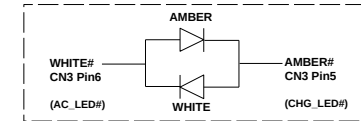
TouchPad on MB



TouchPad LED



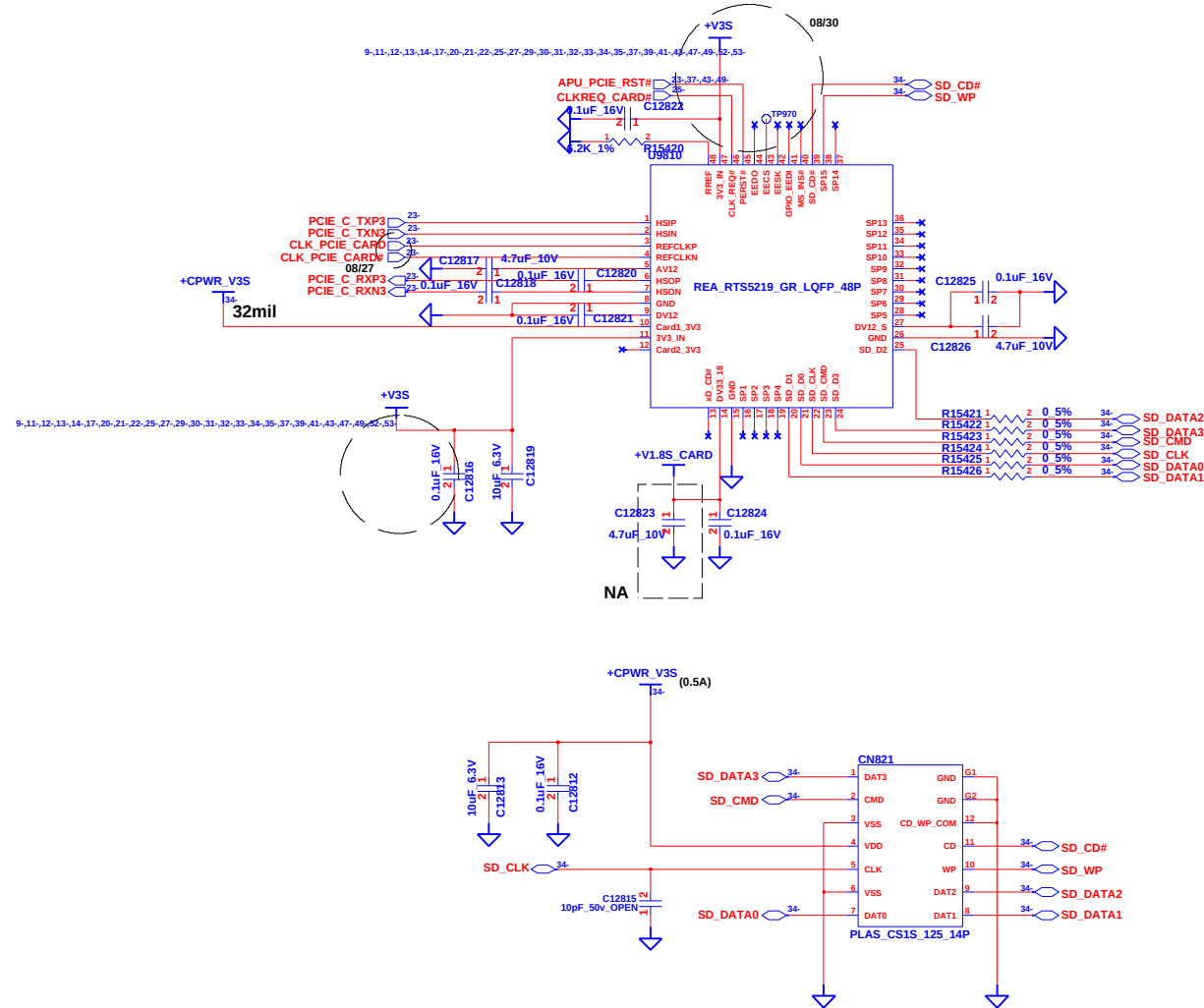
DC-JACK LED



INVENTEC

TITLE			
ROCKY-AMD			
KEYBOARD CONN & Indicate LED			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		33	OF 60

CHANGE by Chiu, Nicholas 26-Aug-2010



Card Reader CONN

Without card	CD	CD / WP Com
	WP	CD / WP Com
Inserted card(unlock)	CD	CD / WP Com
	WP	CD / WP Com
Inserted card(lock)	CD	CD / WP Com
	WP	CD / WP Com

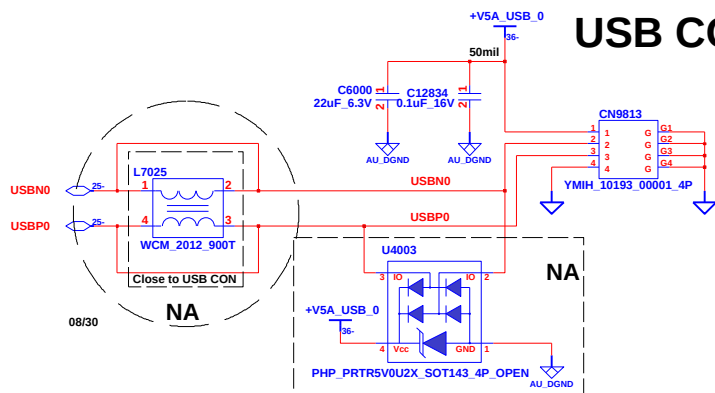
INVENTEC			
TITLE ROCKY-AMD CARDREADER			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by Chiu, Nicholas		26-Aug-2010	
SHEET 34		OF 60	



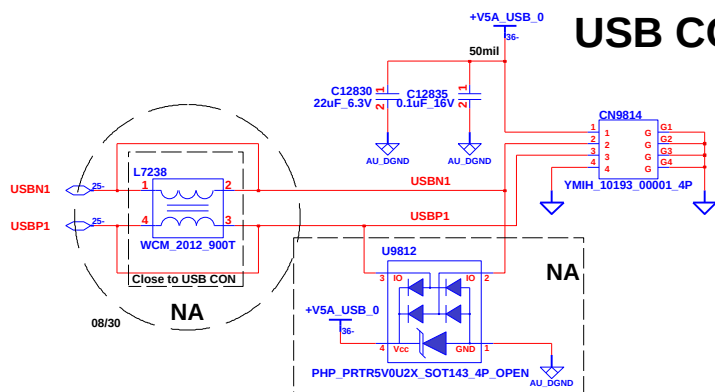
TITLE			
ROCKY-AMD SATA HDD & ODD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>000</i>
SHEET		35	OF 60

USB

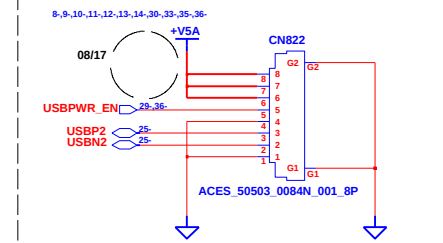
USB CONN01



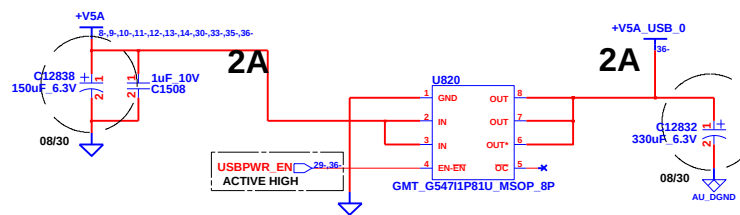
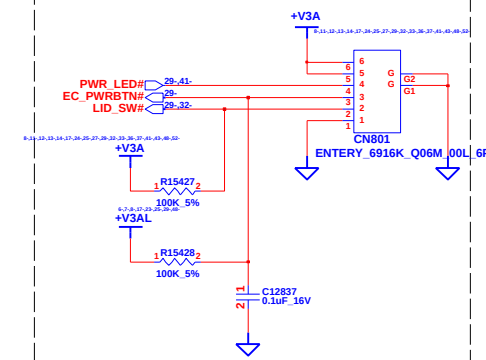
USB CONN02



USB BOARD on MB



PBN on MB



INVENTEC

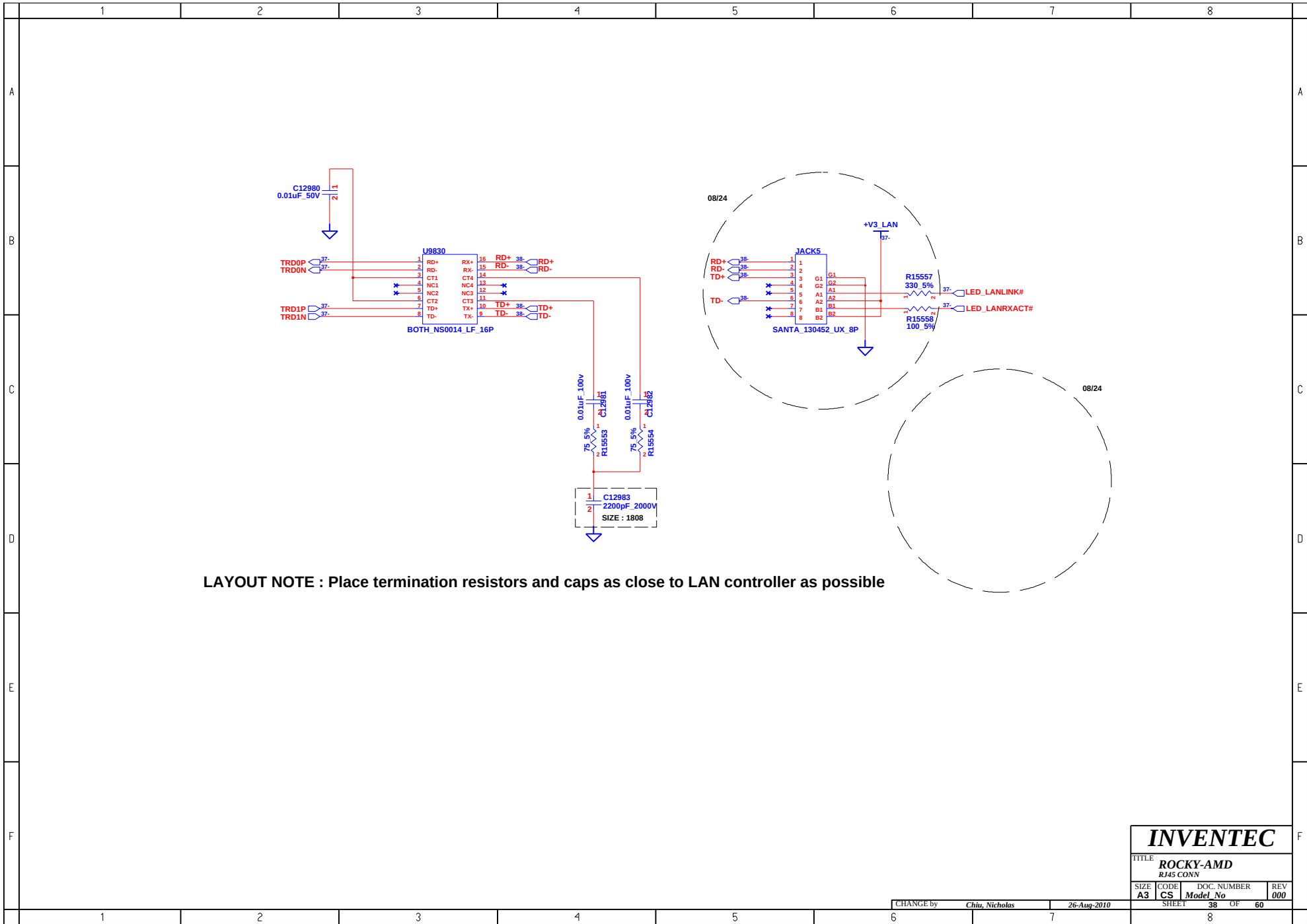
TITLE ROCKY-AMD USB CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
SHEET		36	OF 60

CHANGE by Chiu, Nicholas 26-Aug-2010

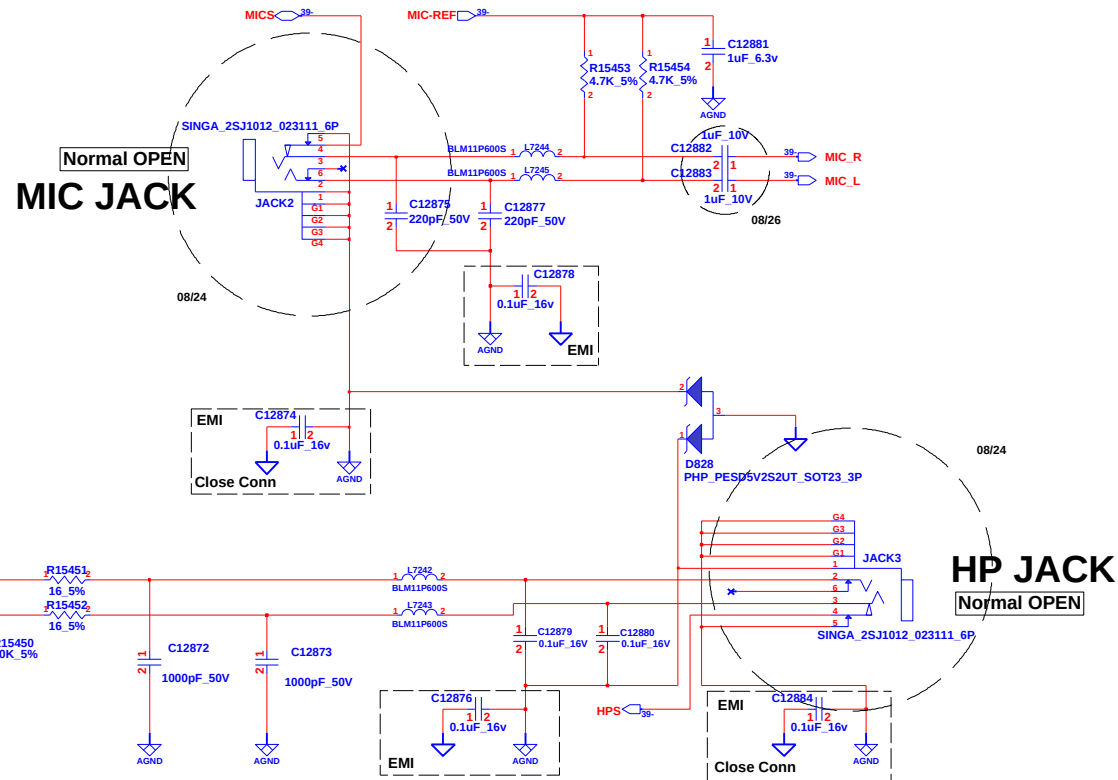
[illegible]

TITLE			
ROCKY-AMD			
LAN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>000</i>
SHEET		37	OF 60

CHANGE by	<i>Chiu, Nicholas</i>	<i>26-Aug-2010</i>
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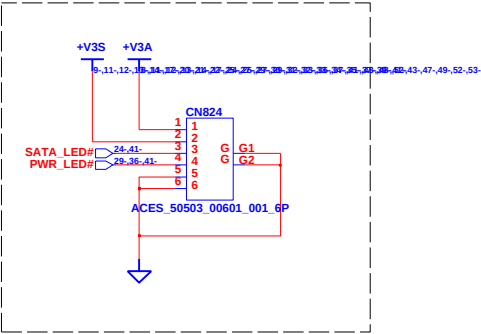
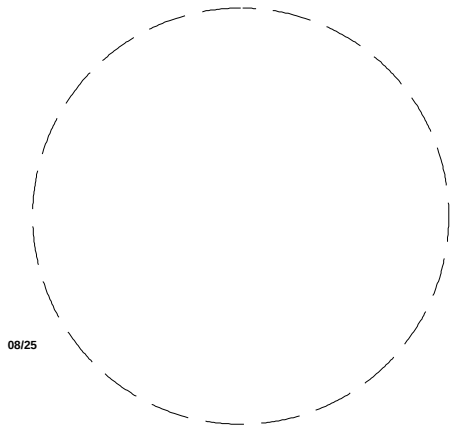
INVENTEC			
TITLE ROCKY-AMD RJ45 CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by Chiu, Nicholas		26-Aug-2010	
SHEET 38		OF 60	



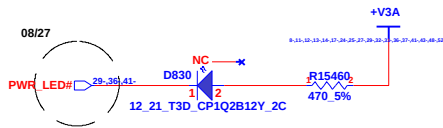
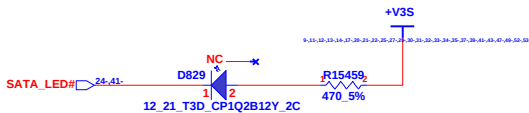
INVENTEC			
TITLE			
ROCKY-AMD			
HP JACK & MIC JACK			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		40	OF 60

CHANGE by Chiu, Nicholas 26-Aug-2010

SATA LED & PWR LED



NA



INVENTEC

TITLE
ROCKY-AMD
SATA LED & PWR LED

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
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CHANGE by *Chiu, Nicholas* 26-Aug-2010

SHEET 41 OF 60

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

FIX21

FIX1

FIX3

FIX5

FIX22

FIX2

FIX4

FIX6

FIX MASK

FIX MASK

FIX MASK

FIX MASK

FIX MASK

FIX MASK

FIX MASK

FIX MASK

S313

SCREW360_500_600_1P

S317

SCREW360_500_600_1P

NUT P/N :

6052B0126801

*BOM

for PCH

S313

SCREW360_500_600_1P

S317

SCREW360_500_600_1P

NUT P/N :

6052B0126801

*BOM

for VGA

S308

SCREW330_600_1P

S310

SCREW330_600_1P

*BOM

CPU's screws

S304

SCREW330_600_1P

S305

SCREW330_600_1P

S311

SCREW330_600_1P

S312

SCREW330_600_1P

for MB

S300

SCREW300_800_1P

S301

SCREW300_800_1P

S302

SCREW300_800_1P

S303

SCREW300_800_1P

S306

SCREW300_800_1P

S309

SCREW300_800_1P

S314

SCREW200_800_1P

S315

SCREW300_800_1P

S319

SCREW300_800_1P

S307

SCREW300_700_1P

S324

SCREW115_300_500_1P

S325

SCREW280_600_1P

S326

SCREW280_600_1P

S327

SCREW115_300_500_1P

08/31

08/31

INVENTEC

TITLE

ROCKY-AMD

SCREW

SIZE

A3

CODE

CS

DOC. NUMBER

Model No

REV

000

CHANGE by

Chiu, Nicholas

26-Aug-2010

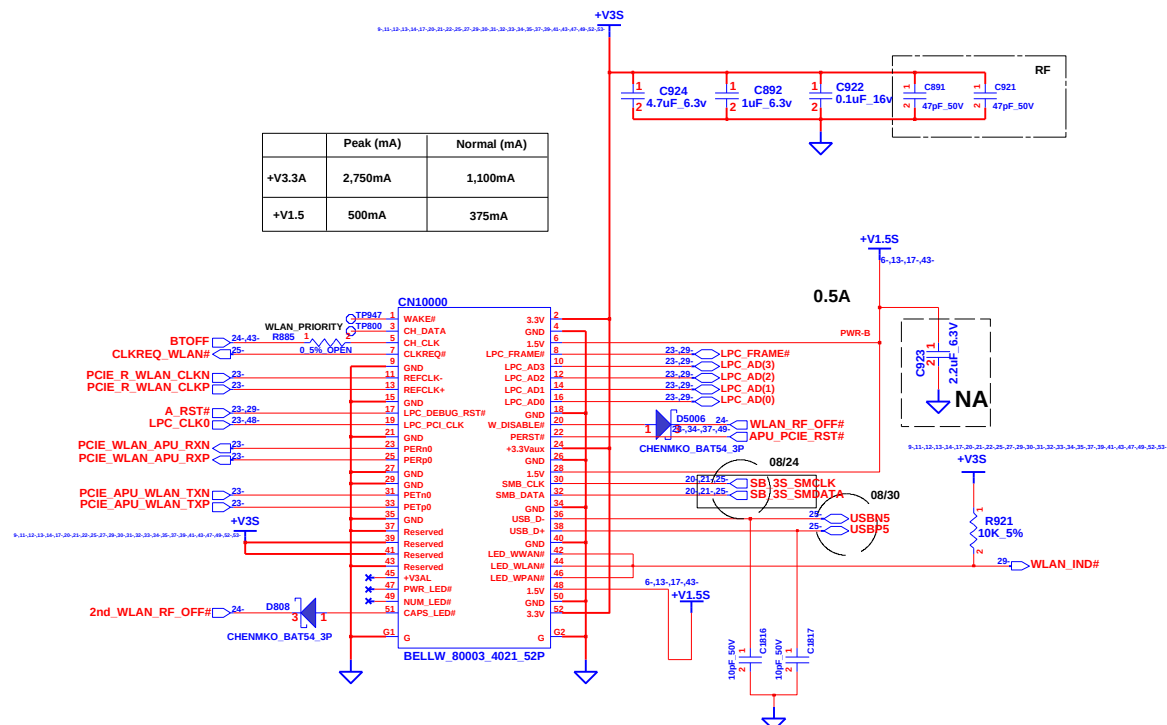
SHEET

42

OF

60

WLAN CONN (MINICARD)



	Peak (mA)	Normal (mA)
+V3.3A	2,750mA	1,100mA
+V1.5	500mA	375mA

	PCH	SB800 820
WLAN_RF_OFF#	GPIO57	GPIO12
WLAN_POWER_OFF	GPIO35	GPIO176

TITLE	ROCKY-AMD WLAN & BT
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SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>000</i>
SHEET 43 OF 60			

CHANGE by	<i>Chiu, Nicholas</i>	26-Aug-2010
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	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>Chiu, Nicholas</i>		26-Aug-2010	
1	2	3	4	5	6	7	8		

INVENTEC			
TITLE ROCKY-AMD PICK BUTTON BOARD			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
SHEET		44	OF 60

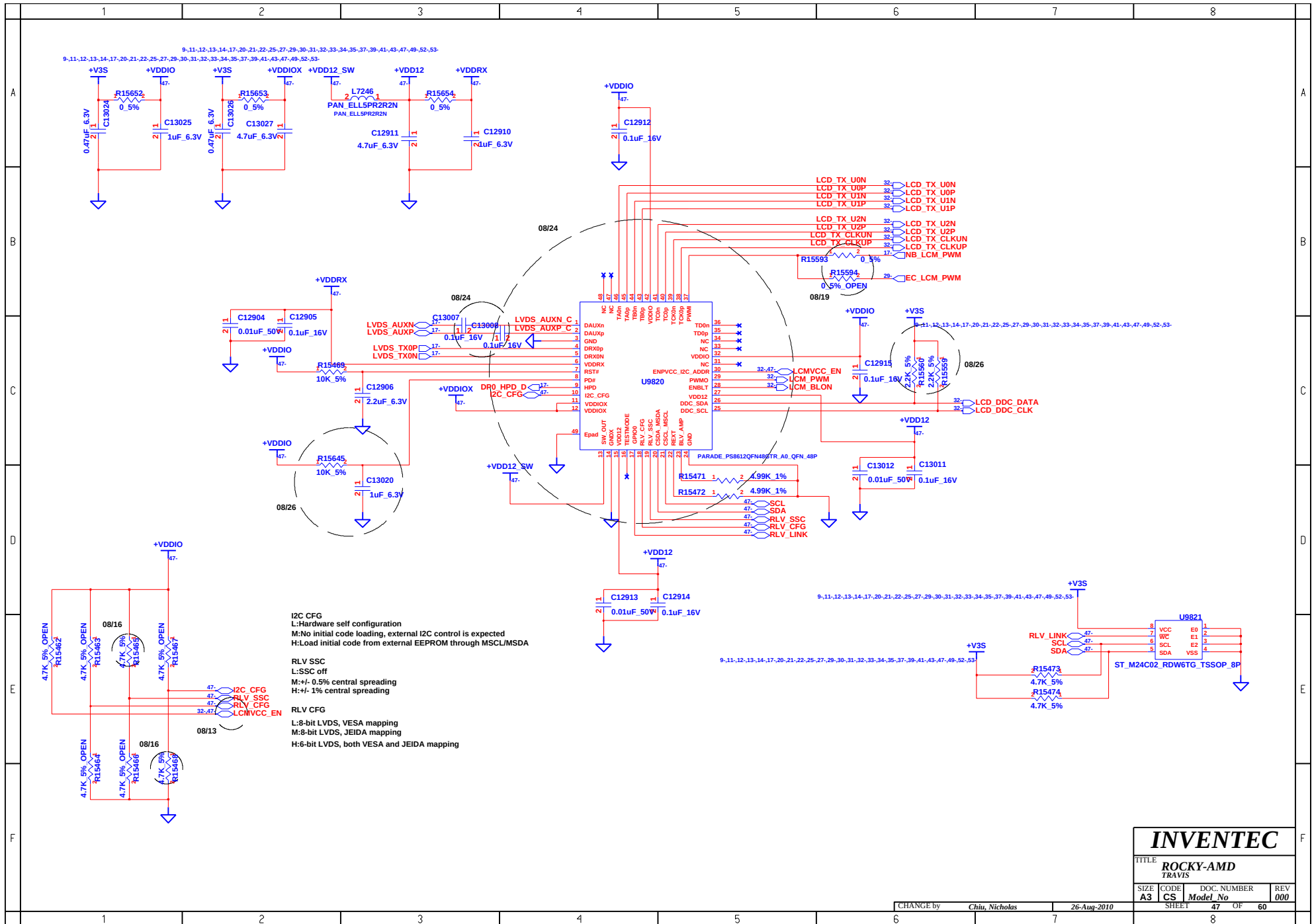
	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>Chiu, Nicholas</i>		26-Aug-2010	
1	2	3	4	5	6	7	8		

INVENTEC			
TITLE ROCKY-AMD USB/B & POWER BUTTON/B			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV 000
SHEET 45		OF 60	

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>Chiu, Nicholas</i>		26-Aug-2010	
1	2	3	4	5	6	7	8		

INVENTEC

TITLE			
ROCKY-AMD SATA HDD BOARD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		46	OF 60



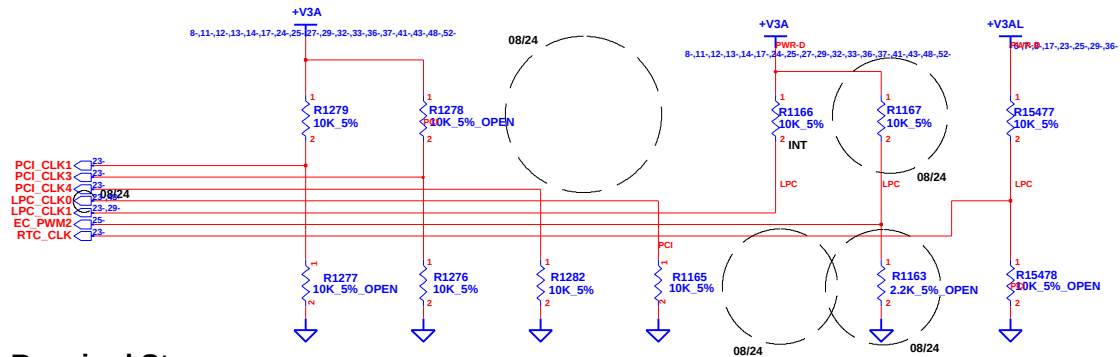
INVENTEC

TITLE
ROCKY-AMD
 TRAVIS

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000

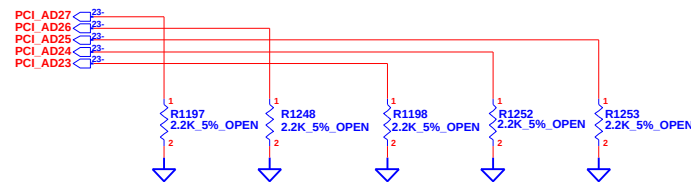
CHANGE by *Chiu, Nicholas* 26-Aug-2010

SHEET 47 OF 60



Required Straps:

RTCCCLK		PCI_CLK1	PCI_CLK3	PCI_CLK4	LPC_CLK0	PCI_CLK1	EC_PWM2
S5 PLUS MODE DISABLE (DEFAULT)	PULL HIGH	ALLOW PCIe Gen2 DEFAULT	USE DEBUG STRAP	non Fusion CLOCK MODE APU_CLK AND DISP_CLK NO CLOCK OUTPUT	EC ENABLED	INTEGRATED CLKGEN ENABLED DEFAULT	LPC ROM DEFAULT
S5 PLUS MODE ENABLE	PULL LOW	FORCE PCIe Gen1	IGNORE DEBUG STRAPE DEFAULT	Fusion CLOCK MODE DEFAULT APU_CLK AND DISP_CLK CLOCK OUTPUT FROM INT CLOCK GEN	EC DISABLED DEFAULT	INTEGRATED CLKGEN DISABLED	SPI ROM

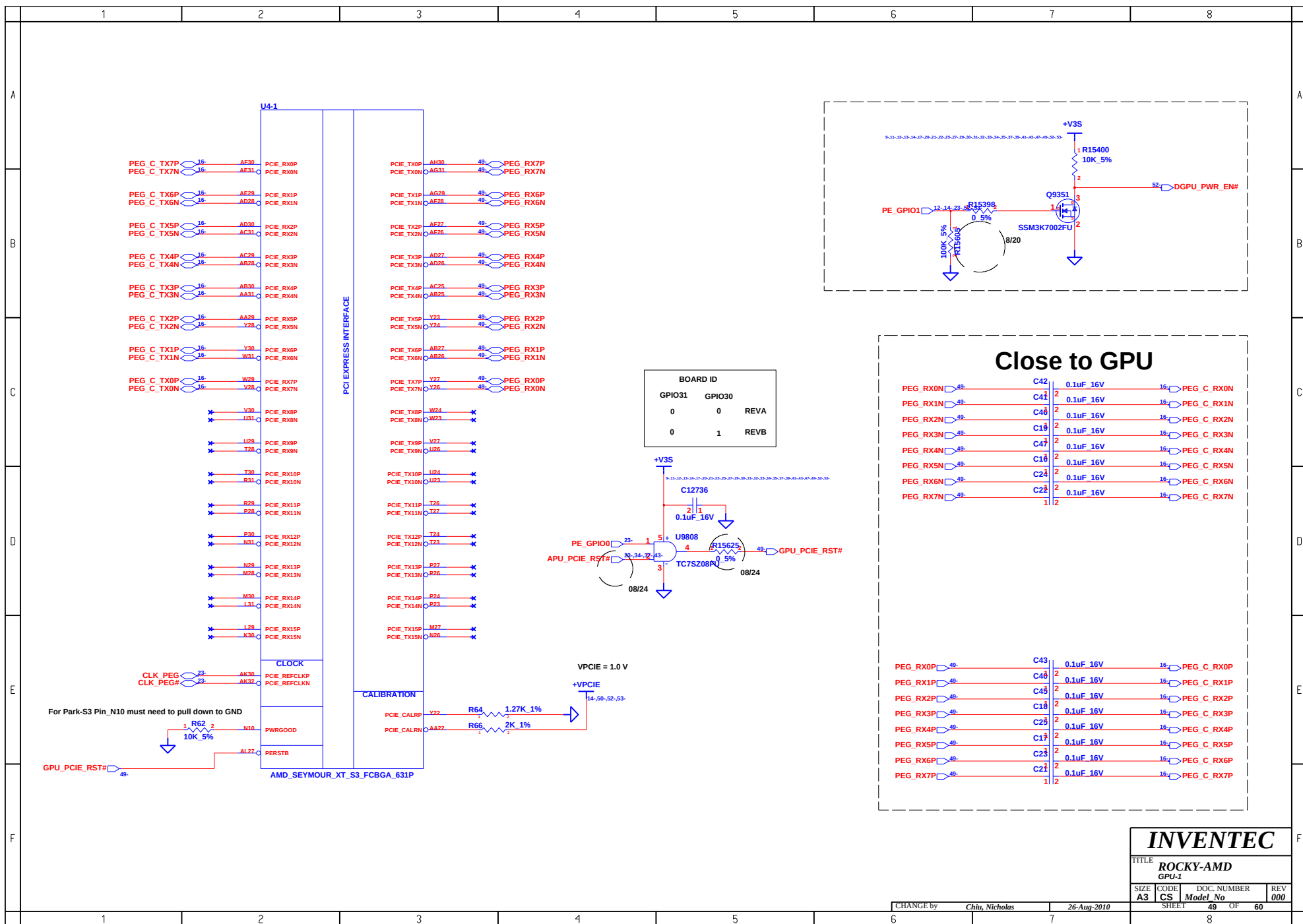


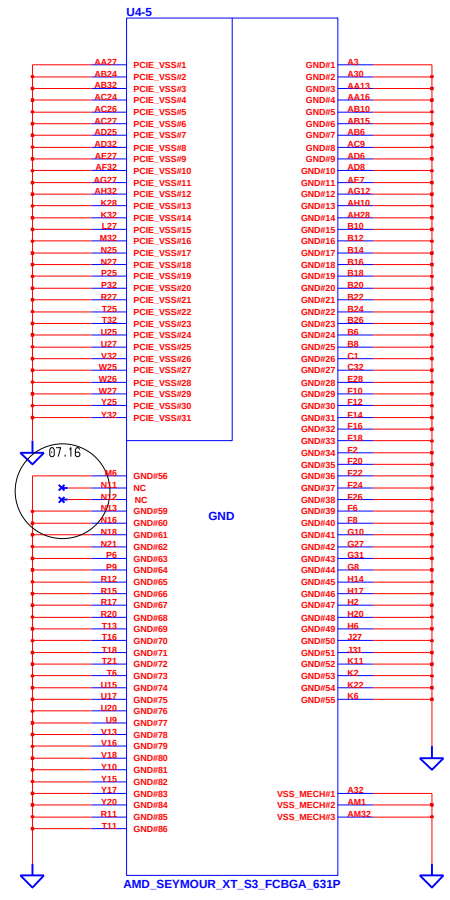
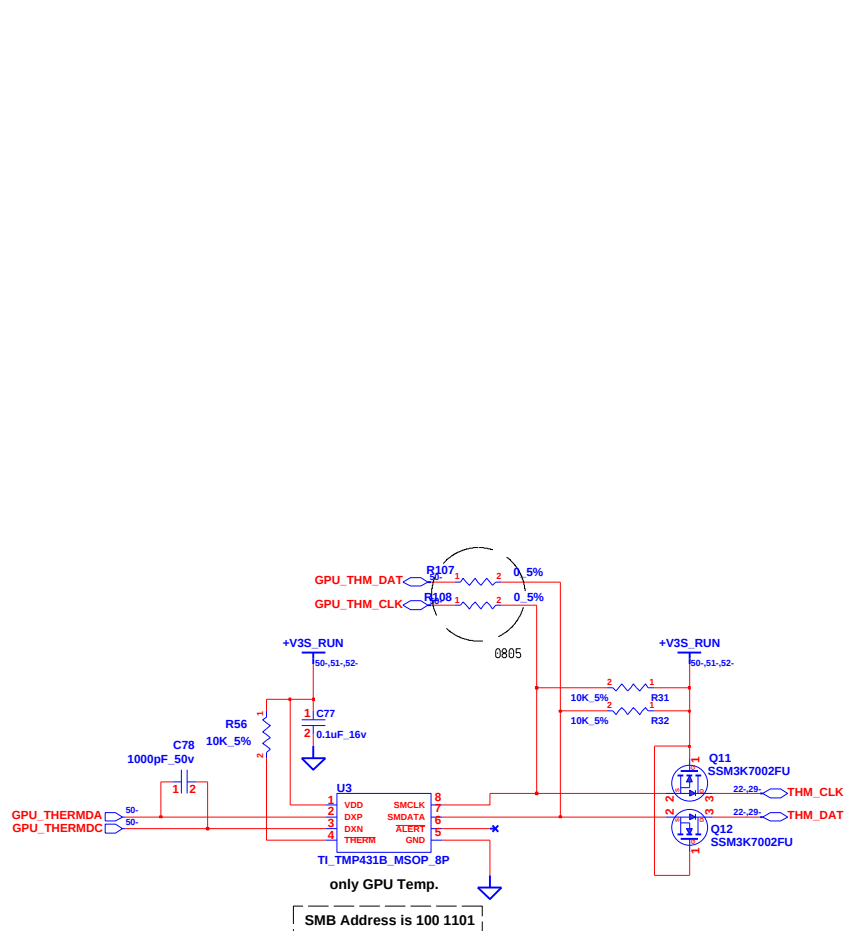
Debug Straps

SB800 HAS 15K INTERNAL PU FOR PCI_AD[27:23]

	PCI_3S_AD27	PCI_3S_AD24	PCI_3S_AD23
PULL HIGH	USE PCI PLL (default)	GETTING I2C ROM ENABLE	DISABLE PCI MEM BOOT (default)
PULL LOW	BYPASS PCI PLL	USE EEPROM PCI STRAPS	ENABLE PCI MEM BOOT

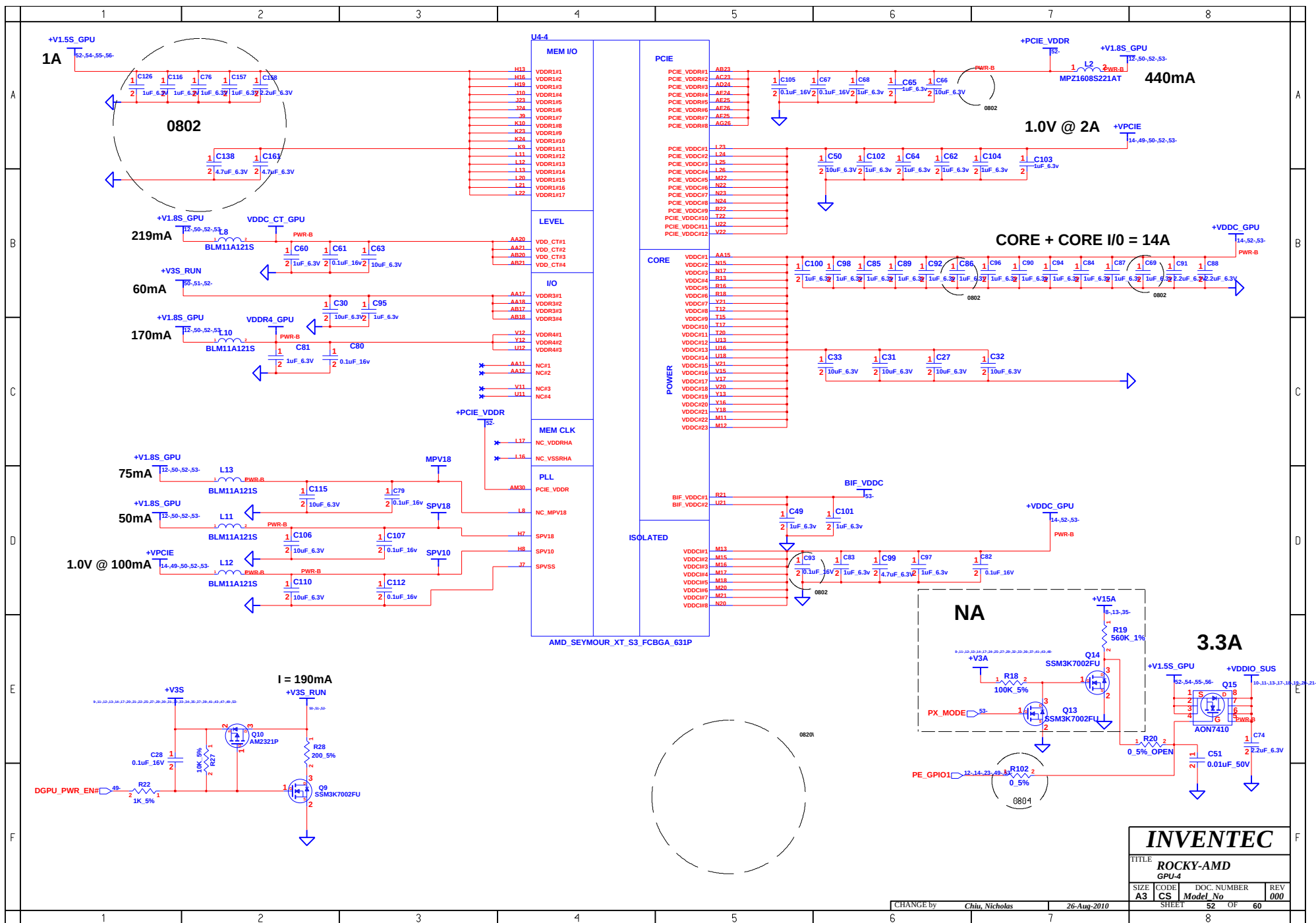
INVENTEC			
TITLE ROCKY-AMD SRTAP			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by Chiu, Nicholas		26-Aug-2010	
SHEET		48 OF 60	

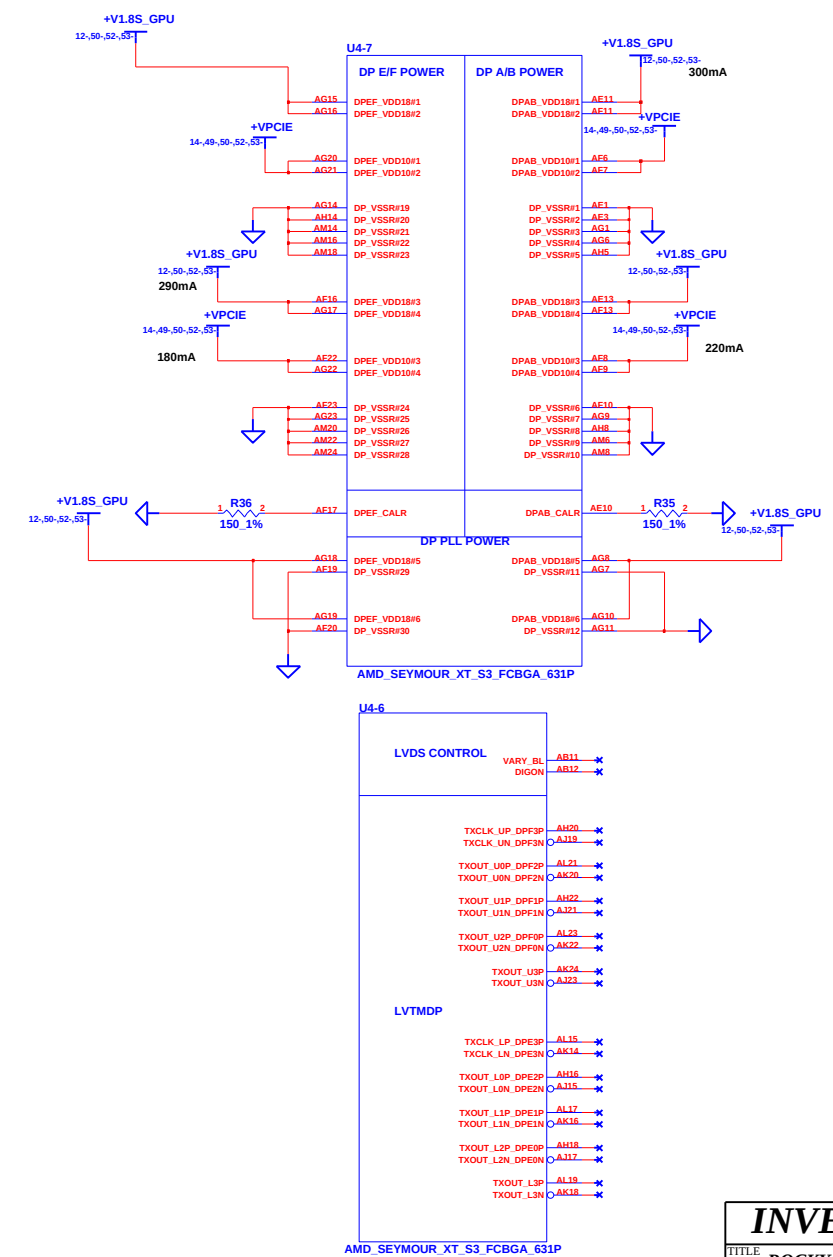
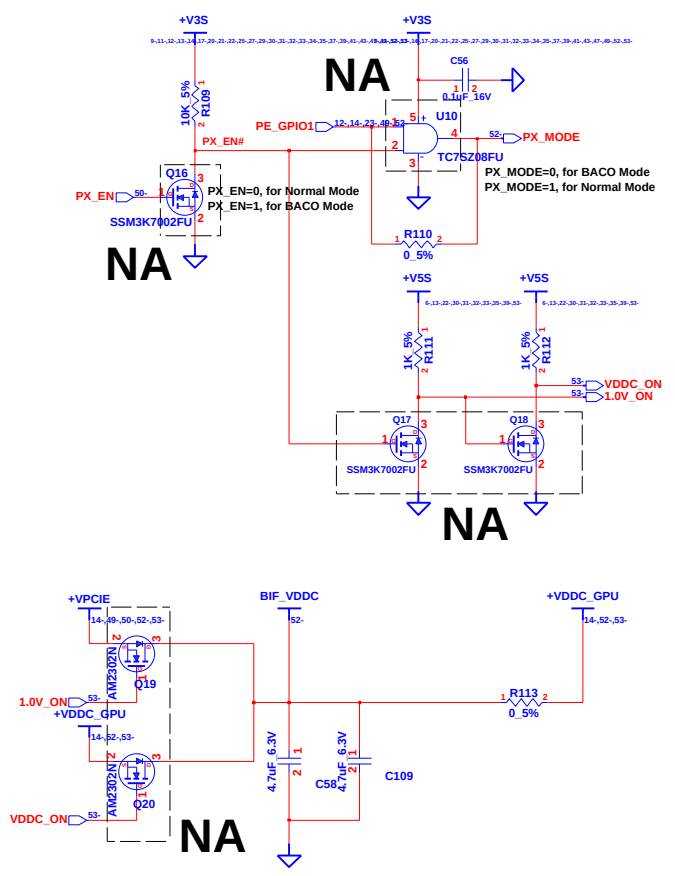




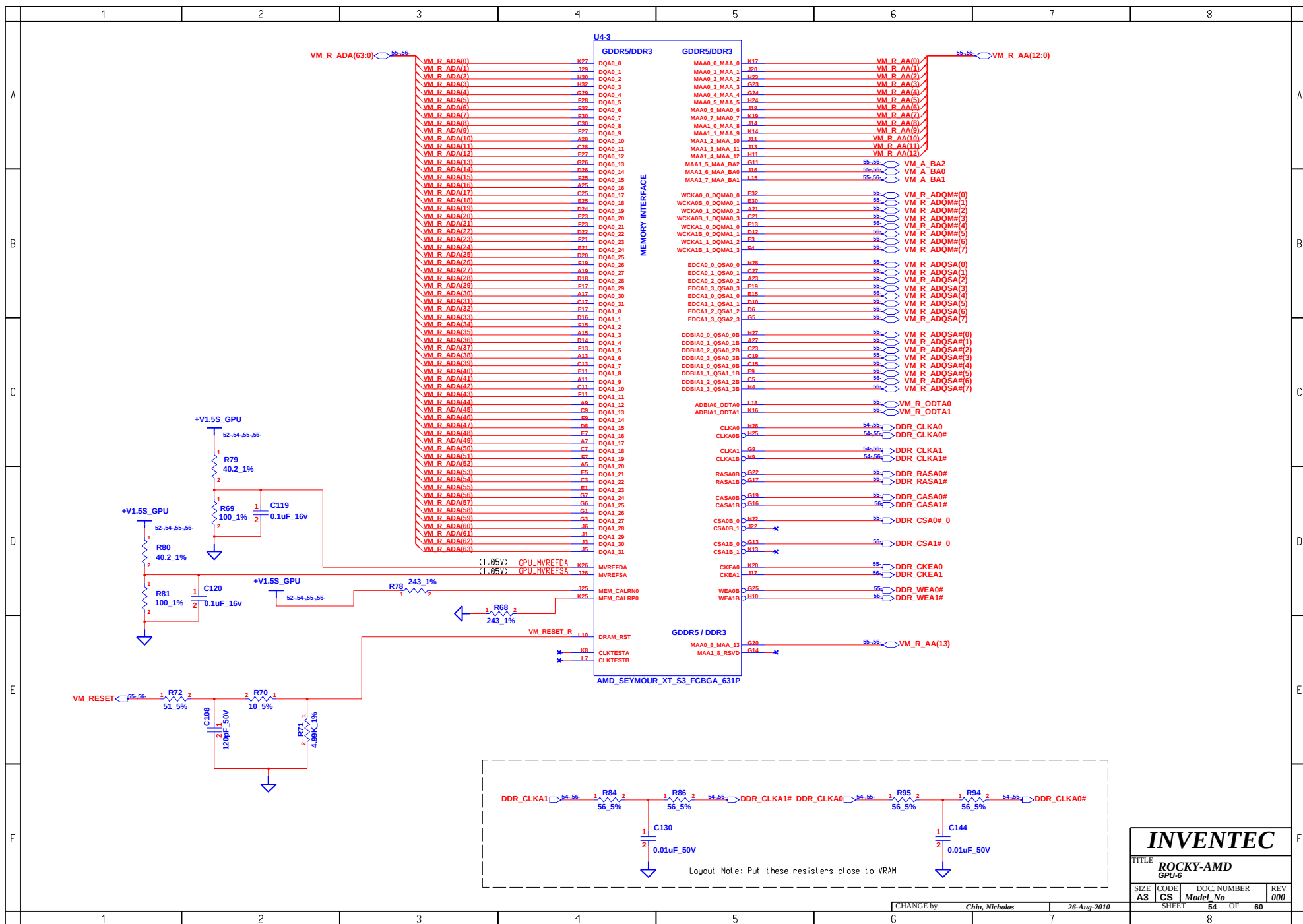
INVENTEC			
TITLE ROCKY-AMD GPU-3			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
CHANGE by Chiu, Nicholas		26-Aug-2010	
SHEET 51		OF 60	

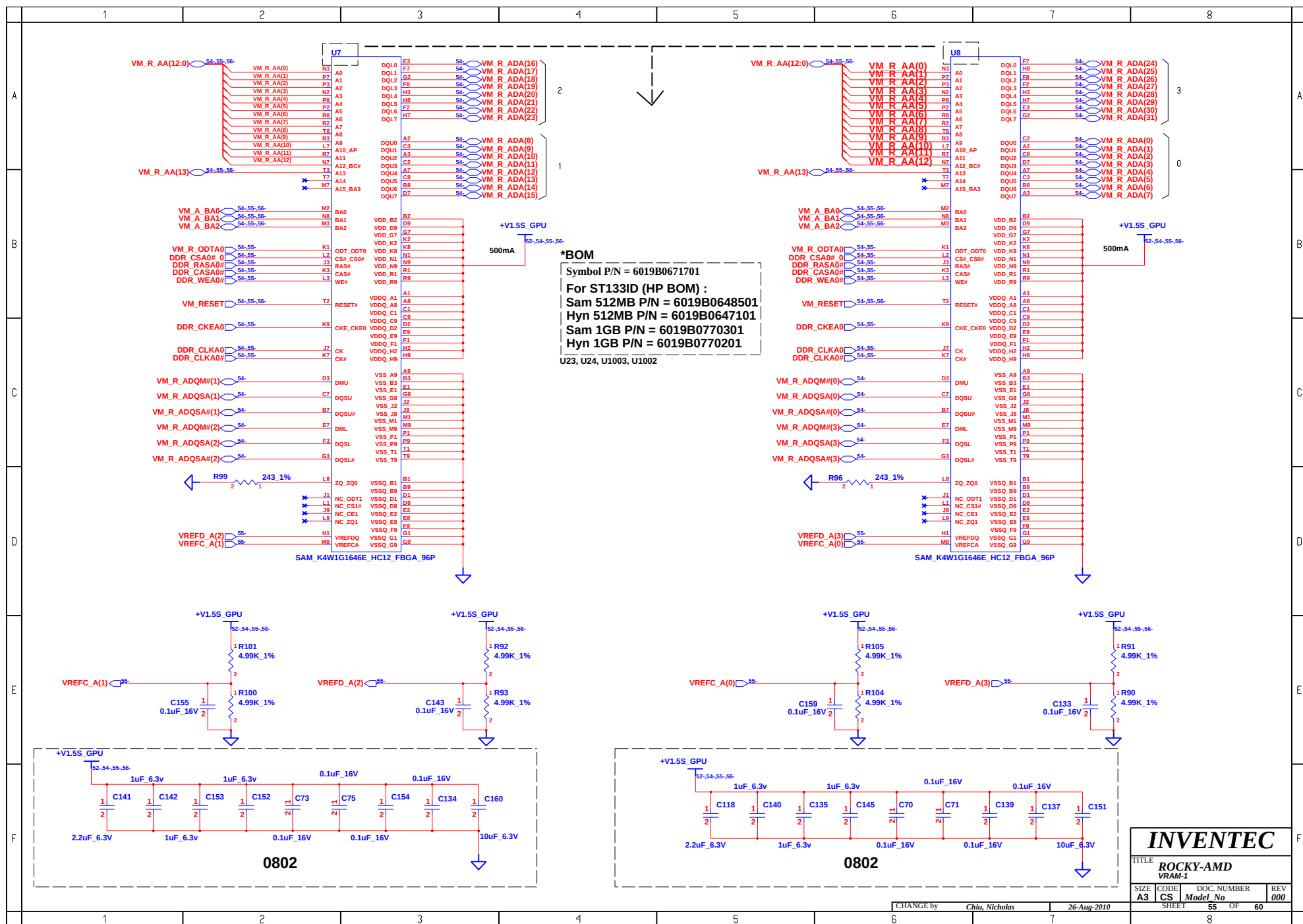
MV Buid :
Main : 6019B0437702 EMC1402-2
2nd : 6019B0671902 TMP431B

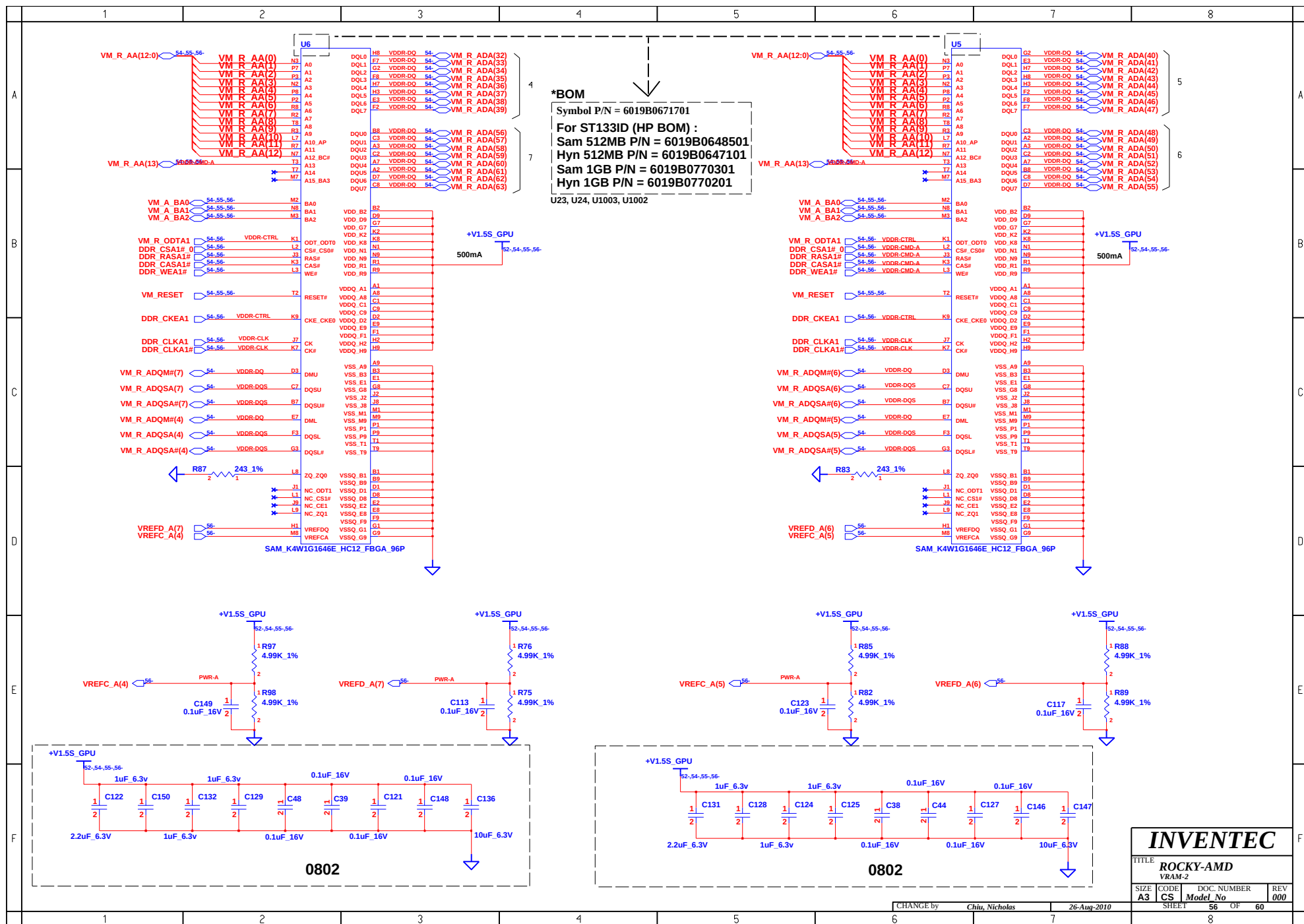




INVENTEC			
TITLE			
ROCKY-AMD GPU-5			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
CHANGE by		26-Aug-2010	
SHEET		53	OF 60







BLANK

INVENTEC

TITLE			
ROCKY-AMD EXT USB CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	000
SHEET		OF	60
		57	8

CHANGE by Chiu, Nicholas 26-Aug-2010

BLANK

INVENTEC

TITLE
ROCKY-AMD

SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
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CHANGE by	Chiu, Nicholas	26-Aug-2010
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SHEET	58	OF	60
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BLANK

INVENTEC

TITLE
ROCKY-AMD

SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
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CHANGE by	Chiu, Nicholas	26-Aug-2010
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SHEET	59	OF	60
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BLANK

INVENTEC			
TITLE RKCKY-AMD			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV 000
SHEET 60		OF 60	

CHANGE by Chiu, Nicholas 26-Aug-2010