

|   |                                                                                                                                                                                                                                                                               |   |   |   |   |   |   |   |   |
|---|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|---|---|---|---|---|---|---|
|   | 1                                                                                                                                                                                                                                                                             | 2 | 3 | 4 | 5 | 6 | 7 | 8 |   |
| A | THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF INVENTEC CORPORATION AND SHALL NOT BE REPRODUCED, COPIED, OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION, INVENTEC CORPORATION 2009 ALL RIGHT RESERVED |   |   |   |   |   |   |   | A |
| B |                                                                                                                                                                                                                                                                               |   |   |   |   |   |   |   | B |
| C |                                                                                                                                                                                                                                                                               |   |   |   |   |   |   |   | C |
| D |                                                                                                                                                                                                                                                                               |   |   |   |   |   |   |   | D |
| E |                                                                                                                                                                                                                                                                               |   |   |   |   |   |   |   | E |
| F |                                                                                                                                                                                                                                                                               |   |   |   |   |   |   |   | F |

Sarah AMD-Sabine 15"

DB

2010/08/24

|      |            |     |
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| DATE | CHANGE NO. | REV |
| 1    | 2          |     |

|                            |              |      |        |      |
|----------------------------|--------------|------|--------|------|
|                            | EE           | DATE | POWER  | DATE |
| DRAWER                     | ALAN W       |      | ALBERT |      |
| DESIGN                     | VENS HUANG   |      | ALBERT |      |
| CHECK                      |              |      |        |      |
| RESPONSIBLE                |              |      |        |      |
| SIZE = 3                   |              |      |        |      |
| FILE NAME : XXXX-XXXXXX-XX |              |      |        |      |
| P/N                        | XXXXXXXXXXXX |      |        |      |
| VER :                      |              |      |        |      |
| SIZE CODE                  |              |      |        |      |
| A3 CS 1234                 |              |      |        |      |
| DOC. NUMBER                |              |      |        |      |
| SHEET 1 OF 60              |              |      |        |      |
| REV X01                    |              |      |        |      |

INVENTEC

ROCKY-AMD

Project Name

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 11- +APU\_VDD&+VDDNB  
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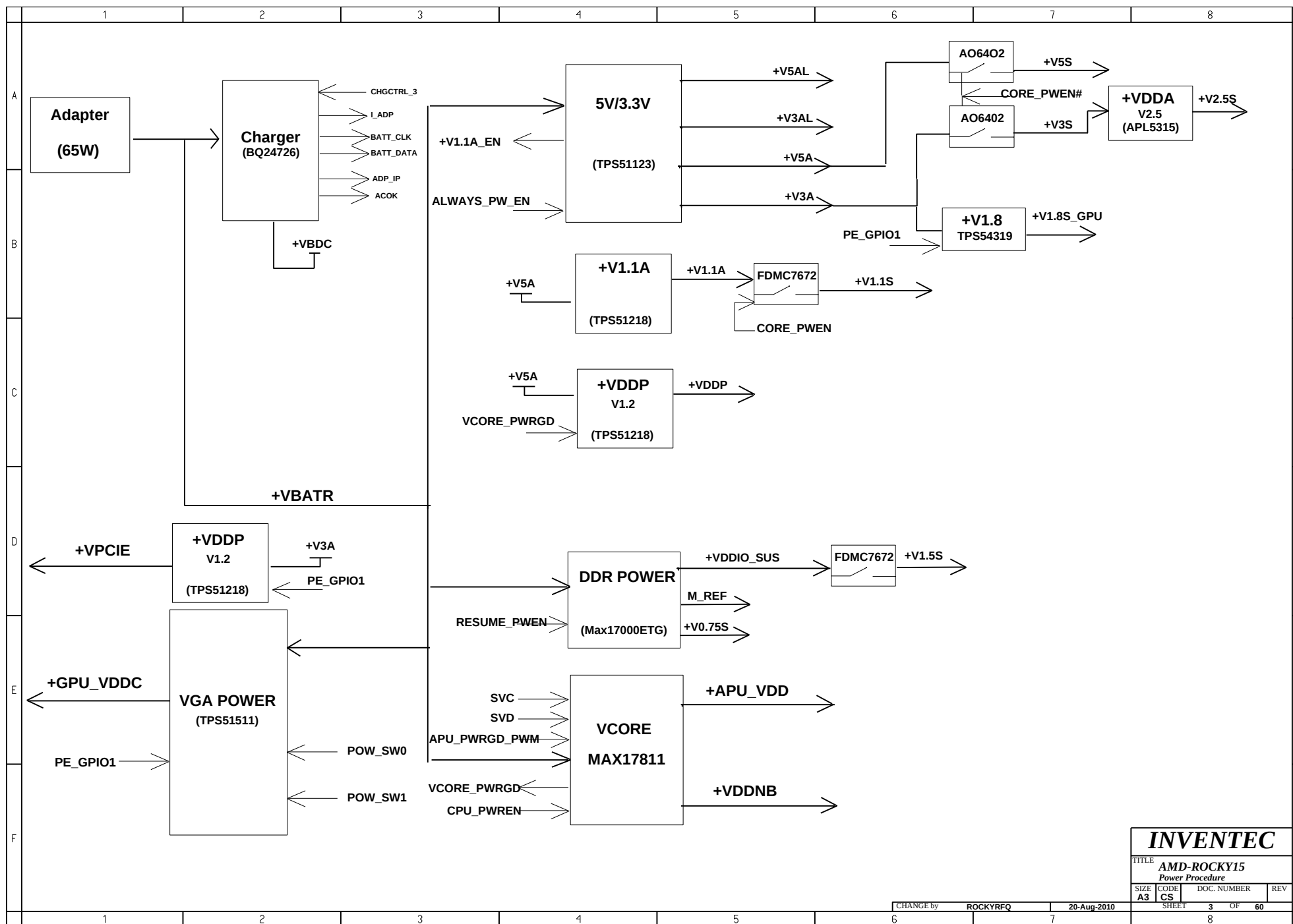
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 34- Card reader-RTS5219  
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 50- GPU-2  
 51- GPU-3  
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 53- GPU-5  
 54- GPU-6  
 55- VRAM-1  
 56- VRAM-2

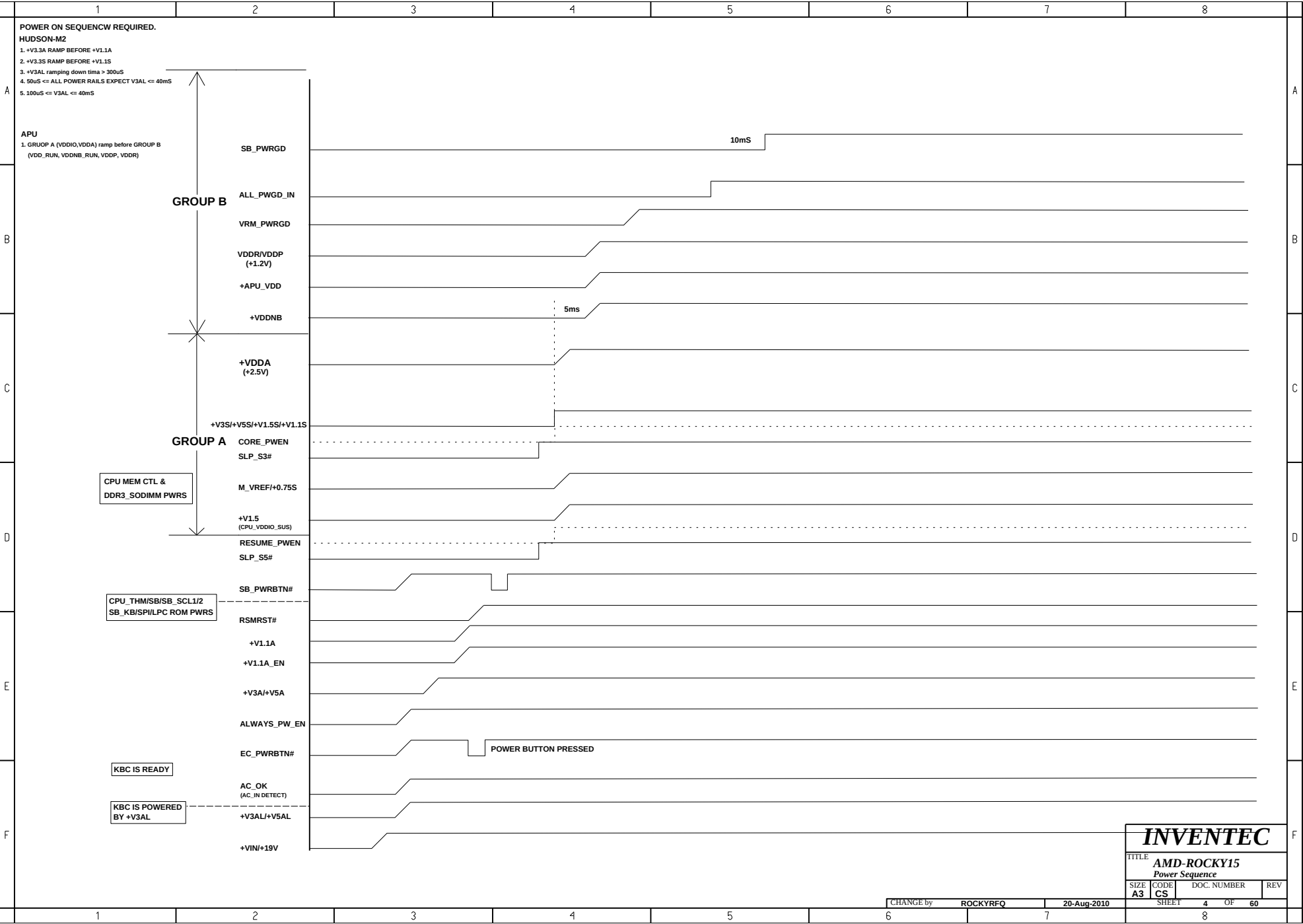
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**INVENTEC**

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| SIZE<br><b>A3</b>                  | CODE<br><b>CS</b> | DOC. NUMBER<br><b>1234</b> | REV<br><b>X01</b> |
| SHEET<br><b>2</b>                  |                   | OF<br><b>60</b>            |                   |

CHANGE by *Ken, Chiang* 18-Aug-2010





INVENTEC

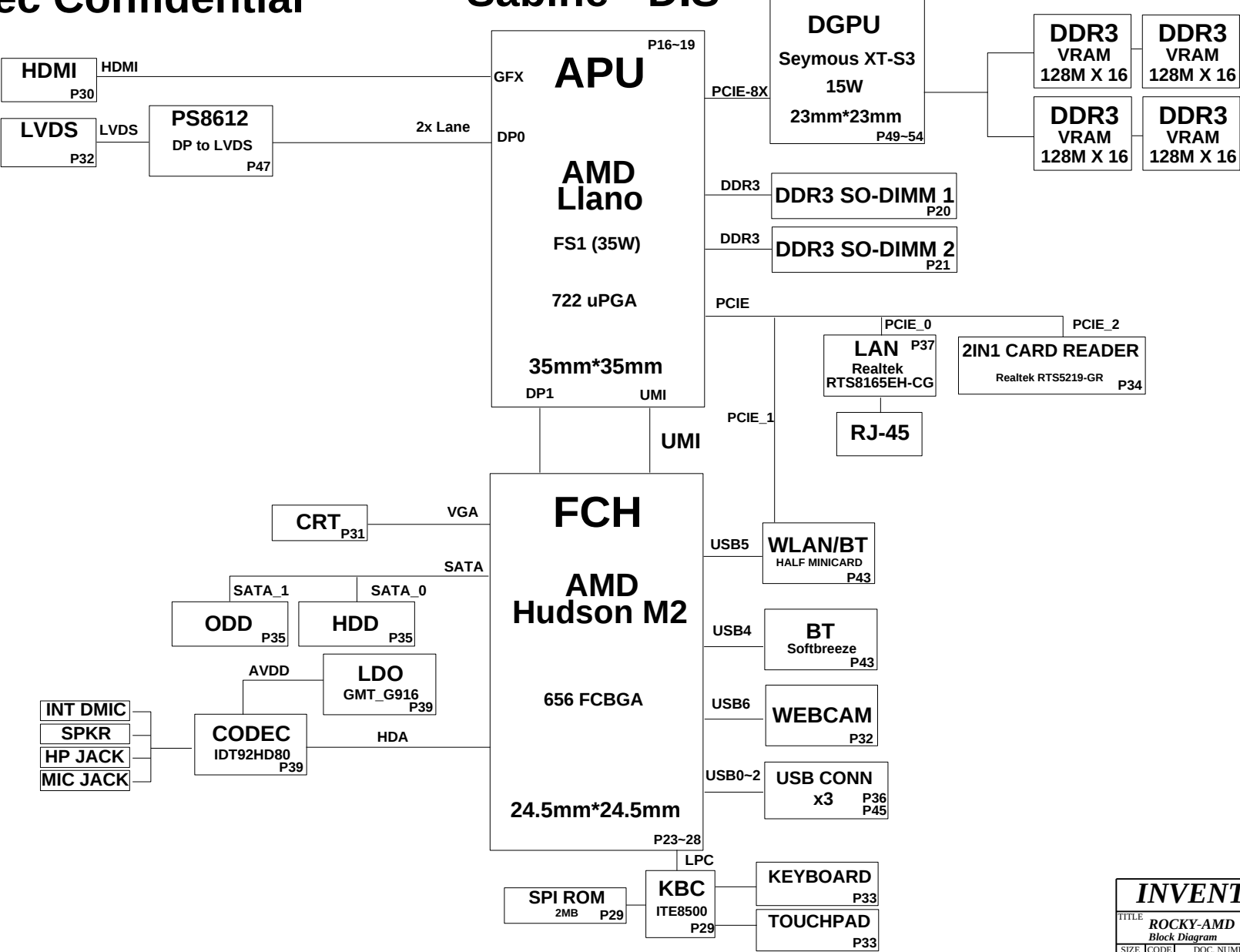
TITLE  
AMD-ROCKY15  
Power Sequence

SIZE CODE DOC. NUMBER REV  
A3 CS

CHANGE by ROCKYRFQ 20-Aug-2010 SHEET 4 OF 60

Inventec Confidential

Sabine - DIS



INVENTEC

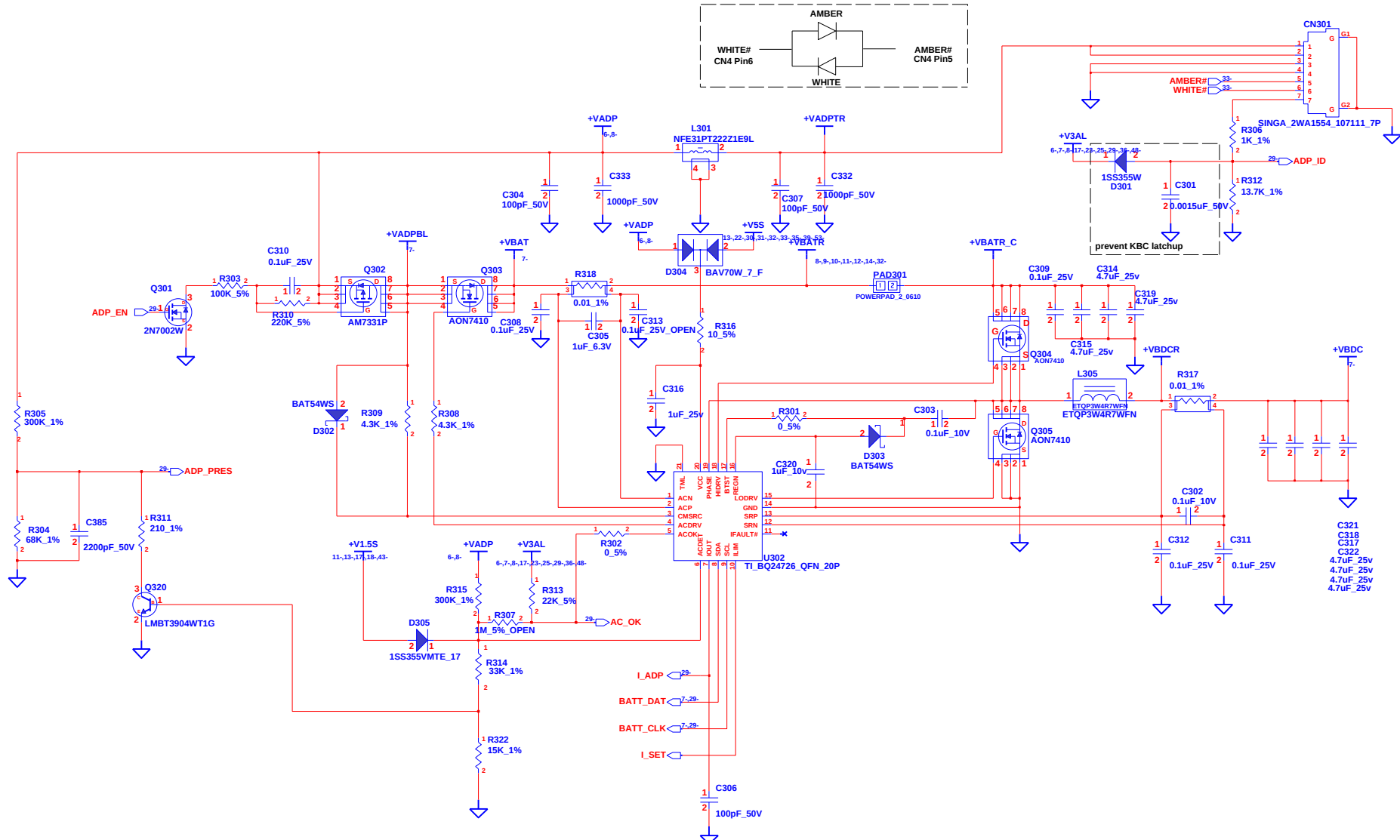
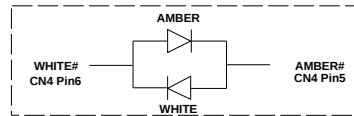
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ROCKY-AMD  
Block Diagram

SIZE CODE CS 1234 DOC. NUMBER REV X01

CHANGE by Ken, Chiang 20-Aug-2010

SHEET 5 OF 60

AC IN

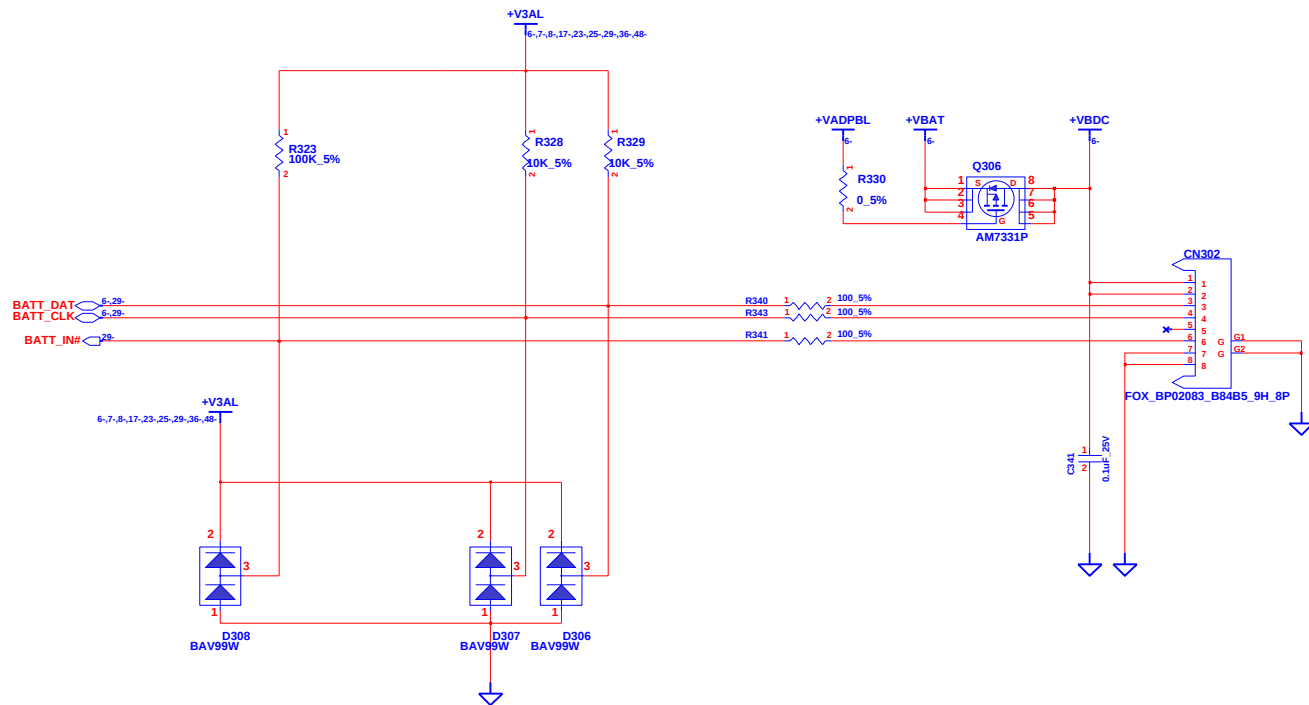


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| ROCKY-AMD<br>DC & BATTERY CHARGER |      |             |     |
| SIZE                              | CODE | DOC. NUMBER | REV |
| A3                                | CS   | 1234        | X01 |

CHANGE by Ken, Chiang 9-Aug-2010

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**INVENTEC**

|                                            |                   |                            |                   |
|--------------------------------------------|-------------------|----------------------------|-------------------|
| TITLE<br><b>ROCKY-AMD<br/>BATTERY CONN</b> |                   |                            |                   |
| SIZE<br><b>A3</b>                          | CODE<br><b>CS</b> | DOC. NUMBER<br><b>1234</b> | REV<br><b>X01</b> |
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CHANGE by *Ken, Chiang* 9-Aug-2010

8

## A



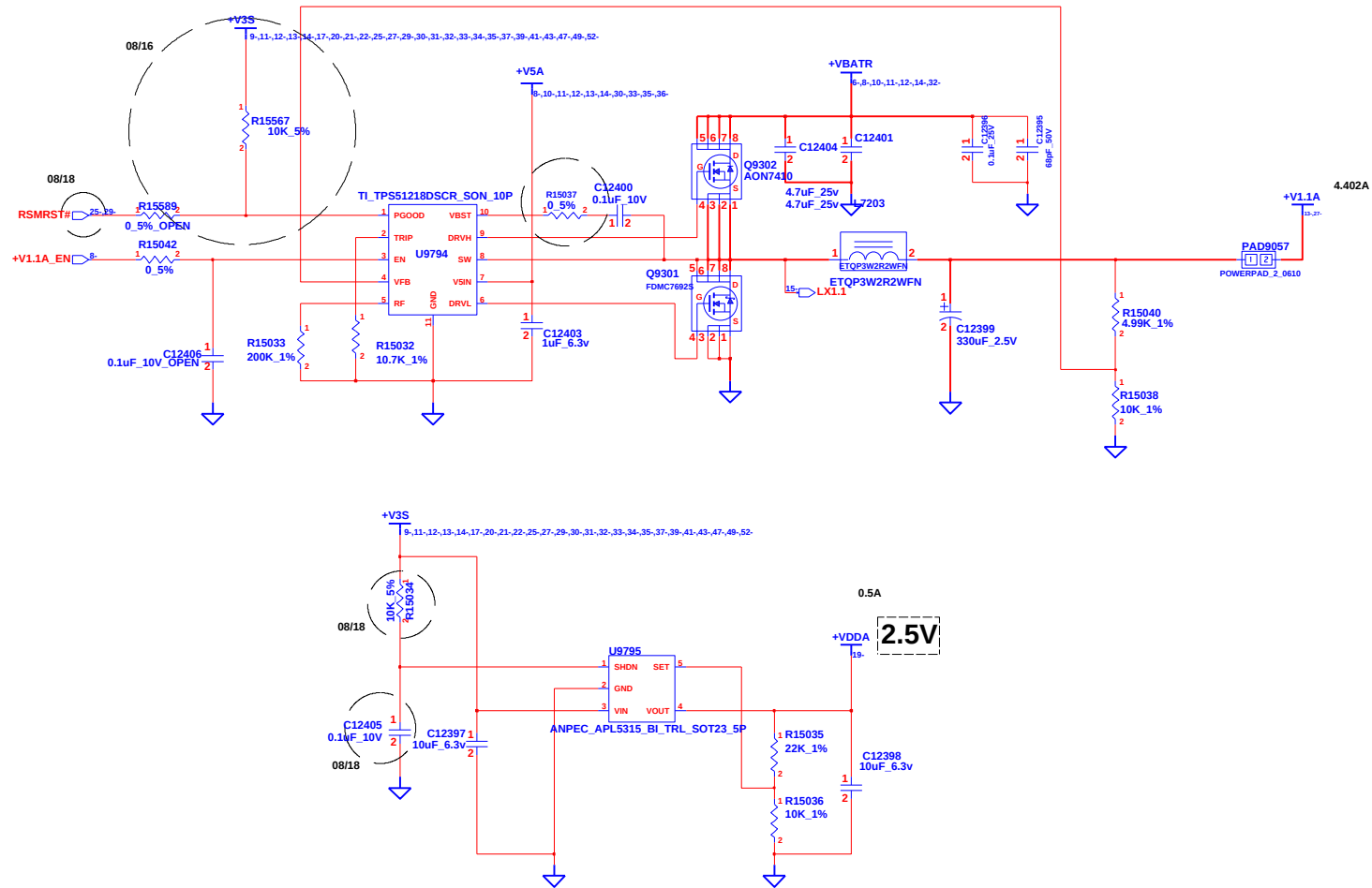
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8

# +V1.1A & +VDDA & +V2.5S



**INVENTEC**

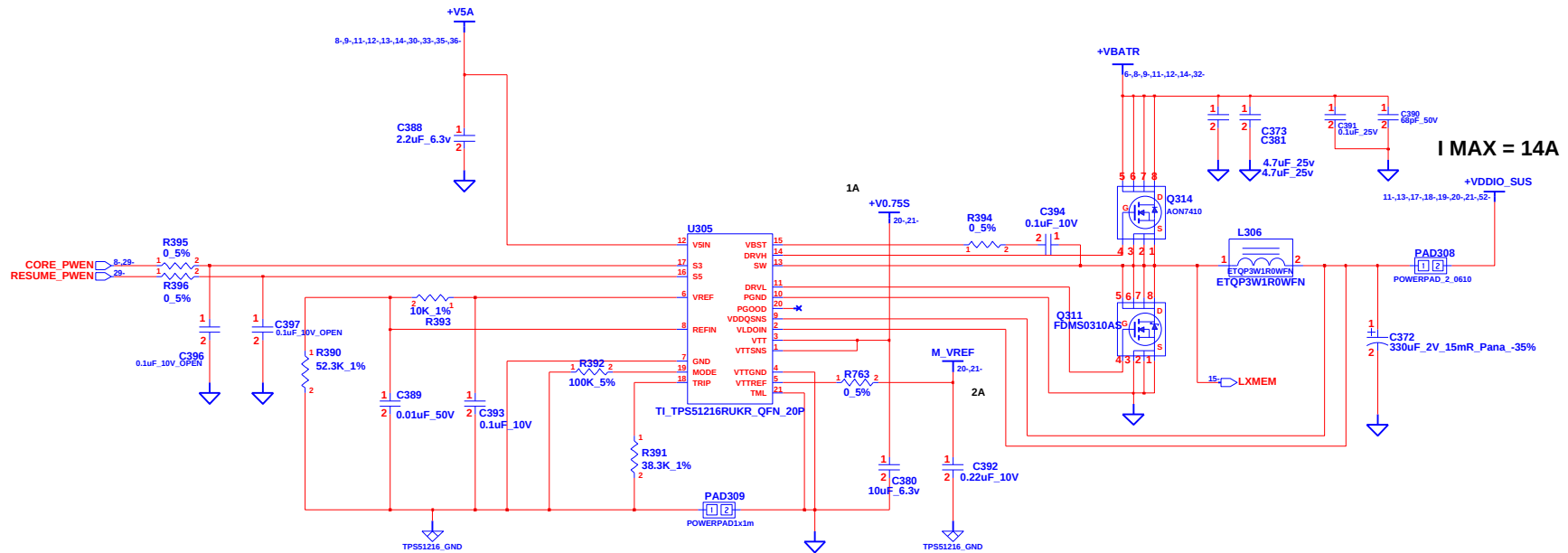
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**ROCKY-AMD**  
+V1.1A & +VDDA & +V2.5S

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| SIZE<br><b>A3</b> | CODE<br><b>CS</b> | DOC. NUMBER<br><b>1234</b> | REV<br><b>X01</b> |
|-------------------|-------------------|----------------------------|-------------------|

CHANGE by **Ken, Chiang** 9-Jun-2010

SHEET **9** OF **60**

# +VCC\_CORE & VDDIO\_SUS & +V0.75S

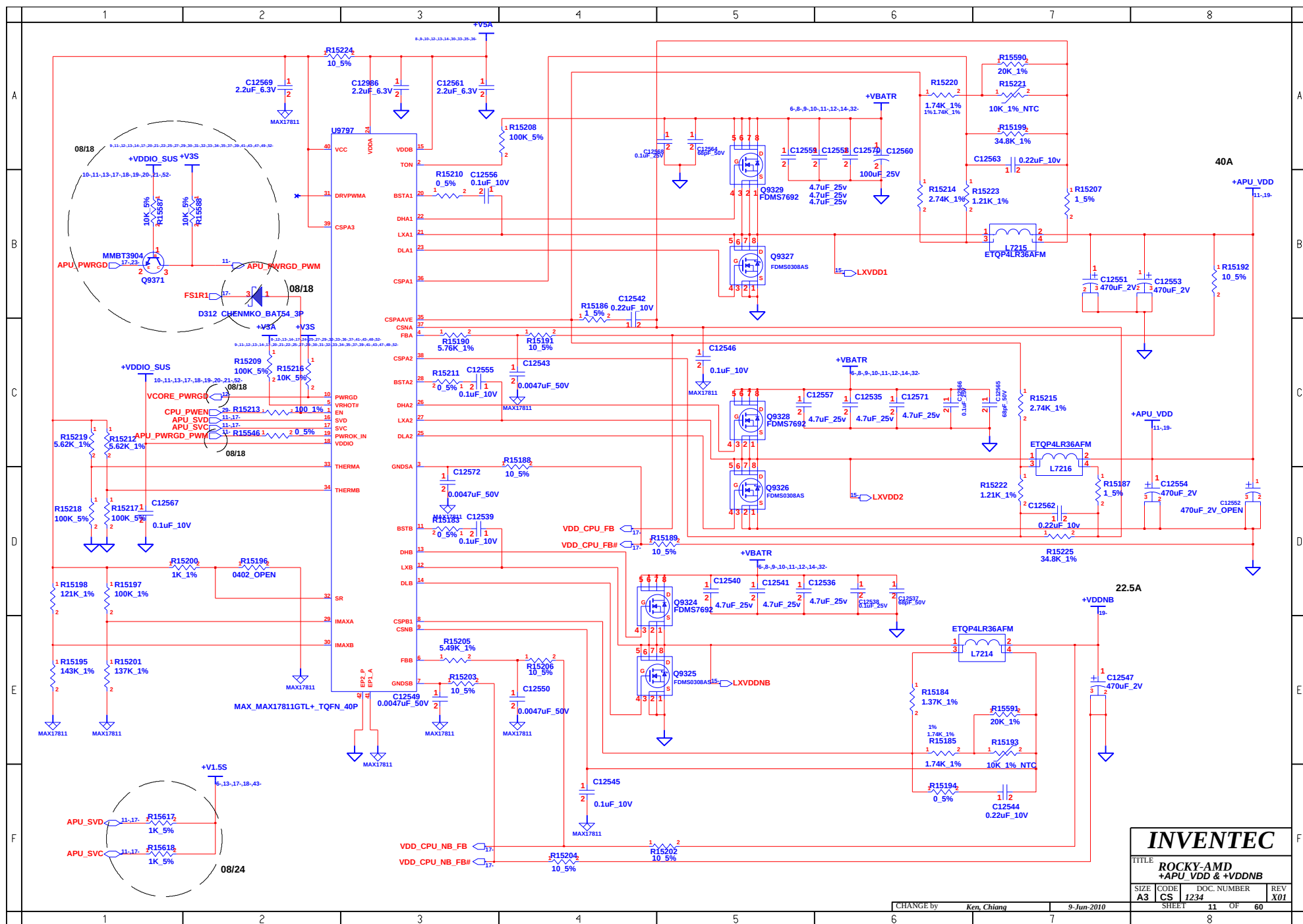


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| ROCKY-AMD                        |      |             |     |
| +VCC_CORE & +VDDIO_SUS & +V0.75S |      |             |     |
| SIZE                             | CODE | DOC. NUMBER | REV |
| A3                               | CS   | 1234        | X01 |

CHANGE by Ken, Chiang 9-Jun-2010

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## 5



|       |      |    |    |
|-------|------|----|----|
| CS    | 1234 |    |    |
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## A

C

F

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|           |                    |            |
|-----------|--------------------|------------|
| CHANGE by | <i>Ken, Chiang</i> | 9-Jun-2010 |
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CS 1234  
SHEET 13 OF 60

## A

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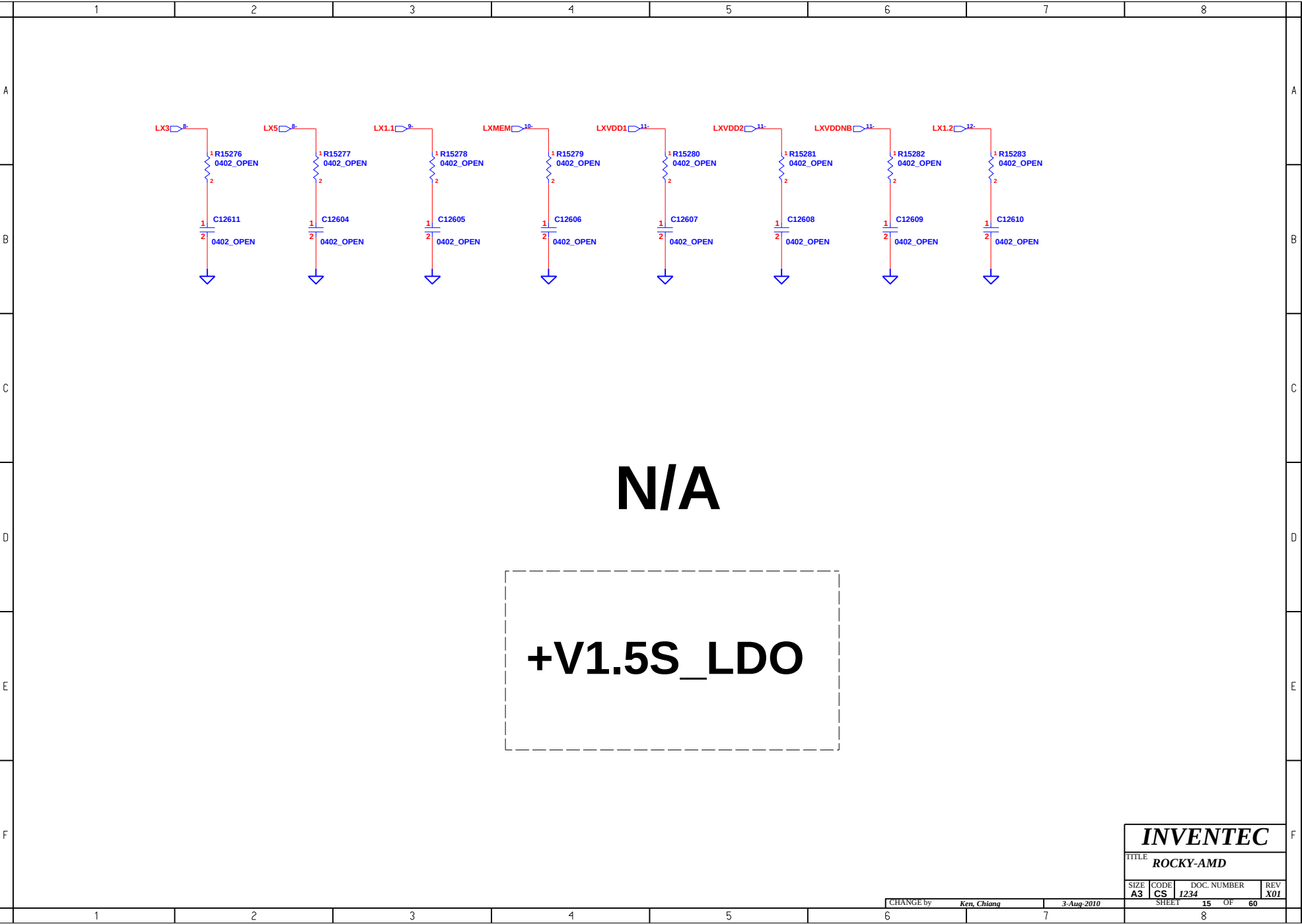
C

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| CHANGE by | <i>Ken, Chiang</i> | 9-Aug-2010 |
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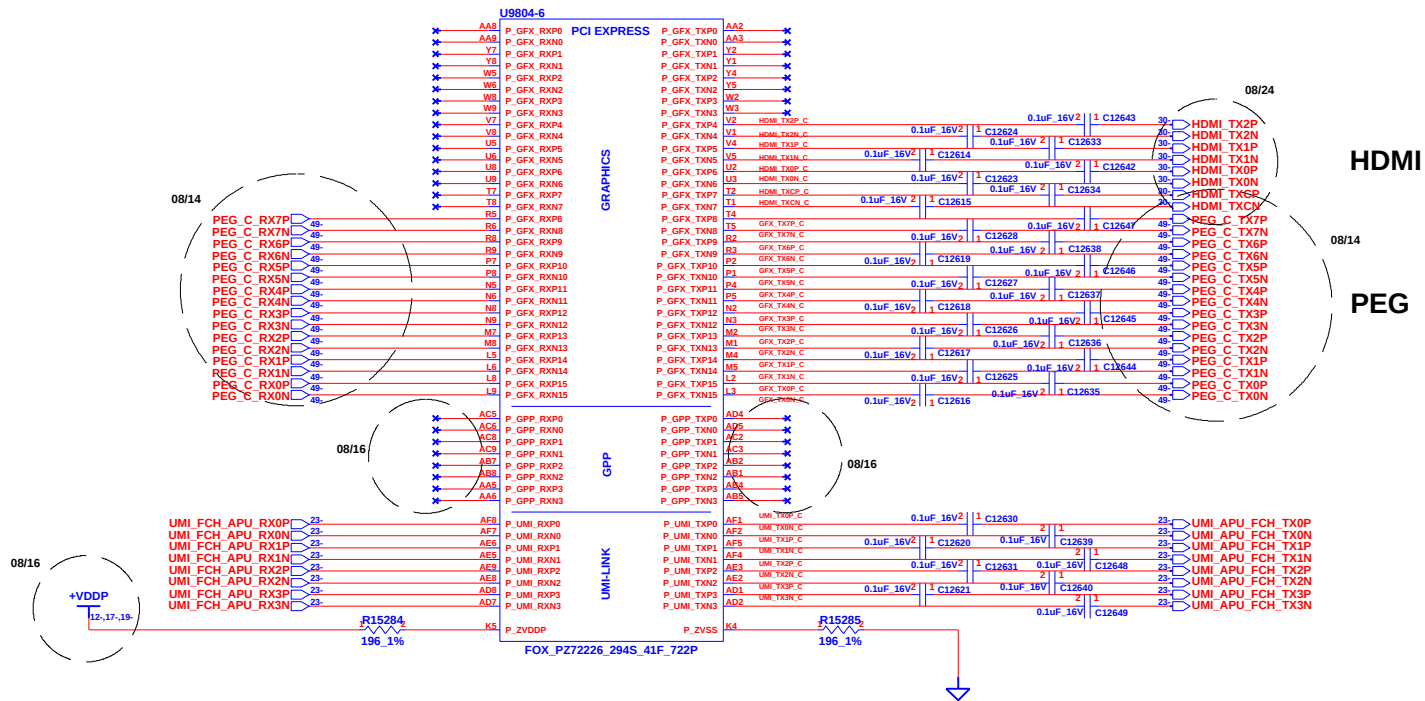
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TITLE  
**ROCKY-AMD**

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| A3   | CS   | 1234        | X01 |

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|-------|--|----|--|-------|--|
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|-------|--|----|--|-------|--|

# CPU



**INVENTEC**

|                             |            |                     |            |
|-----------------------------|------------|---------------------|------------|
| TITLE<br>ROCKY-AMD<br>CPU-1 |            |                     |            |
| SIZE<br>A3                  | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
| SHEET                       |            | 16                  | OF 60      |

CHANGE by Ken, Chiang 9-Jun-2010

# CPU

To VGA

To FCH

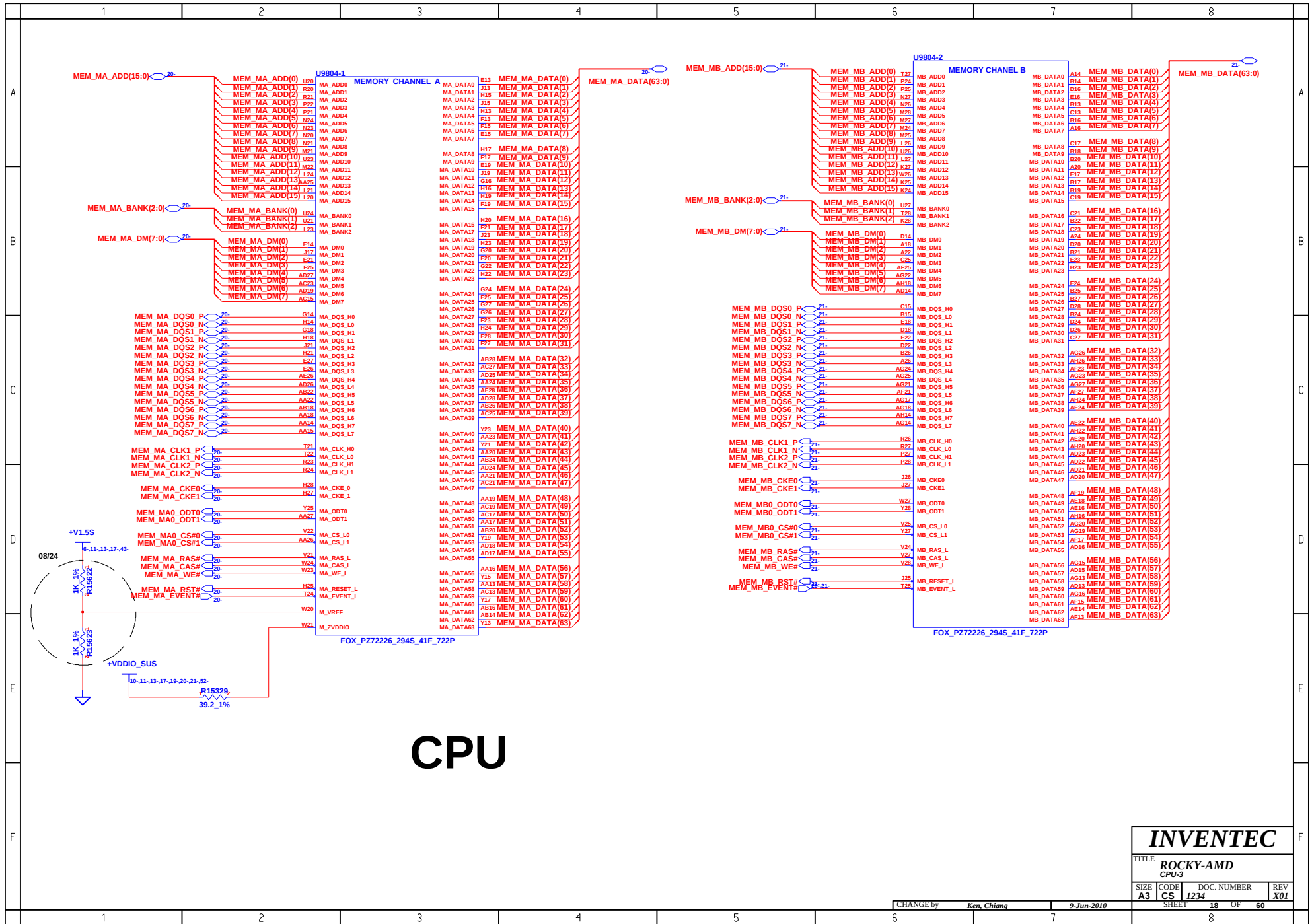
## HDT Header

**INVENTEC**

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|-------|------|-------------|-----------|----|--|
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|       |      |             | CPU-2     |    |  |
| SIZE  | CODE | DOC. NUMBER | REV       |    |  |
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| SHEET |      | 17          | OF        | 60 |  |

CHANGE by Ken, Chiang

9-Jun-2010

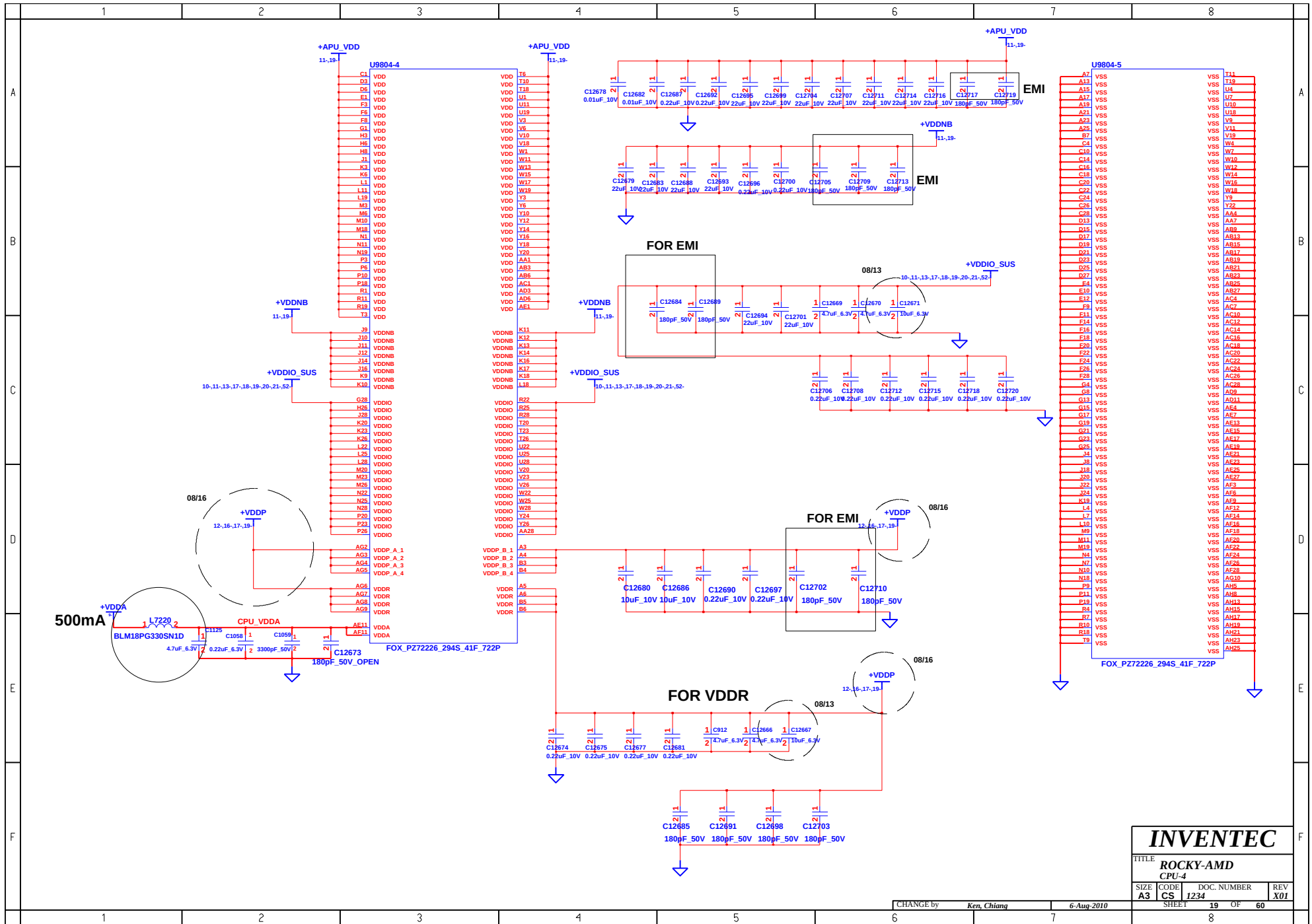


CPU

INVENTEC

|                             |            |                     |            |
|-----------------------------|------------|---------------------|------------|
| TITLE<br>ROCKY-AMD<br>CPU-3 |            |                     |            |
| SIZE<br>A3                  | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
| SHEET                       |            | 18 OF               | 60         |

CHANGE by Ken, Chiang 9-Jun-2010



**SO-DIMM0**

MEM\_MA\_ADD(15:0) MEM\_MA\_DATA(63:0)

U9805-1 U9805-2

MEM\_MA\_BANK(2:0) MEM\_MA\_CS#0 MEM\_MA\_CLK1\_P MEM\_MA\_CLK1\_N MEM\_MA\_CLK2\_P MEM\_MA\_CLK2\_N MEM\_MA\_CKE0 MEM\_MA\_CKE1 MEM\_MA\_CAS# MEM\_MA\_RAS# MEM\_MA\_WE# SA0\_DIM0 SA1\_DIM0 SB\_3S\_SMLCLK SB\_3S\_SMDATA MEM\_MA\_ODT0 MEM\_MA\_ODT1 SDDR-DQ MEM\_MA\_DM(7:0) MEM\_MA\_DQS0\_P MEM\_MA\_DQS1\_P MEM\_MA\_DQS2\_P MEM\_MA\_DQS3\_P MEM\_MA\_DQS4\_P MEM\_MA\_DQS5\_P MEM\_MA\_DQS6\_P MEM\_MA\_DQS7\_P MEM\_MA\_DQS0\_N MEM\_MA\_DQS1\_N MEM\_MA\_DQS2\_N MEM\_MA\_DQS3\_N MEM\_MA\_DQS4\_N MEM\_MA\_DQS5\_N MEM\_MA\_DQS6\_N MEM\_MA\_DQS7\_N

FOX\_AS0A626\_U4SG\_7H\_204P

Layout NOTE: Place these Caps near SO-DIMM0 power pin

+VDDIO\_SUS PWR-B 10-11,13-17,18-19,20-21,52

C971 C932 C972 C974 C1017 C934 C1016 C1018

100uF\_6.3V\_OPEN 22uF\_6.3V 22uF\_6.3V 22uF\_6.3V 0.1uF\_10V 0.1uF\_10V 0.1uF\_10V 0.1uF\_10V

+V3S 9-11,12,13,14,17,20-21,22-25,27,29-30,31-32,33-34,35-37,39,41,43,47,49,52

C874 C875 C1121 C1120

1uF\_10V 0.1uF\_10V 2.2uF\_6.3V 0.1uF\_10V

M\_VREF M\_VREF

RWP6-21 RWP6-21

MEM\_MA\_EVENT# MEM\_MA\_RST# CPU1\_180 SDDR-Ctrl\_38

VREF\_DQ VREF\_CA

+V0.75S 10-21 PWR-A 1.5A

Note:Place CA100 on common path for both DIMM's

C873 C872

0.1uF\_10V 4.7uF\_6.3V

Place these caps close to VTT1 and VTT2

NOTE:

IF SA0\_DIM0=0, SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0xA0  
SO-DIMMA TS ADDRESS IS 0x30

IF SA0\_DIM0=1, SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0xA2  
SO-DIMMA TS ADDRESS IS 0x32

APU\_MEM#HOT# 08/24

MMBT3904 Q9348 MMBT3904 Q9349

INVENTEC

TITLE ROCKY-AMD SO-DIMM0

SIZE A3 CODE CS DOC. NUMBER 1234 REV X01

SHEET 20 OF 60

CHANGE by Ken, Chiang 30-Jul-2010

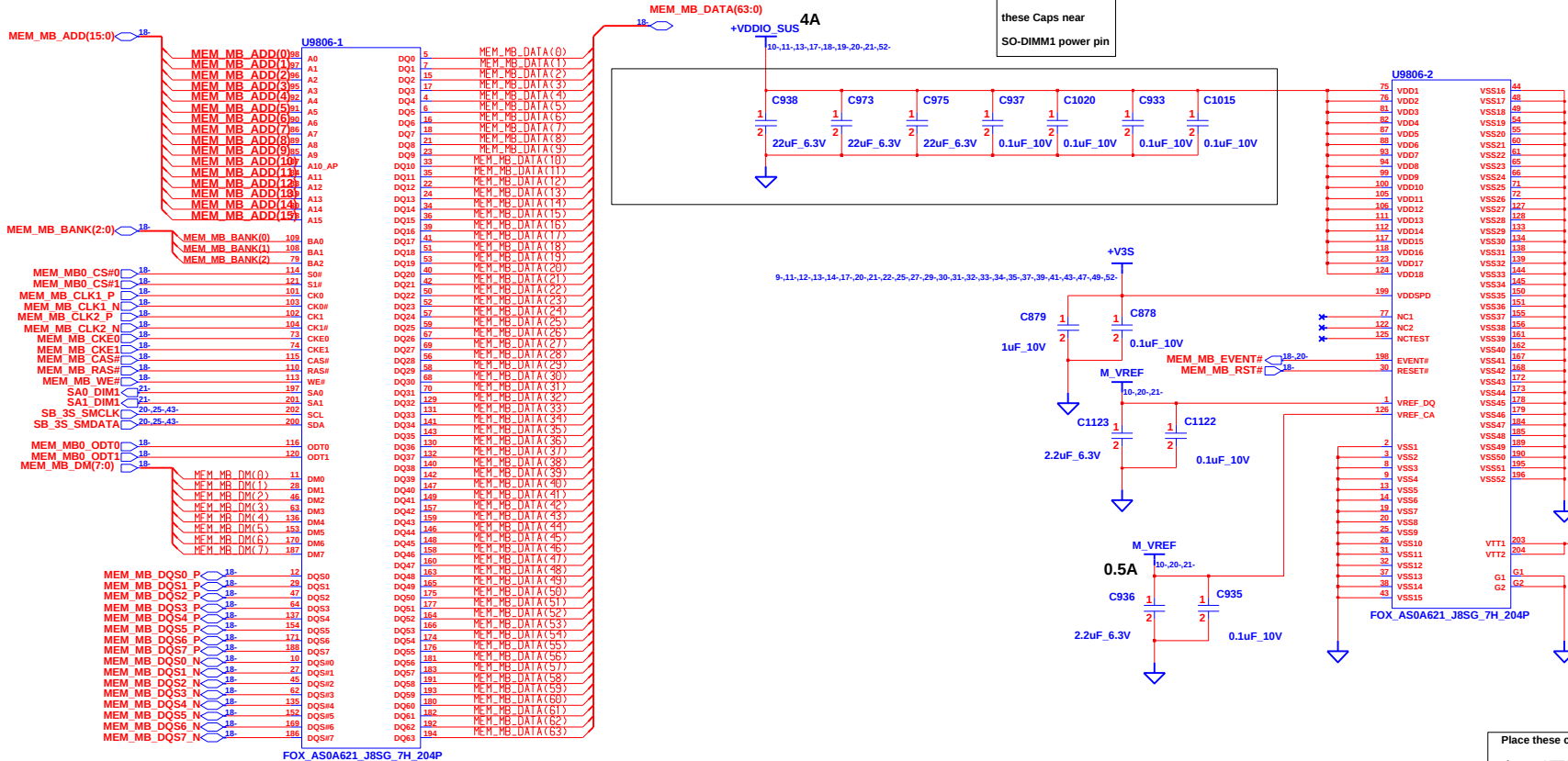
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| TITLE                               |           |             |            |
| <b>ROCKY-AMD</b><br><b>SO-DIMM0</b> |           |             |            |
| SIZE                                | CODE      | DOC. NUMBER | REV        |
| <b>A3</b>                           | <b>CS</b> | <b>1234</b> | <b>X01</b> |
| SHEET 20 OF 60                      |           |             |            |

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| CHANGE by | <i>Ken, Chiang</i> | 30-Jul-2010 |
|-----------|--------------------|-------------|

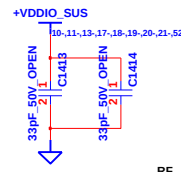
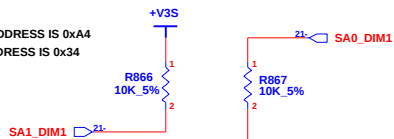
34 OF 60

# SO-DIMM1

Layout Note: Place  
these Caps near  
SO-DIMM1 power pin



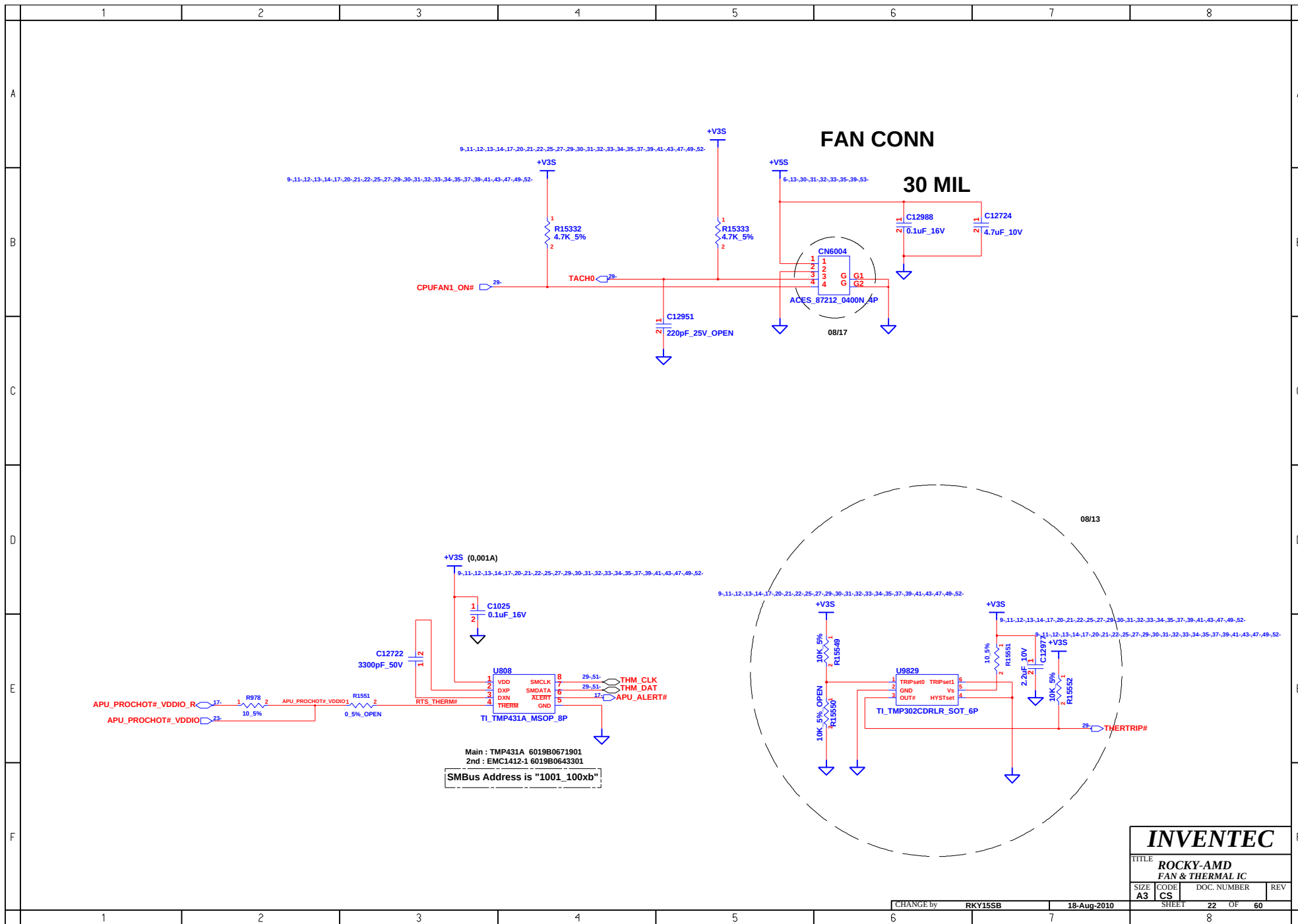
NOTE:  
SO-DIMM SPD ADDRESS IS 0xA4  
SO-DIMM TS ADDRESS IS 0x34



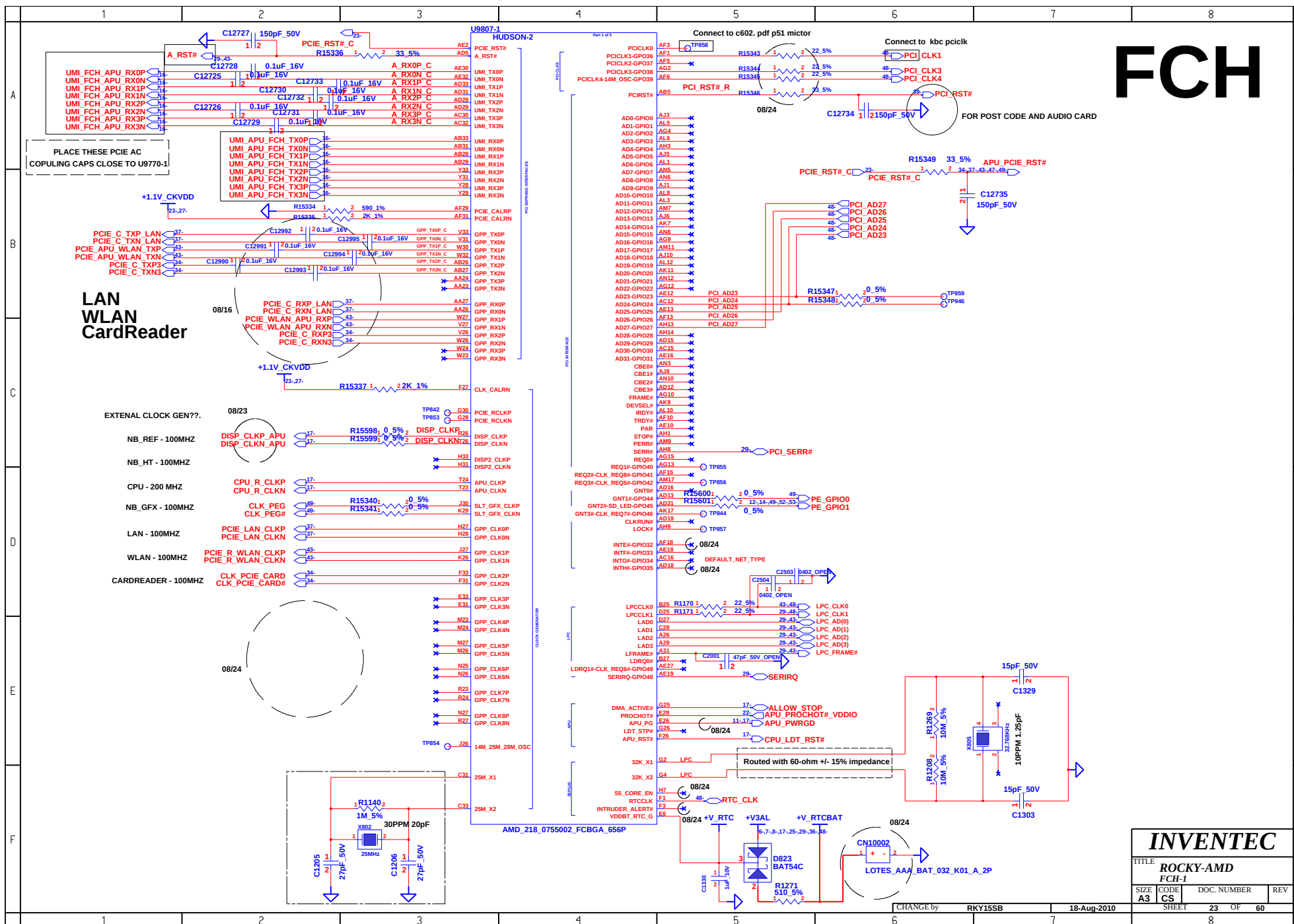
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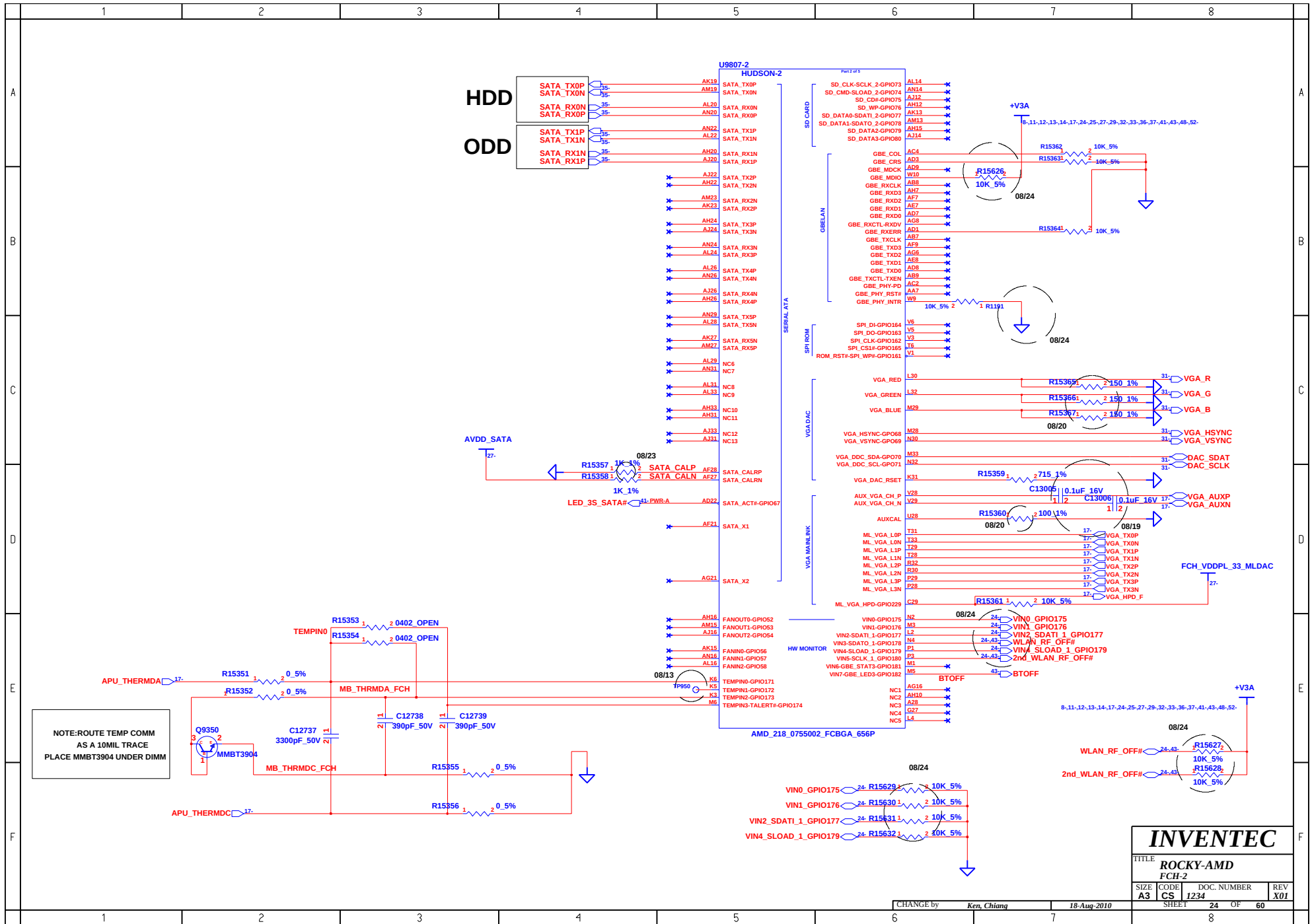
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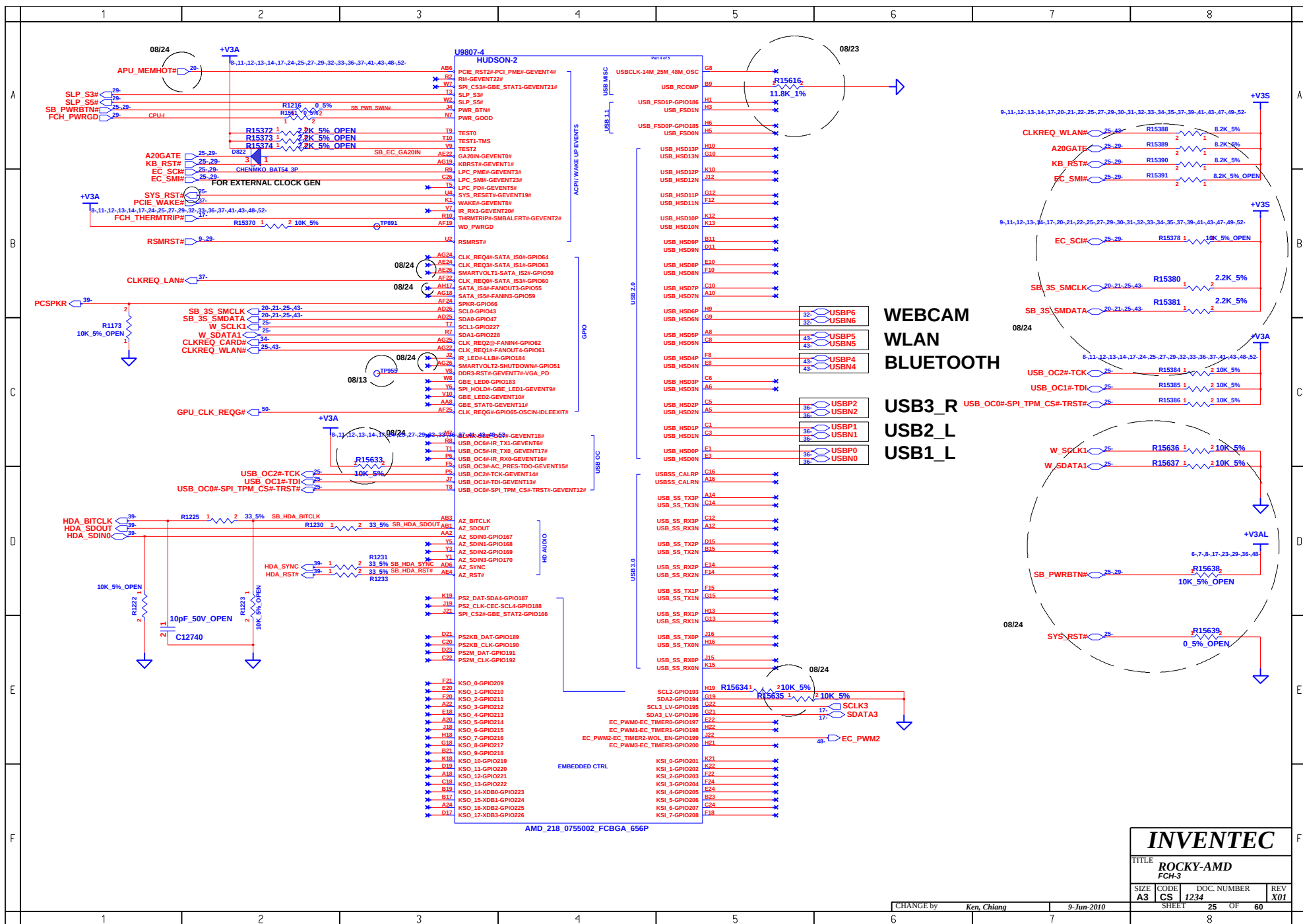
CHANGE by Ken, Chiang 18-Aug-2010



# FCH

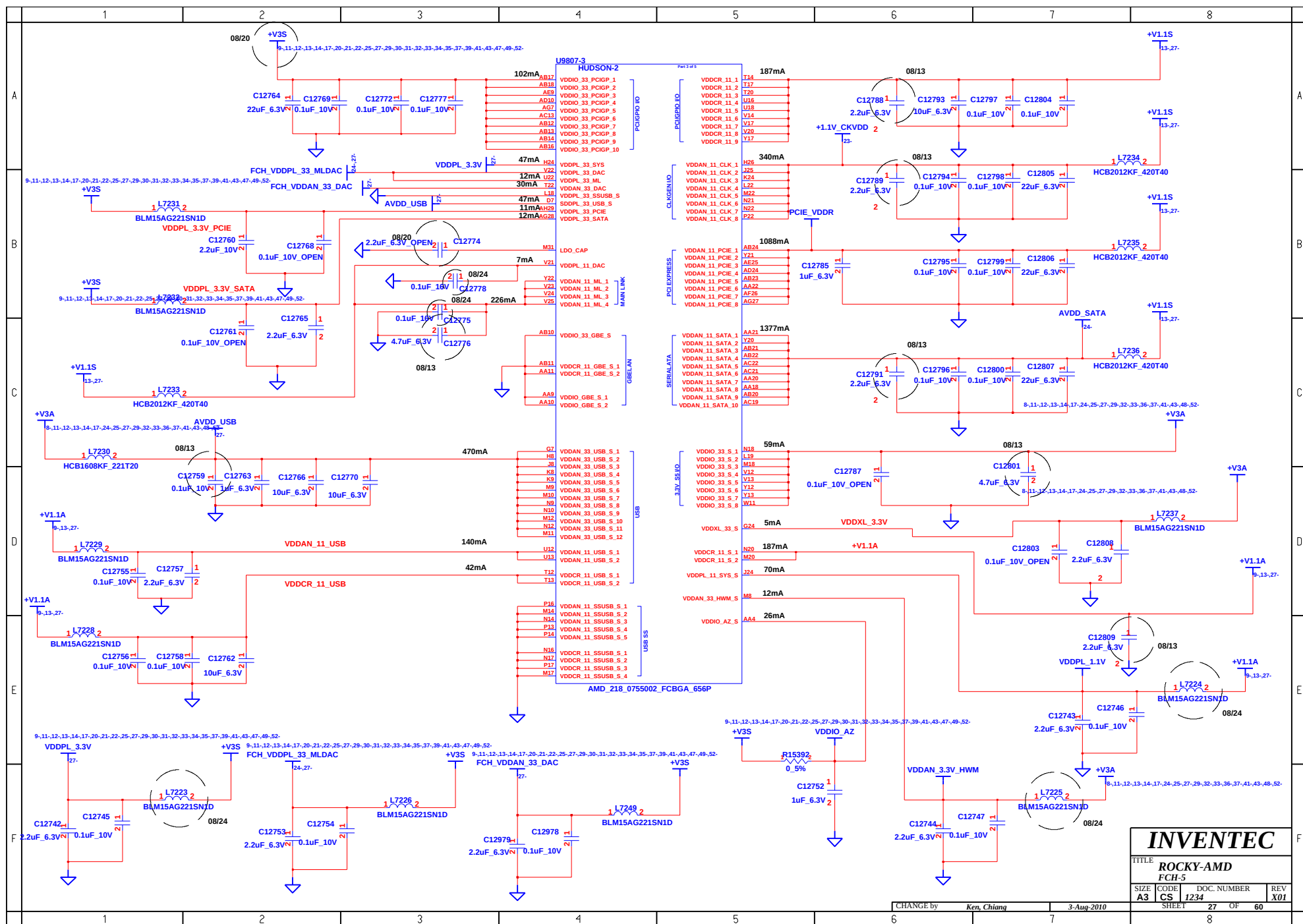


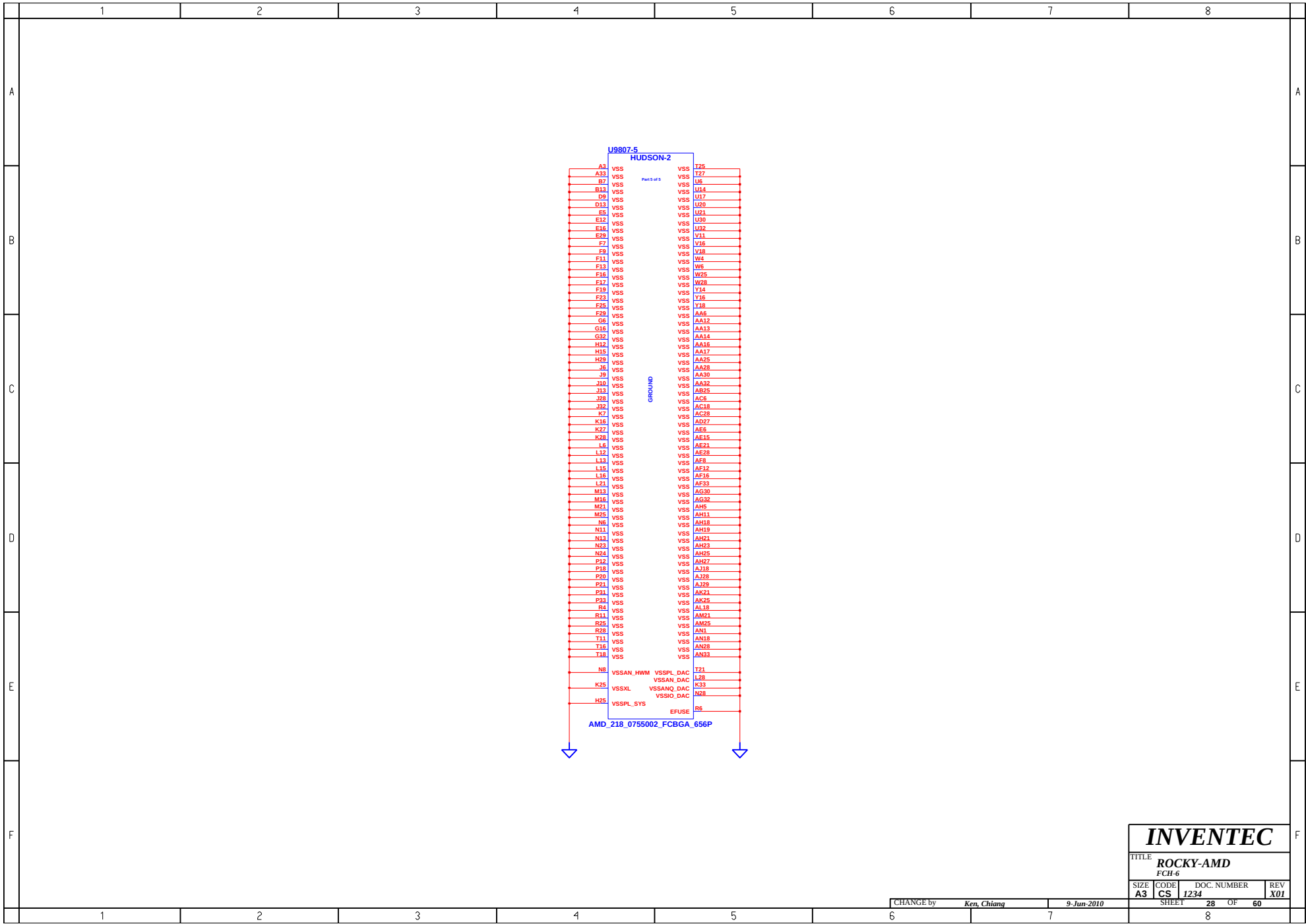




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|---|---|---|---|---|---|------------------------------|---|-------------|---|
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| A |   |   |   |   |   |                              |   |             | A |
| B |   |   |   |   |   |                              |   |             | B |
| C |   |   |   |   |   |                              |   |             | C |
| D |   |   |   |   |   |                              |   |             | D |
| E |   |   |   |   |   |                              |   |             | E |
| F |   |   |   |   |   |                              |   |             | F |
|   |   |   |   |   |   | CHANGE by <i>Ken, Chiang</i> |   | 24-Aug-2010 |   |
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|                                        |                   |                            |                   |
|----------------------------------------|-------------------|----------------------------|-------------------|
| <b>INVENTEC</b>                        |                   |                            |                   |
| TITLE <b>ROCKY-AMD</b><br><b>FCH-4</b> |                   |                            |                   |
| SIZE<br><b>A3</b>                      | CODE<br><b>CS</b> | DOC. NUMBER<br><b>1234</b> | REV<br><b>X01</b> |
| SHEET                                  |                   | 26                         | OF 60             |





# INVENTEC

TITLE  
**ROCKY-AMD**  
FCH-6

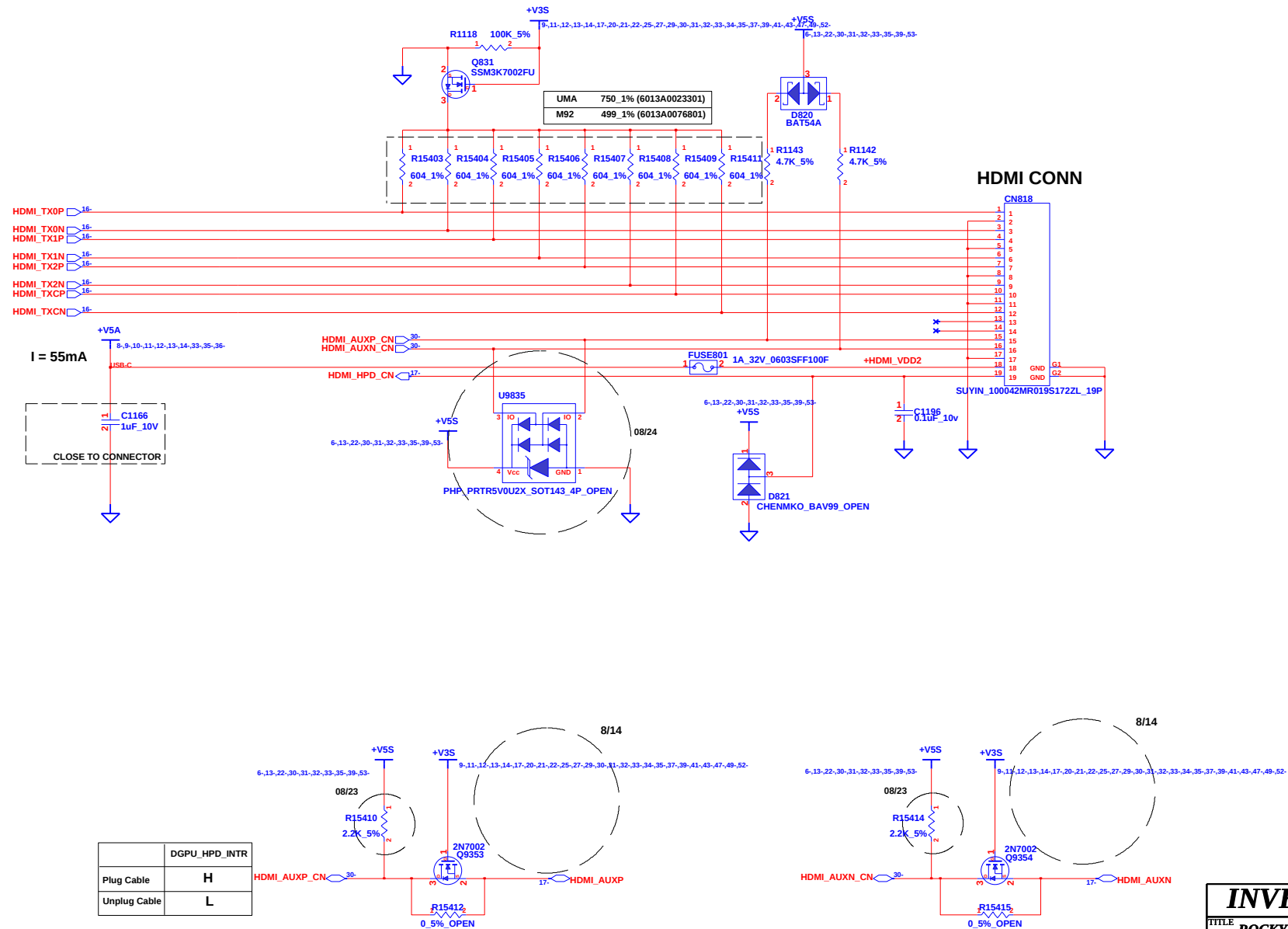
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|------|------|-------------|-----|
| A3   | CS   | 1234        | X01 |

CHANGE by *Ken, Chiang* 9-Jun-2010

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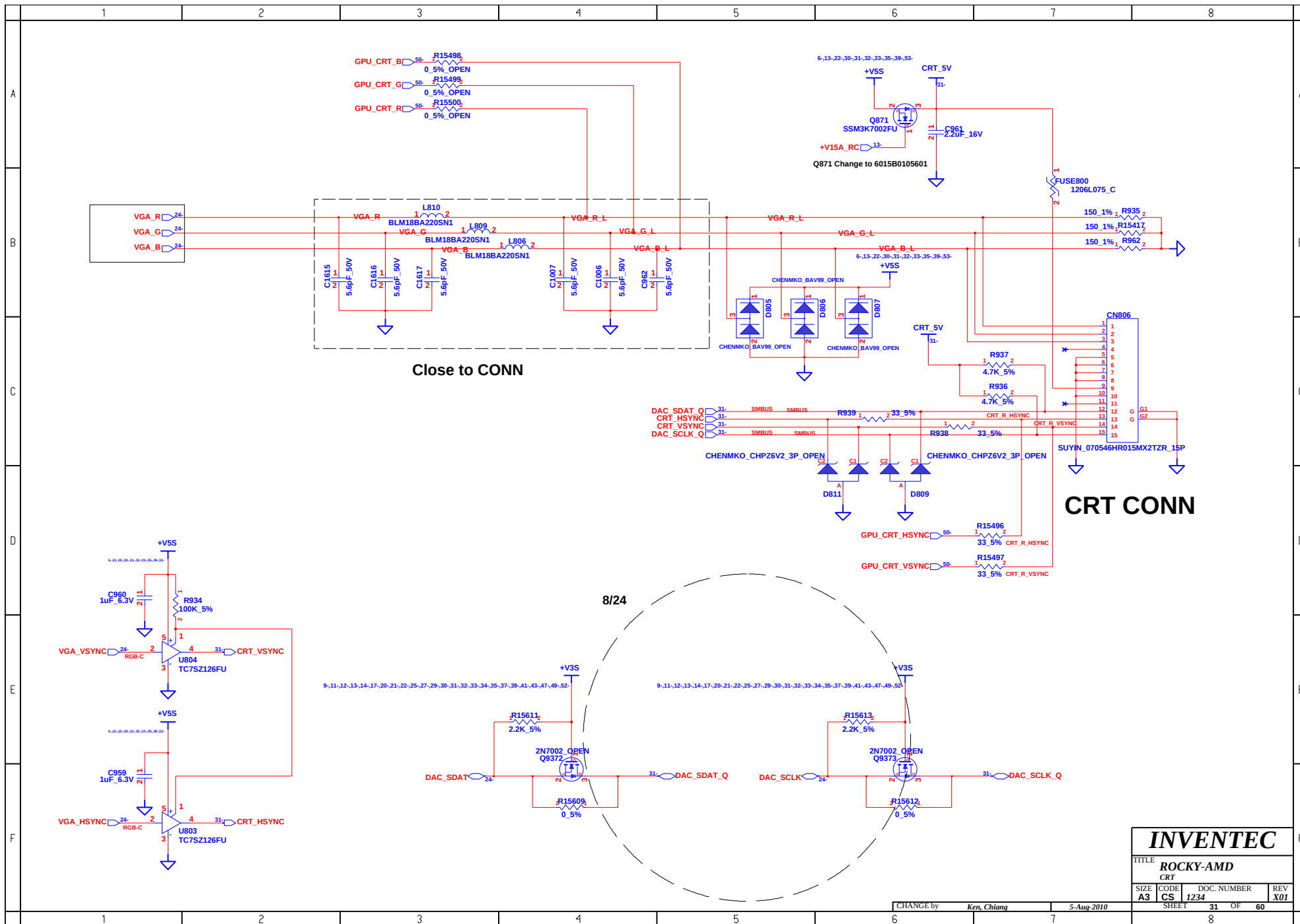
# HDMI



|              |                |
|--------------|----------------|
|              | DGPU_HPDC_INTR |
| Plug Cable   | H              |
| Unplug Cable | L              |

|                            |            |                     |            |
|----------------------------|------------|---------------------|------------|
| <b>INVENTEC</b>            |            |                     |            |
| TITLE<br>ROCKY-AMD<br>HDMI |            |                     |            |
| SIZE<br>A3                 | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
| SHEET 30 OF 60             |            |                     |            |

CHANGE by Ken Chiang 9-Jun-2010



**INVENTEC**

TITLE  
**ROCKY-AMD**  
CRT

| SIZE | CODE | DOC. NUMBER | REV |
|------|------|-------------|-----|
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CHANGE by *Ken Chiang* 5-Aug-2010

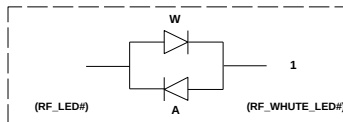
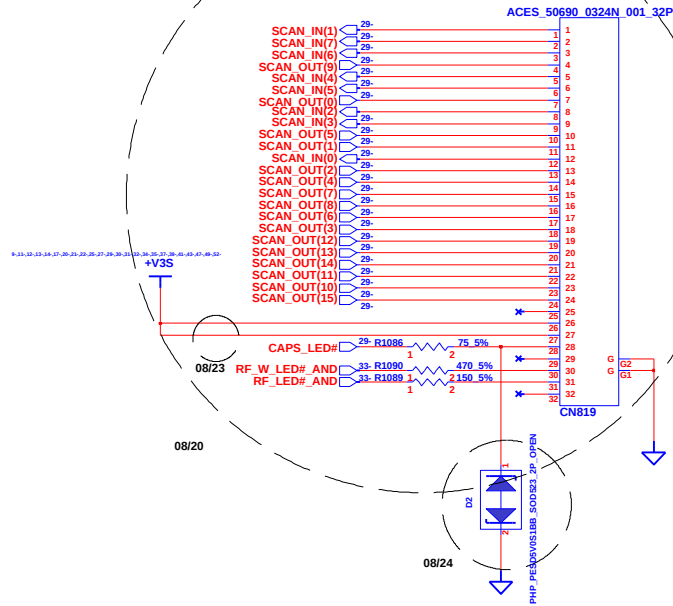
SHEET 31 OF 60

|                  |           |             |       |
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| <b>ROCKY-AMD</b> |           |             |       |
| LVDS             |           |             |       |
| SIZE             | CODE      | DOC. NUMBER | REV   |
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| SHEET            |           | 32          | OF 60 |
| 8                |           |             |       |

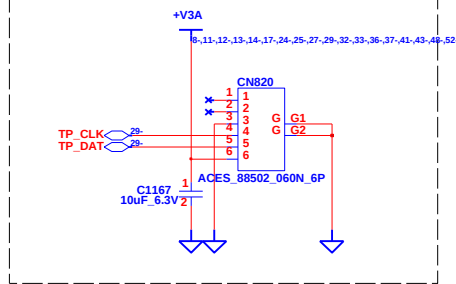
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| CHANGE by | RKY15SB | 9-Aug-2010 |
|-----------|---------|------------|

CS  
SHEET 32 OF 60

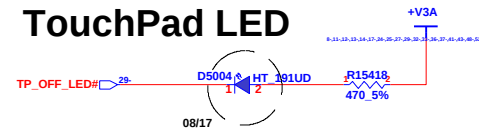
## KeyBoard CONN (32 pin)



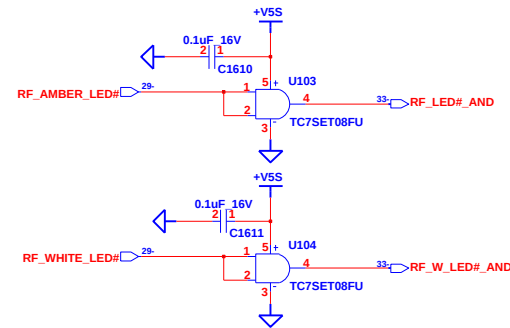
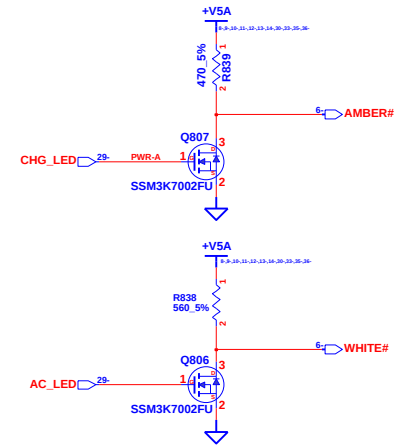
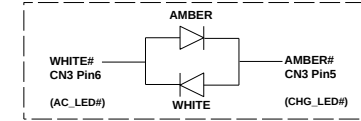
## TouchPad on MB



## TouchPad LED



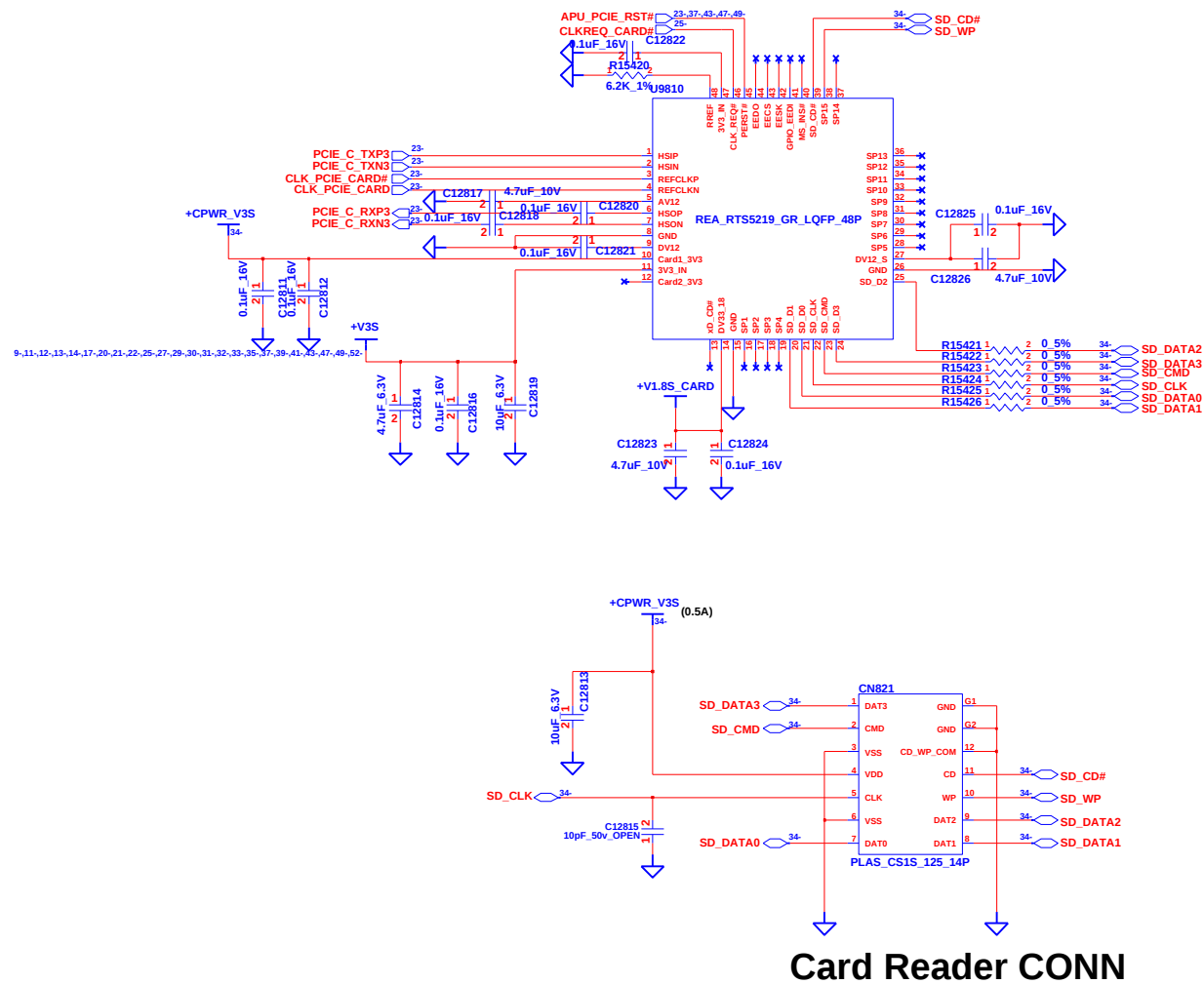
## DC-JACK LED



**INVENTEC**

|                              |      |             |       |
|------------------------------|------|-------------|-------|
| TITLE                        |      |             |       |
| ROCKY-AMD                    |      |             |       |
| KEYBOARD CONN & Indicate LED |      |             |       |
| SIZE                         | CODE | DOC. NUMBER | REV   |
| A3                           | CS   | 1234        | X01   |
| SHEET                        |      | 33          | OF 60 |

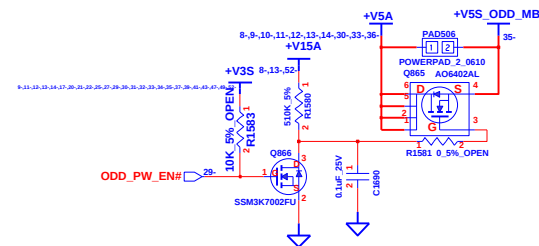
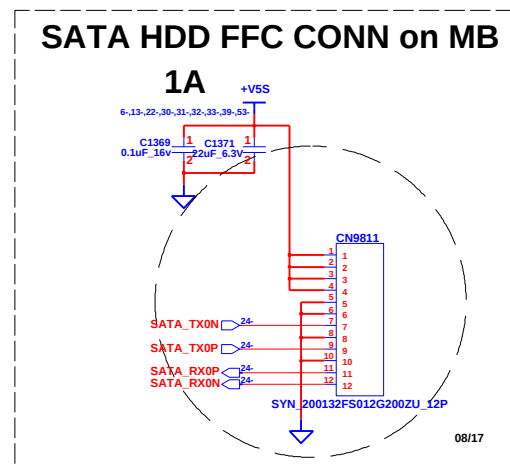
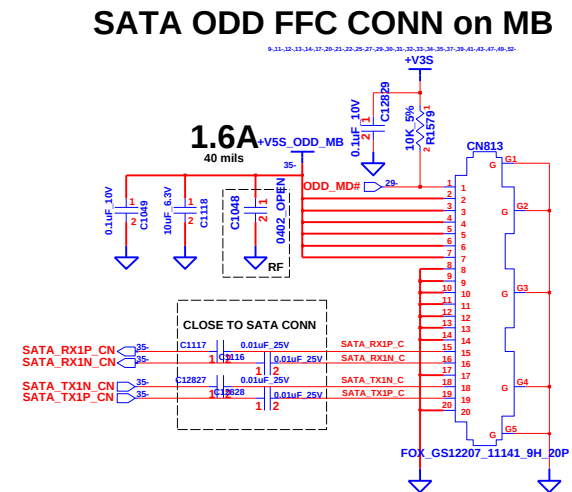
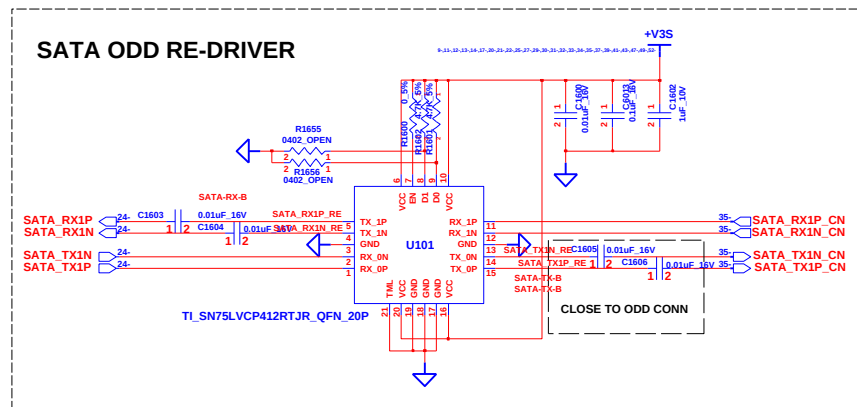
CHANGE by Ken, Chiang 5-Aug-2010



|                       |      |             |
|-----------------------|------|-------------|
| Without card          | CD — | CD / WP Com |
|                       | WP — | CD / WP Com |
| Inserted card(unlock) | CD — | CD / WP Com |
|                       | WP — | CD / WP Com |
| Inserted card(lock)   | CD — | CD / WP Com |
|                       | WP — | CD / WP Com |

Card Reader CONN

| INVENTEC                         |            |                     |            |
|----------------------------------|------------|---------------------|------------|
| TITLE<br>ROCKY-AMD<br>CARDREADER |            |                     |            |
| SIZE<br>A3                       | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
| CHANGE by<br>Ken, Chiang         |            | 5-Aug-2010          |            |
| SHEET                            |            | 34                  | OF 60      |
|                                  |            | 8                   |            |



**INVENTEC**

|       |                                         |
|-------|-----------------------------------------|
| TITLE | <b>ROCKY-AMD</b><br>SATA HDD & ODD CONN |
|-------|-----------------------------------------|

|                   |                   |                            |                   |
|-------------------|-------------------|----------------------------|-------------------|
| SIZE<br><b>A3</b> | CODE<br><b>CS</b> | DOC. NUMBER<br><b>1234</b> | REV<br><b>X01</b> |
|-------------------|-------------------|----------------------------|-------------------|

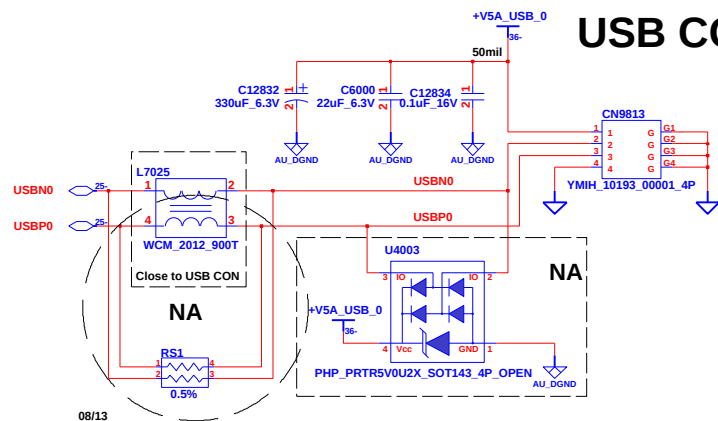
|           |                    |                   |
|-----------|--------------------|-------------------|
| CHANGE by | <i>Ken, Chiang</i> | <i>5-Aug-2010</i> |
|-----------|--------------------|-------------------|

---

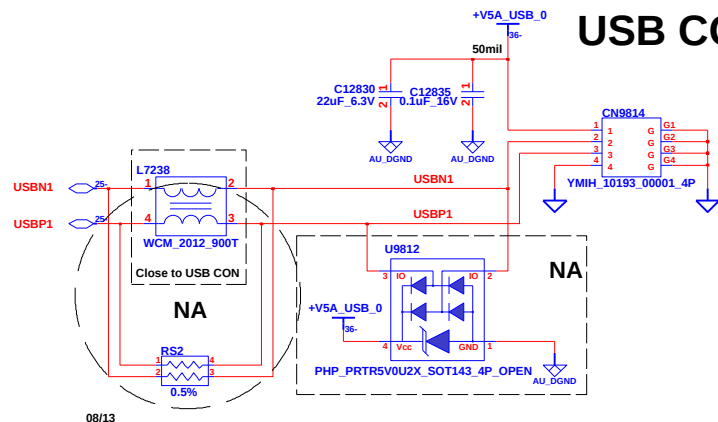
8

# USB

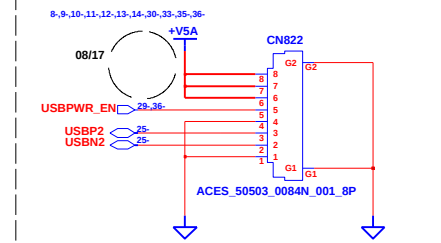
## USB CONN01



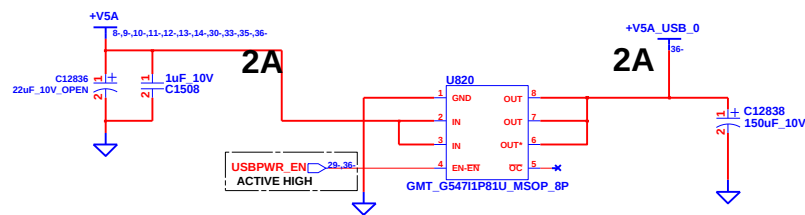
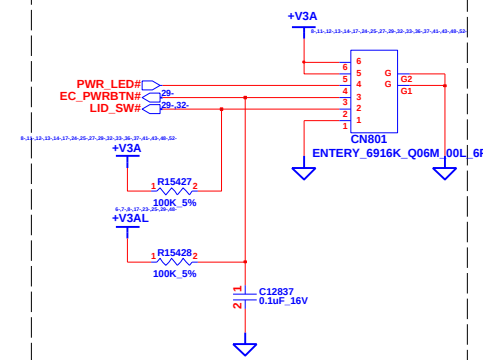
## USB CONN02



## USB BOARD on MB



## PBN on MB

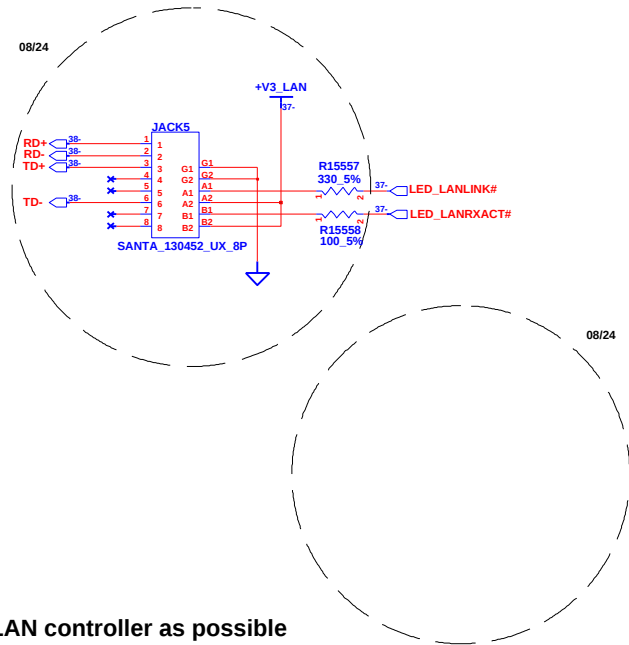
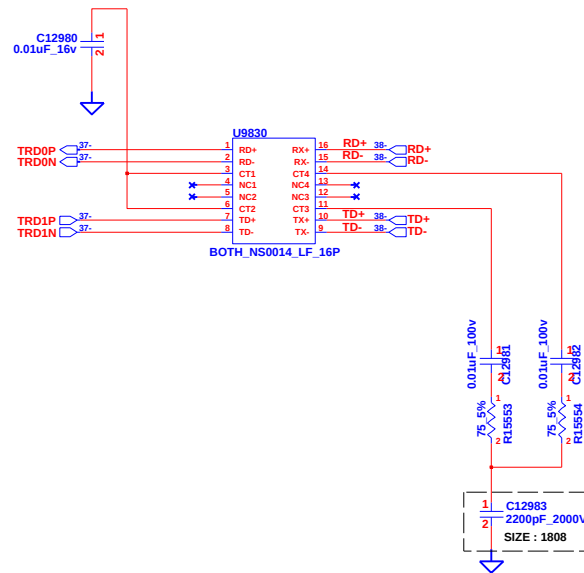


**INVENTEC**

|           |      |             |       |
|-----------|------|-------------|-------|
| TITLE     |      |             |       |
| ROCKY-AMD |      |             |       |
| USB CONN  |      |             |       |
| SIZE      | CODE | DOC. NUMBER | REV   |
| A3        | CS   | 1234        | X01   |
| SHEET     |      | 36          | OF 60 |

CHANGE by Ken Chiang 5-Aug-2010

|                         |           |             |            |
|-------------------------|-----------|-------------|------------|
| TITLE                   |           |             |            |
| <b>ROCKY-AMD</b><br>LAN |           |             |            |
| SIZE                    | CODE      | DOC. NUMBER | REV        |
| <b>A3</b>               | <b>CS</b> | <b>1234</b> | <b>X01</b> |



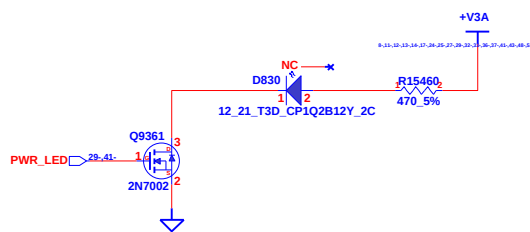
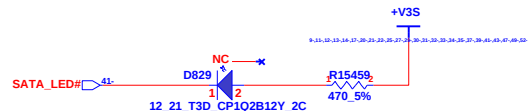
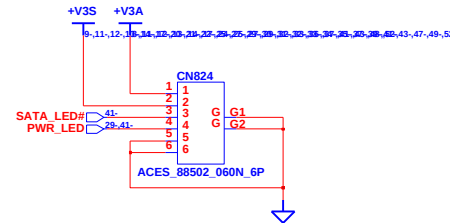
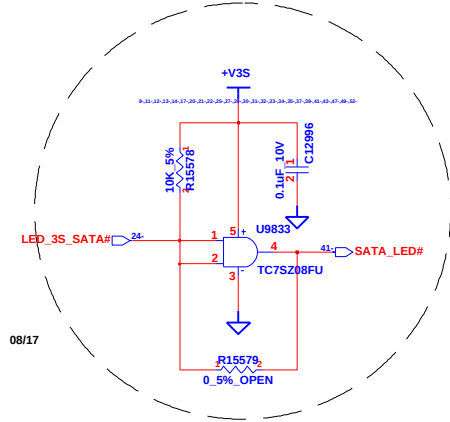
LAYOUT NOTE : Place termination resistors and caps as close to LAN controller as possible

| INVENTEC                        |            |                     |            |
|---------------------------------|------------|---------------------|------------|
| TITLE<br>ROCKY-AMD<br>RJ45 CONN |            |                     |            |
| SIZE<br>A3                      | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
| CHANGE by<br>Ken, Chiang        |            | 5-Aug-2010          |            |
| SHEET<br>38                     |            | OF<br>60            |            |
| 8                               |            |                     |            |



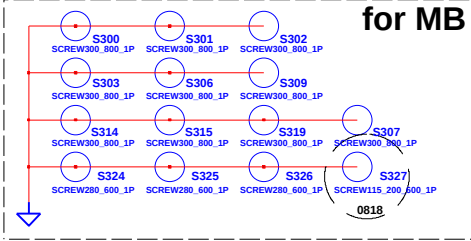
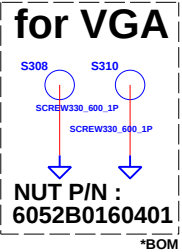
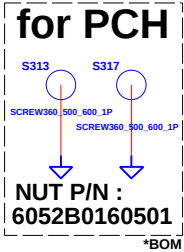
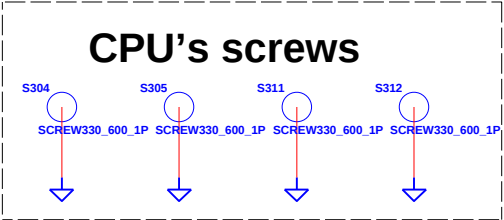
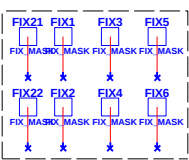


# SATA LED & PWR LED



| INVENTEC           |      |             |            |
|--------------------|------|-------------|------------|
| TITLE              |      |             |            |
| ROCKY-AMD          |      |             |            |
| SATA LED & PWR LED |      |             |            |
| SIZE               | CODE | DOC. NUMBER | REV        |
| A3                 | CS   | 1234        | X01        |
| CHANGE by          |      | Ken, Chiang | 4-Aug-2010 |
| SHEET              |      | 41          | OF 60      |

|   |   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|---|
|   | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |   |
| A |   |   |   |   |   |   |   |   | A |
| B |   |   |   |   |   |   |   |   | B |
| C |   |   |   |   |   |   |   |   | C |
| D |   |   |   |   |   |   |   |   | D |
| E |   |   |   |   |   |   |   |   | E |
| F |   |   |   |   |   |   |   |   | F |
|   | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |   |



**INVENTEC**

|                             |            |                     |            |
|-----------------------------|------------|---------------------|------------|
| TITLE<br>ROCKY-AMD<br>SCREW |            |                     |            |
| SIZE<br>A3                  | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |

CHANGE by Ken, Chiang 9-Jun-2010

SHEET 42 OF 60

**Power Requirements Table:**

|        | Peak (mA) | Normal (mA) |
|--------|-----------|-------------|
| +V3.3A | 2,750mA   | 1,100mA     |
| +V1.5  | 500mA     | 375mA       |

**Pinout and Connections:**

- WLAN Priority:** TP947, TP800, R885, 0.5% OPEN.
- Power Connections:** +V3S, +V1.5S, +V3S.
- Capacitors:** C924 (4.7uF, 6.3V), C892 (1uF, 6.3V), C922 (0.1uF, 16V), C891 (47pF, 50V), C921 (47pF, 50V), C923 (2.2uF, 6.3V), C816 (10uF, 50V), C817 (10uF, 50V).
- Resistors:** R821 (10K, 5%), R885 (0.5% OPEN).
- Signal Connections:** WLAN\_RF\_OFF#, APU\_PCIE\_RST#, USBP5, USBN5, WLAN\_IND#.
- Other Components:** D5006, CHENMKO BAT54\_3P, BELLW\_80003 4021\_52P.

|        | Peak (mA) | Normal (mA) |
|--------|-----------|-------------|
| +V3.3A | 2,750mA   | 1,100mA     |
| +V1.5  | 500mA     | 375mA       |

|                |        |           |
|----------------|--------|-----------|
|                | PCH    | SB800/820 |
| WLAN_RF_OFF#   | GPIO57 | GPIO12    |
| WLAN_POWER_OFF | GPIO35 | GPIO176   |

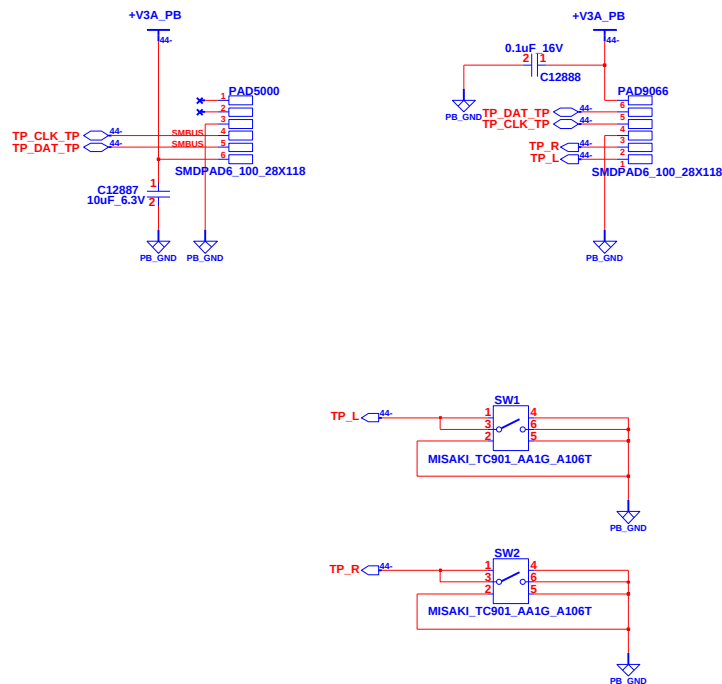
|                                          |           |             |            |
|------------------------------------------|-----------|-------------|------------|
| TITLE                                    |           |             |            |
| <b>ROCKY-AMD</b><br><b>WLAN &amp; BT</b> |           |             |            |
| SIZE                                     | CODE      | DOC. NUMBER | REV        |
| <b>A3</b>                                | <b>CS</b> | <b>1234</b> | <b>X01</b> |
| SHEET 43 OF 60                           |           |             |            |

|           |                    |
|-----------|--------------------|
| CHANGE by | <i>Ken, Chiang</i> |
|-----------|--------------------|

9-Jun-2010

|       |      |    |    |
|-------|------|----|----|
| CS    | 1234 |    |    |
| SHEET | 43   | OF | 60 |

# PICK BUTTON BOARD



**INVENTEC**

|                                |      |             |     |
|--------------------------------|------|-------------|-----|
| TITLE                          |      |             |     |
| ROCKY-AMD<br>PICK BUTTON BOARD |      |             |     |
| SIZE                           | CODE | DOC. NUMBER | REV |
| A3                             | CS   | 1234        | X01 |

CHANGE by Ken, Chiang 9-Jun-2010

SHEET 44 OF 60

## A



1



1

1

## A

## B



1



1



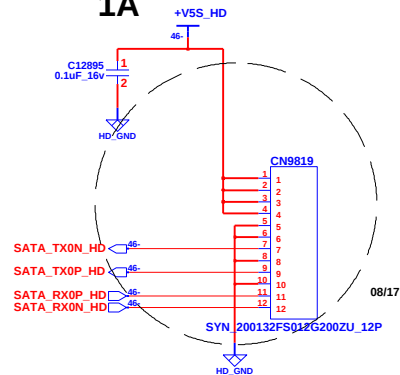
|  |       |    |    |    |
|--|-------|----|----|----|
|  | SHEET | 45 | OF | 60 |
|  |       | 8  |    |    |

|    |    |    |
|----|----|----|
| 45 | OF | 60 |
| 8  |    |    |

# SATA HDD BOARD

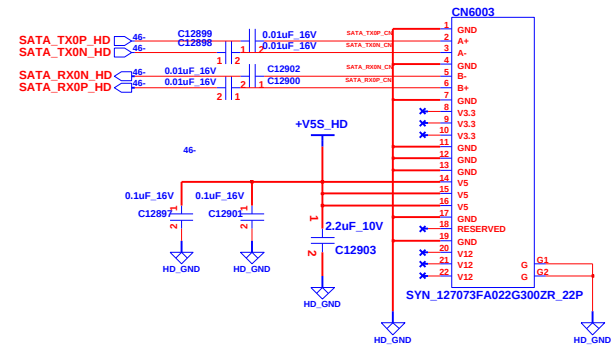
## SATA HDD FFC CONN on HDD/B

1A



08/17

## HDD CONN

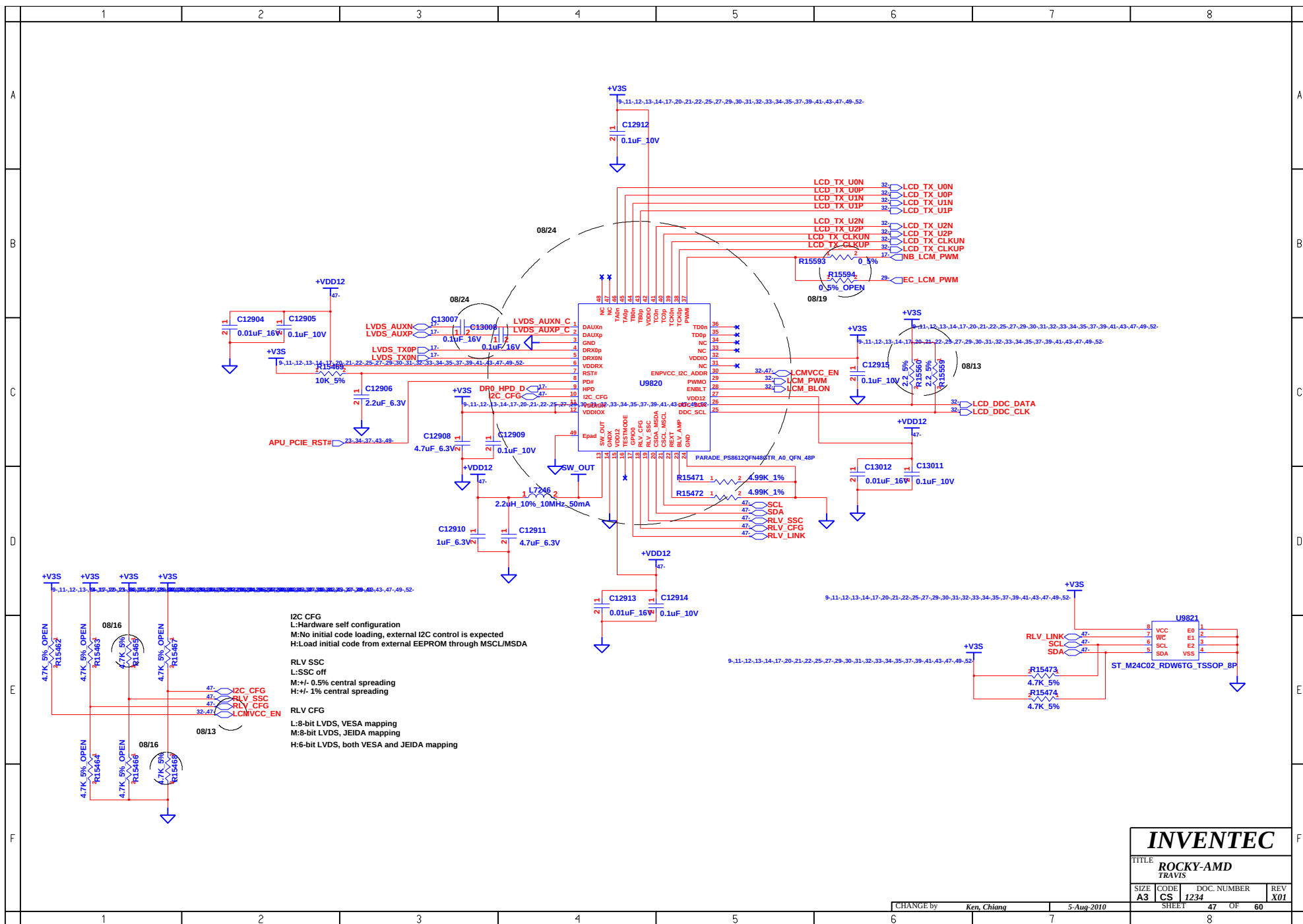


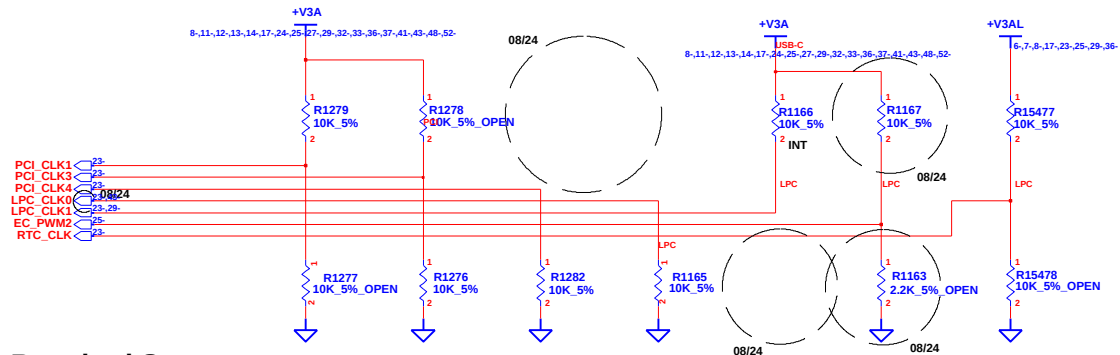
**INVENTEC**

|                |      |             |     |
|----------------|------|-------------|-----|
| TITLE          |      |             |     |
| ROCKY-AMD      |      |             |     |
| SATA HDD BOARD |      |             |     |
| SIZE           | CODE | DOC. NUMBER | REV |
| A3             | CS   | 1234        | X01 |

CHANGE by Ken Chiang 9-Jun-2010

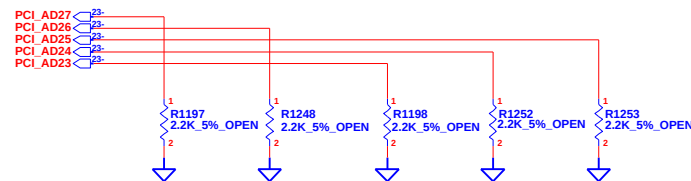
SHEET 46 OF 60





### Required Straps:

| RTCCCLK                           |                  | PCI_CLK1                      | PCI_CLK3                             | PCI_CLK4                                                                                      | LPC_CLK0                  | PCI_CLK1                                   | EC_PWM2            |
|-----------------------------------|------------------|-------------------------------|--------------------------------------|-----------------------------------------------------------------------------------------------|---------------------------|--------------------------------------------|--------------------|
| S5 PLUS MODE<br>DISABLE (DEFAULT) | <b>PULL HIGH</b> | ALLOW<br>PCIe Gen2<br>DEFAULT | USE<br>DEBUG<br>STRAP                | non Fusion<br>CLOCK MODE<br>APU_CLK AND DISP_CLK<br>NO CLOCK OUTPUT                           | EC<br>ENABLED             | INTEGRATED<br>CLKGEN<br>ENABLED<br>DEFAULT | LPC ROM<br>DEFAULT |
| S5 PLUS MODE<br>ENABLE            | <b>PULL LOW</b>  | FORCE<br>PCIe Gen1            | IGNORE<br>DEBUG<br>STRAPE<br>DEFAULT | Fusion<br>CLOCK MODE<br>DEFAULT<br>APU_CLK AND DISP_CLK<br>CLOCK OUTPUT<br>FROM INT CLOCK GEN | EC<br>DISABLED<br>DEFAULT | INTEGRATED<br>CLKGEN<br>DISABLED           | SPI ROM            |

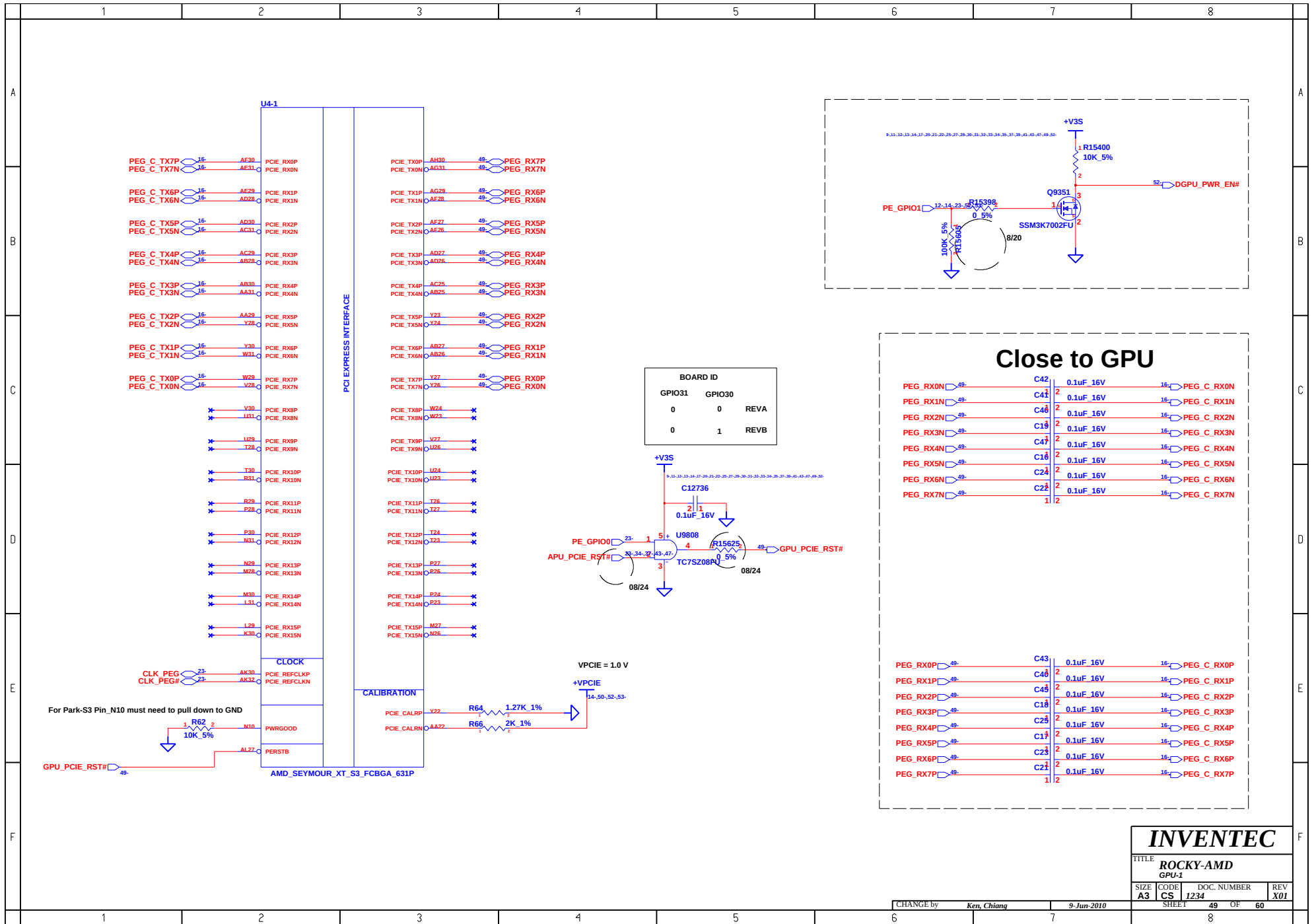


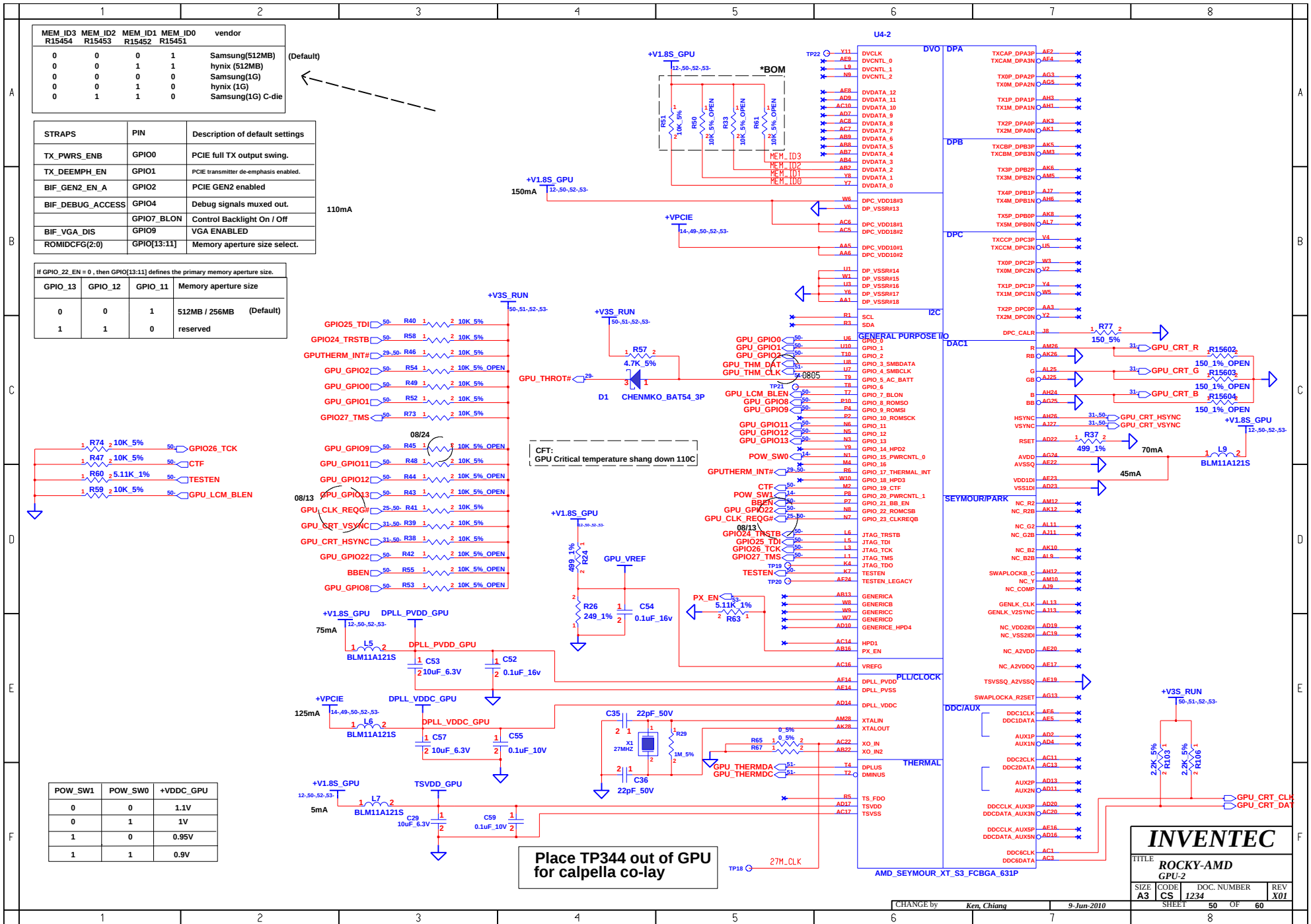
### Debug Straps

SB800 HAS 15K INTERNAL PU FOR PCI\_AD[27:23]

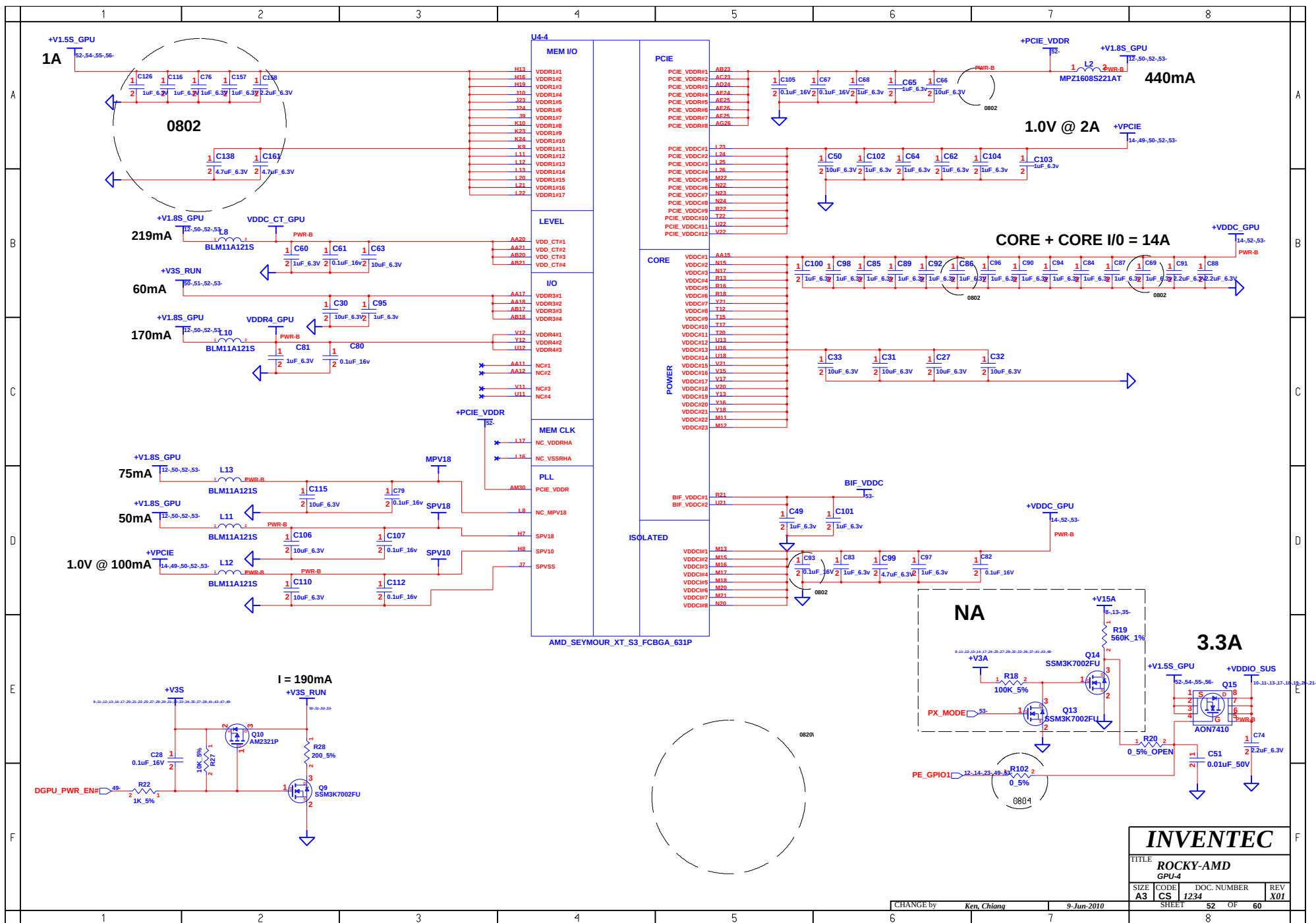
|                  | PCI_3S_AD27                 | PCI_3S_AD24               | PCI_3S_AD23                          |
|------------------|-----------------------------|---------------------------|--------------------------------------|
| <b>PULL HIGH</b> | USE PCI<br>PLL<br>(default) | GETTING<br>I2C ROM ENABLE | DISABLE PCI<br>MEM BOOT<br>(default) |
| <b>PULL LOW</b>  | BYPASS<br>PCI PLL           | USE EEPROM<br>PCI STRAPS  | ENABLE PCI<br>MEM BOOT               |

| INVENTEC                    |            |                     |            |
|-----------------------------|------------|---------------------|------------|
| TITLE<br>ROCKY-AMD<br>SRTAP |            |                     |            |
| SIZE<br>A3                  | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
| CHANGE by<br>Ken Chiang     |            | 9-Jun-2010          |            |
| SHEET                       |            | 48 OF 60            |            |

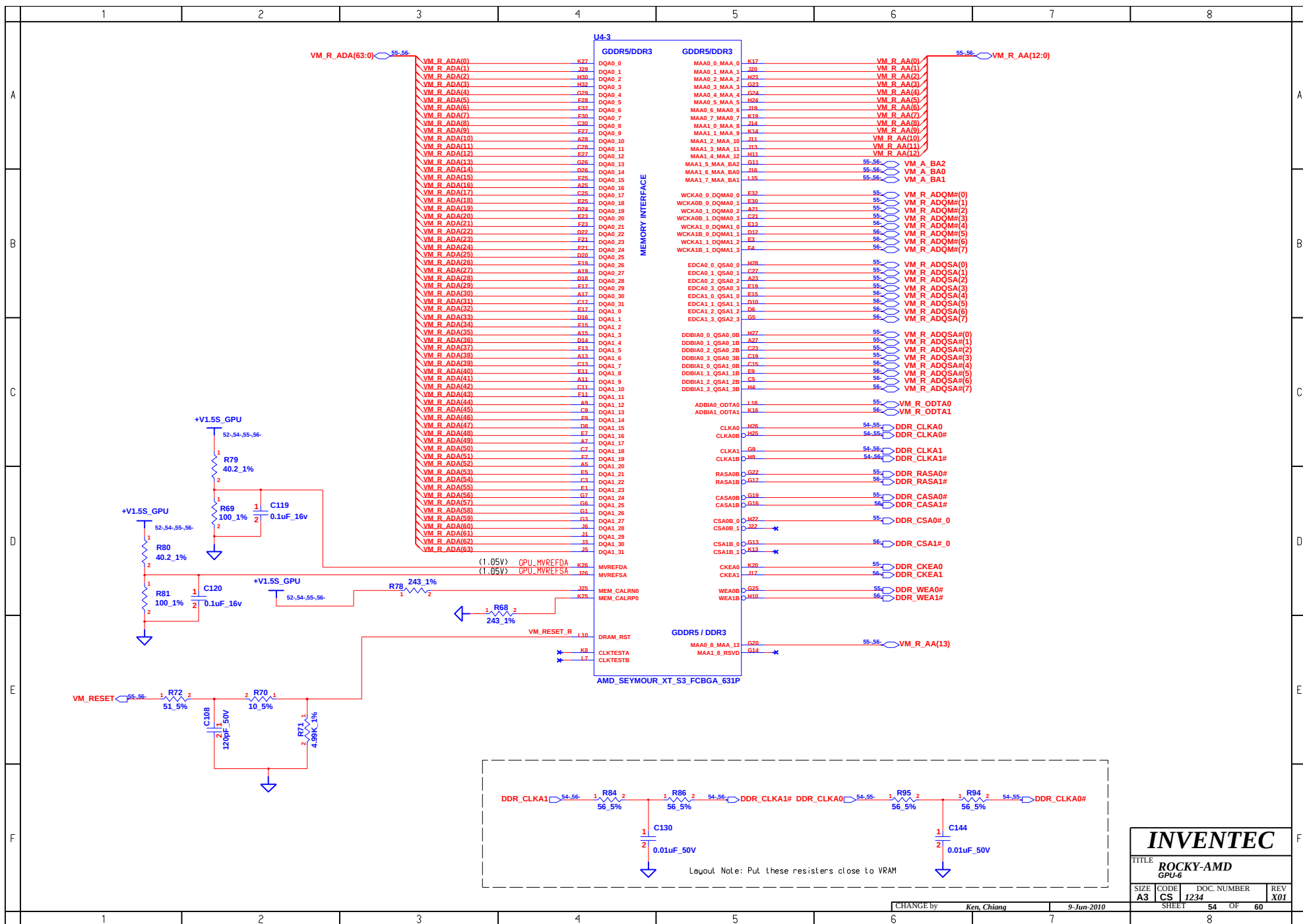


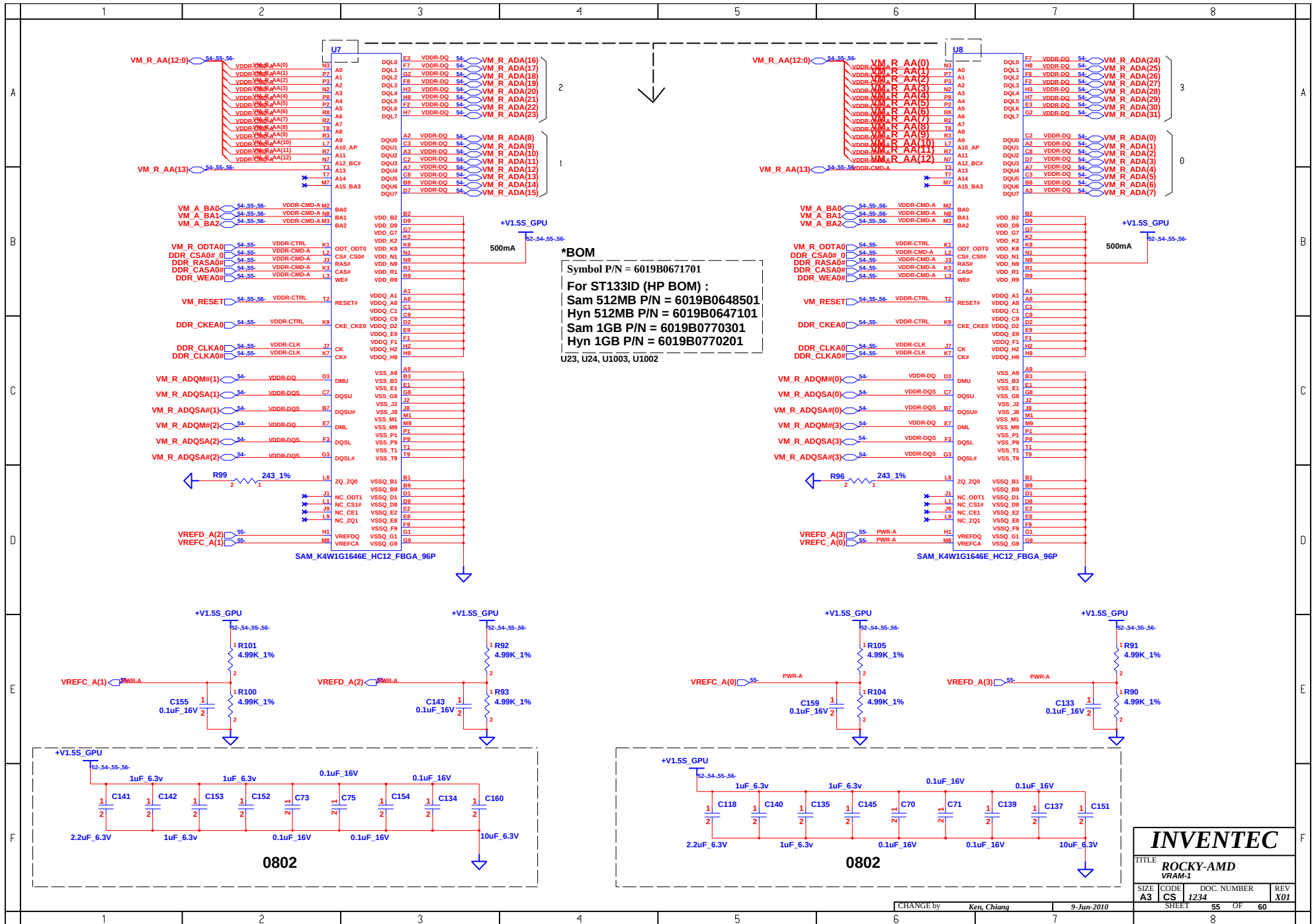


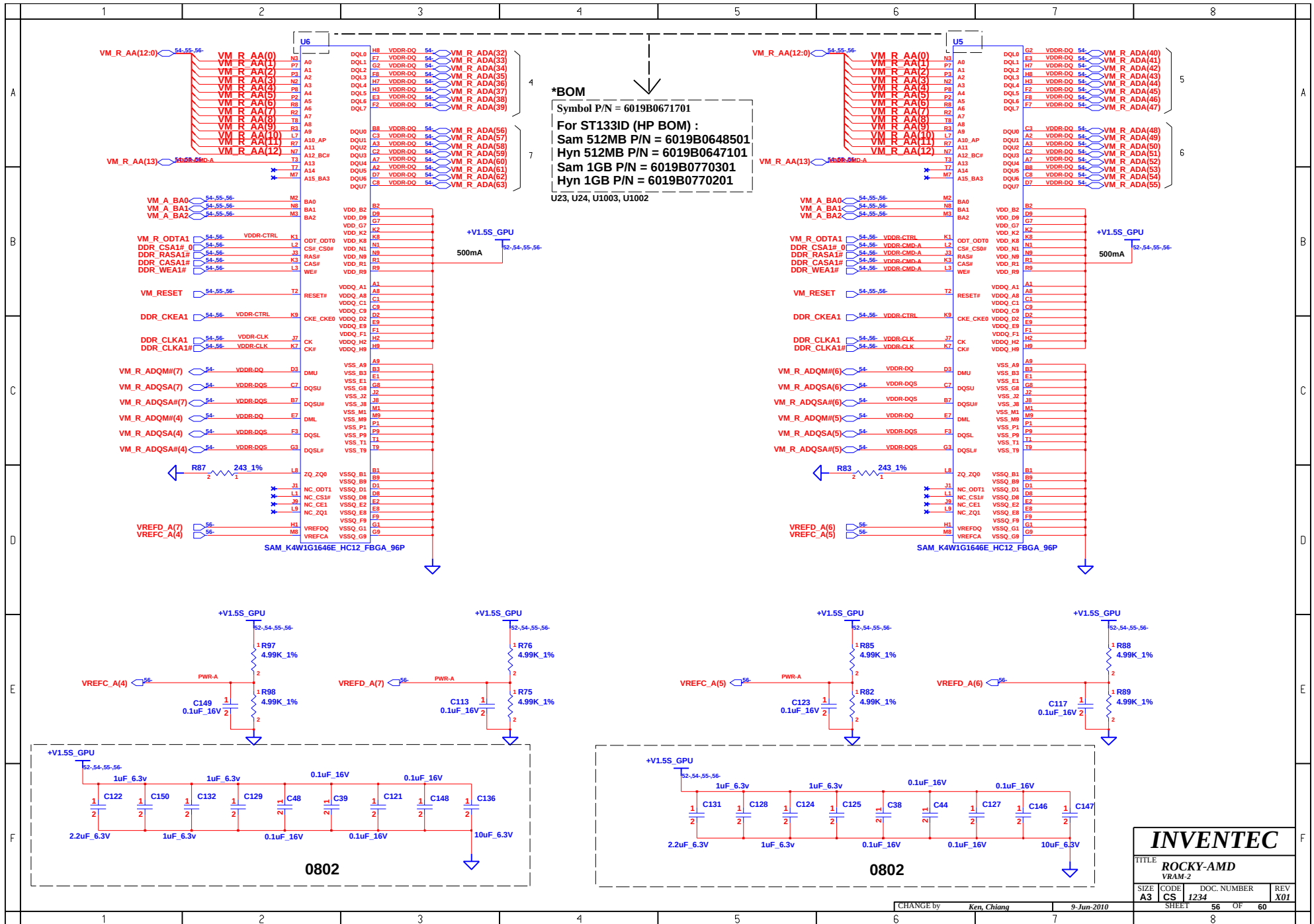












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INVENTEC

|                           |      |             |     |
|---------------------------|------|-------------|-----|
| TITLE                     |      |             |     |
| ROCKY-AMD<br>EXT USB CONN |      |             |     |
| SIZE                      | CODE | DOC. NUMBER | REV |
| A3                        | CS   | 1234        | X01 |

CHANGE by Ken, Chiang 10-Aug-2010

SHEET 57 OF 60

BLANK

INVENTEC

TITLE  
ROCKY-AMD

|            |            |                     |            |
|------------|------------|---------------------|------------|
| SIZE<br>A3 | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
|------------|------------|---------------------|------------|

CHANGE by Ken, Chiang 10-Aug-2010

SHEET 58 OF 60

BLANK

INVENTEC

TITLE  
ROCKY-AMD

|            |            |                     |            |
|------------|------------|---------------------|------------|
| SIZE<br>A3 | CODE<br>CS | DOC. NUMBER<br>1234 | REV<br>X01 |
|------------|------------|---------------------|------------|

CHANGE by Ken, Chiang 10-Aug-2010

SHEET 59 OF 60

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|                 |         |                  |         |
|-----------------|---------|------------------|---------|
| INVENTEC        |         |                  |         |
| TITLE ROCKY-AMD |         |                  |         |
| SIZE A3         | CODE CS | DOC. NUMBER 1234 | REV X01 |
| SHEET 60        |         | OF 60            | 8       |

CHANGE by Ken, Chiang 10-Aug-2010