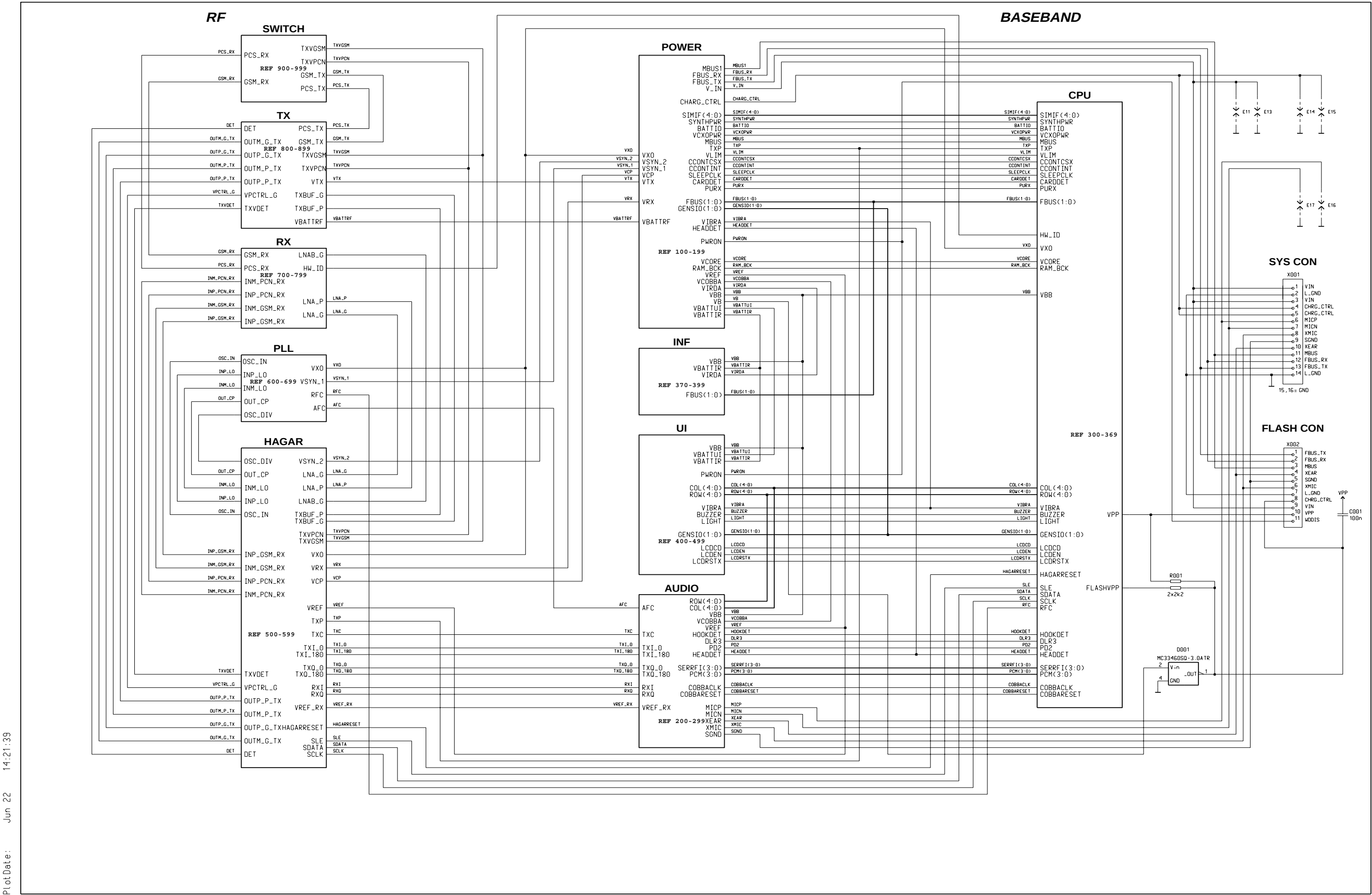
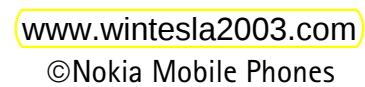


Baseband / RF Interconnections



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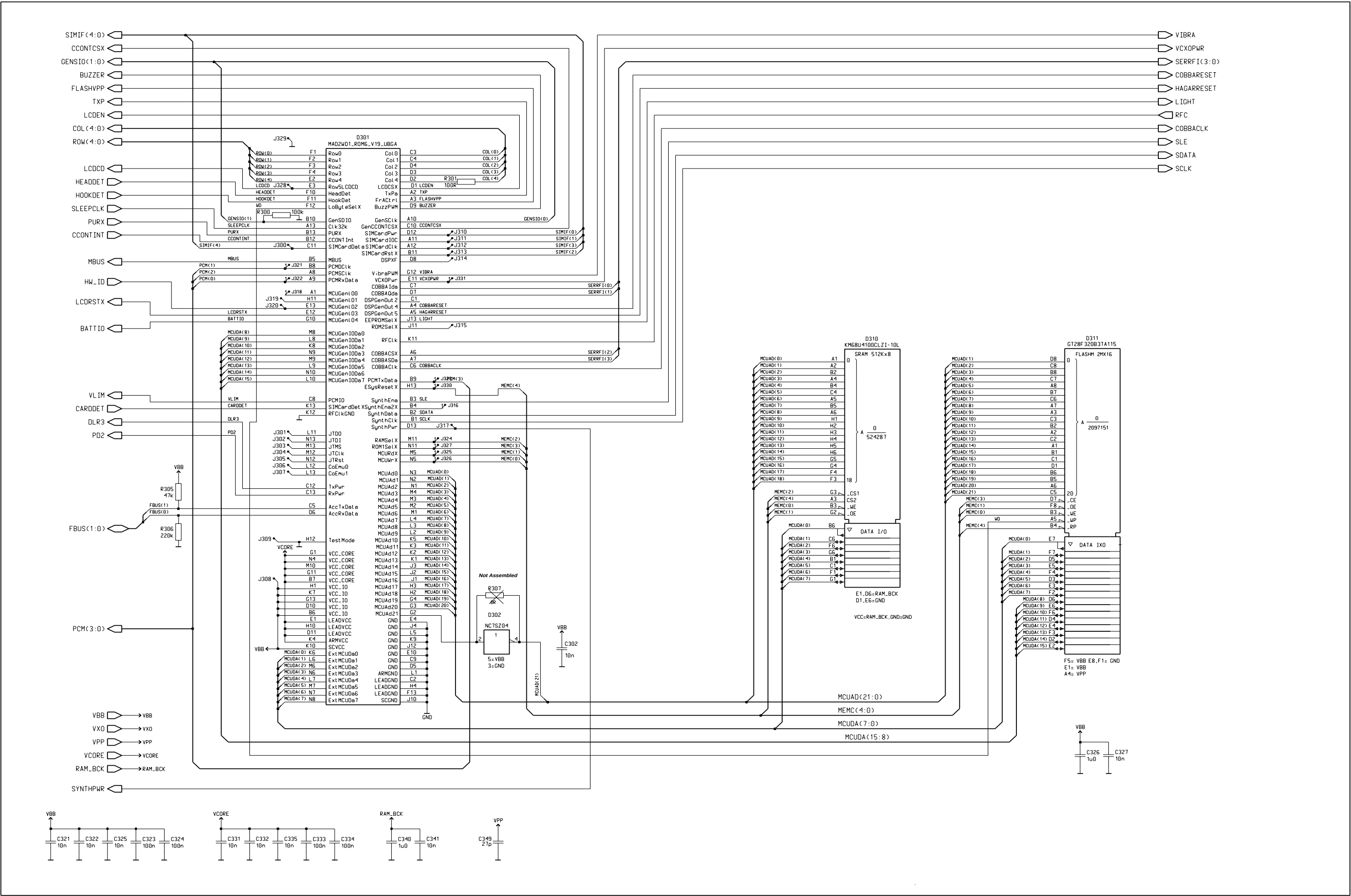
PlotDate: Jun 22 14:21:39





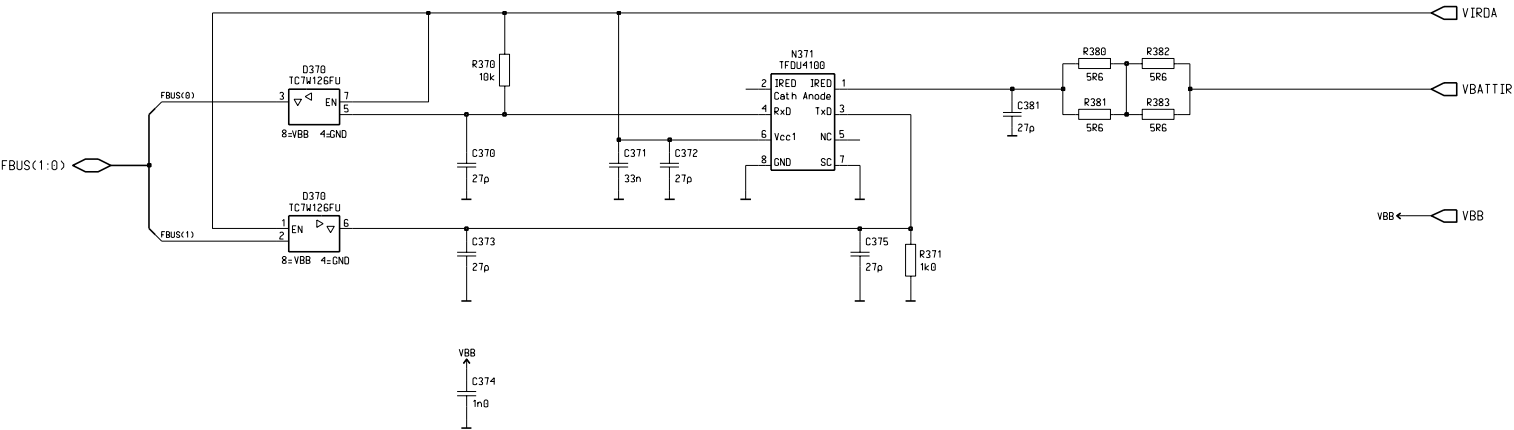
CPU

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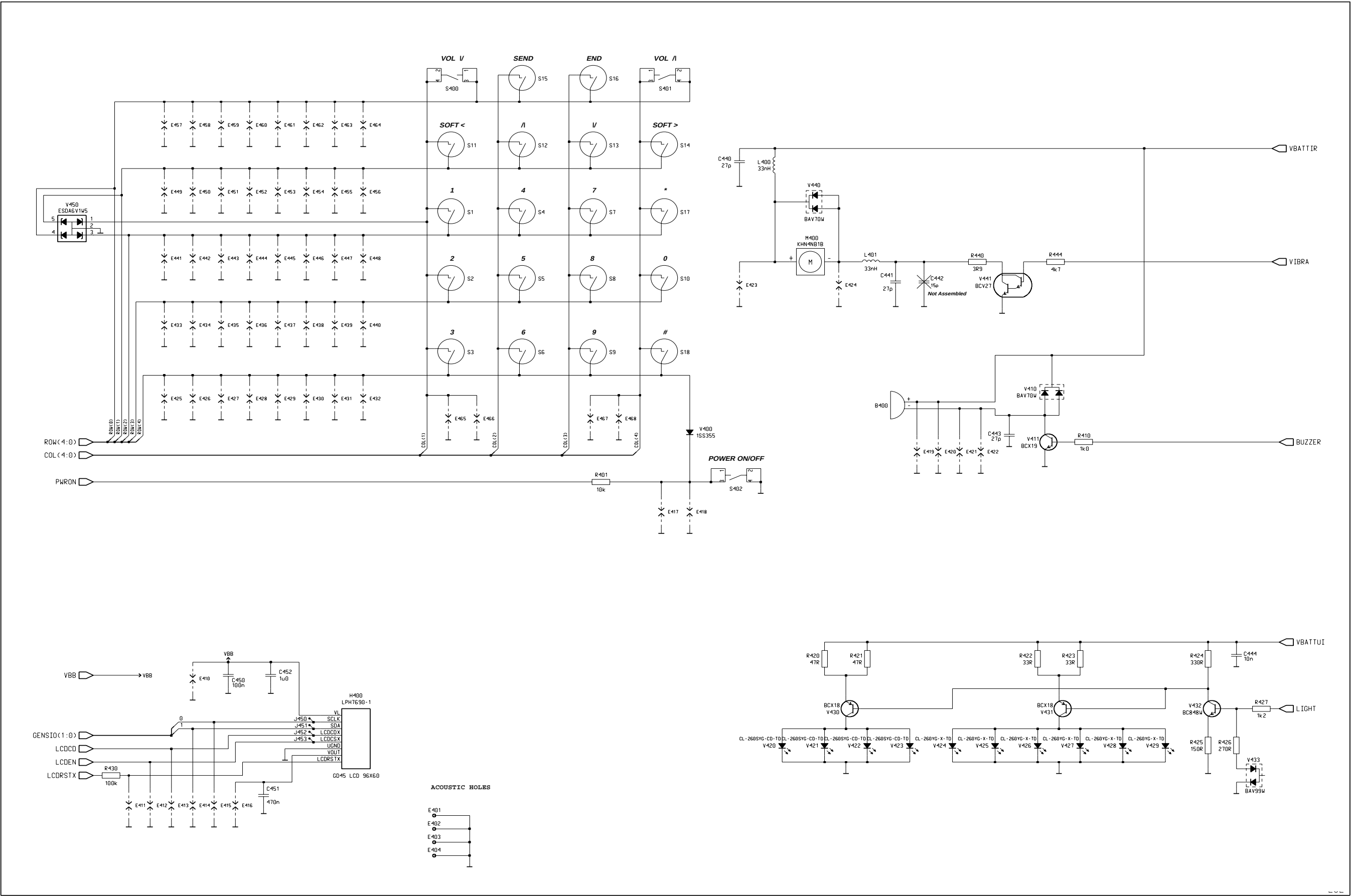
Infra red Module

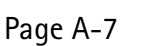
Plot Date: Jun 22 14:21:39



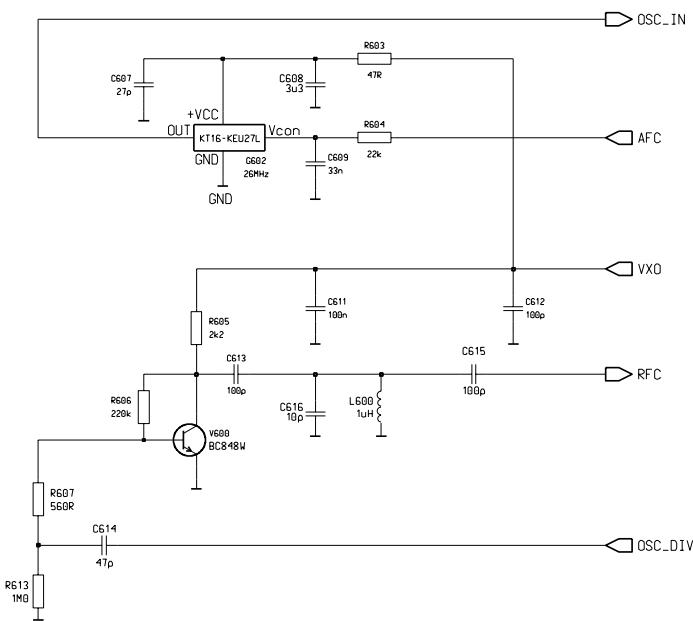
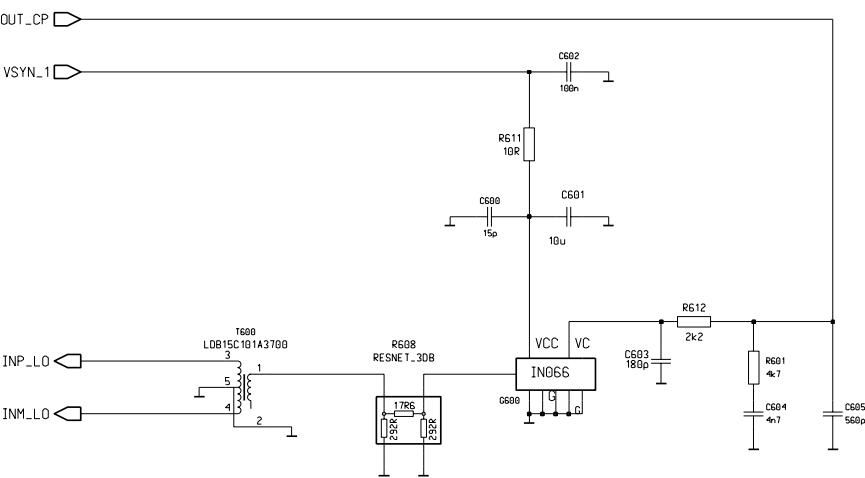
User Interface

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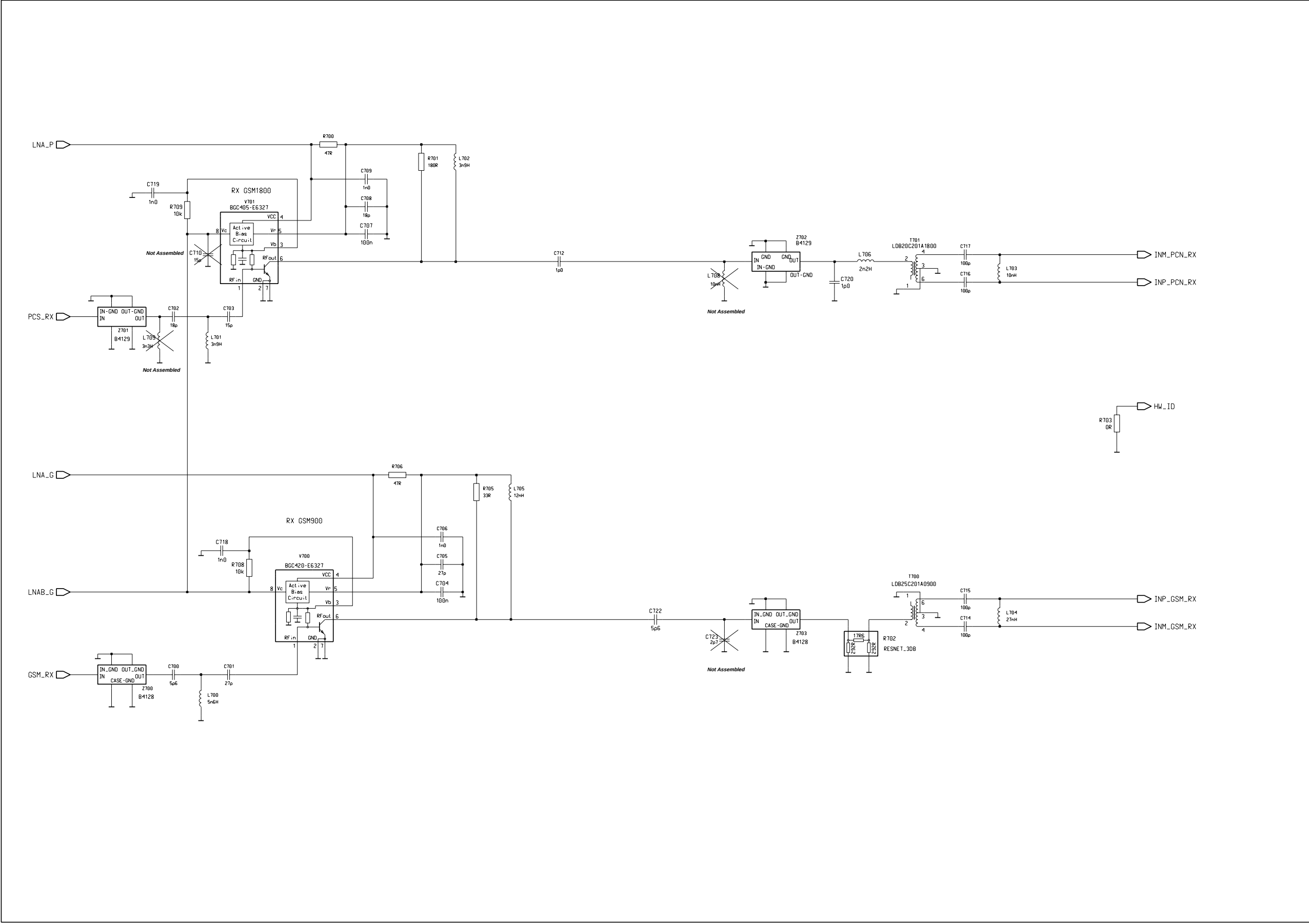


PLL



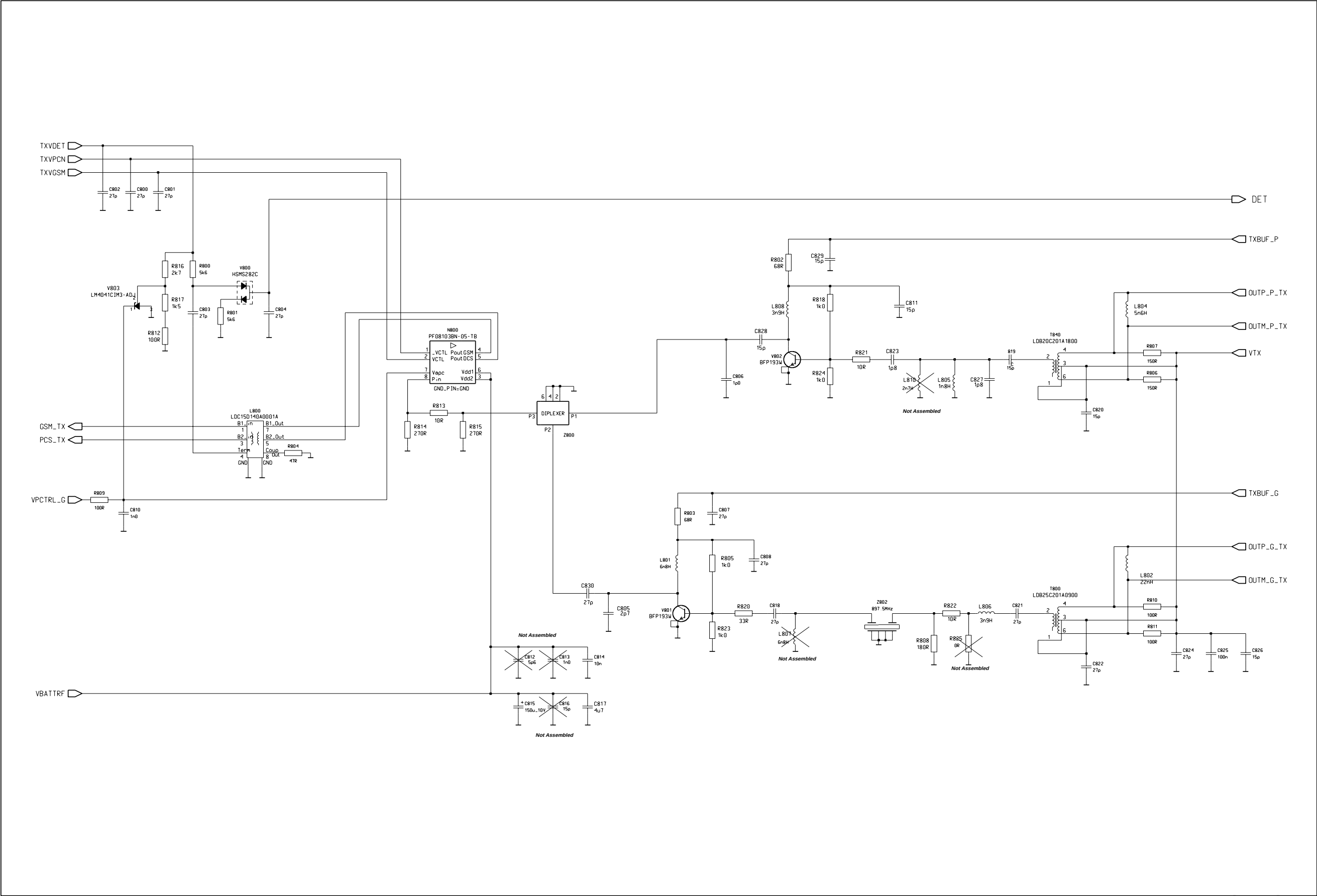
RX

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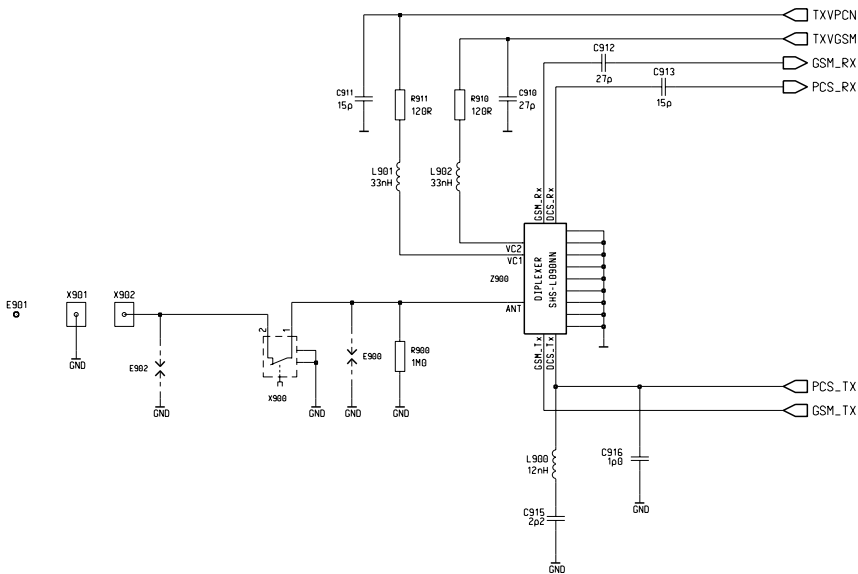
TX

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Switch

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Component Layouts

