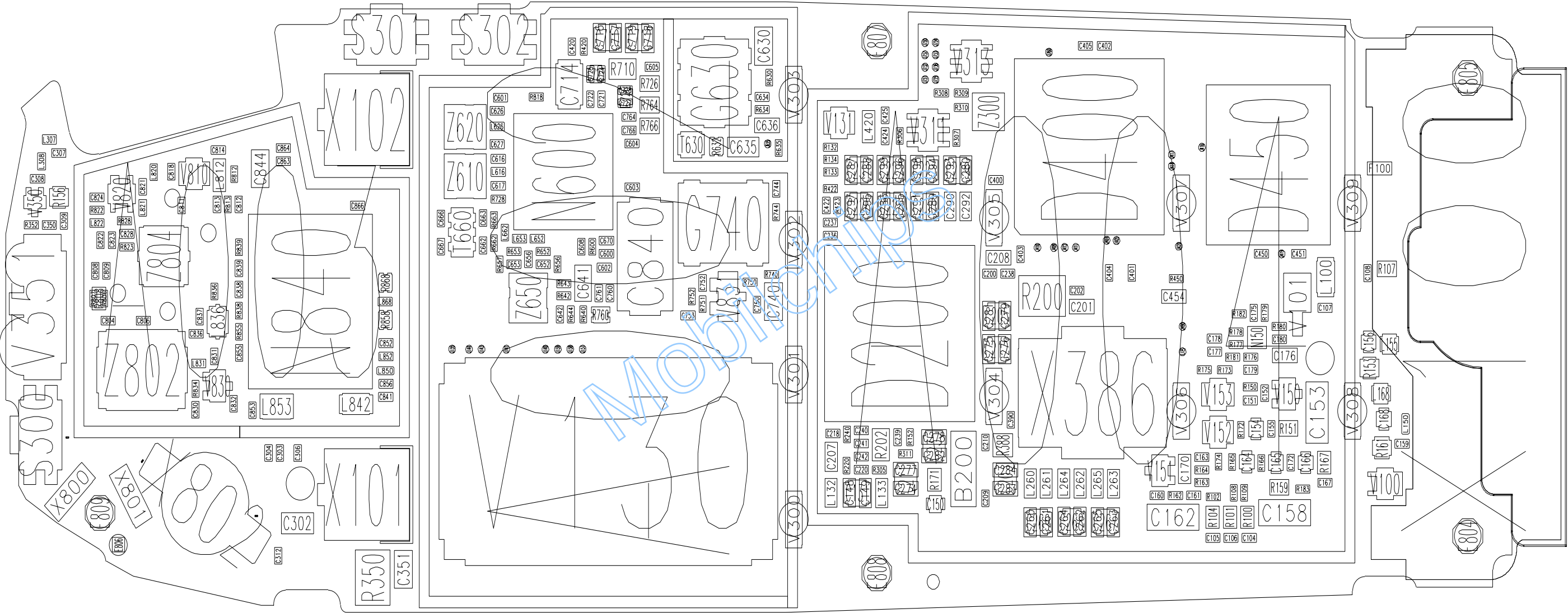


10 - Layout

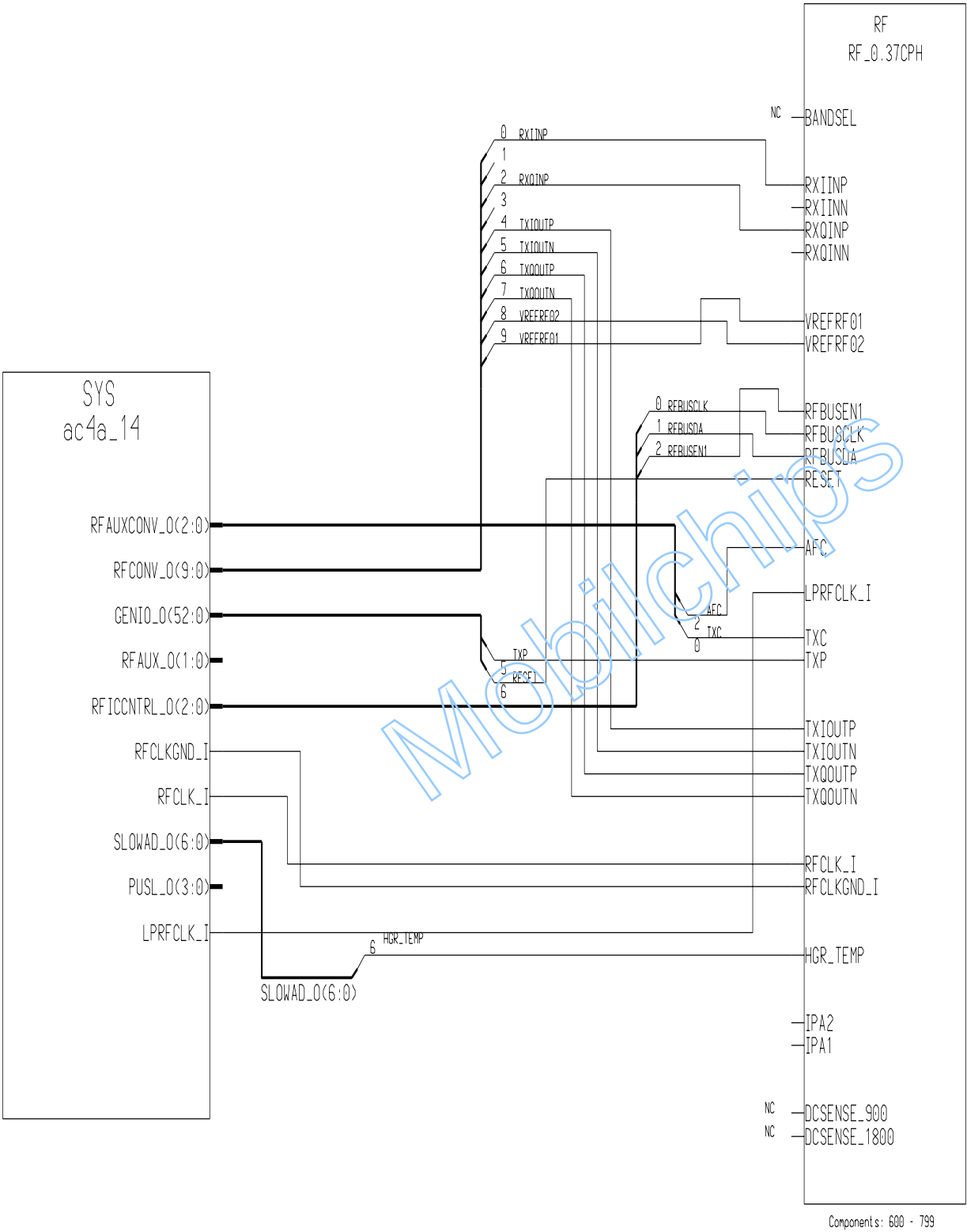


11 – SCHEMATICS

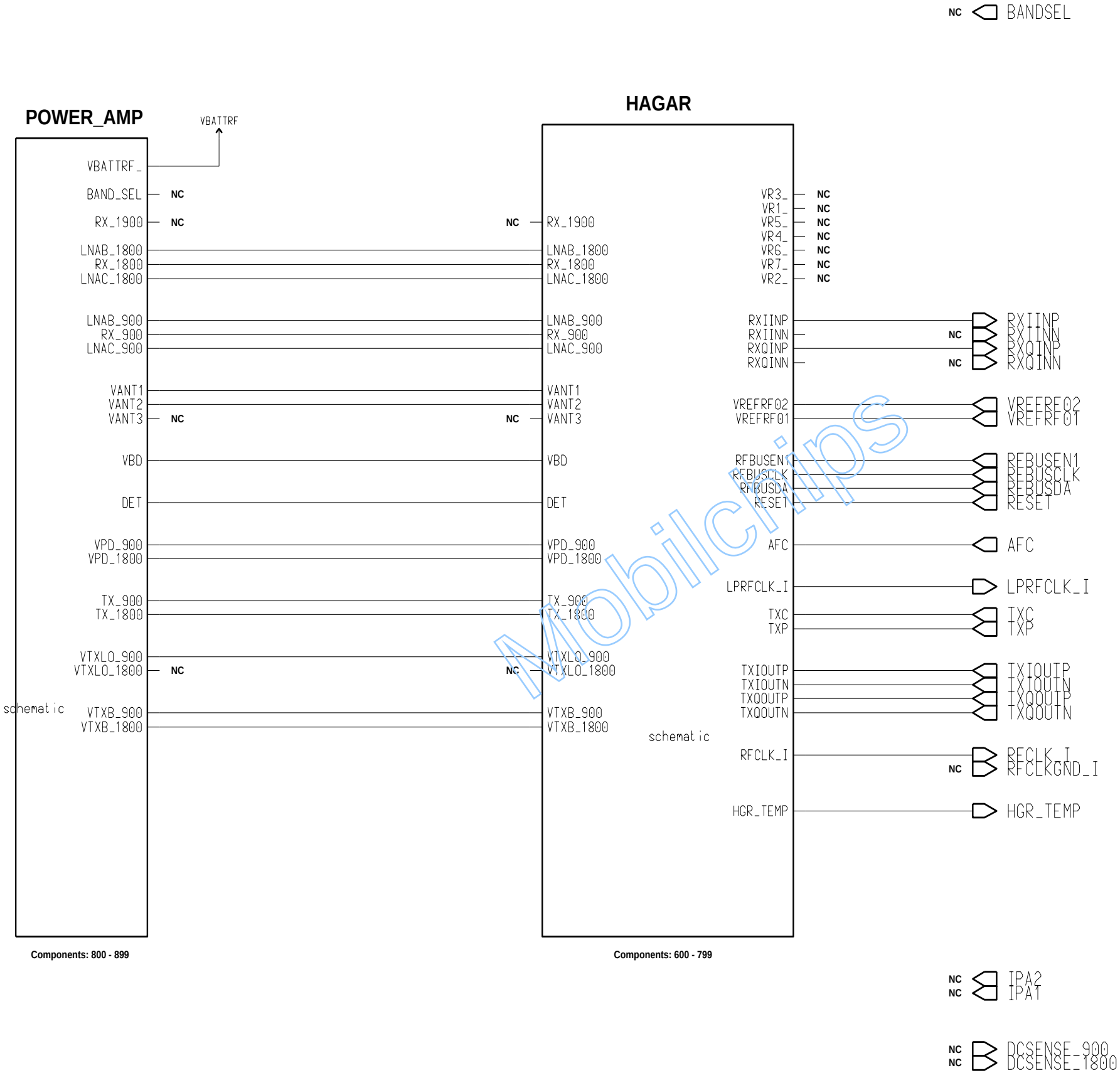
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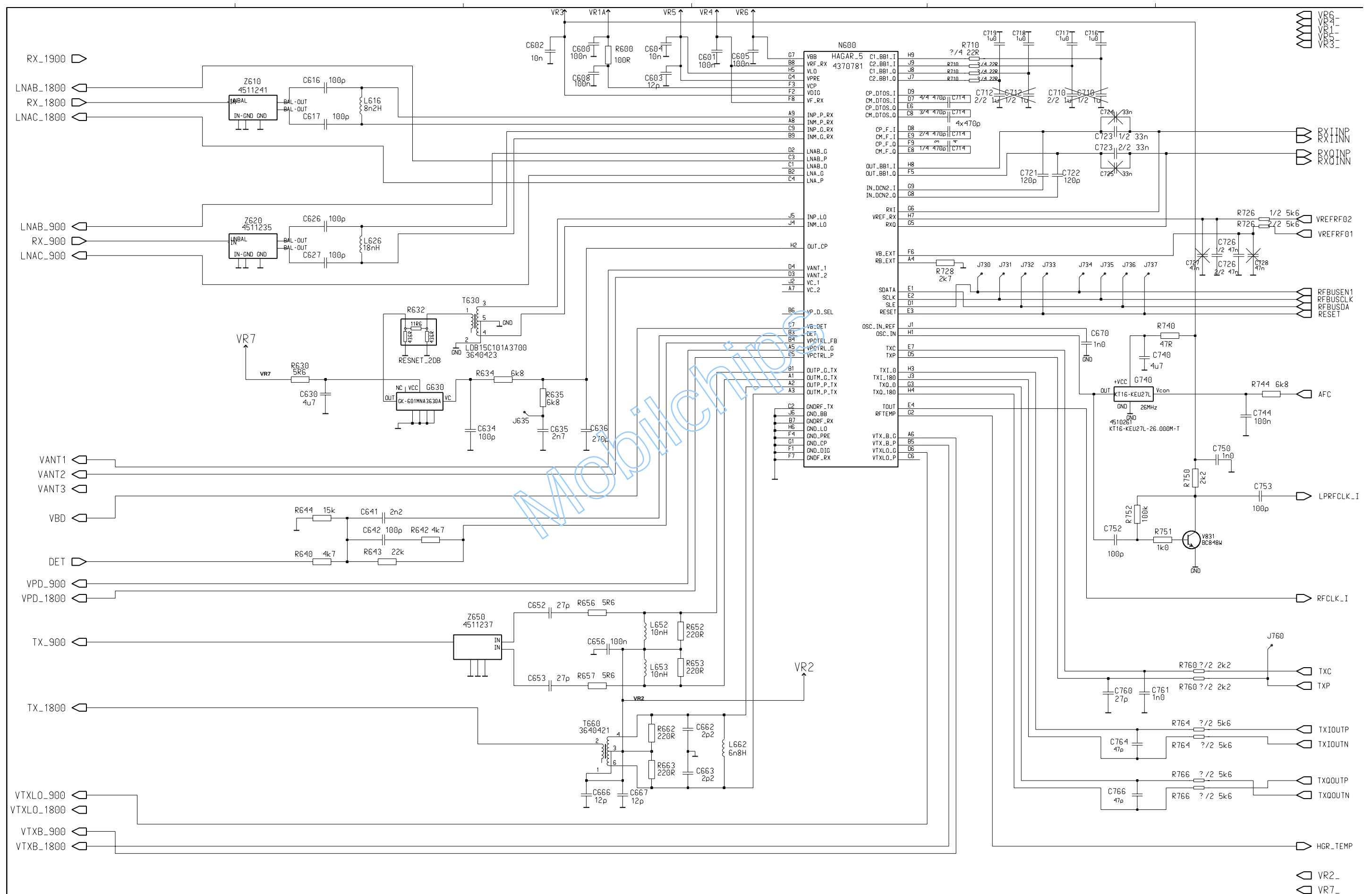
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ac4a-14(1) RF - BB

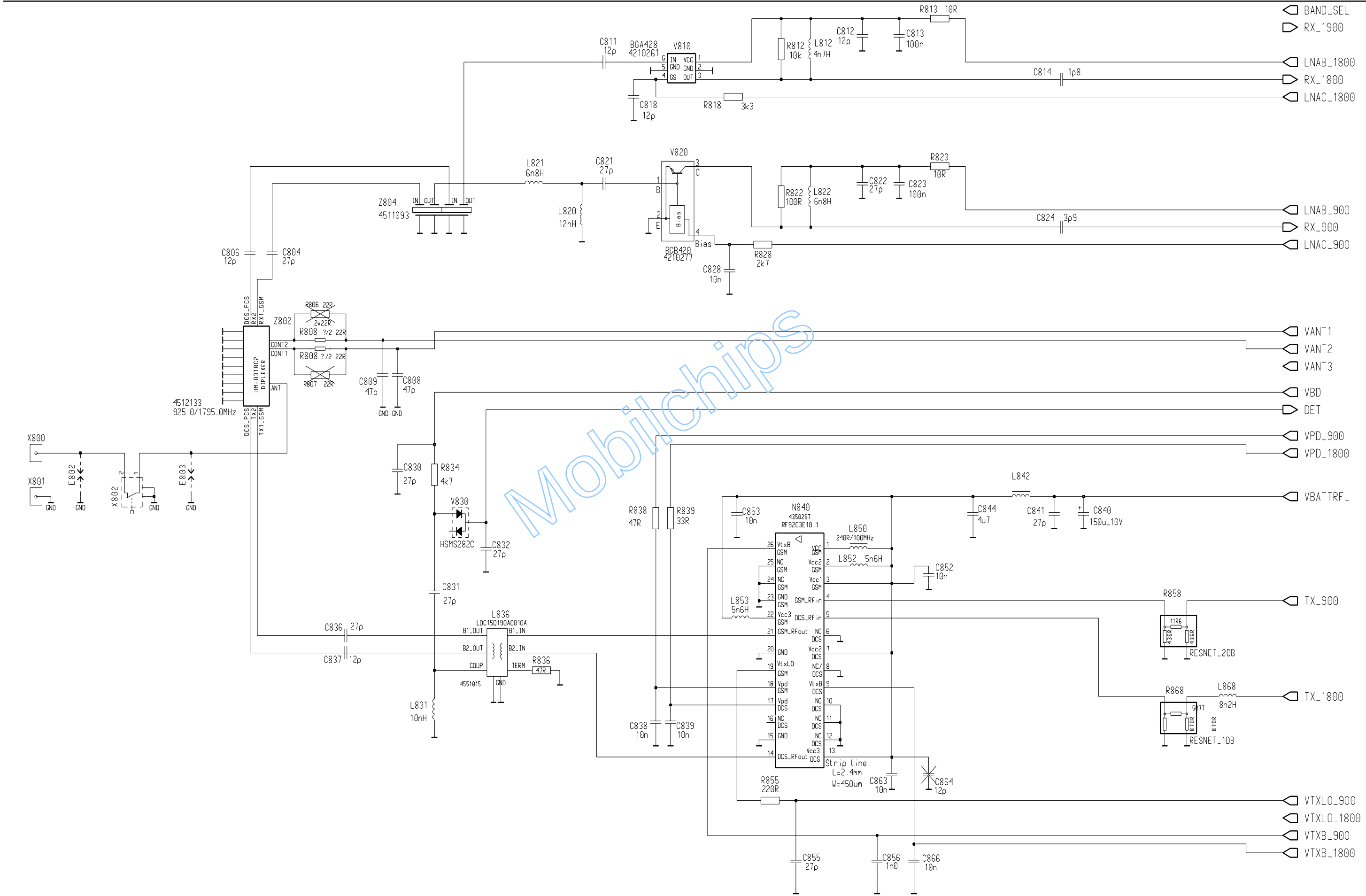


ac4a-14(2) RF - RF 0.37 CPH (Connections between RF & BB modules)

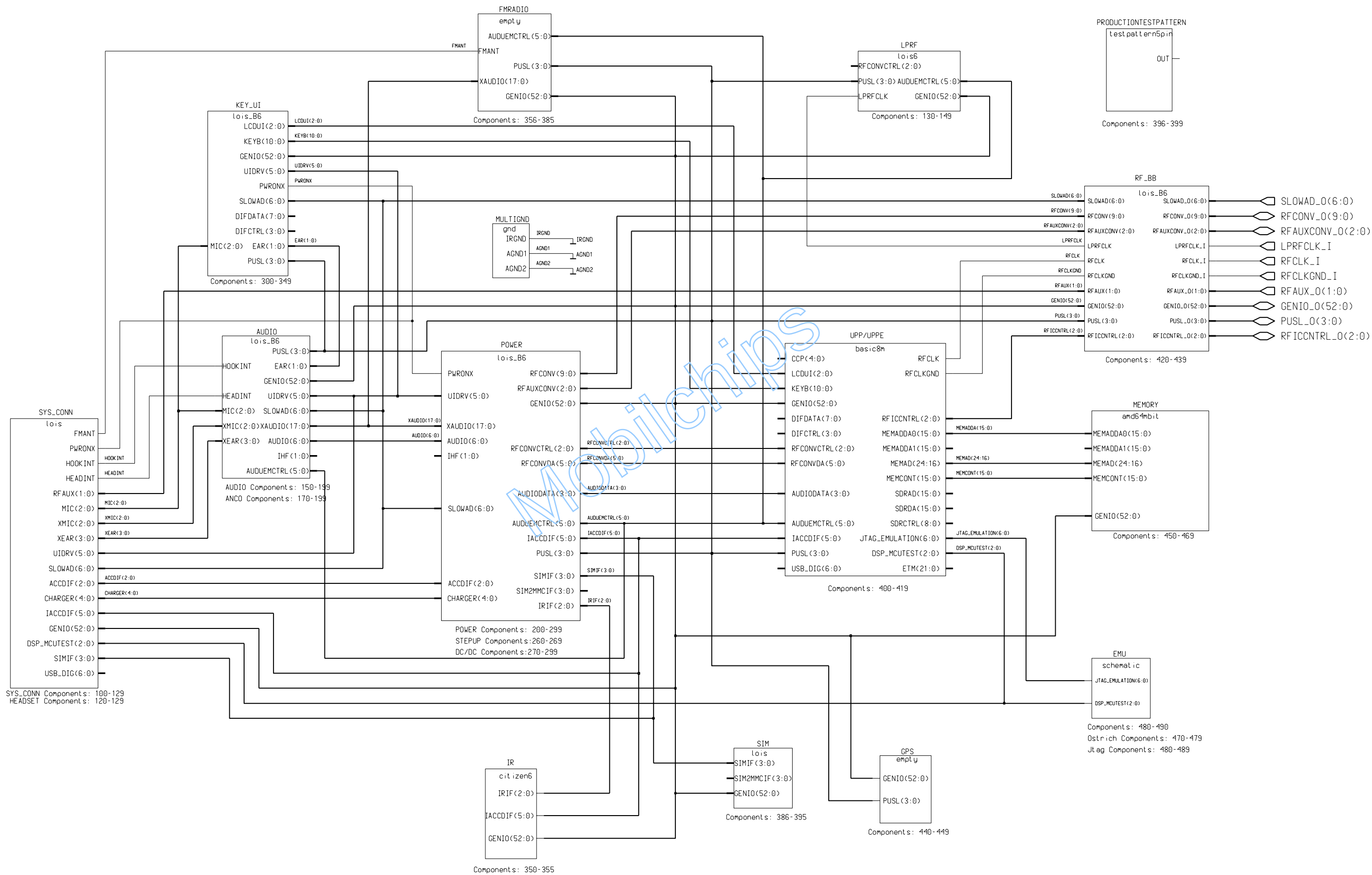




ac4a-14(4) RF / POWER AMP



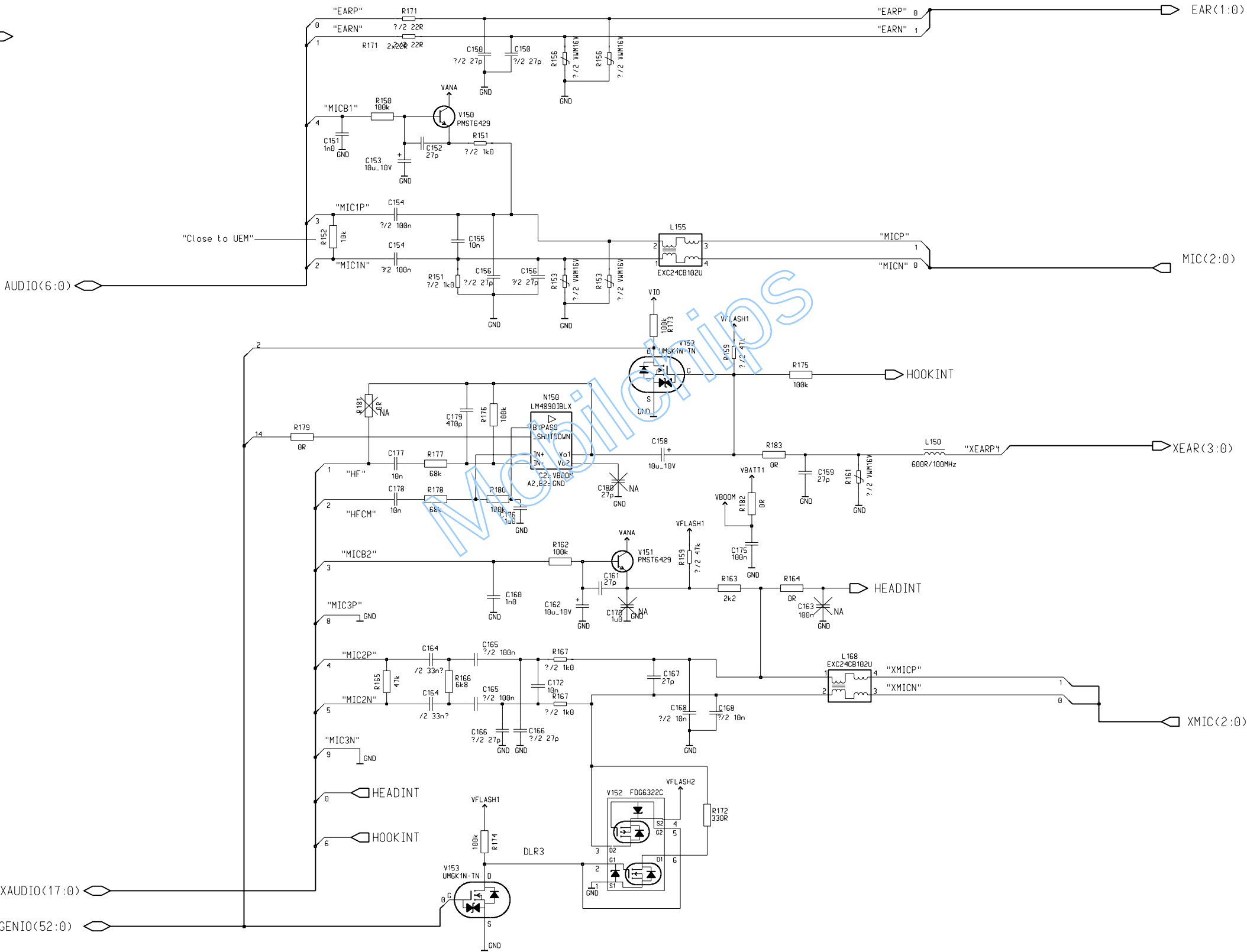
ac4a-14(5) SYS ac4a (DCT4 Common BB)



ac4a-14(6) SYS / AUDIO (Audio Interface)

AUDUEMCTRL<5:0>

SLOWAD<6:0>



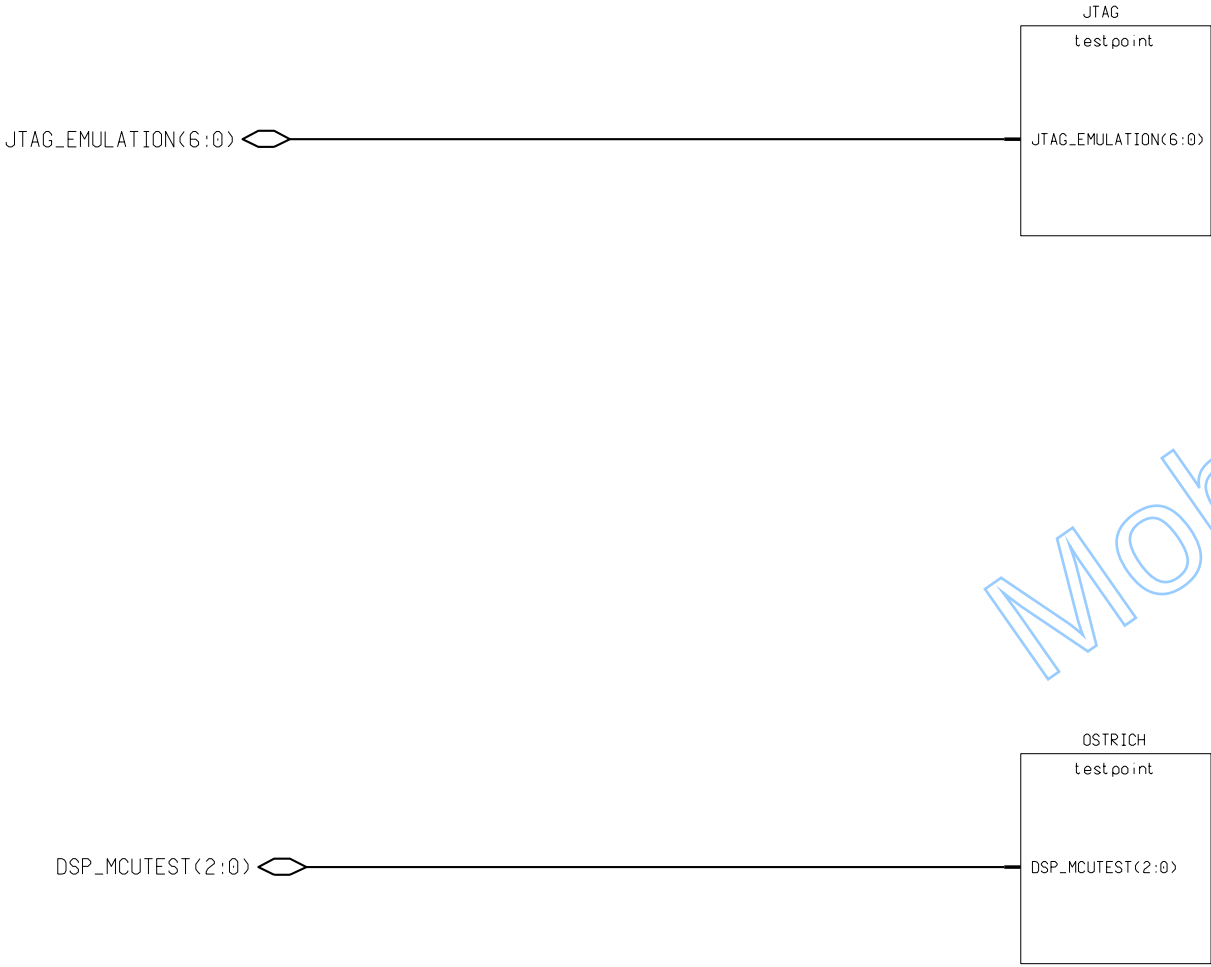
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UIDRV<5:0>

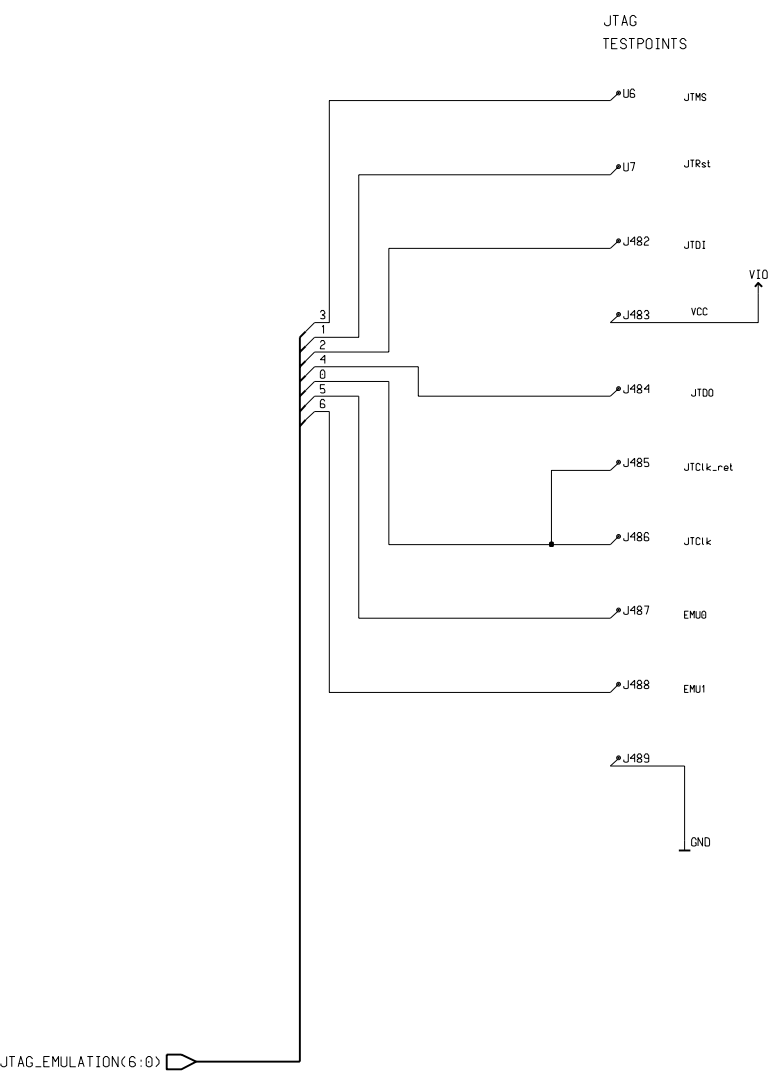
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XAUDIO<17:0>

GENIO<52:0>

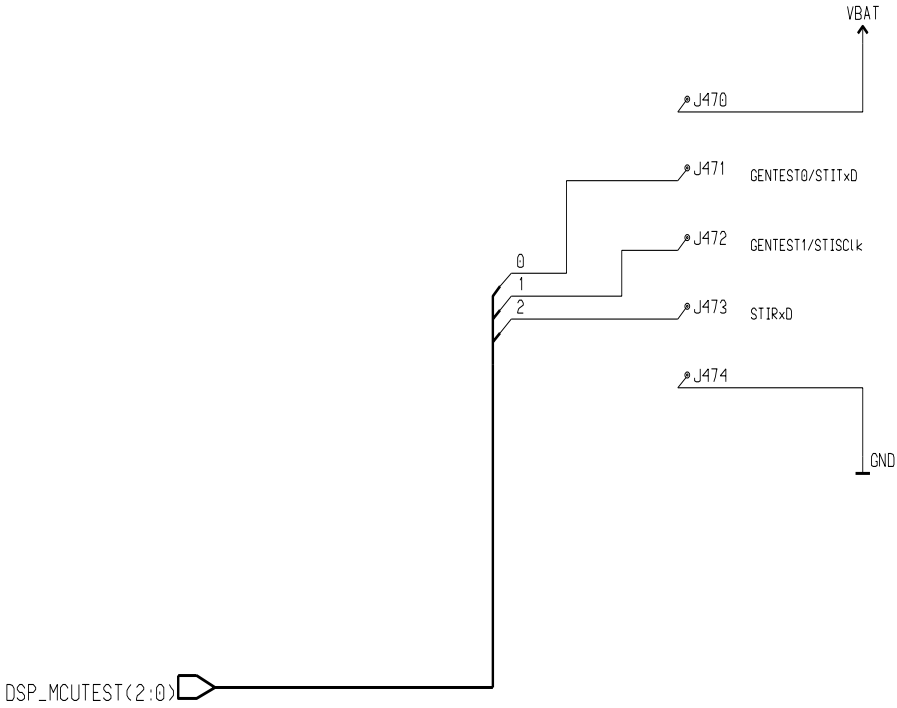


ac4a-14(8) SYS / EMU / JTAG (Testpoints for JTAG Emulator) .



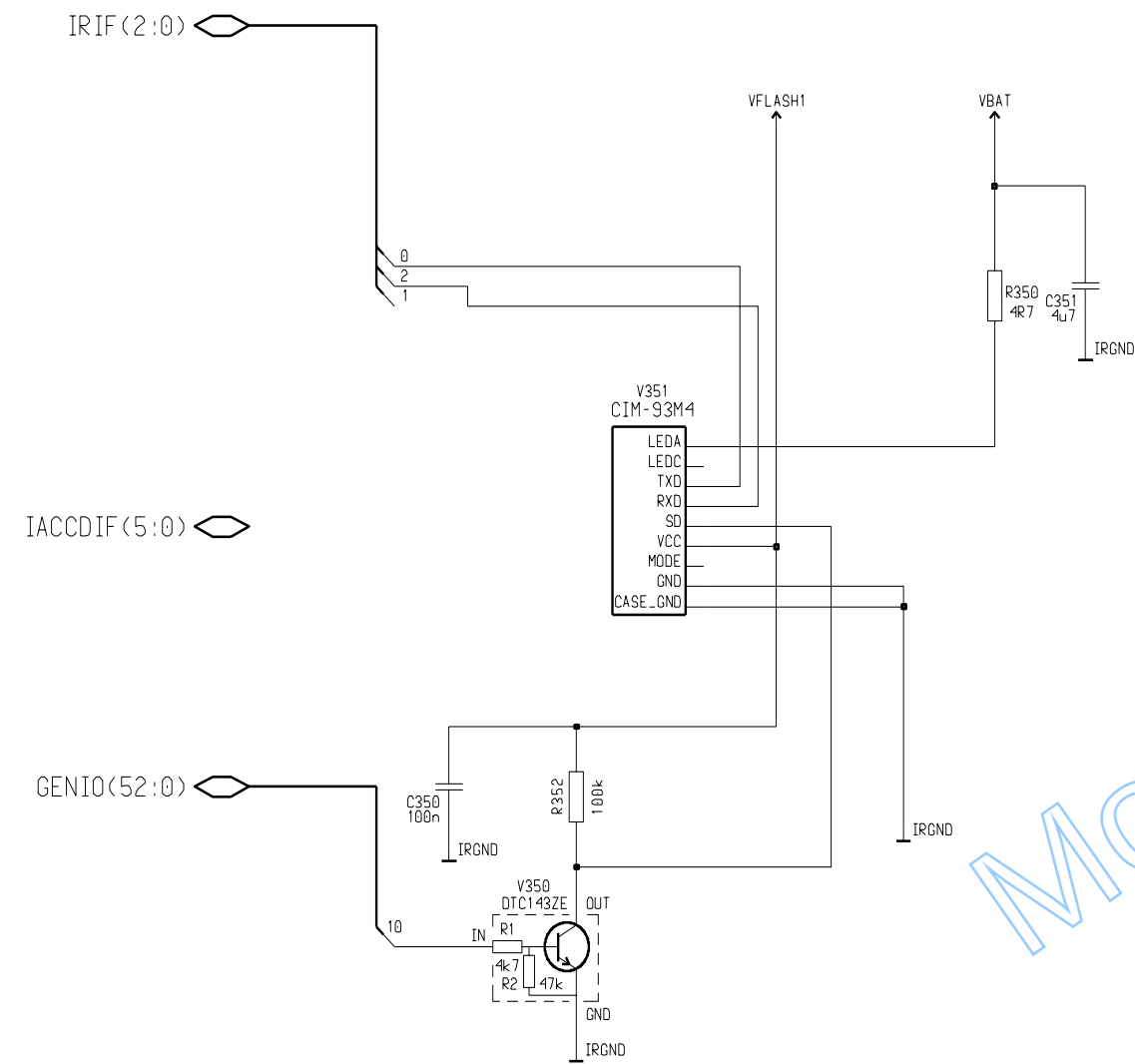
Mobilchips

ac4a-14(9) SYS / EMU / OSTRICH (Testpoints based Ostrich interface)



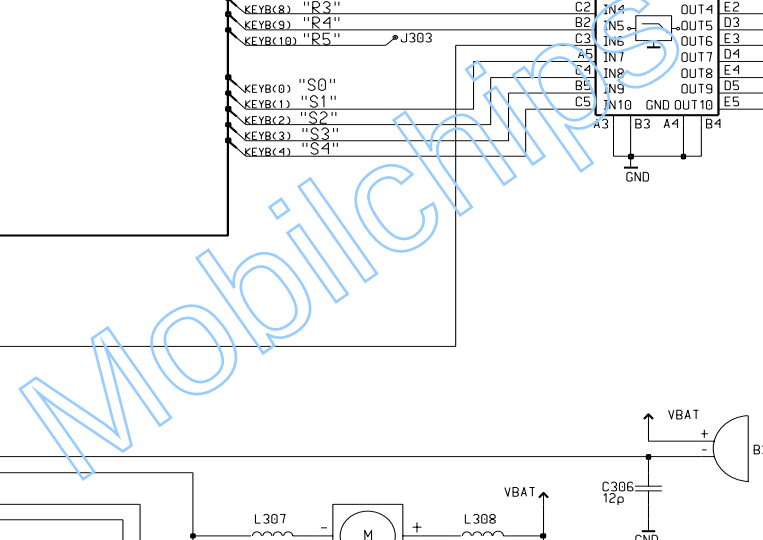
Mobilchips

ac4a-14(10) SYS / IR (IR module based implementation)

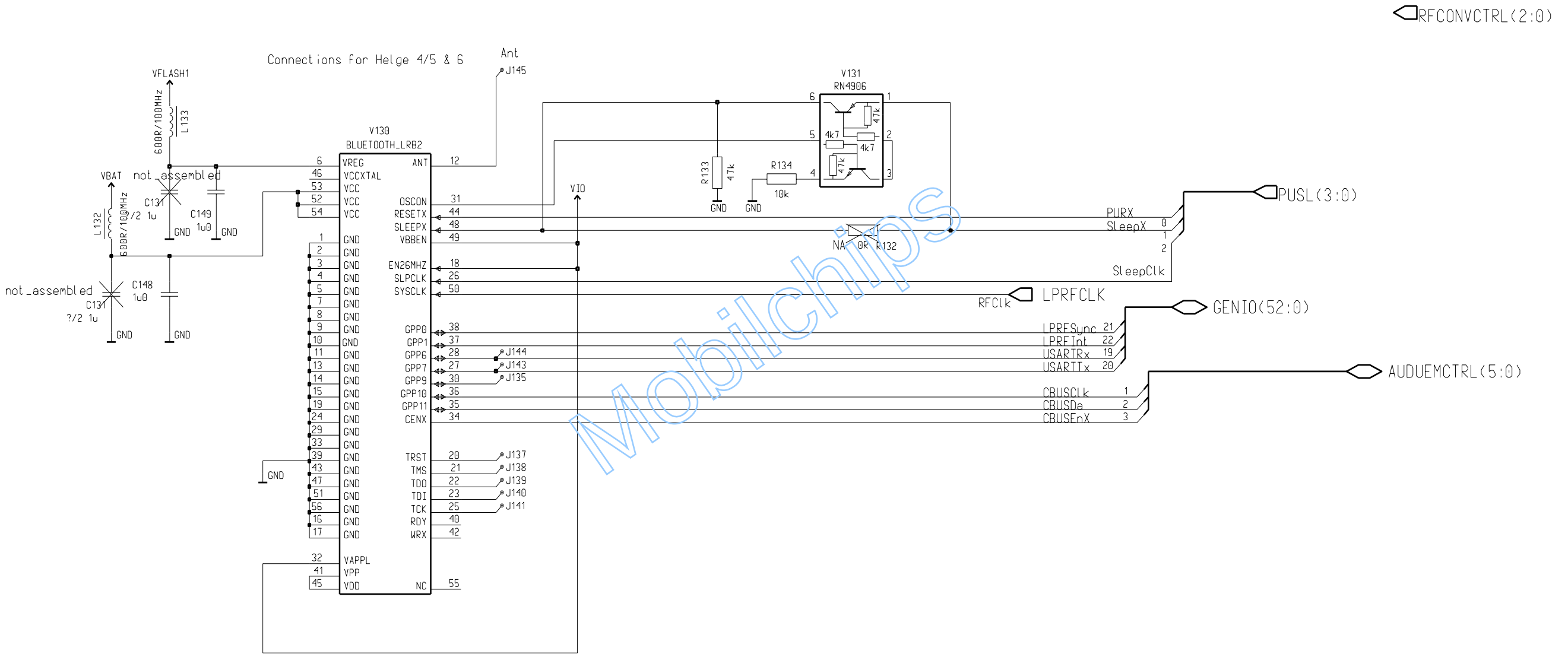


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ac4a-14(11) SYS / KEY (User interface)

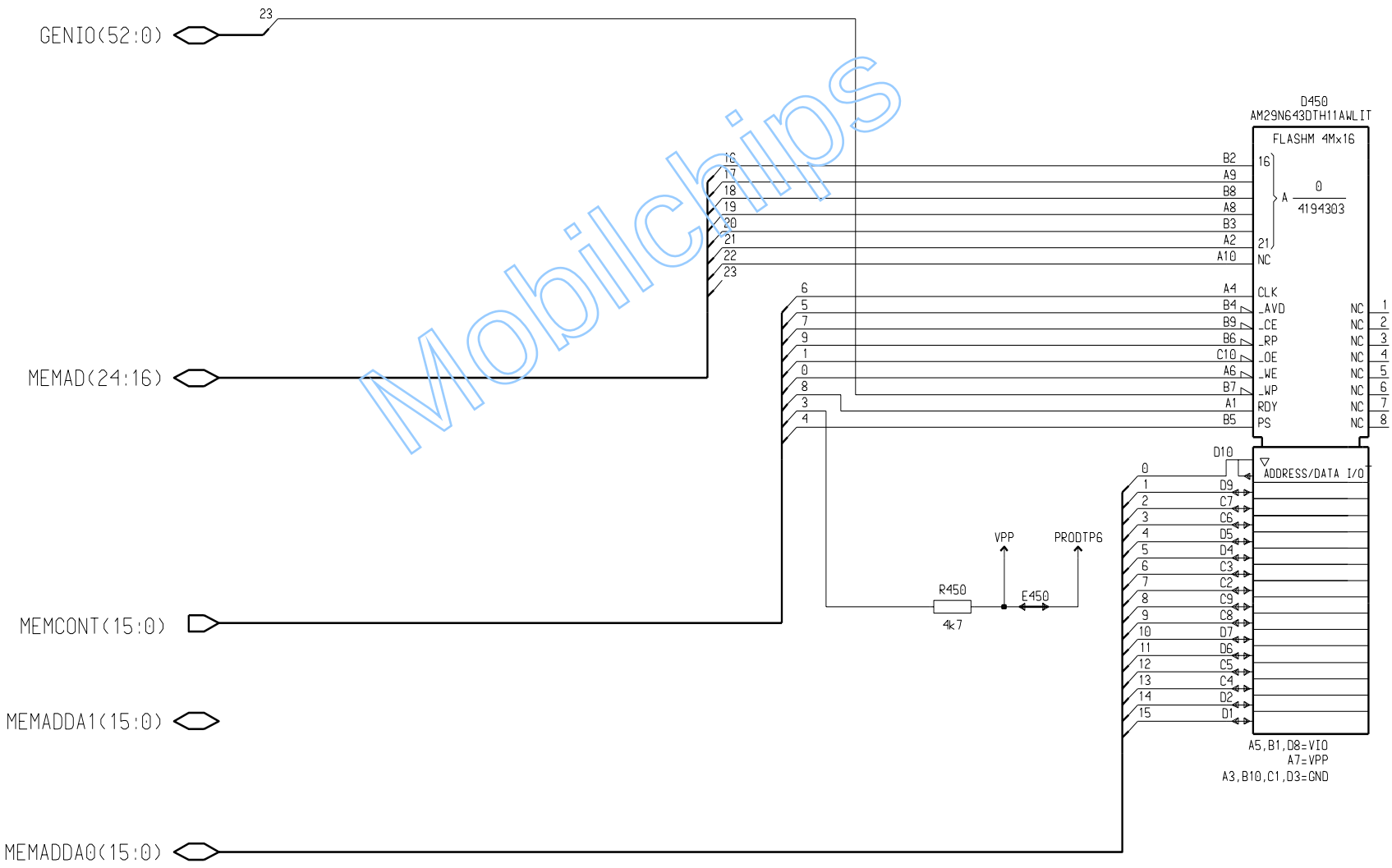
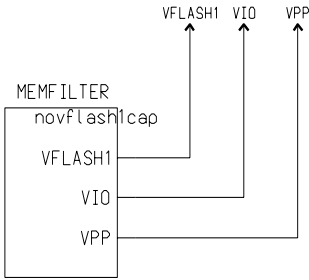


ac4a-14(12) SYS / LPRF (Power RF interface)

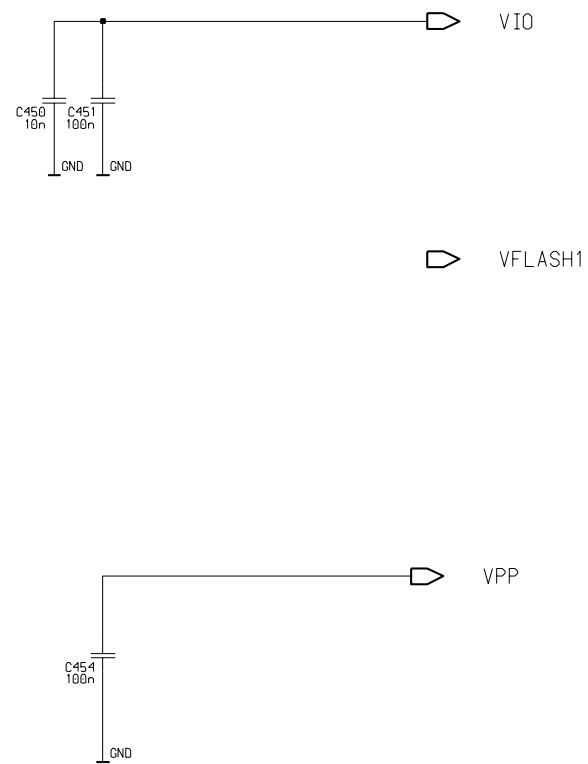


ac4a-14(13) SYS / MEMORY AMD64 (Burst Read / Write)

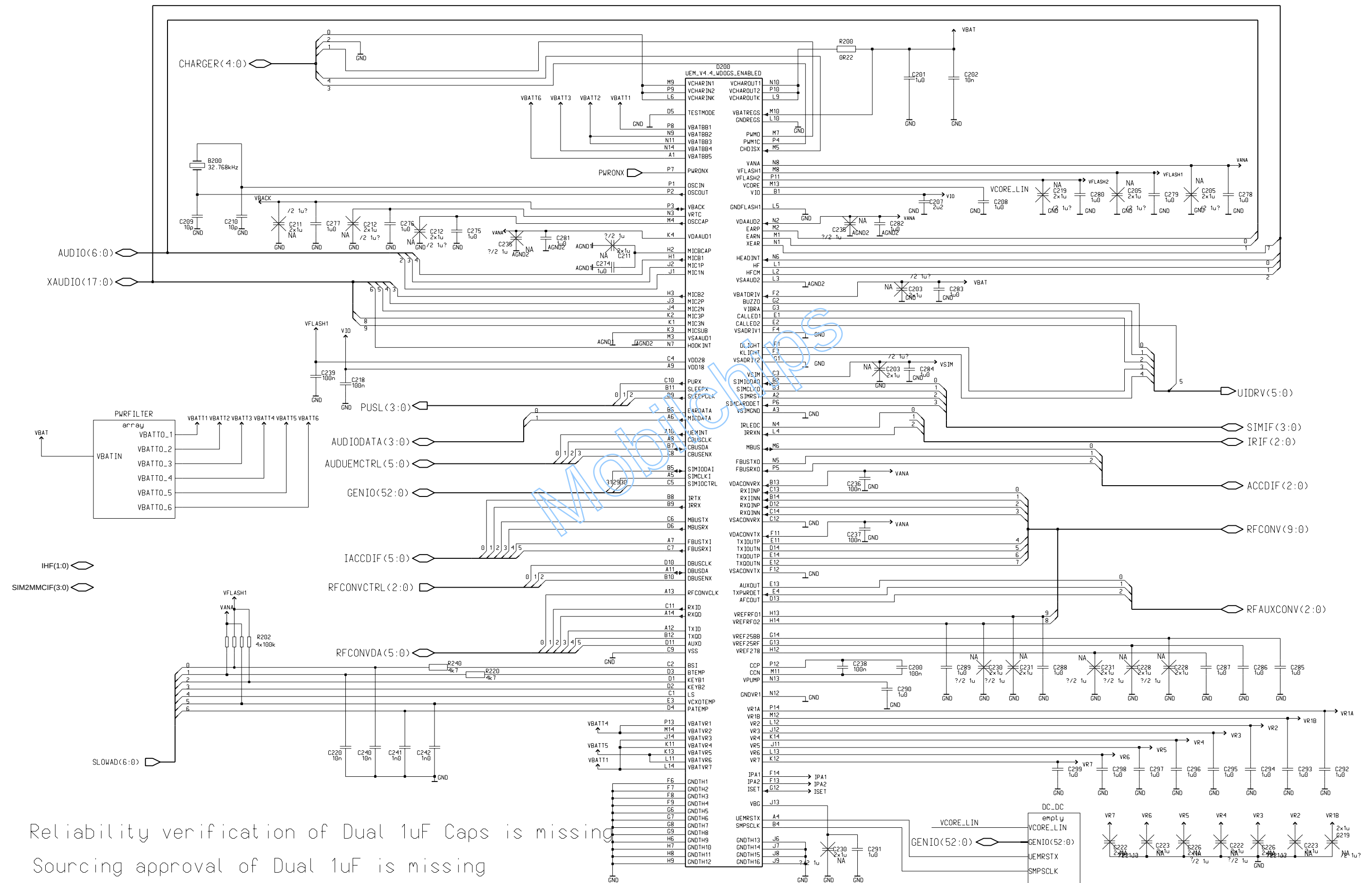
Do not use this sheet with UPP version 1



ac4a-14(14) SYS / MEMORY / MEMFILTER (Discrete capacitors for memory without VFlash1).

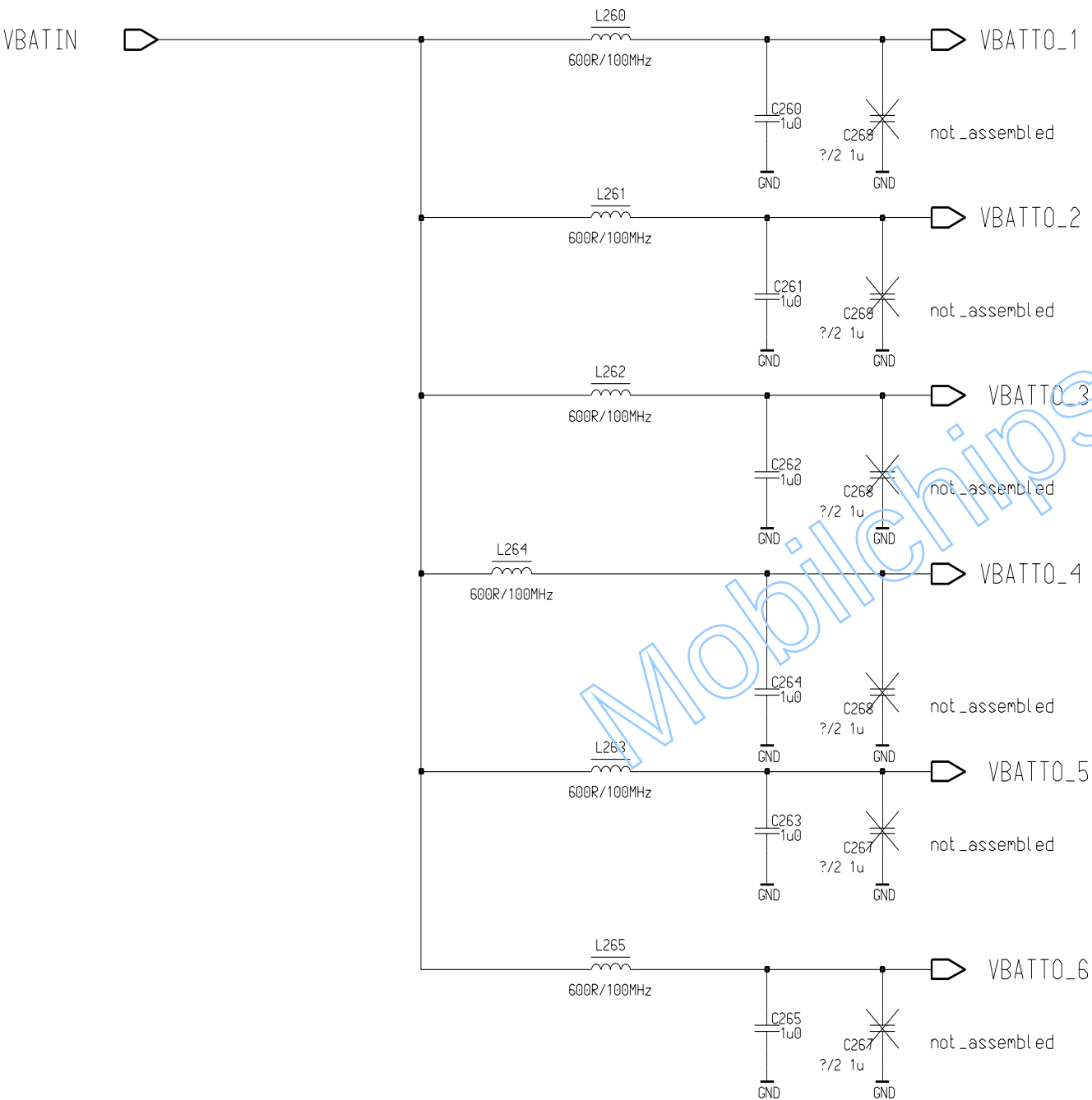


ac4a-14(15) SYS / POWER (Dual power management)



Reliability verification of Dual 1uF Caps is missing

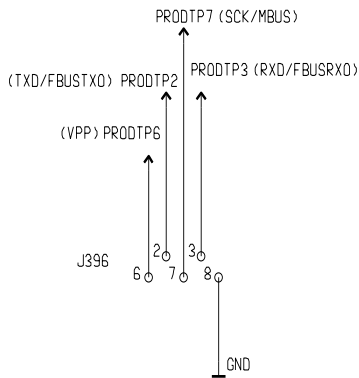
Sourcing approval of Dual 1uF is missing



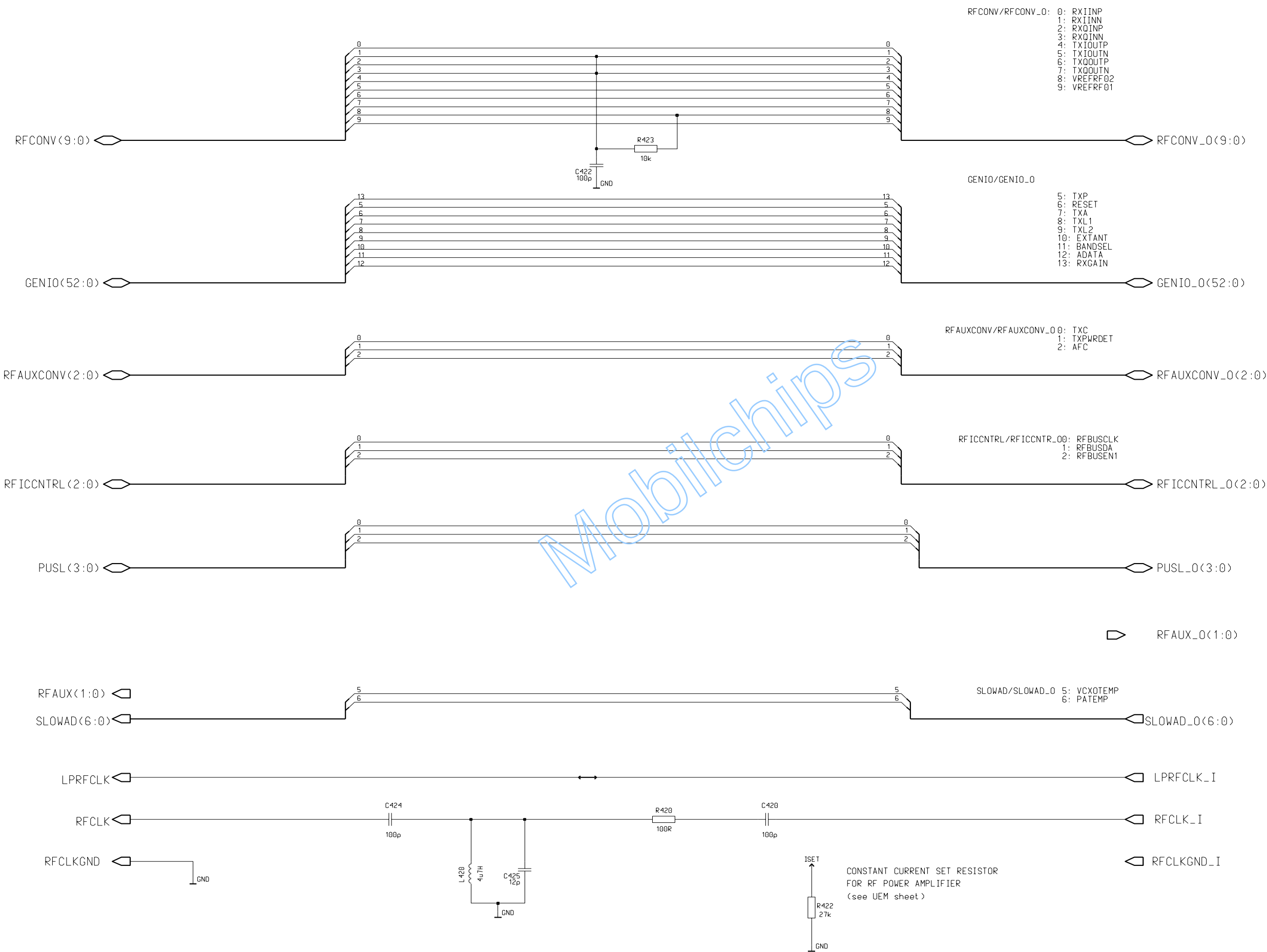
All dual capacitors have alternative discrete capacitors
These capacitors should be placed in layout
so that they will overlap

ac4a-14(17) SYS / Production Test Pattern

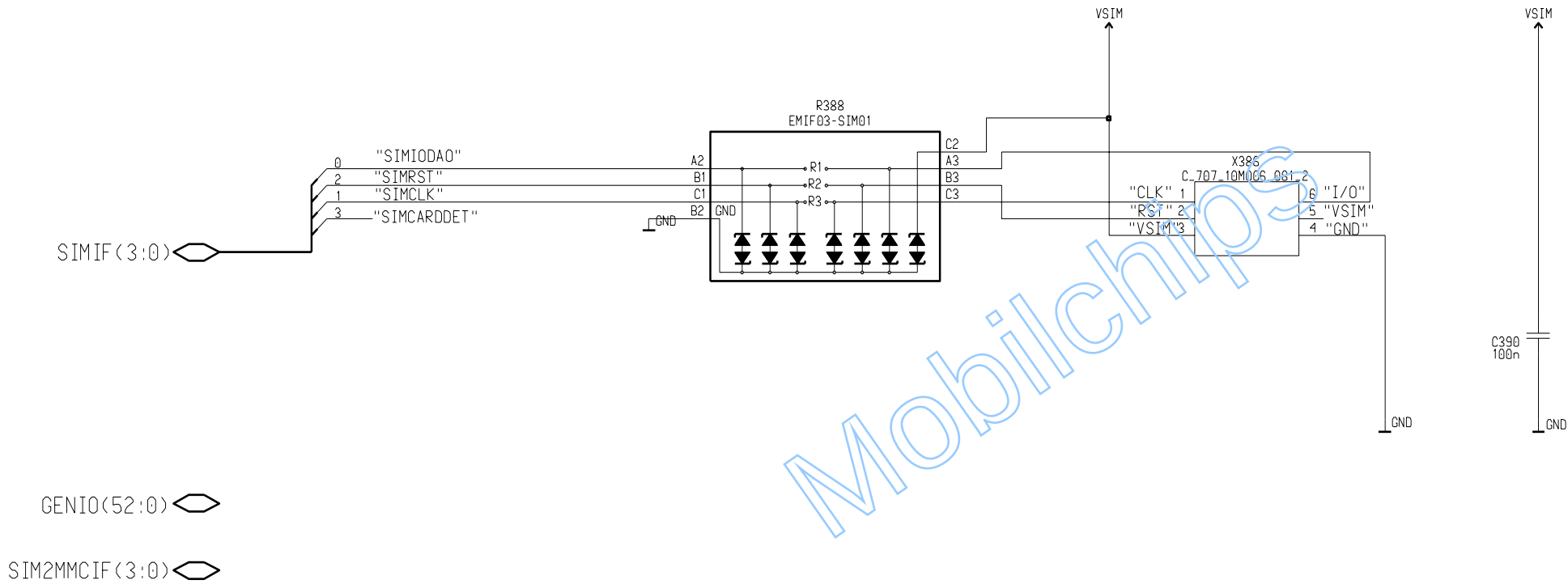
OUT

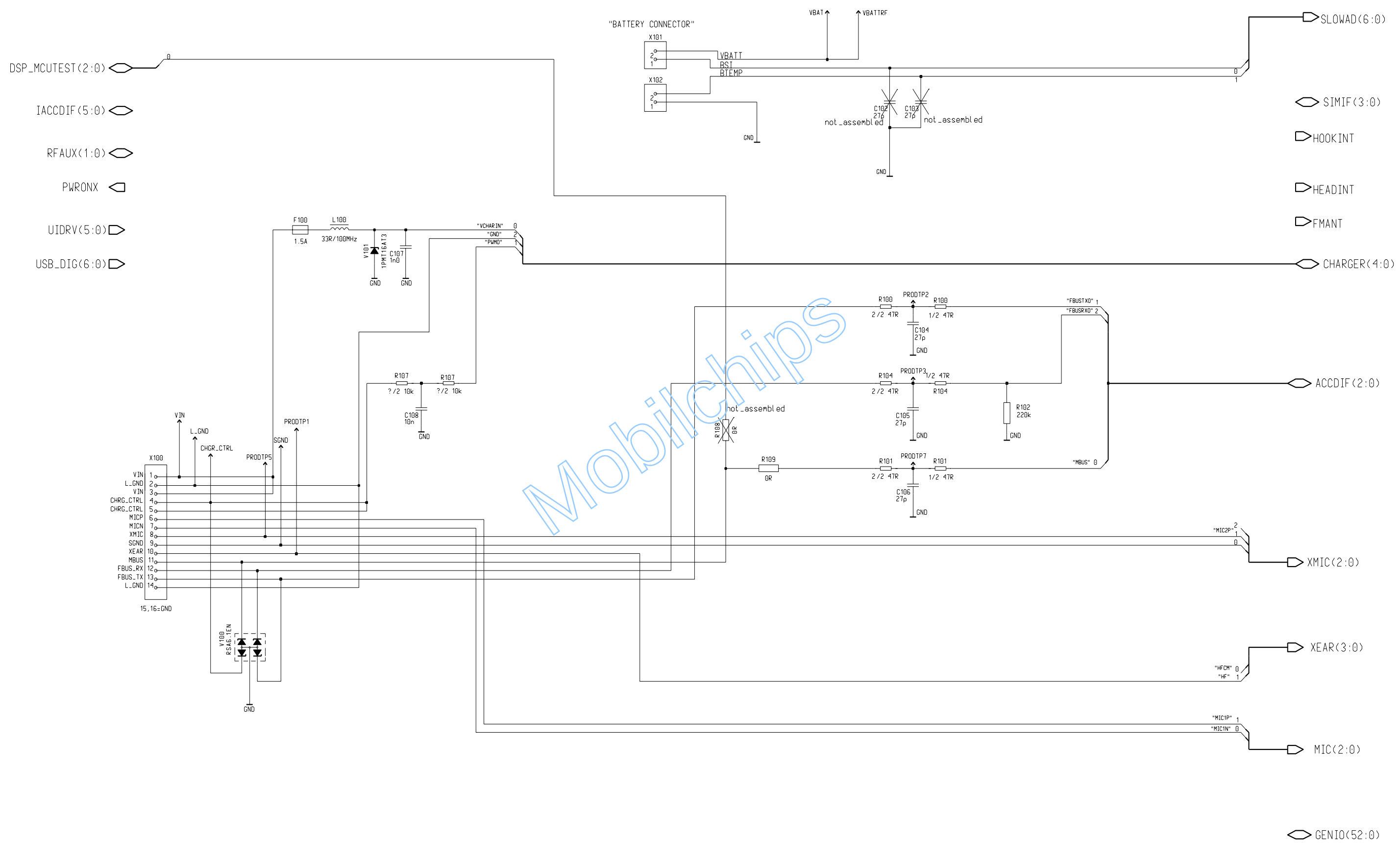


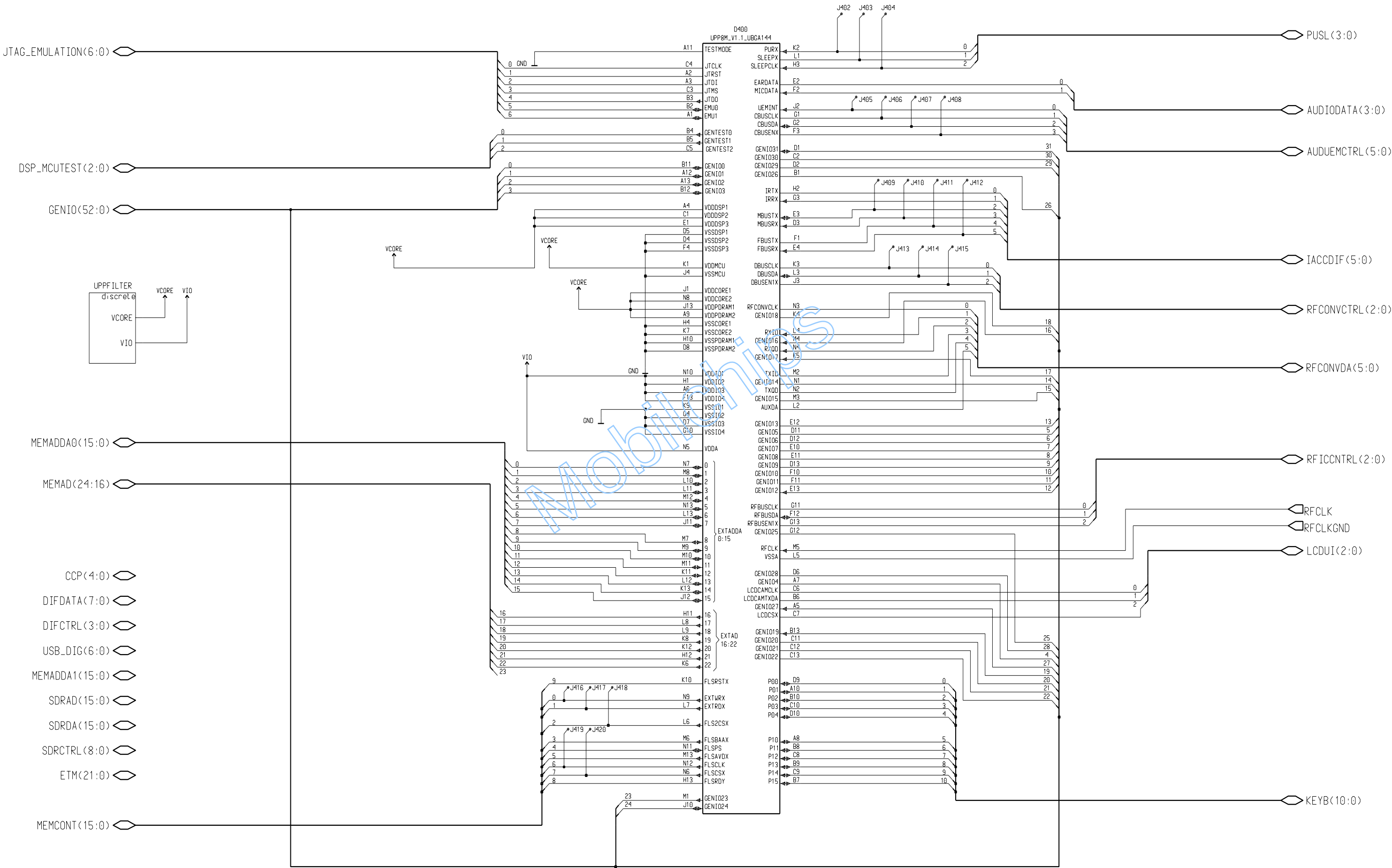
Mobilchips



ac4a-14(19) SYS / SIM (SIM reader)







ac4a-14(22) SYS / UPP / UPP FILTER (Discrete decoupling capacitors for UPP)

