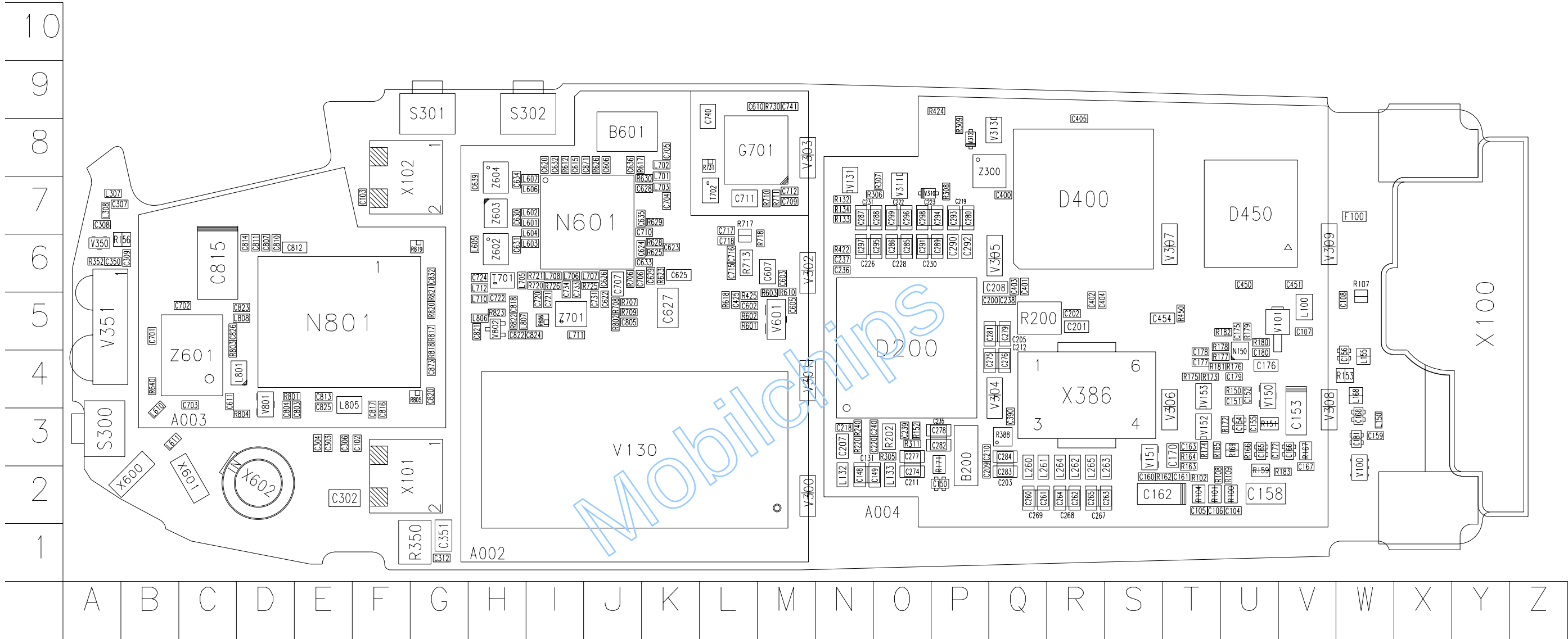
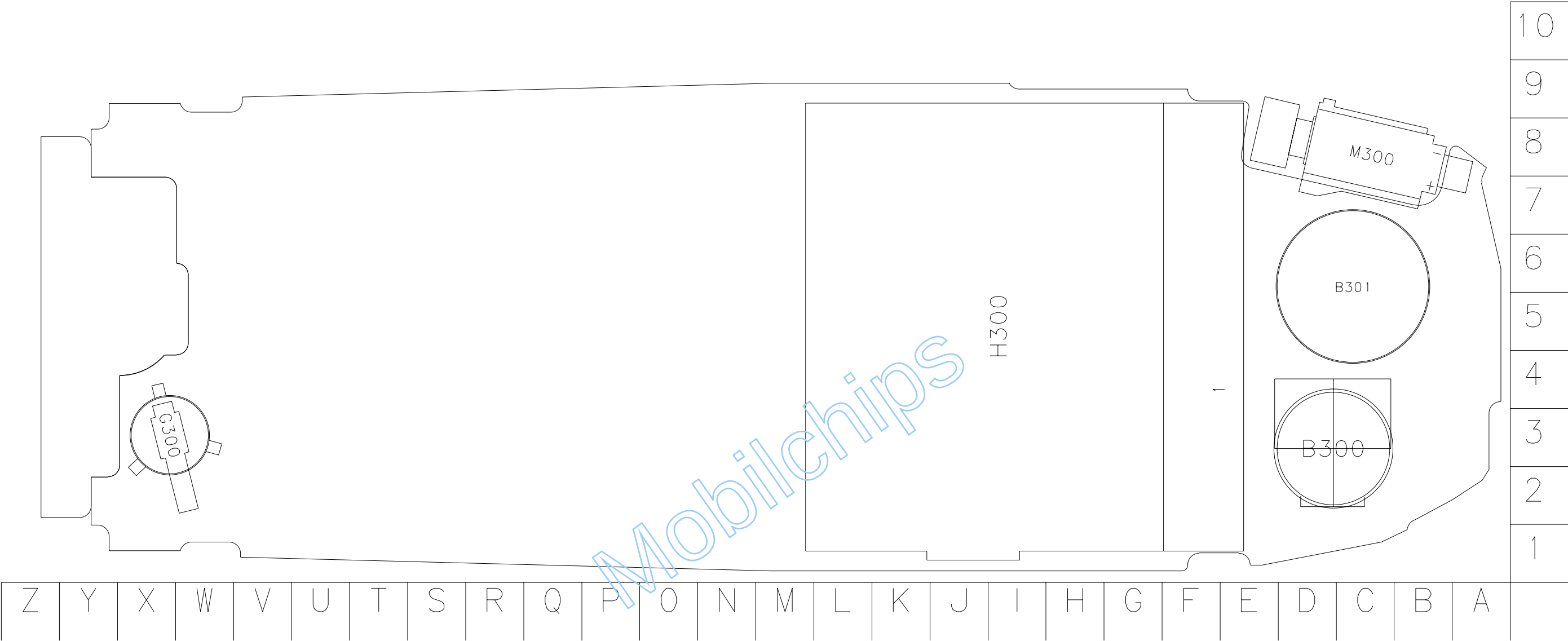


10 - Layout



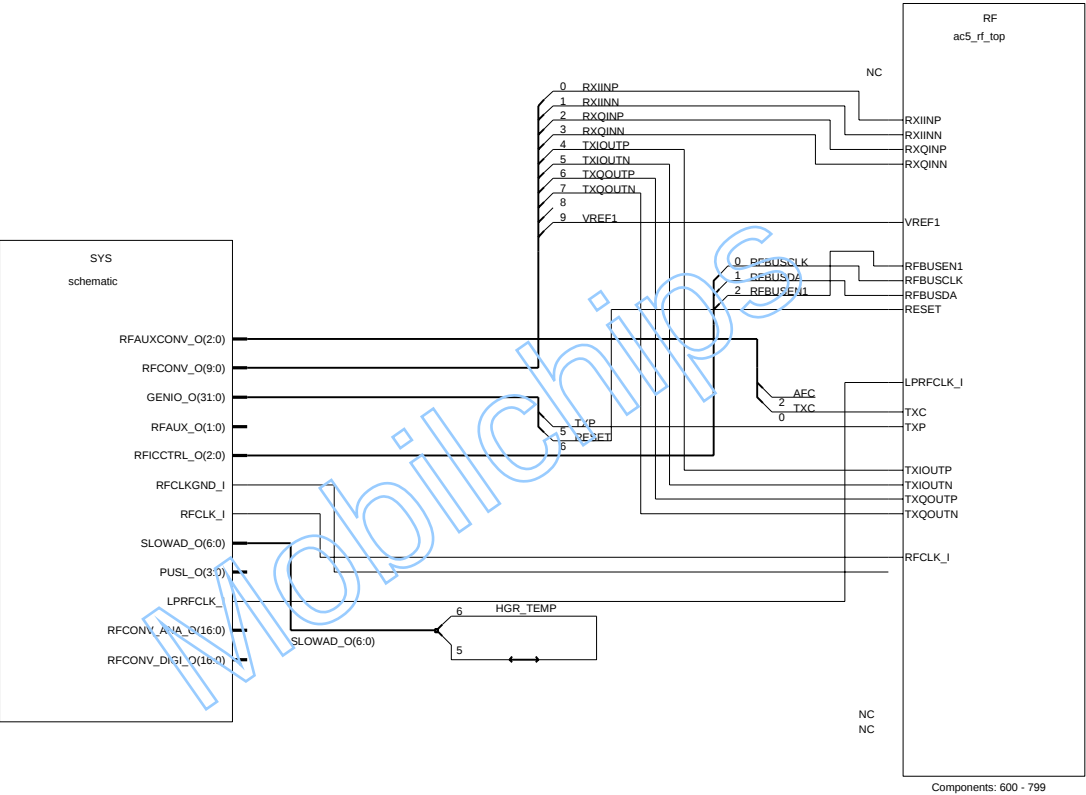


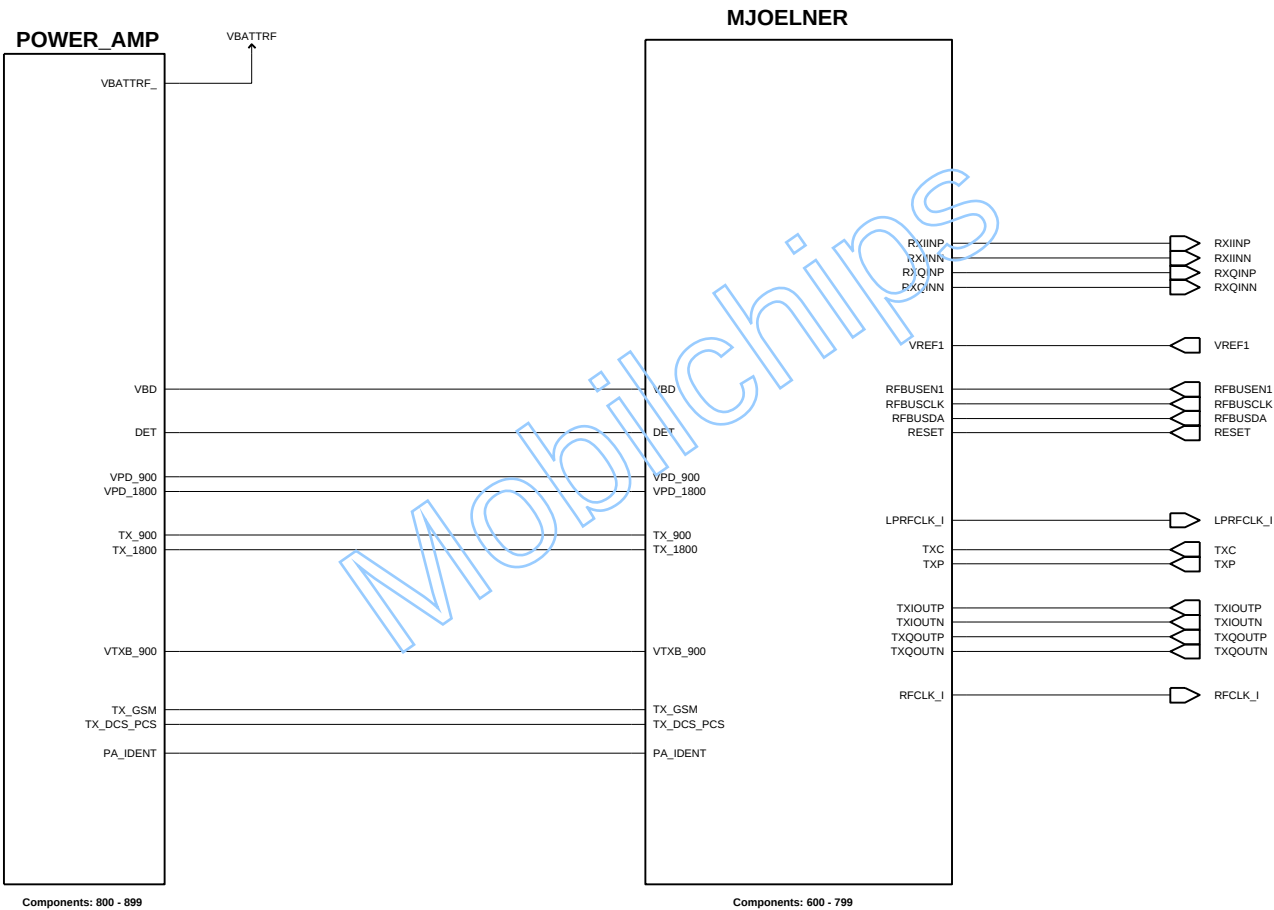
11 – SCHEMATICS

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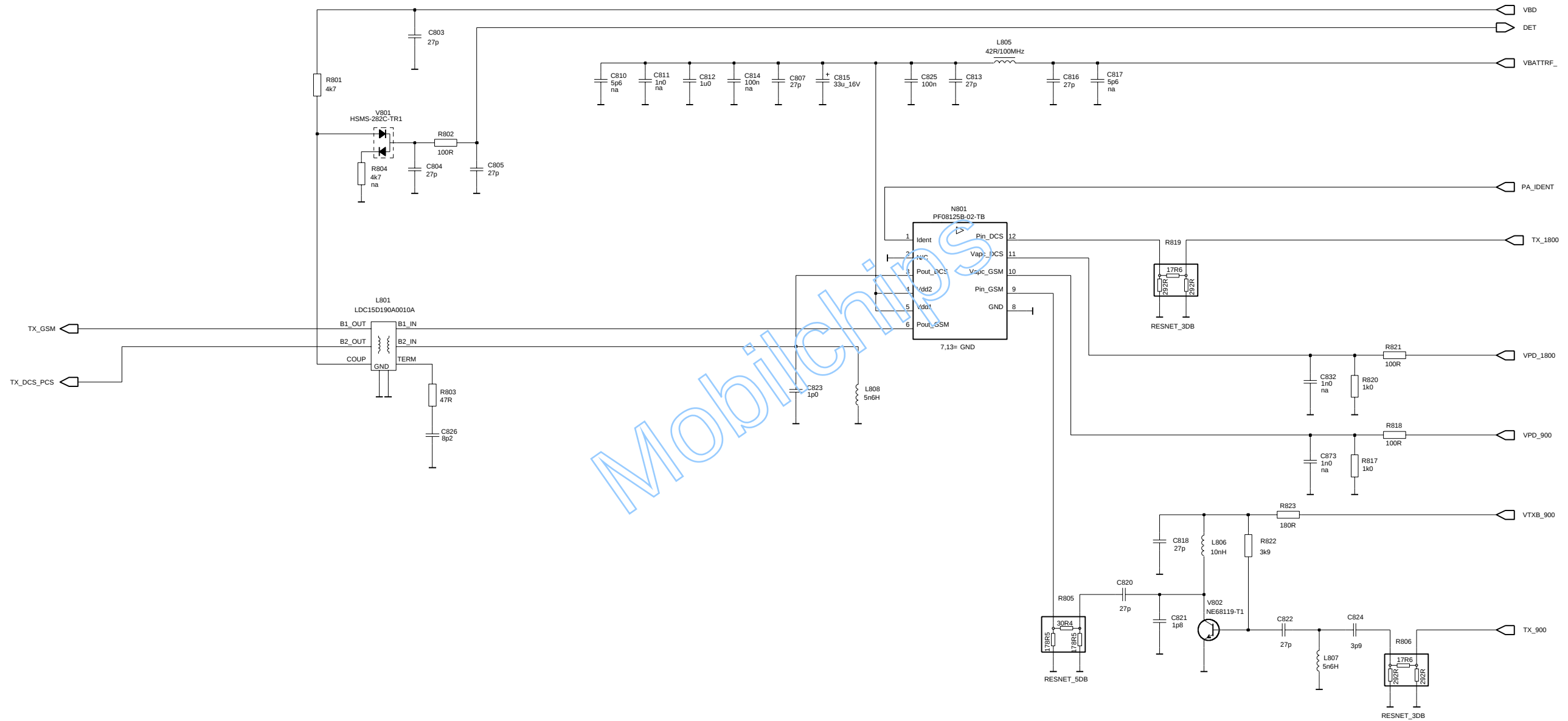
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Block Diagram

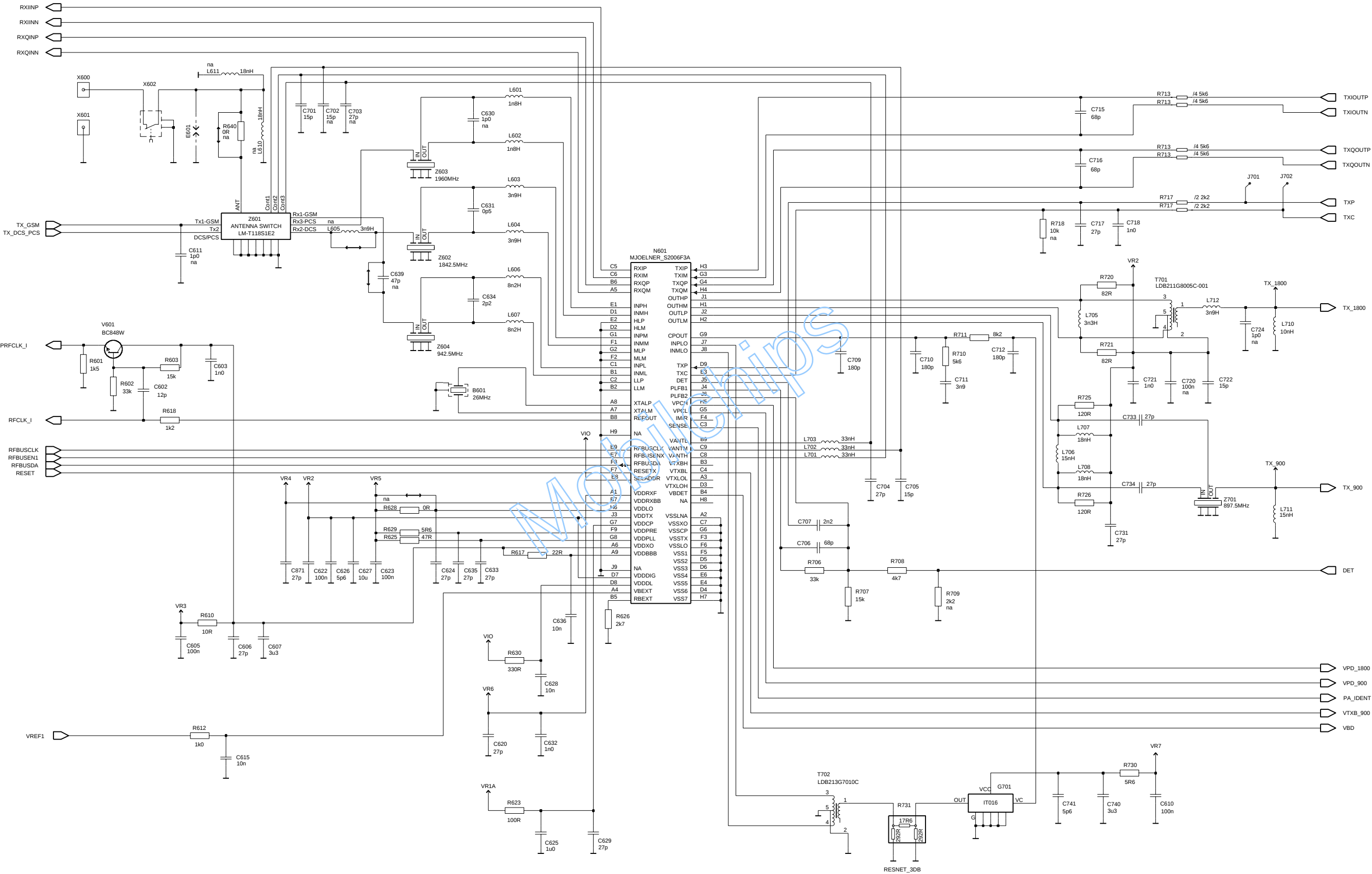




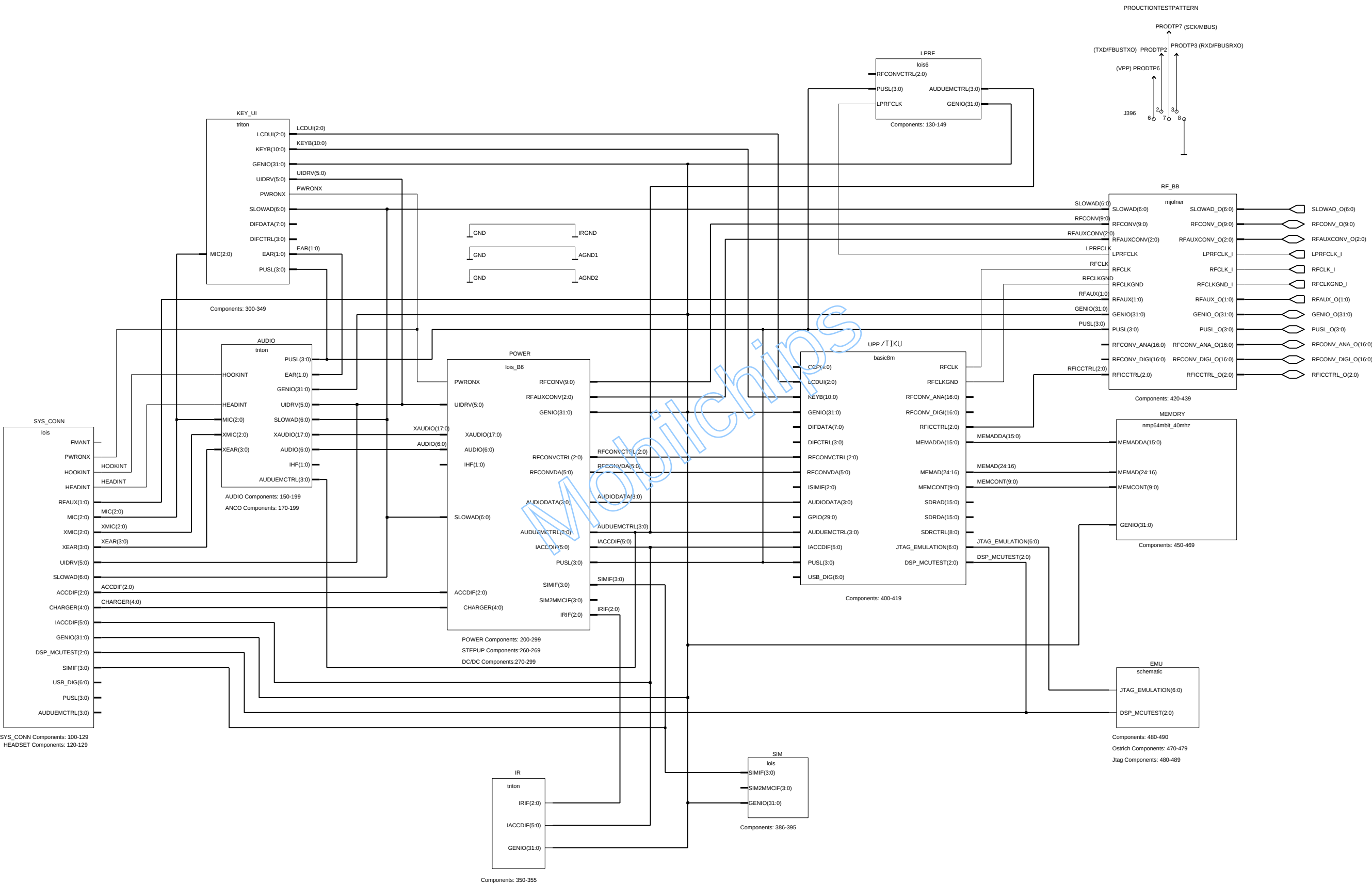
Power Amplifier (ac5a_15)



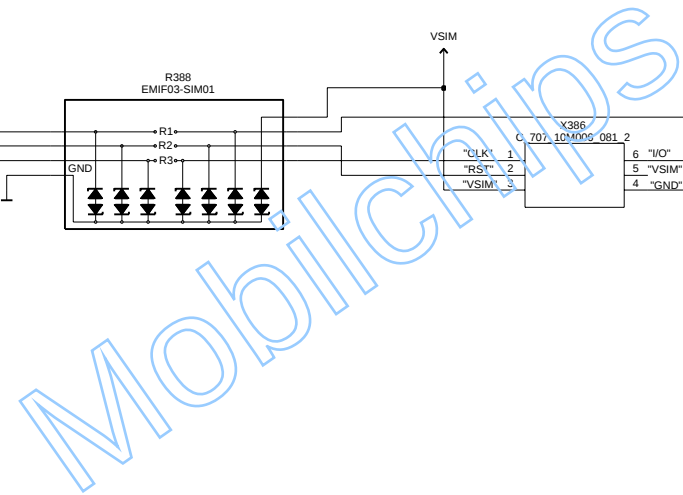
Mjoelner (ac5a_15)



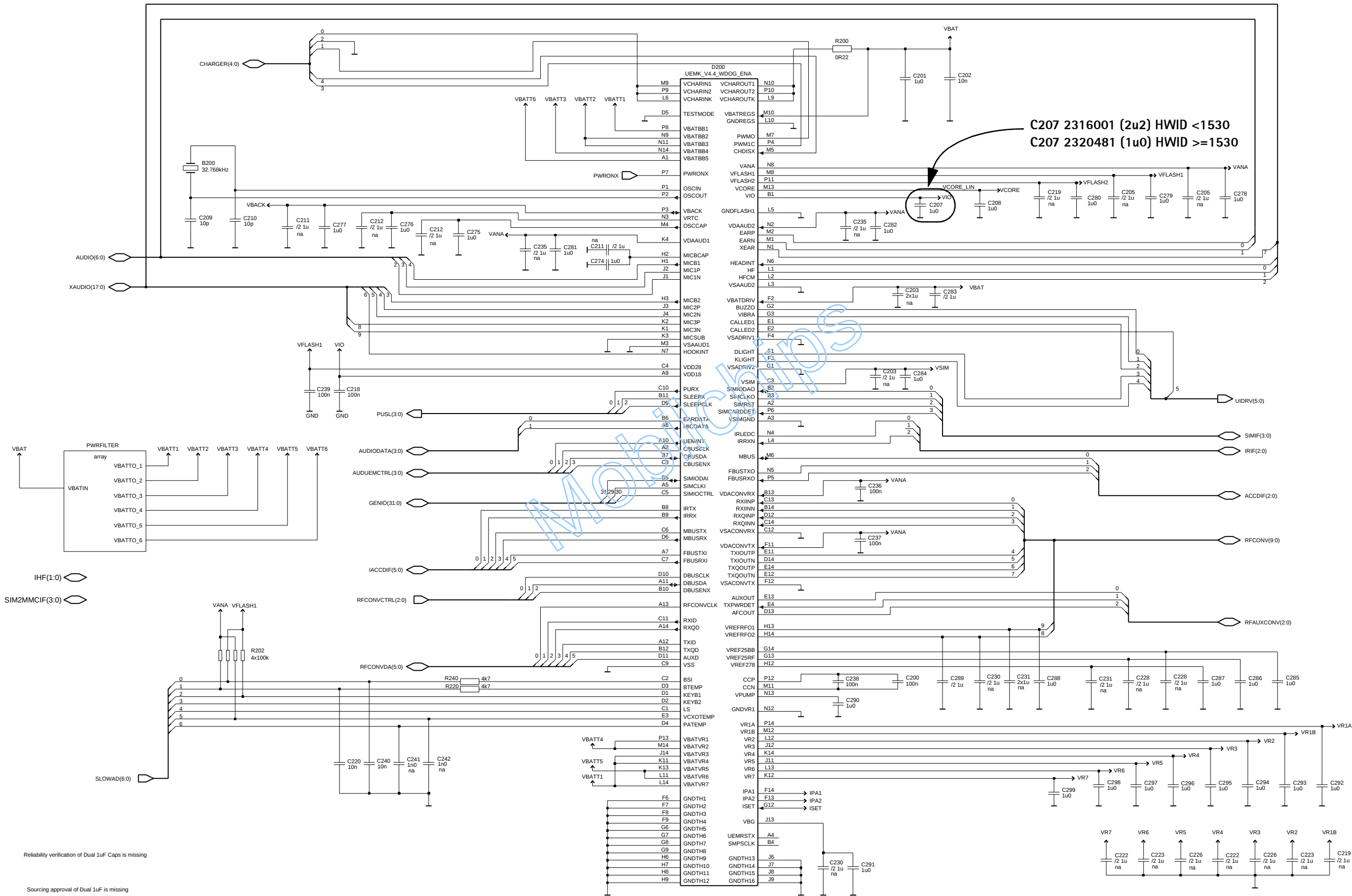
DCT4 Common Baseband



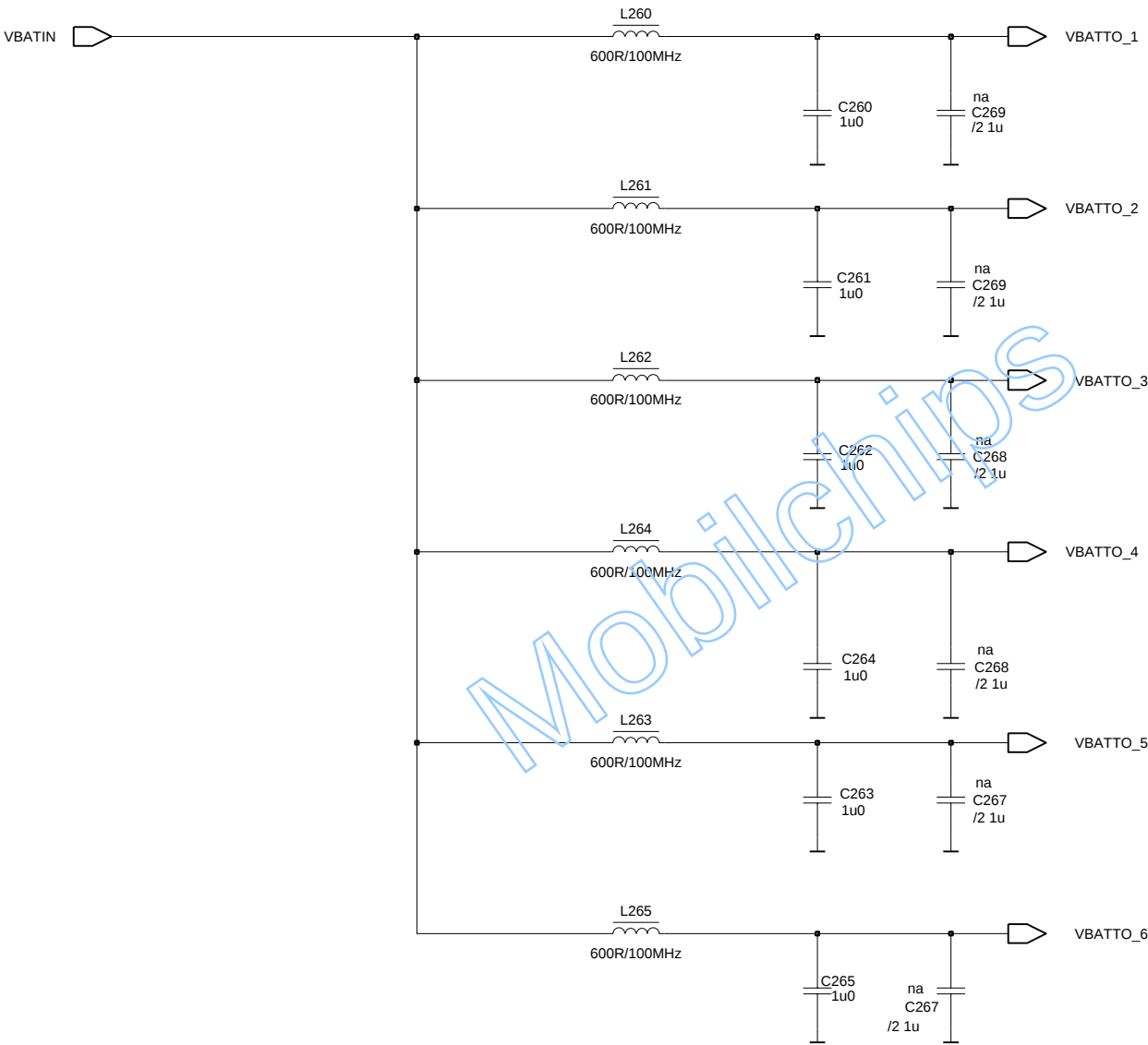
DCT4 SIM Interface



Dual Power Management



Light Filtering

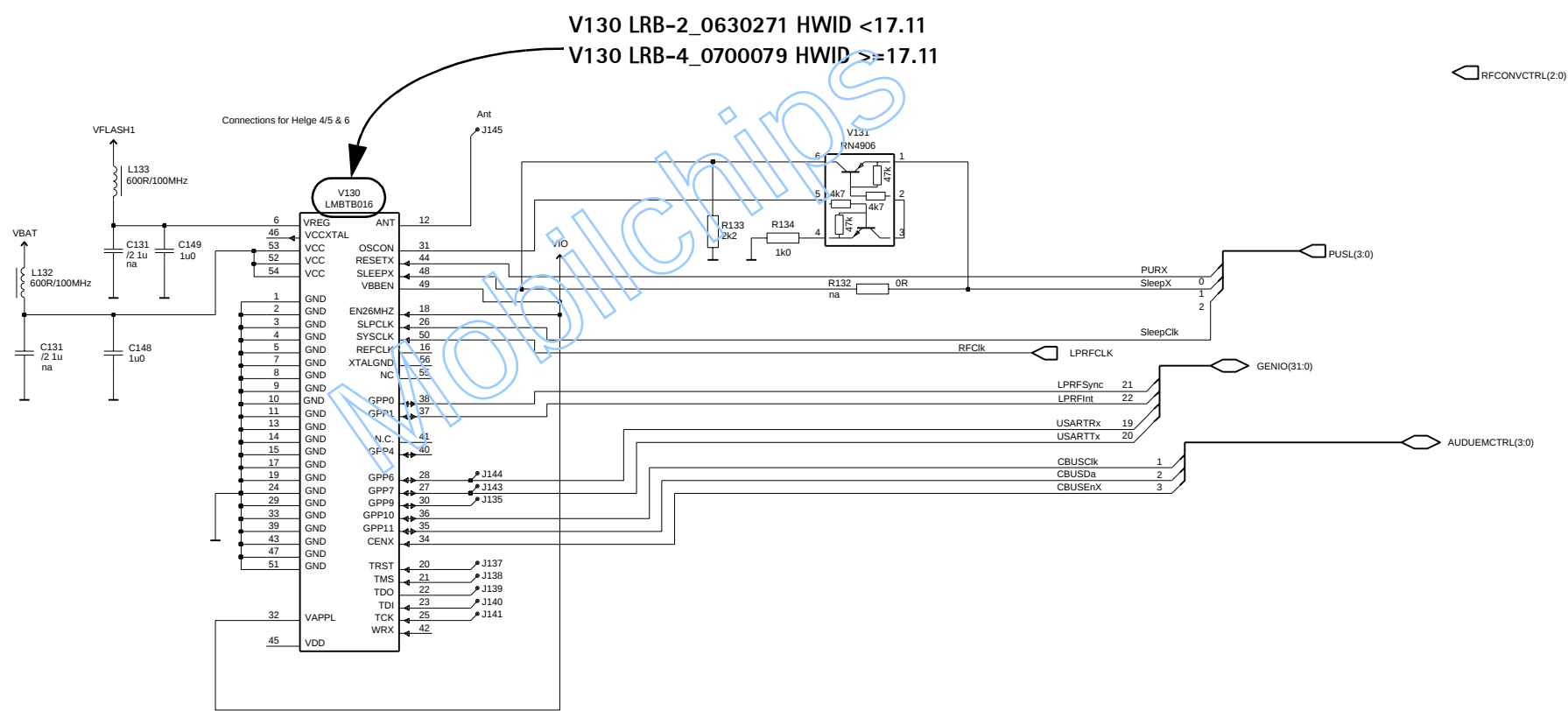


All dual capacitors have alternative discrete capacitors

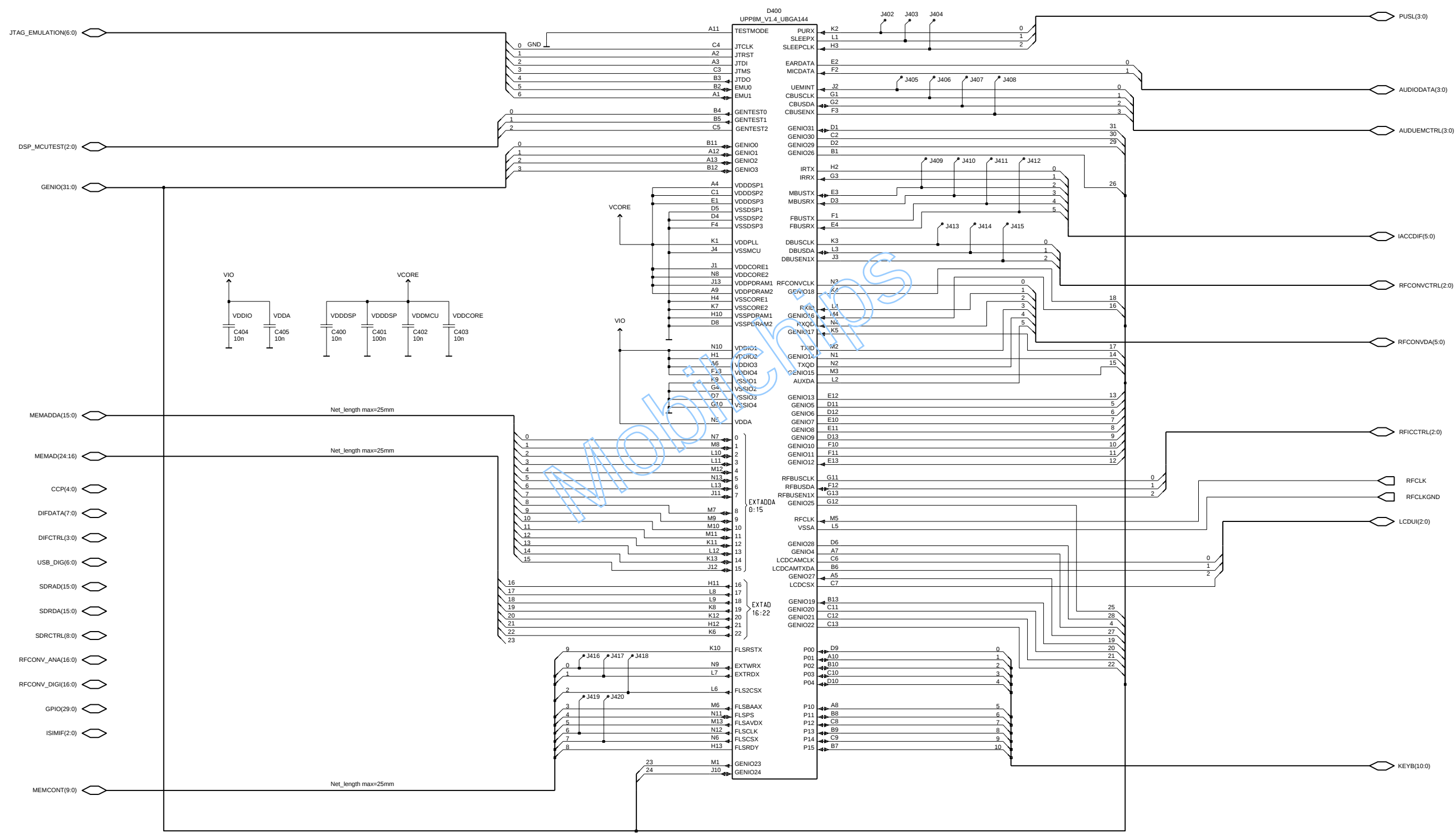
These capacitors should be placed in layout

so that they will overlap

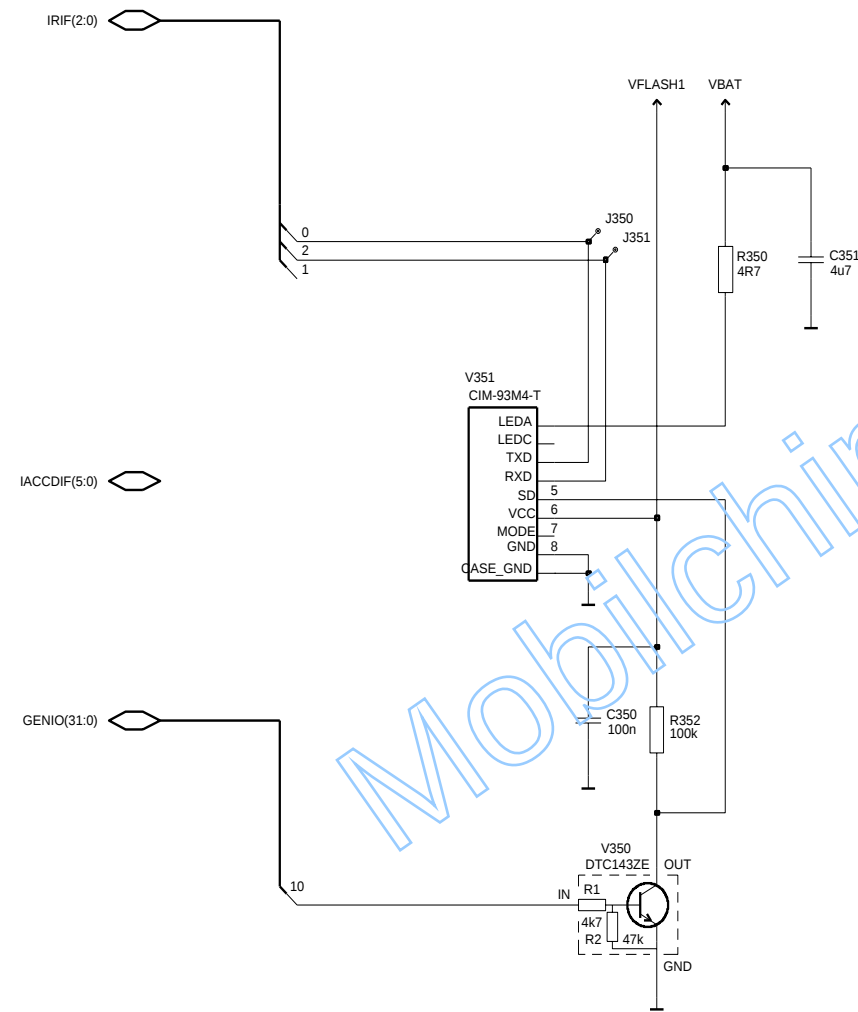
Low Power RF Module



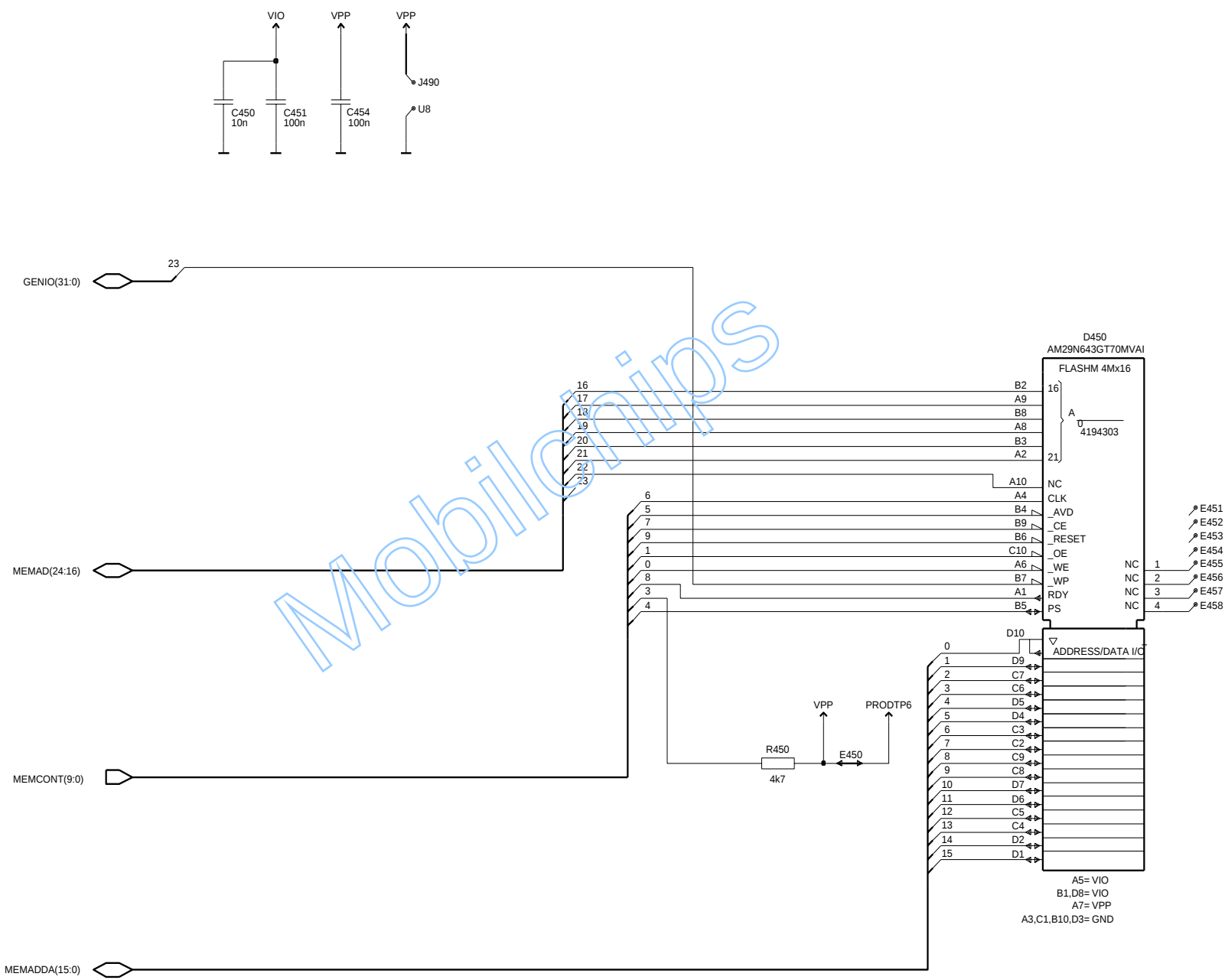
UPP 8M Implementation



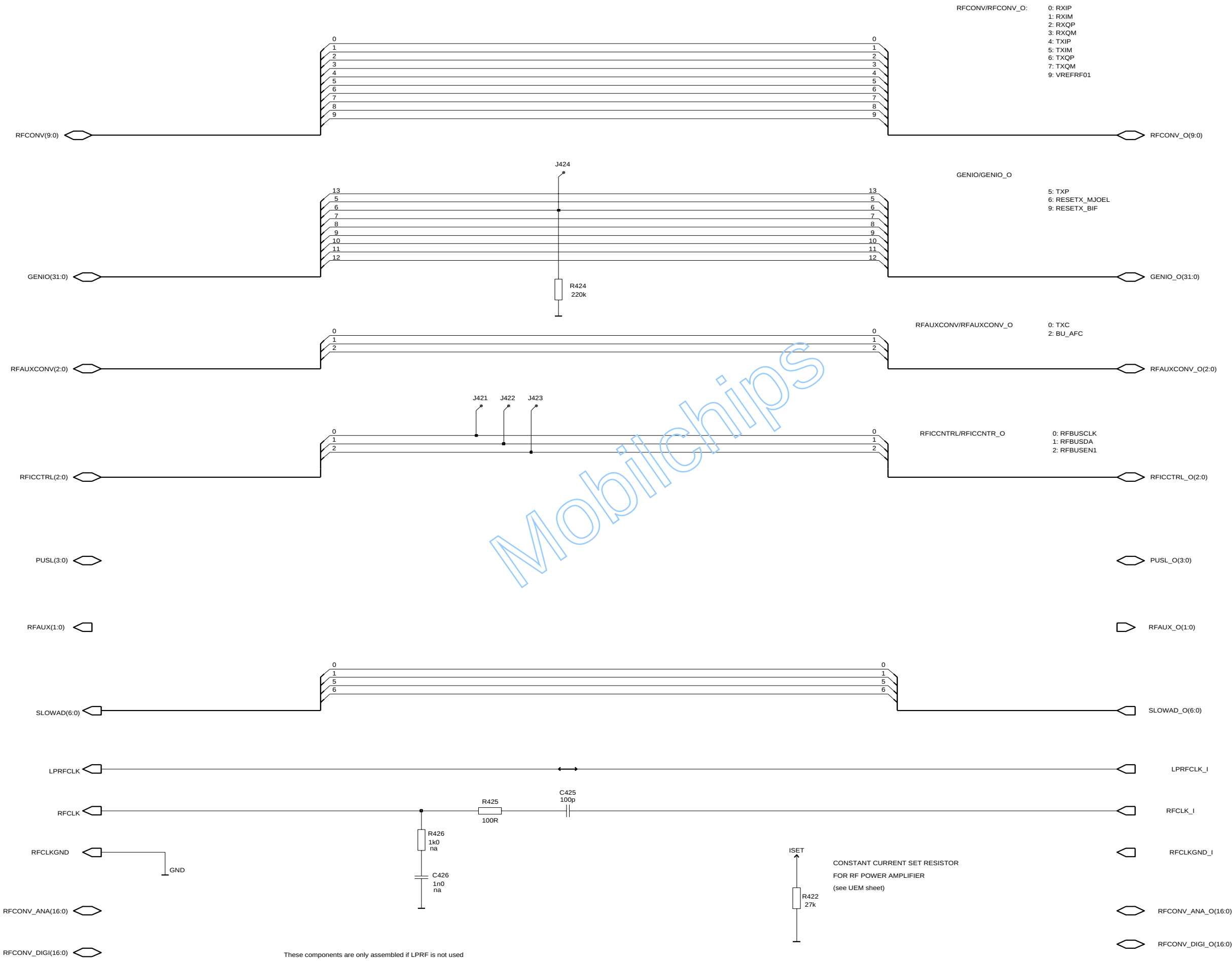
IR Module Based Implementation



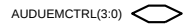
Flash Memeory 64 Mbit



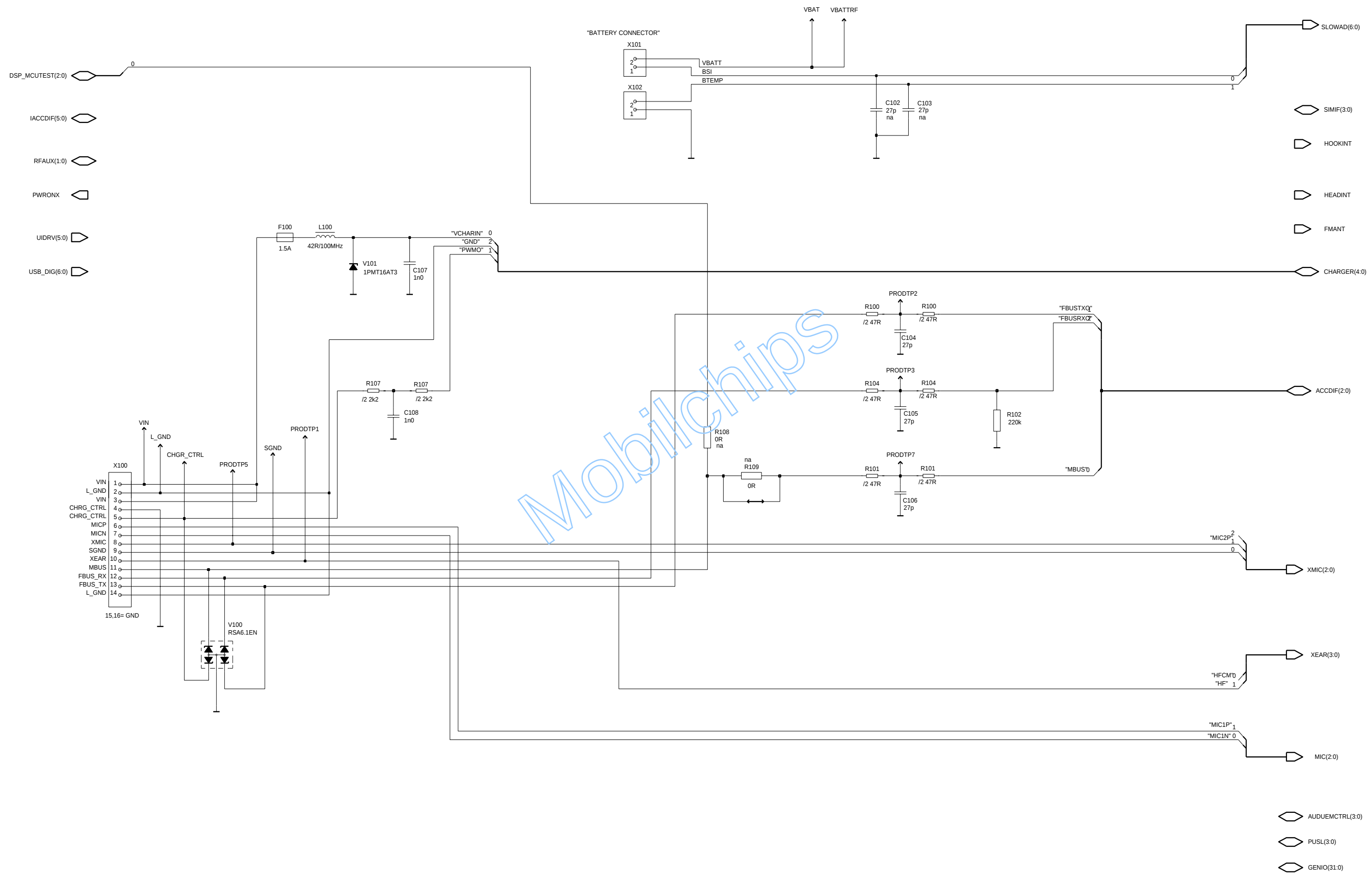
GSM RF Baseband Interface



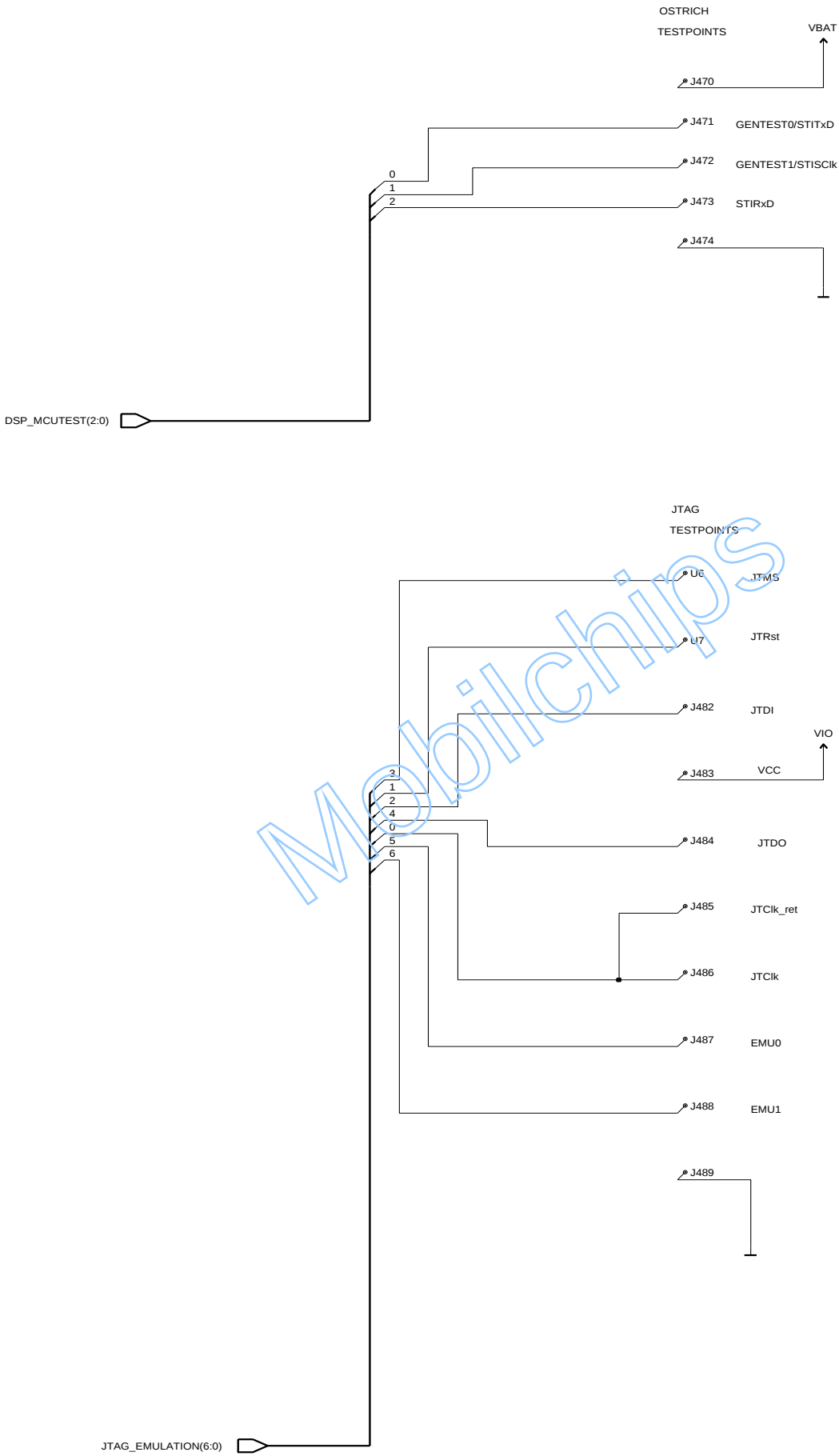
Audio Interface



DCT4 System Connector



DCT4 Test Interface



DCT4 User Interface

