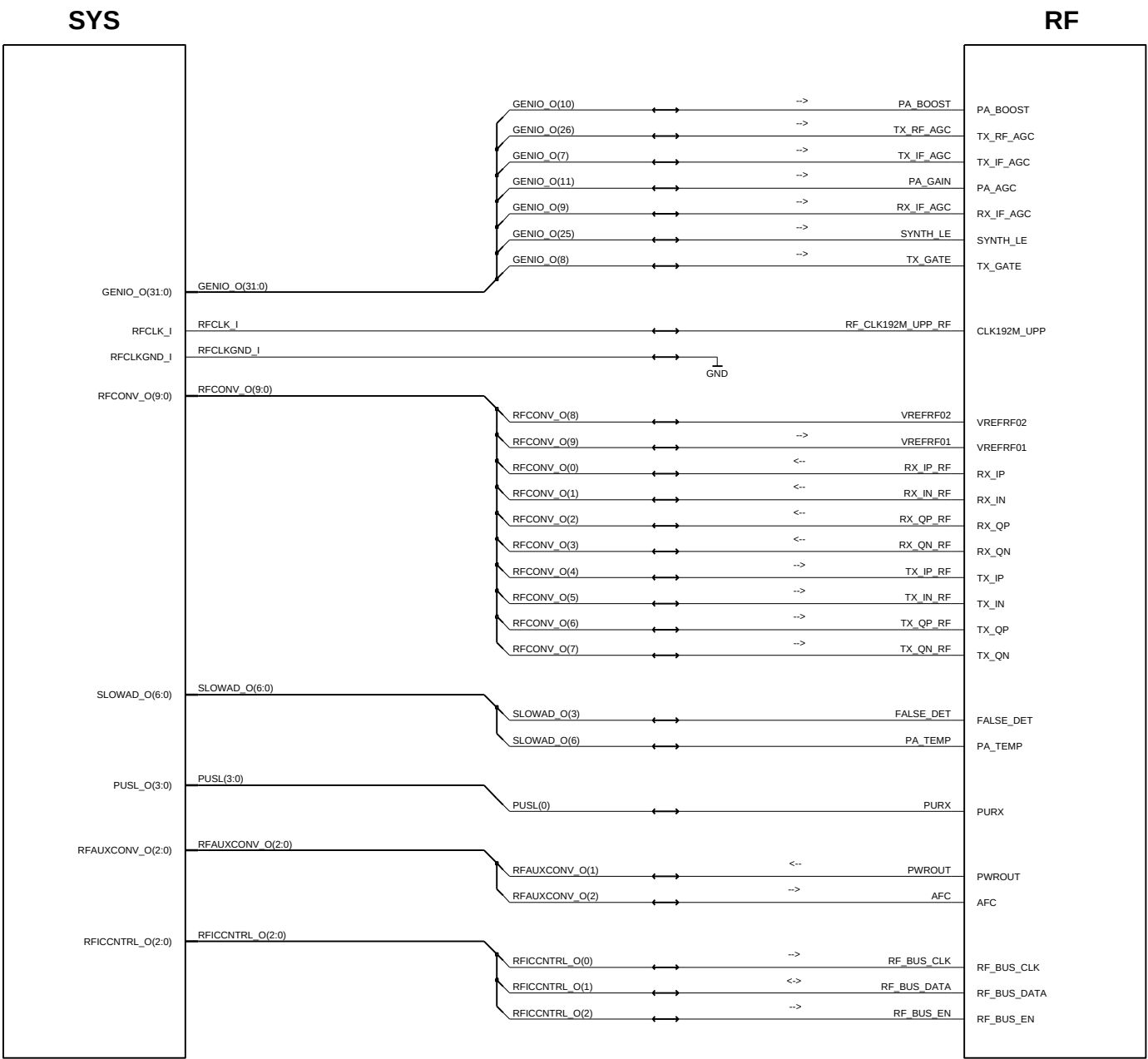
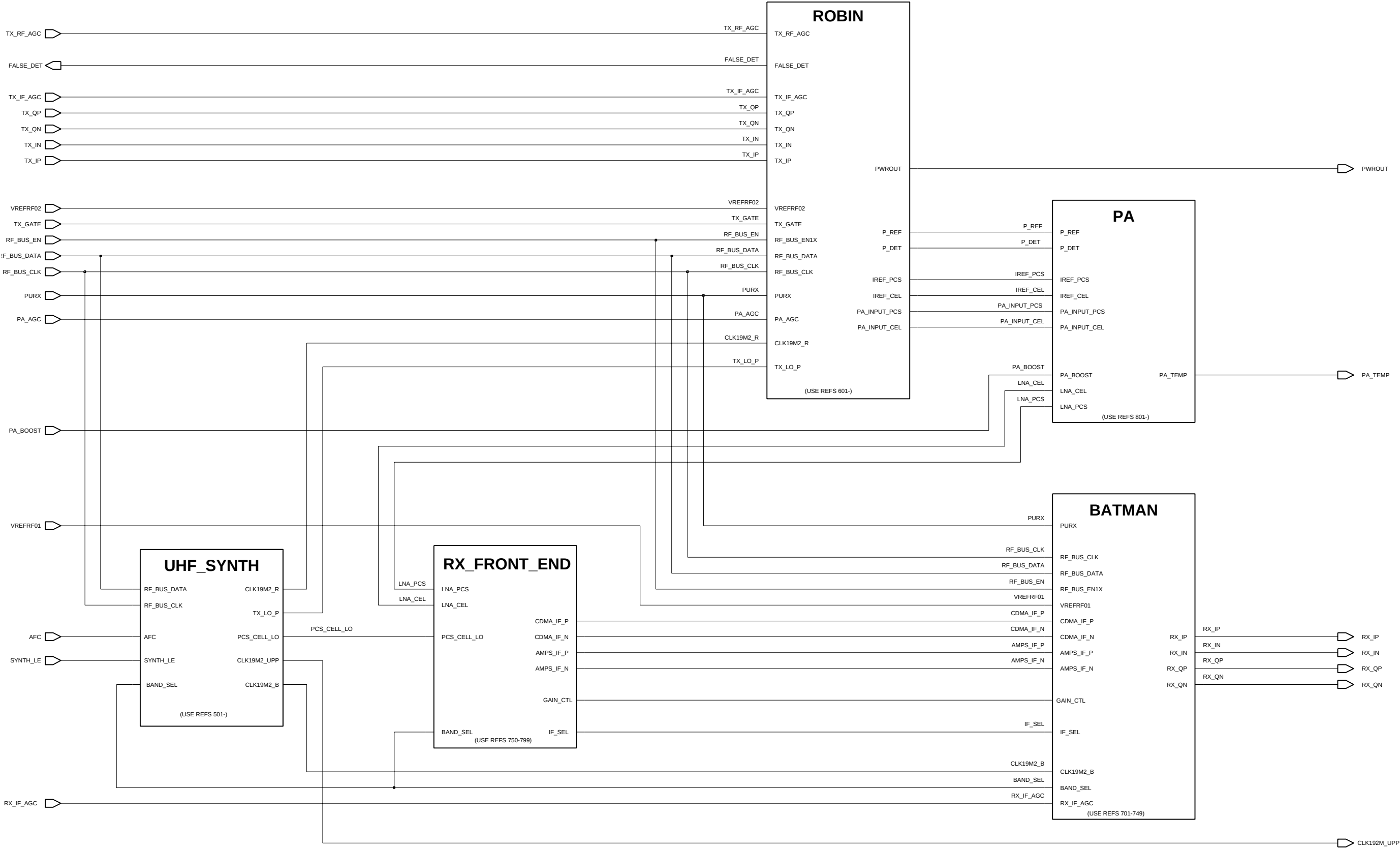


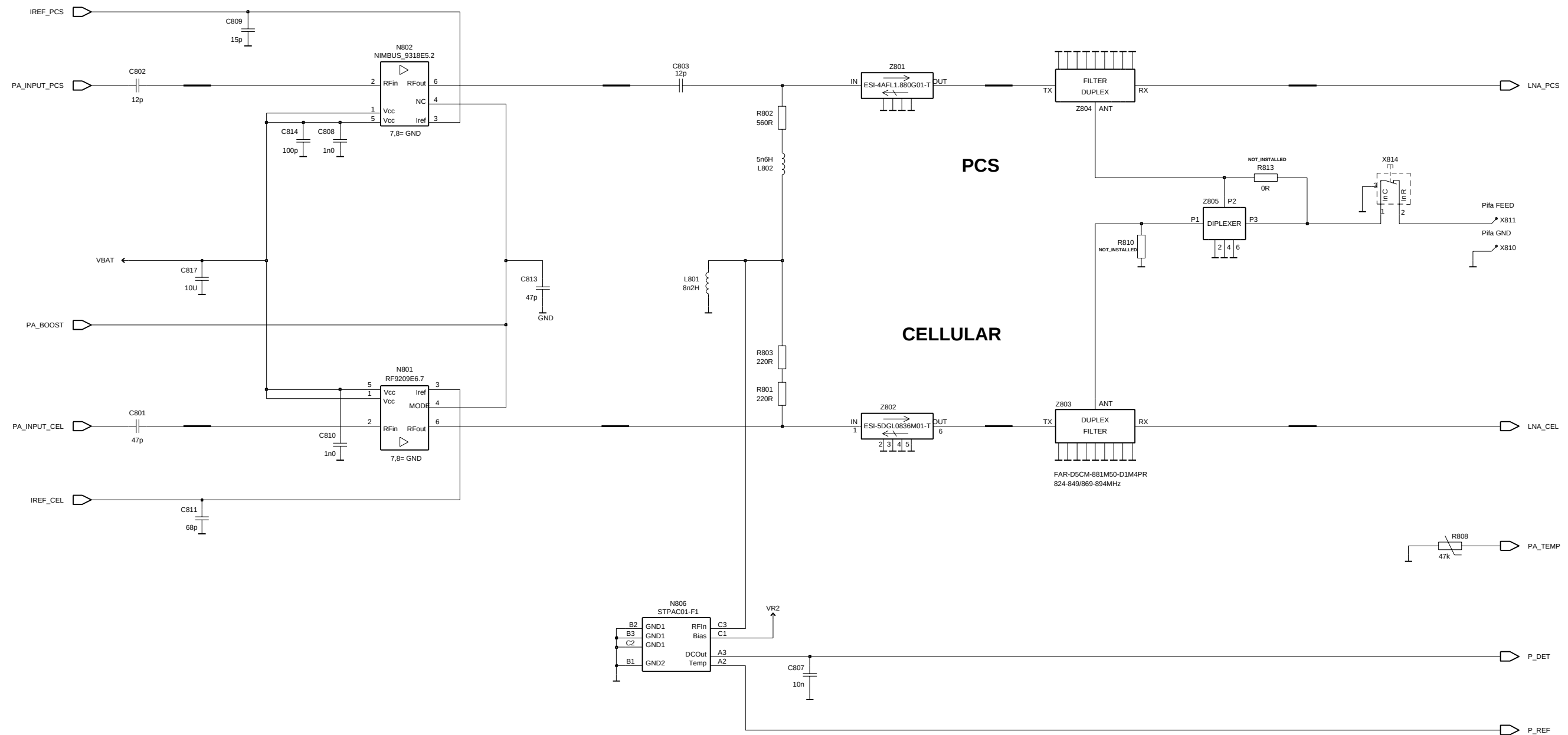
Top-level Diagram



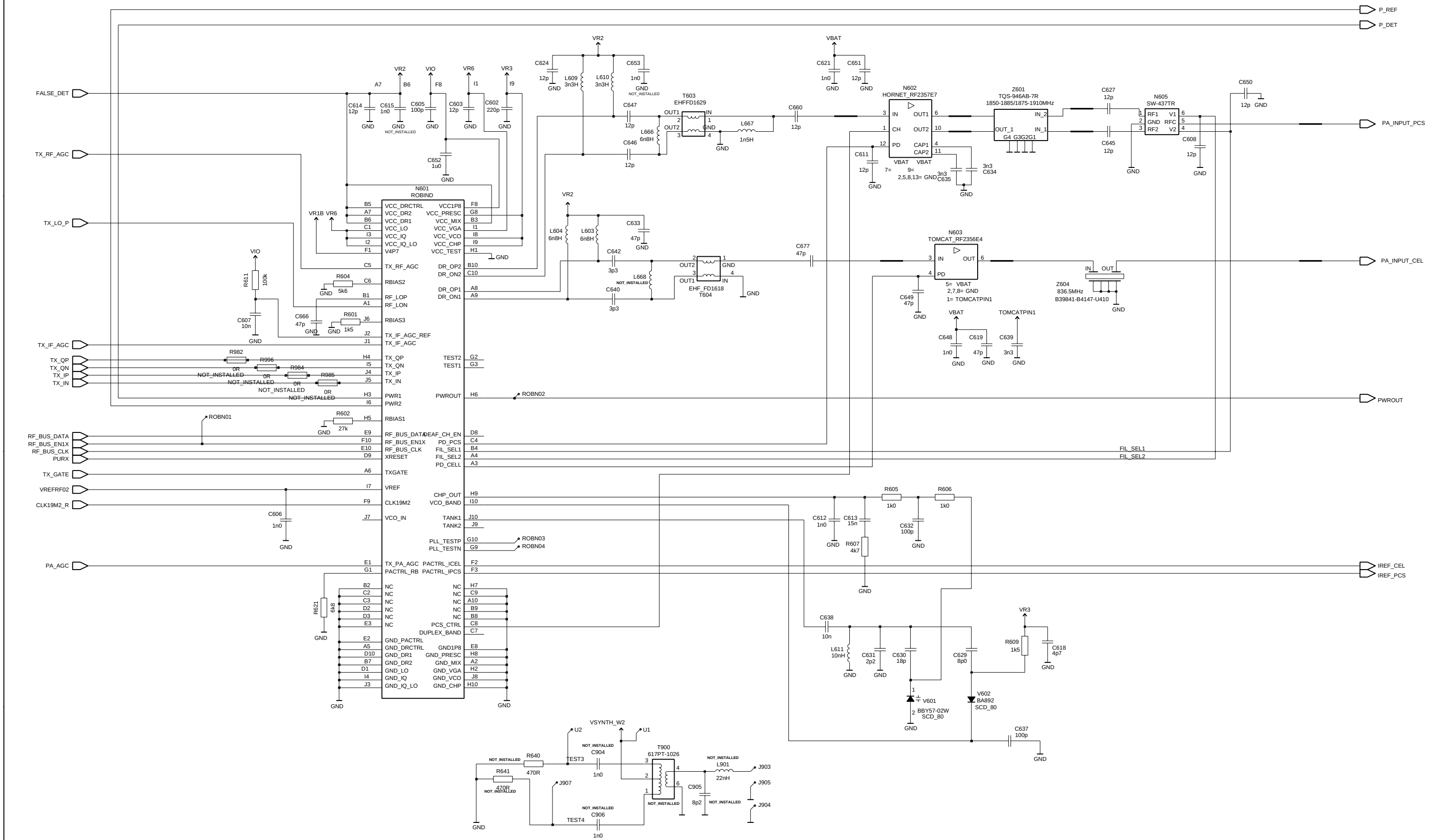
RF Top-level Diagram



PA



Robin



Truth Table for Split-Band Filter PATH:

DRV CNTL	DRV OUT STATE	VCONT1	VCONT2	SWITCH IN STATE
0 volt	RFOUT1	0 volt	2.7 volt	RF1
2.7 volt	RFOUT2	2.7 volt	0 volt	RF2

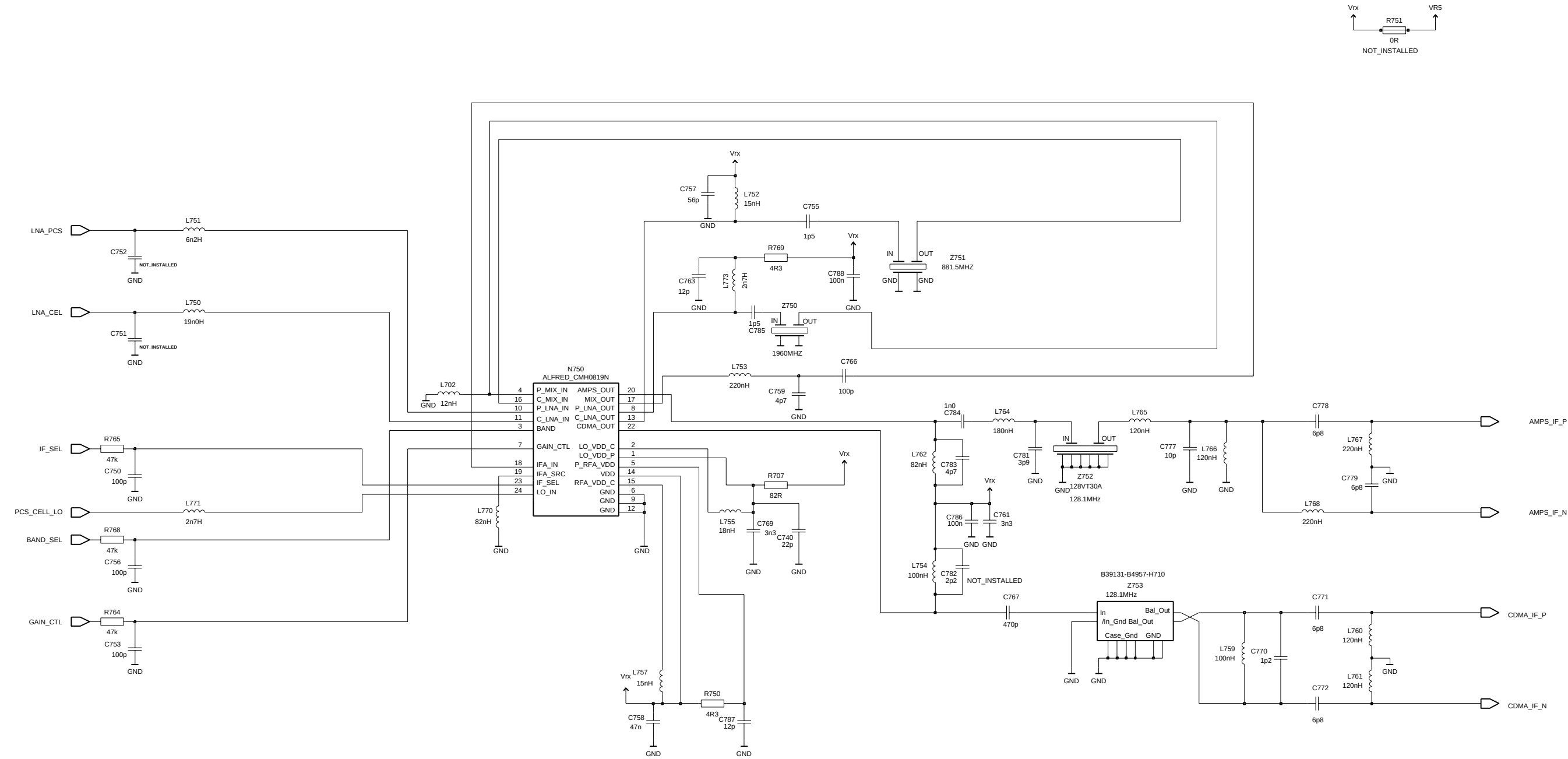
	VCO_BAND
Cell band	low (shorted to ground)
PCS band	tri-stated (open circuit)

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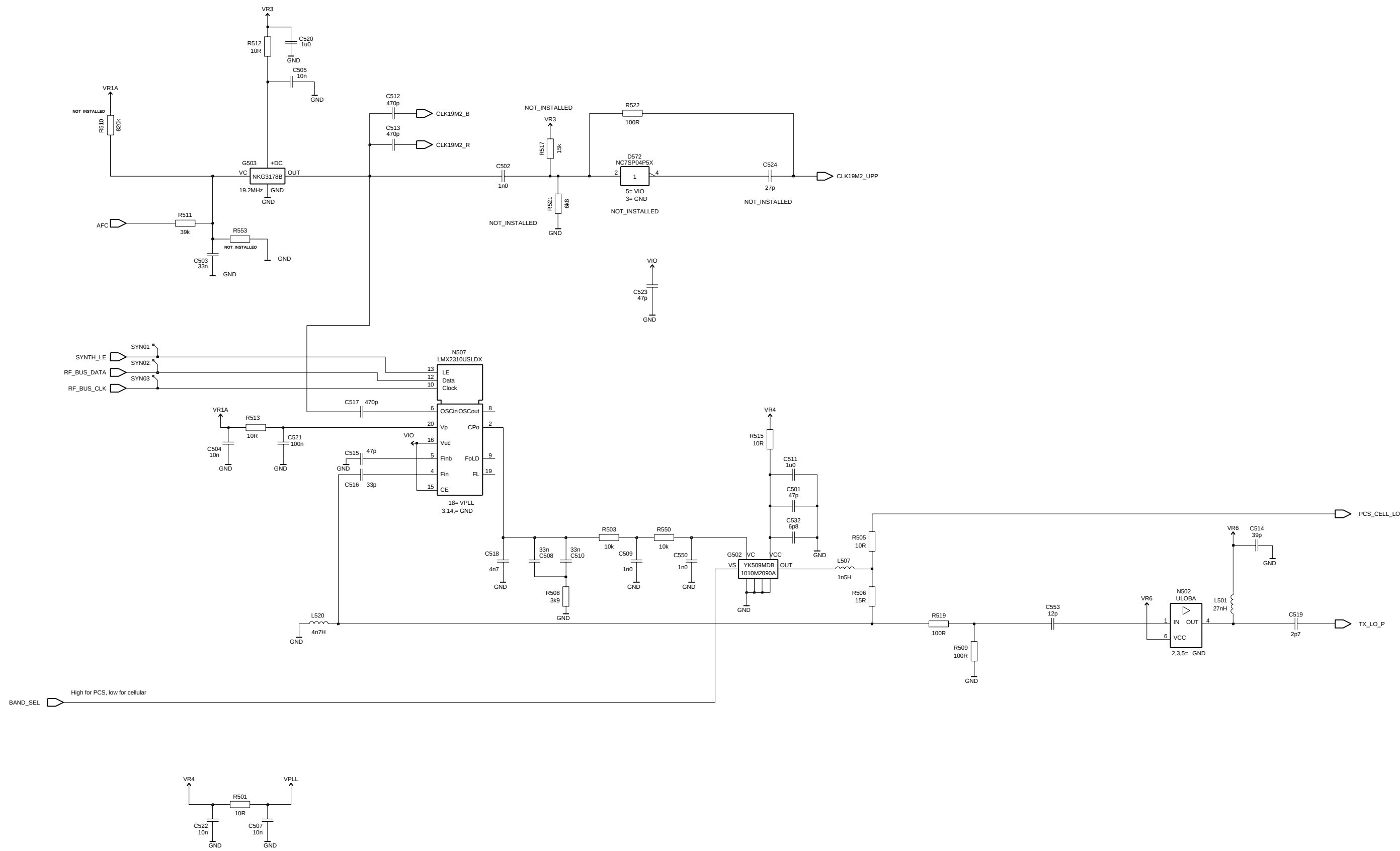
Name	ROBIN	Appr	
Assoc		Des.	
		Dr.	TUSSEY 16-Oct-
File	\$pcb/hdca3_gui/1bp_ag1_gui_07/RF/robin	Sheet	XX of yy 1 (01)
Proj		Drw.	DHB00172-EN
		Print	Yes V. 7.1 Edit

Front End

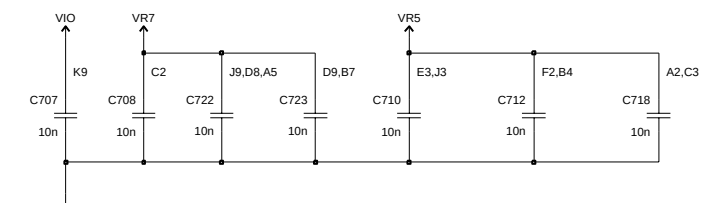
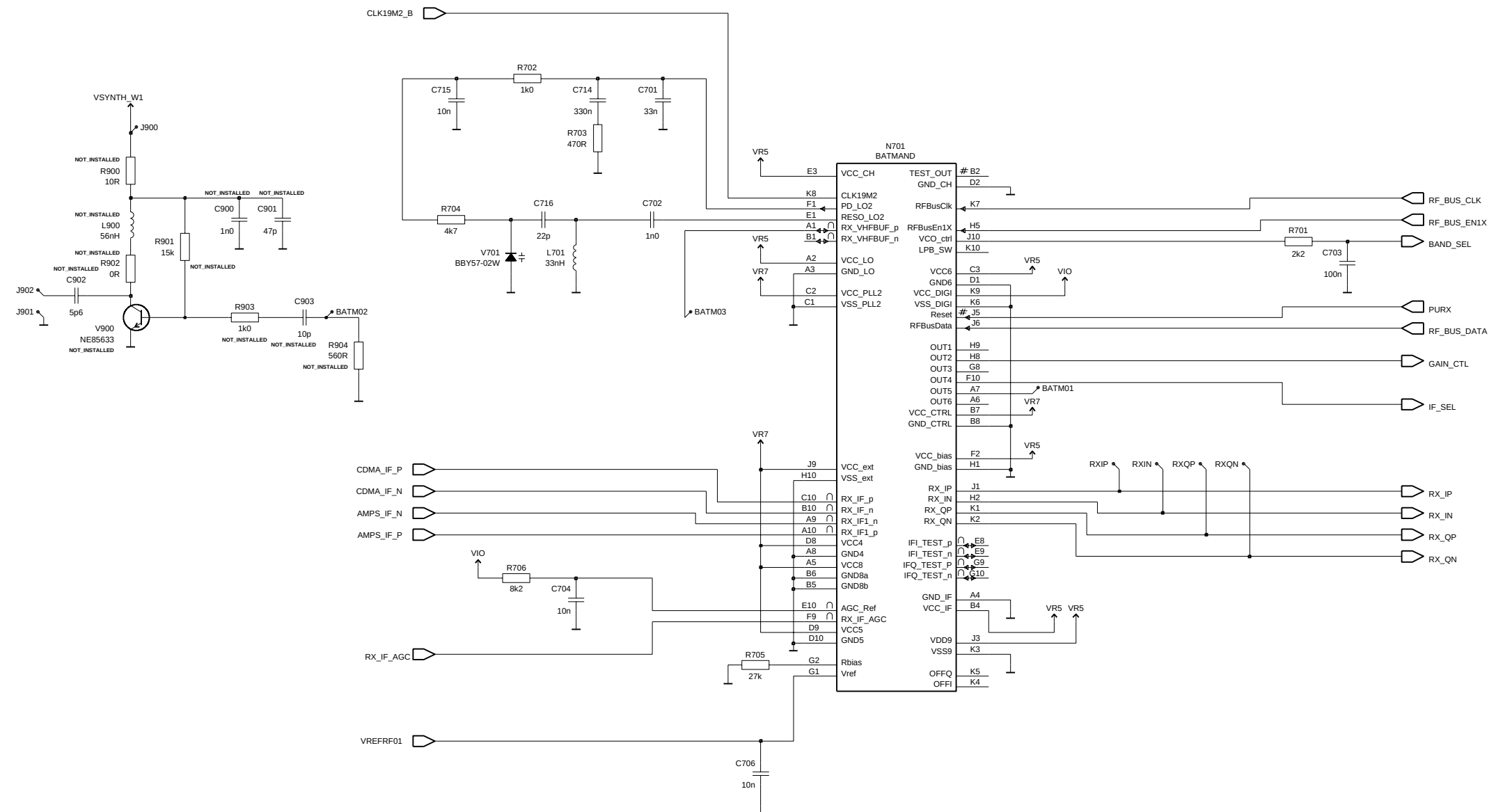


MODES	LOGIC INPUTS		
	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

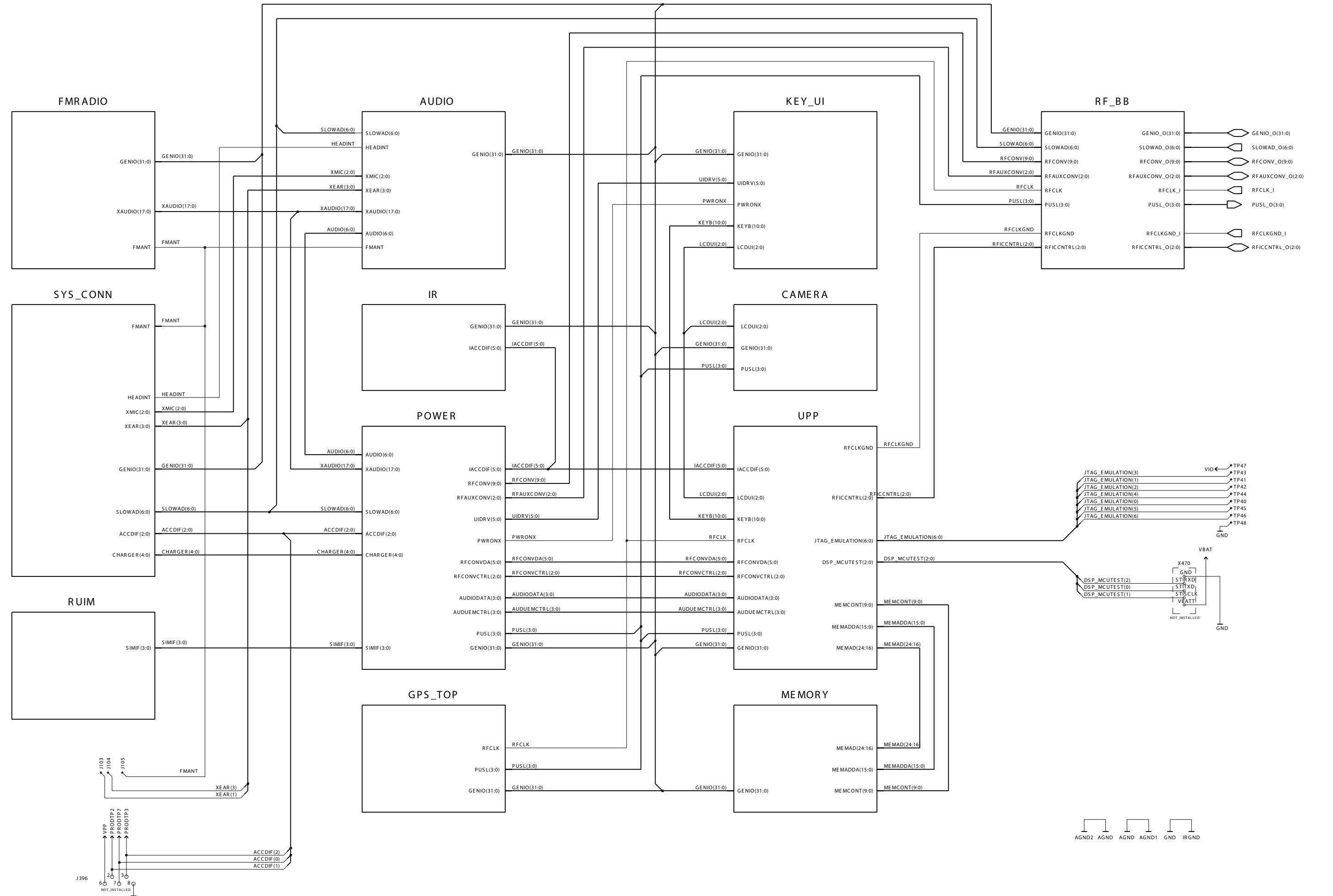
UHF Synthesizer



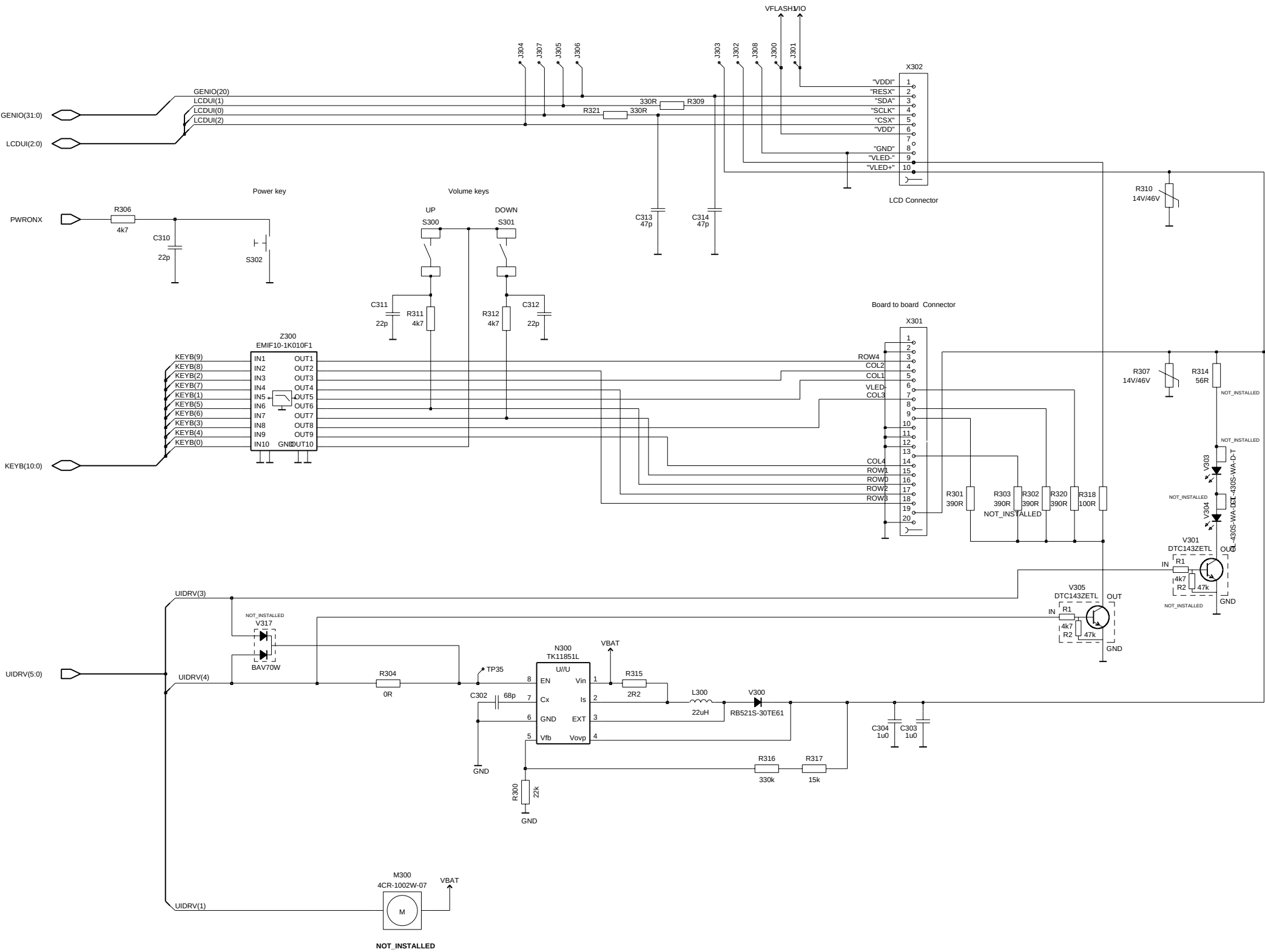
Batman



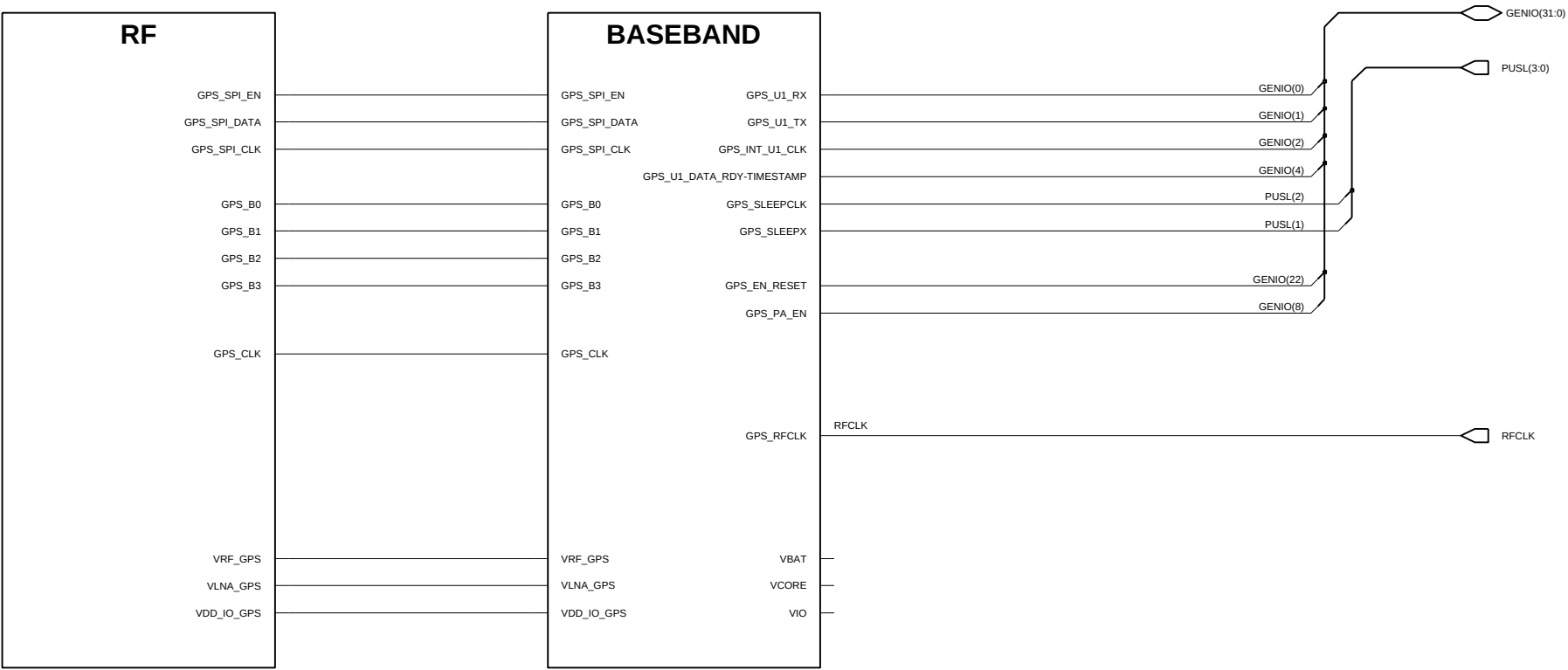
System Module



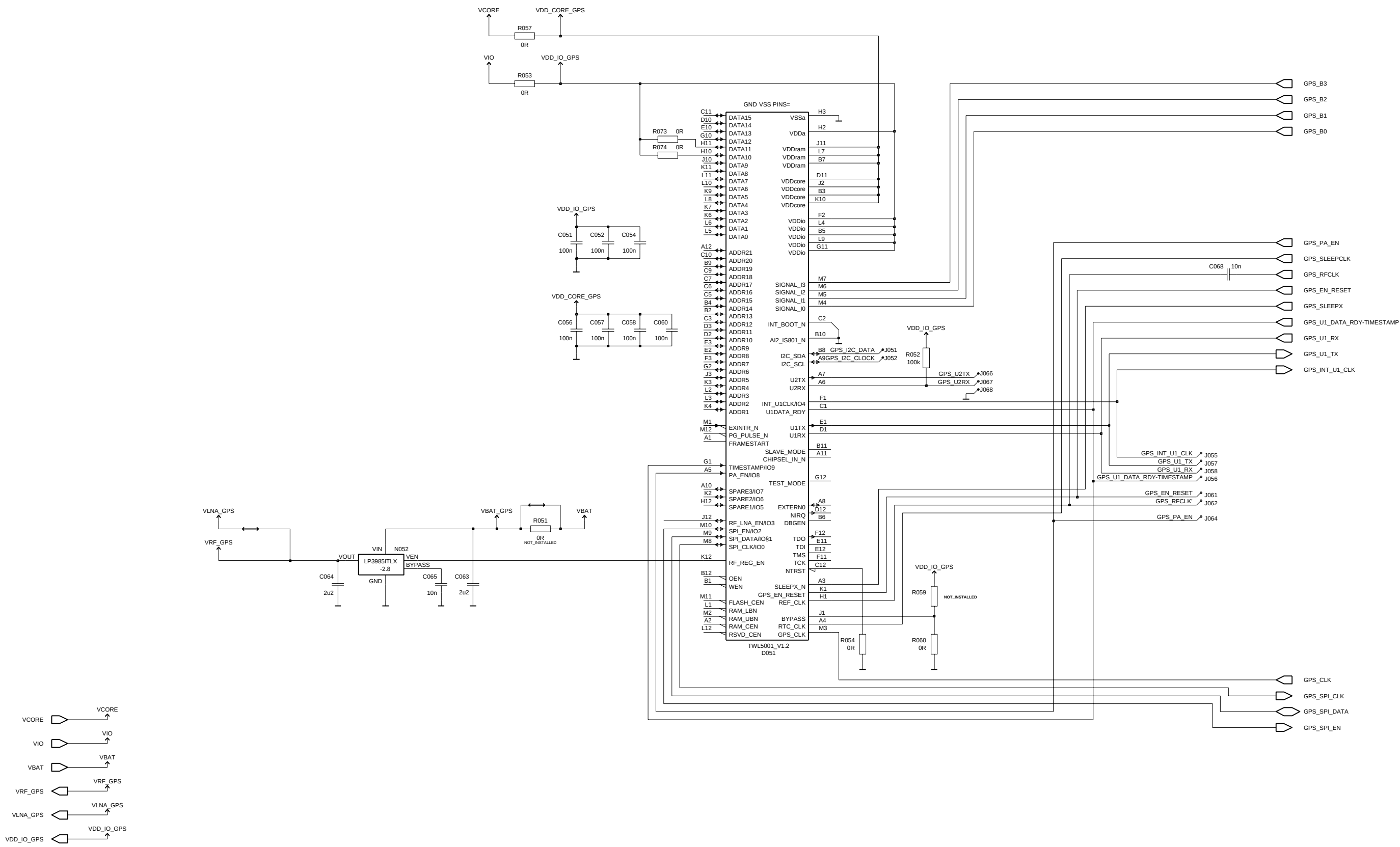
User Interface



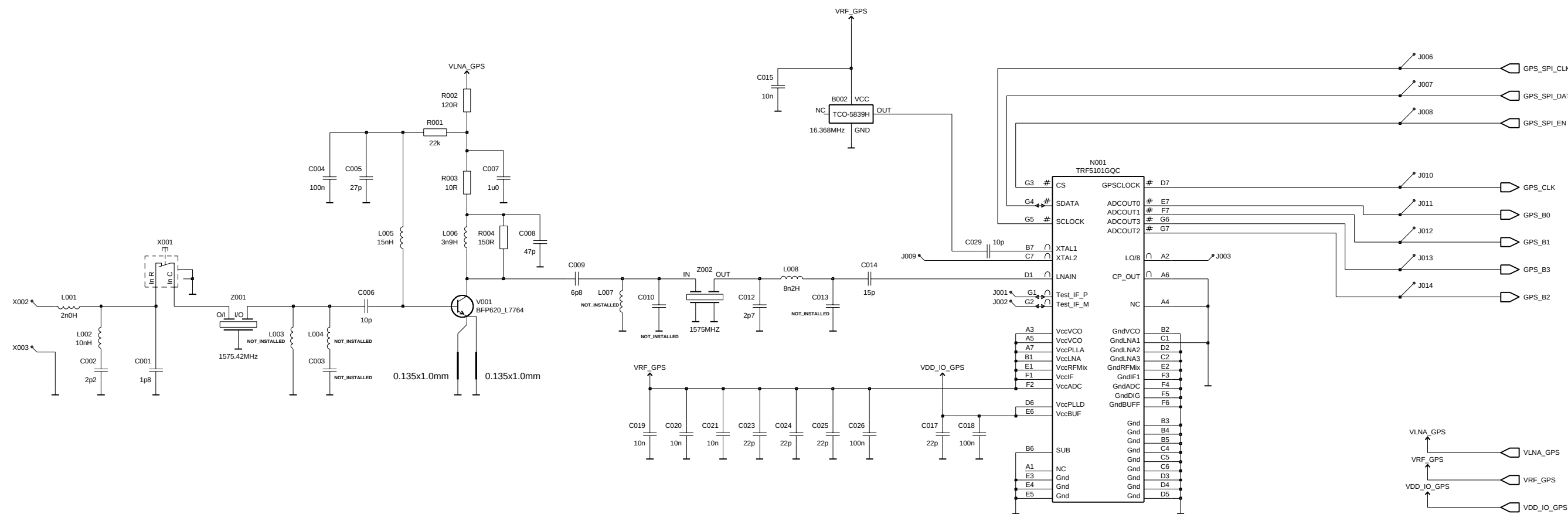
GPS RF-BB Interface



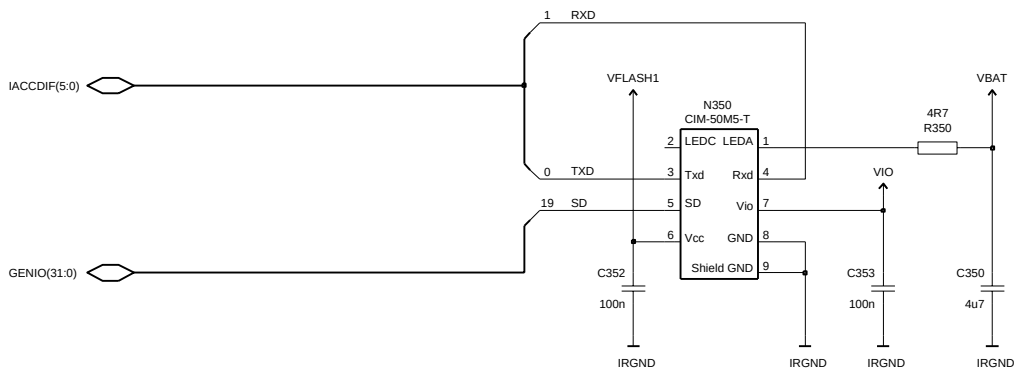
GPS Baseband Circuit Diagram



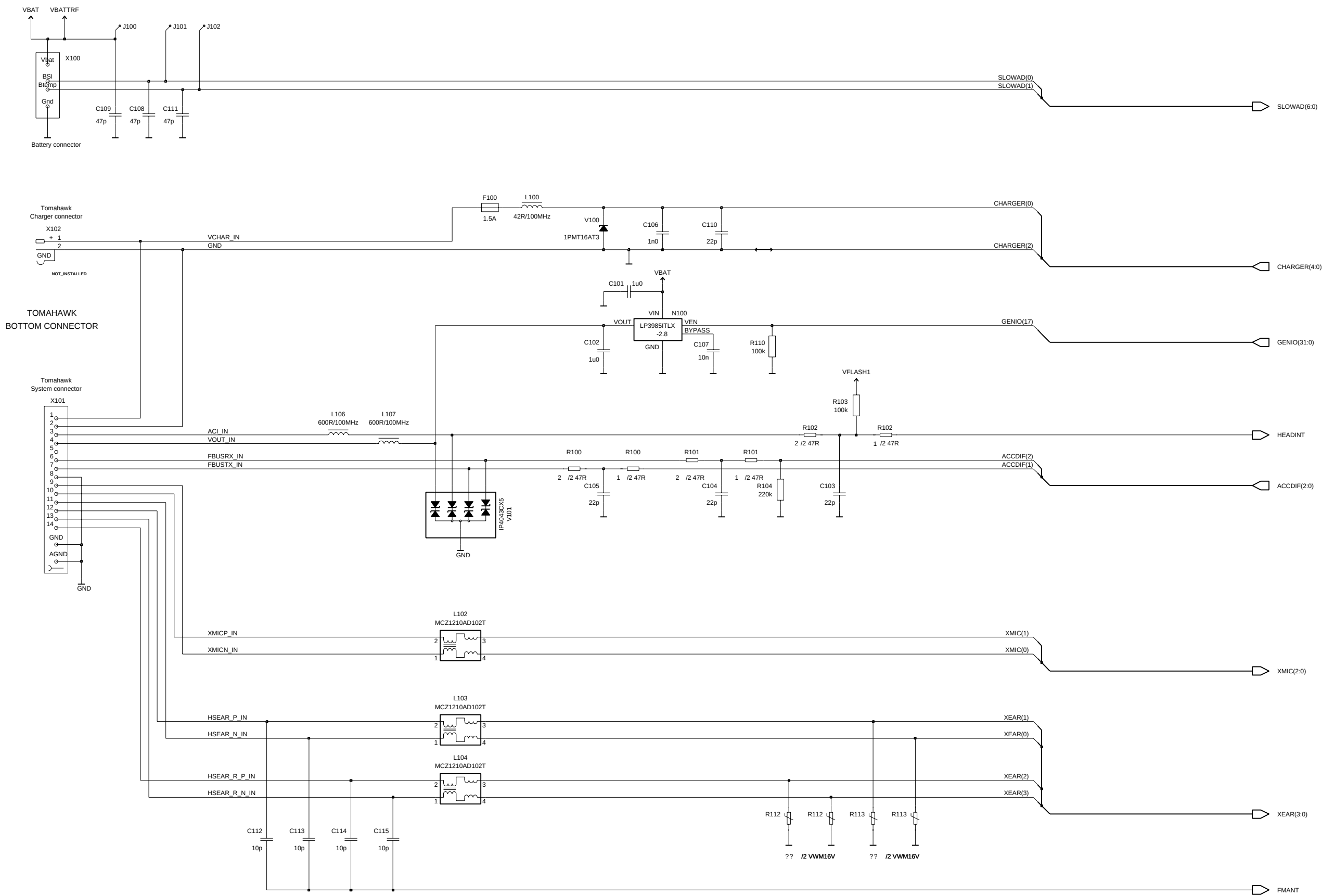
GPS RF Circuit Diagram



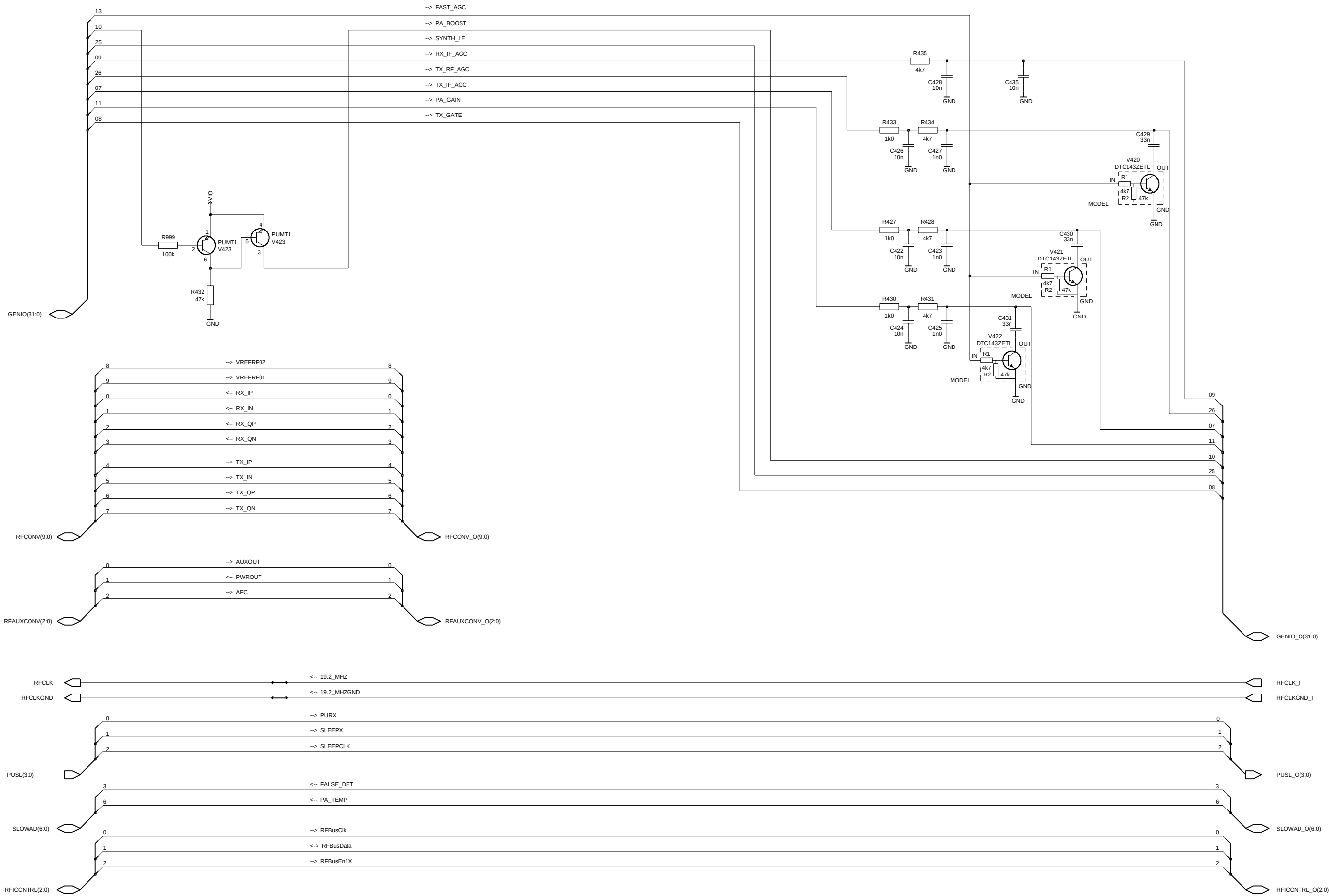
IR Module 1.8V



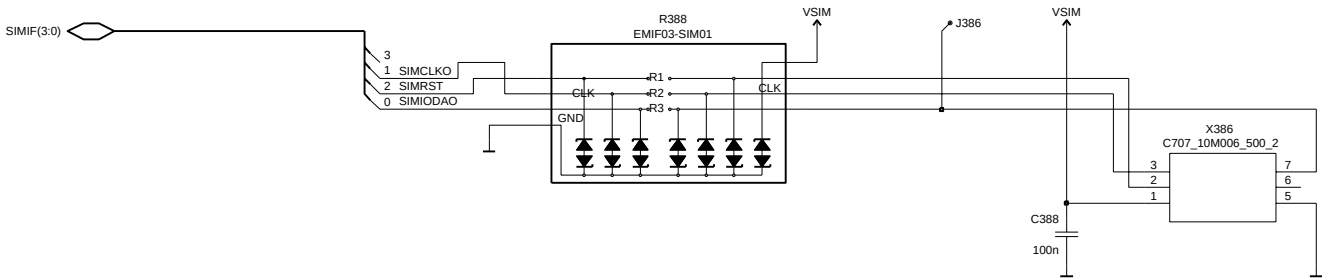
System Connector



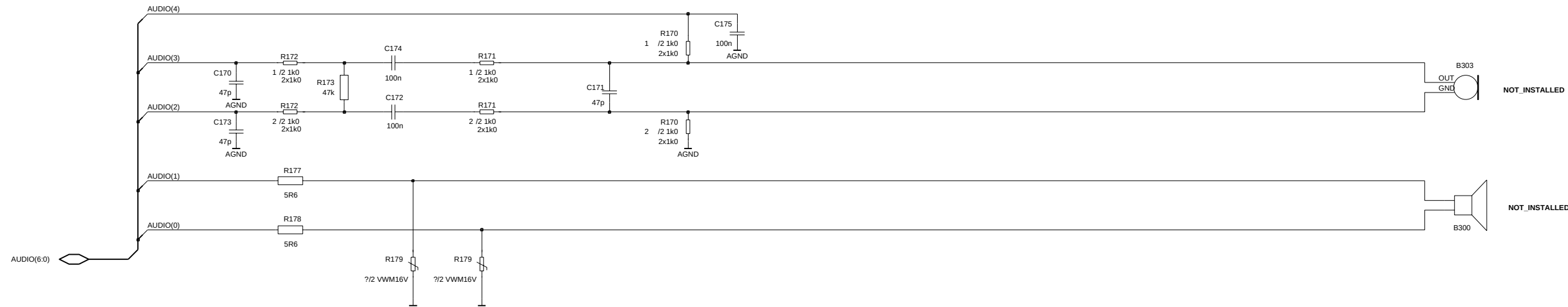
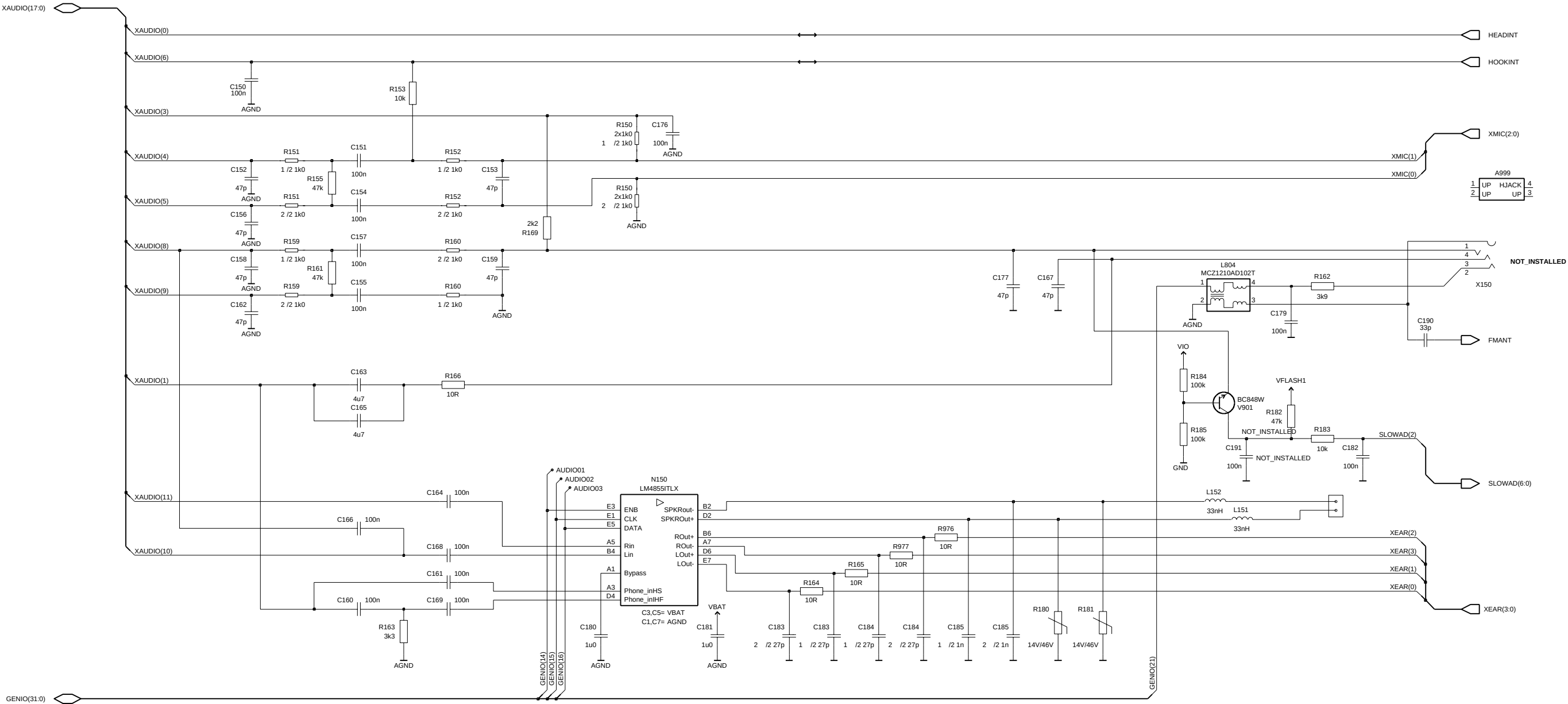
BB-RF Interface



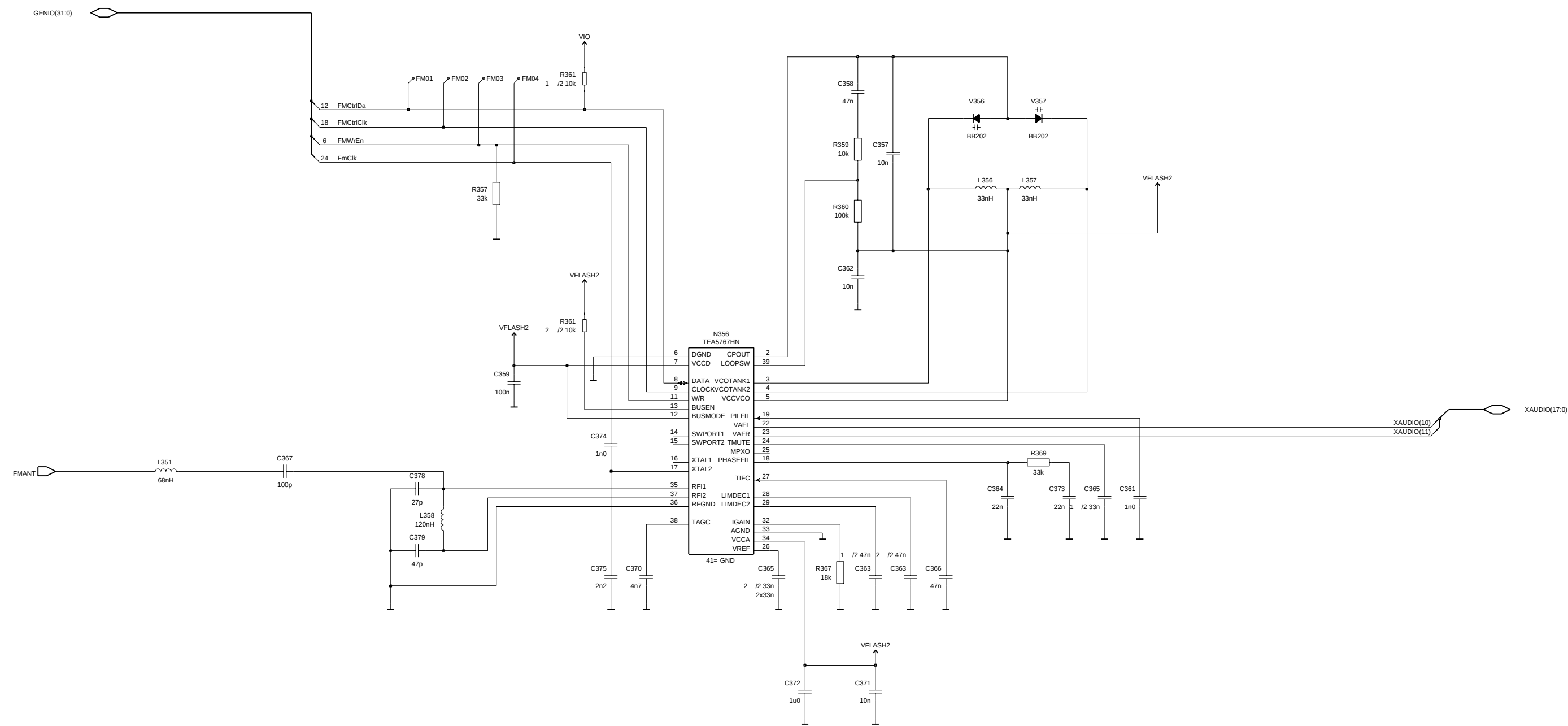
SIM Reader



Audio

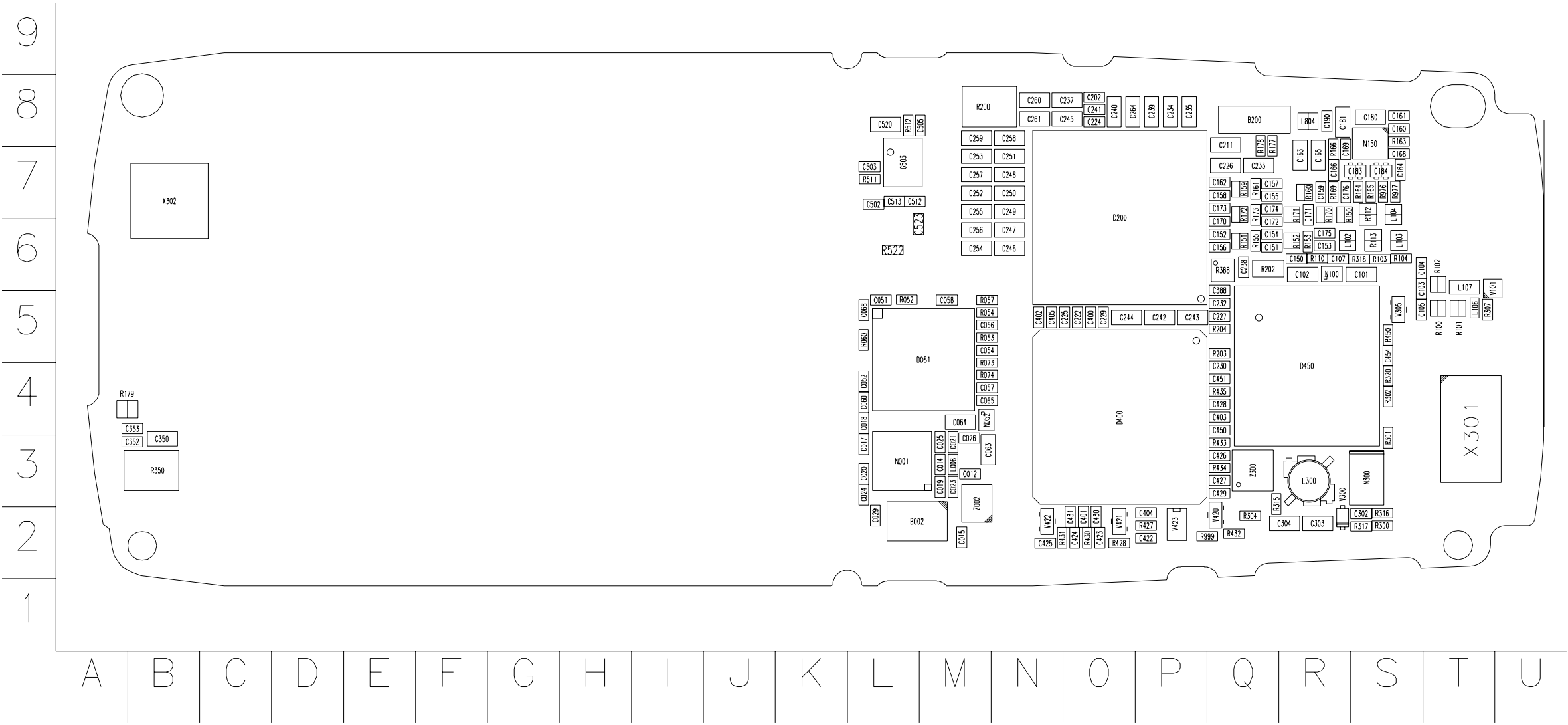


TEA5767 FM Radio Unit

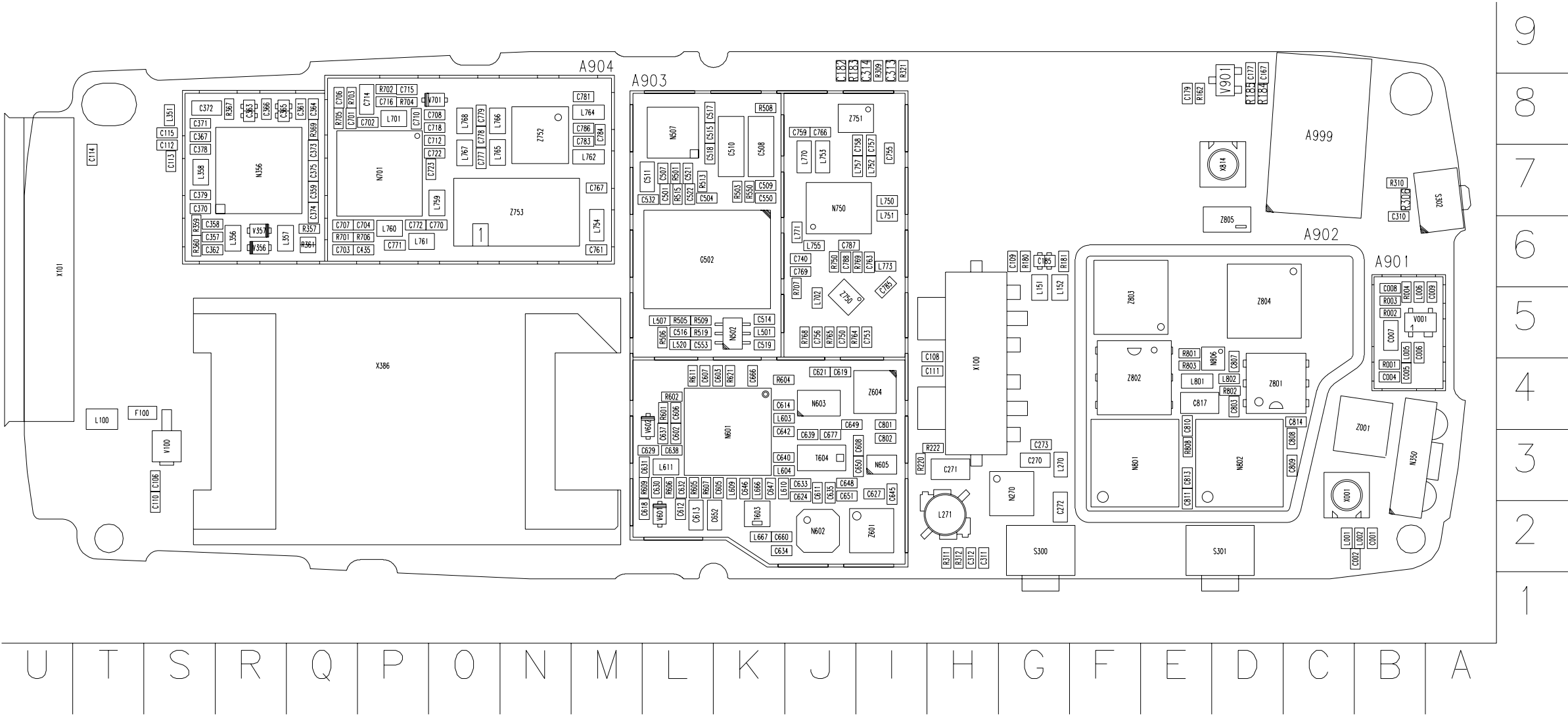


Notice:
C374 (1n0) and C375 (2n2) are configured for 32kHz reference clock
If reference clock is 6.5MHz, use C374 (3p9) and C375 (10p)

Component Layout — Top View



Component Layout — Bottom View



The diagram illustrates a PCB layout with a coordinate grid from row 1 to 8 and column A to U. Components are distributed across the board, including:

- Microcontroller:** Located near the top center, surrounded by numerous resistors.
- Memory:** Several EEPROM and SRAM chips are placed around the microcontroller.
- Passive Components:** Numerous resistors and capacitors are scattered throughout the layout, often with specific values like 10k, 100n, or 100p.
- Connectors:** Various connector pins and headers are visible along the edges.

The layout is highly detailed, showing the physical placement and orientation of each component on the PCB.