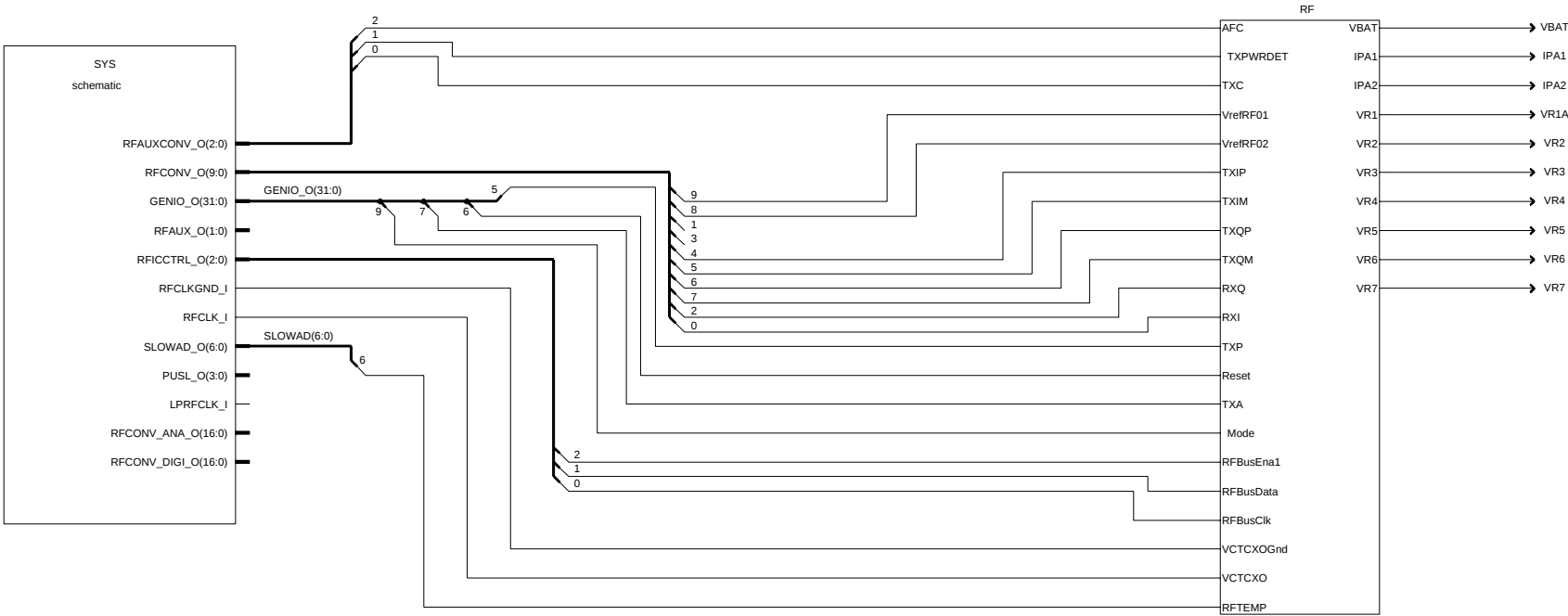
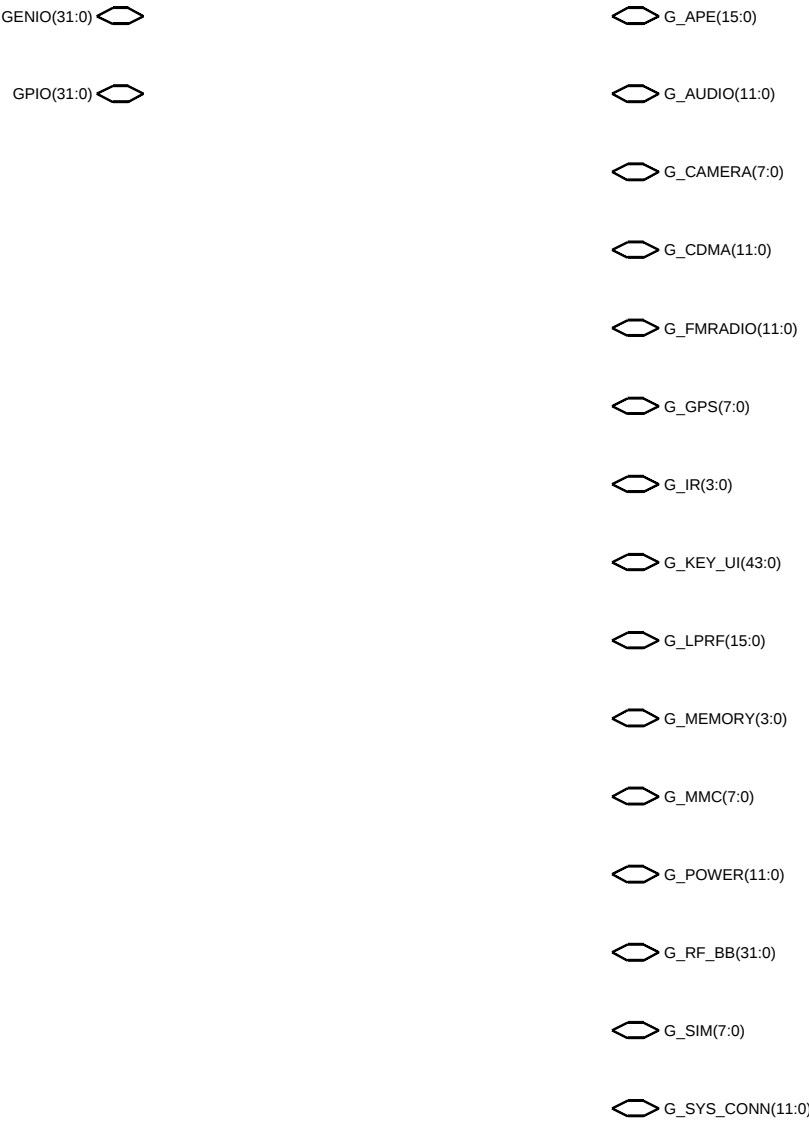


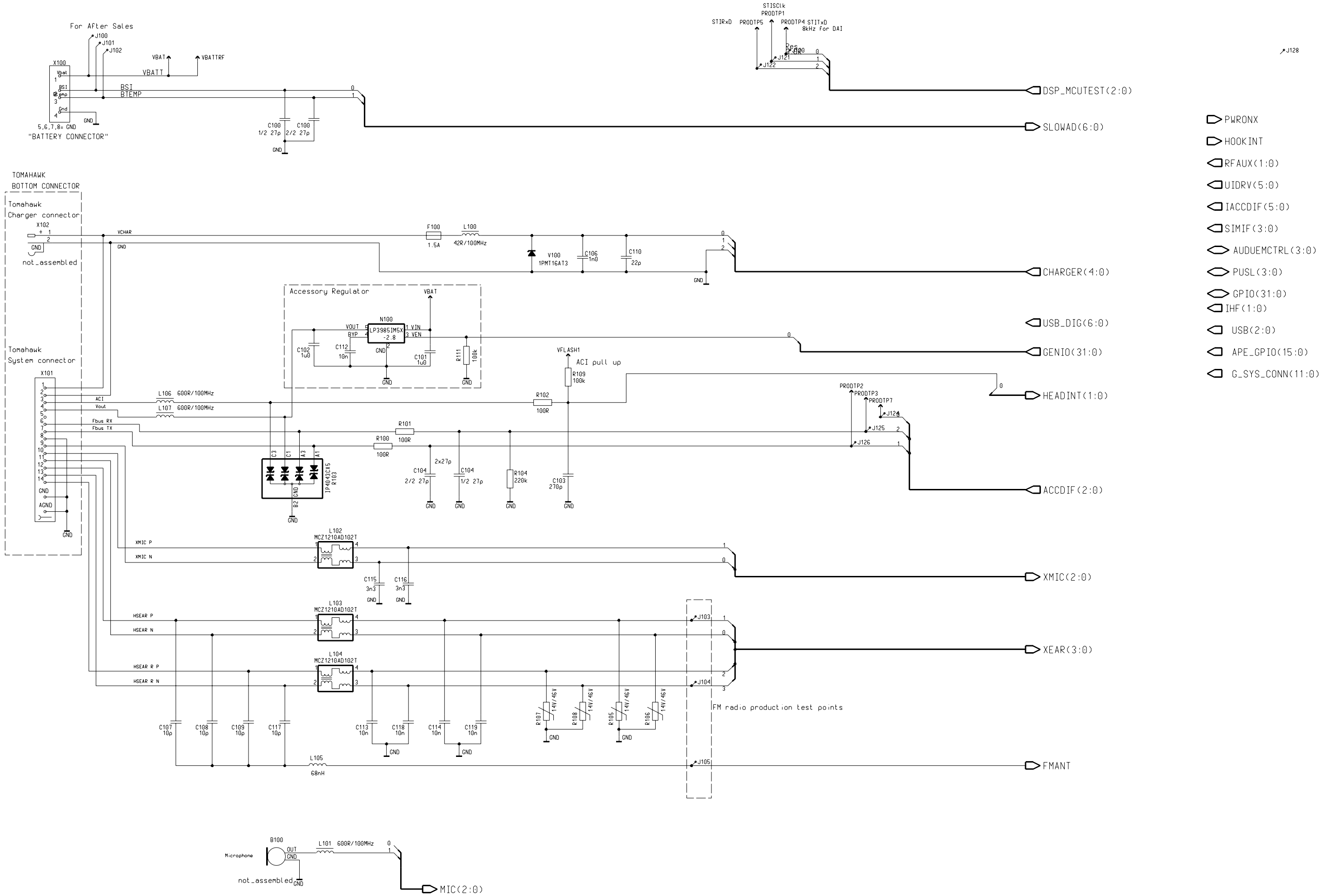
RM-37 Top Level



GENIO and GPIO Connection Block

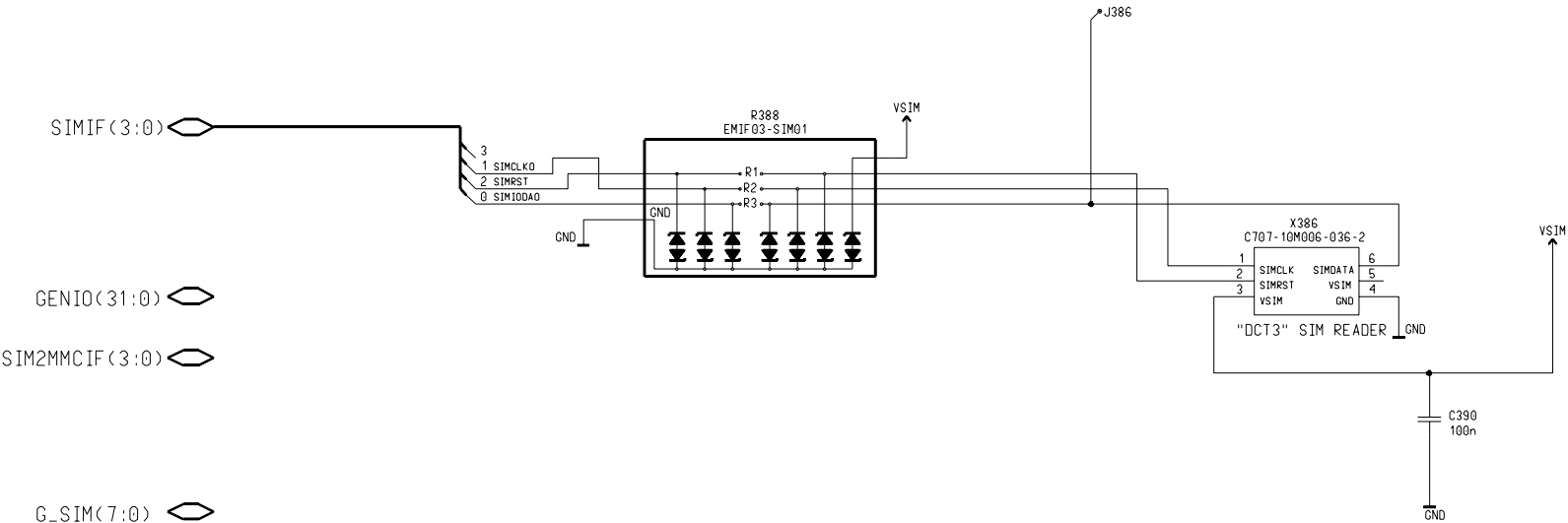


System Connector

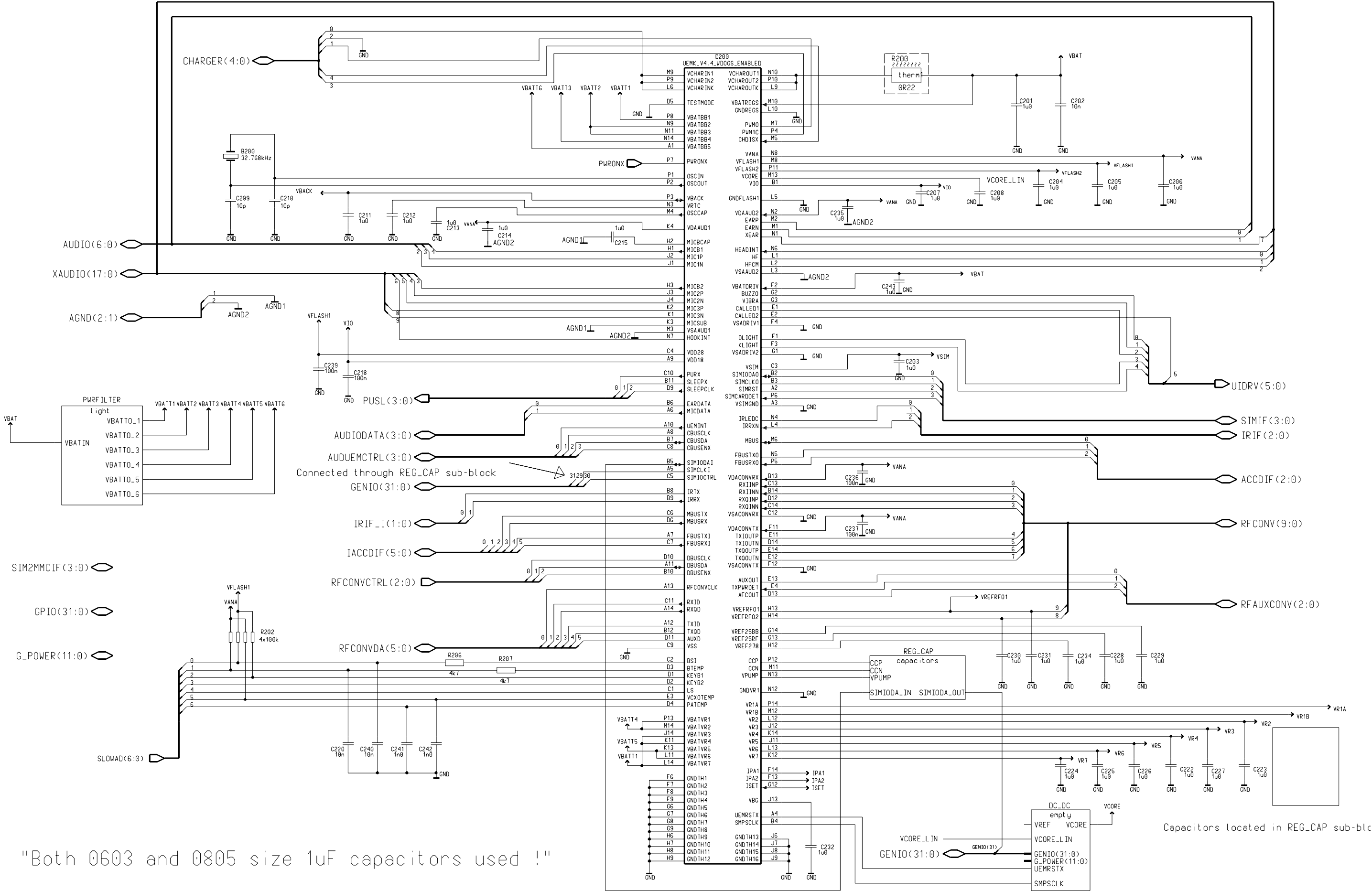




SIM Reader



Discrete Power Management

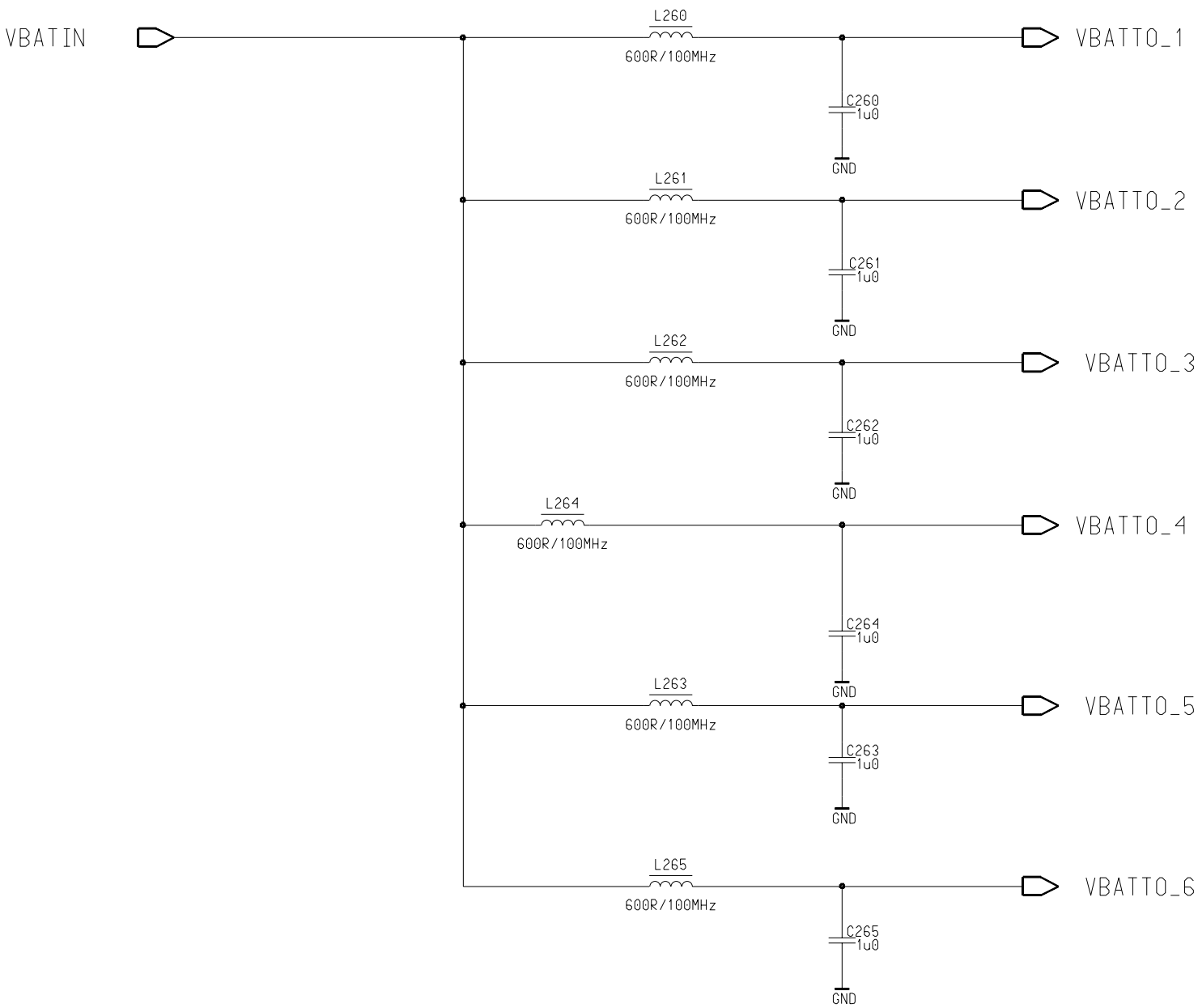


Capacitors located in REG_CAP sub-blk

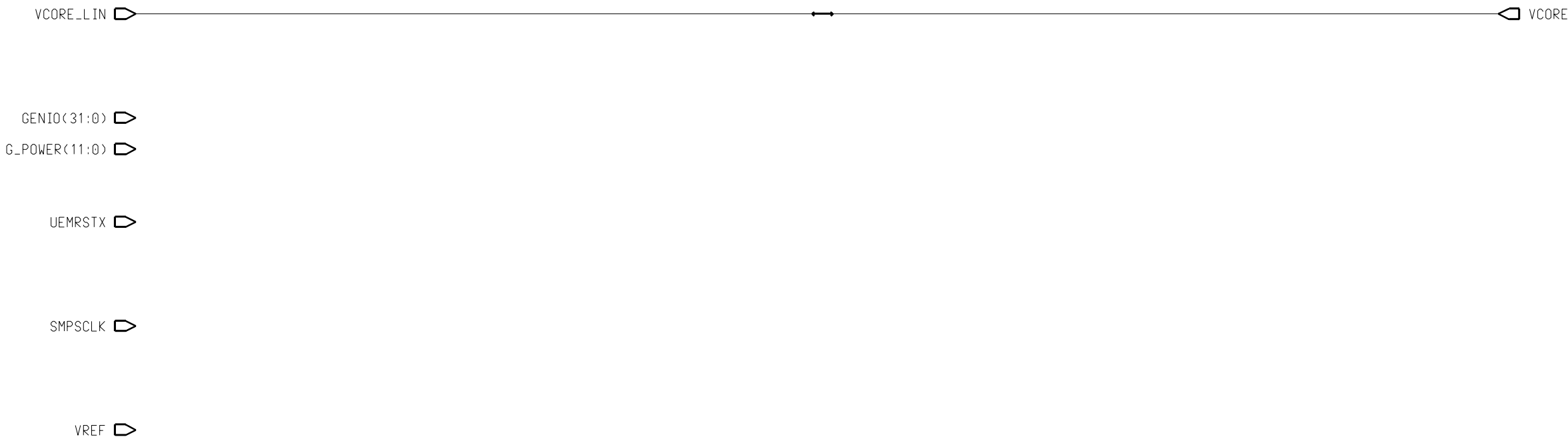
"Both 0603 and 0805 size 1uF capacitors used !"

Used refs in REG_CAP: C238, C200, C219, C233, C221 and R205

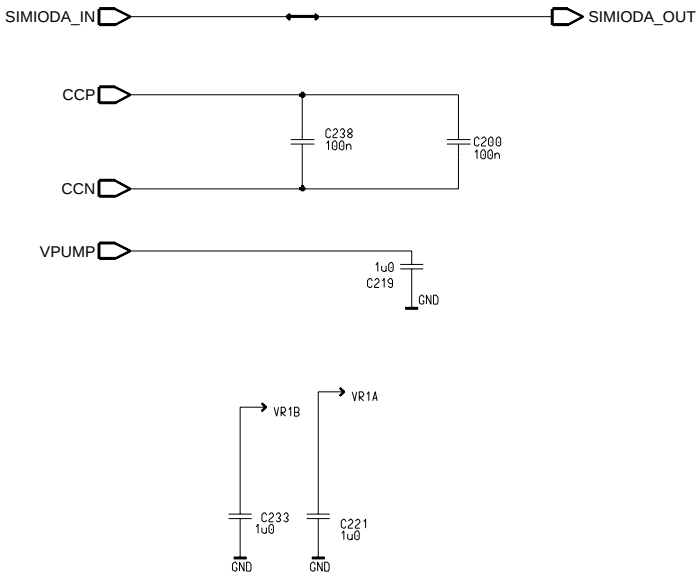
Light Filtering



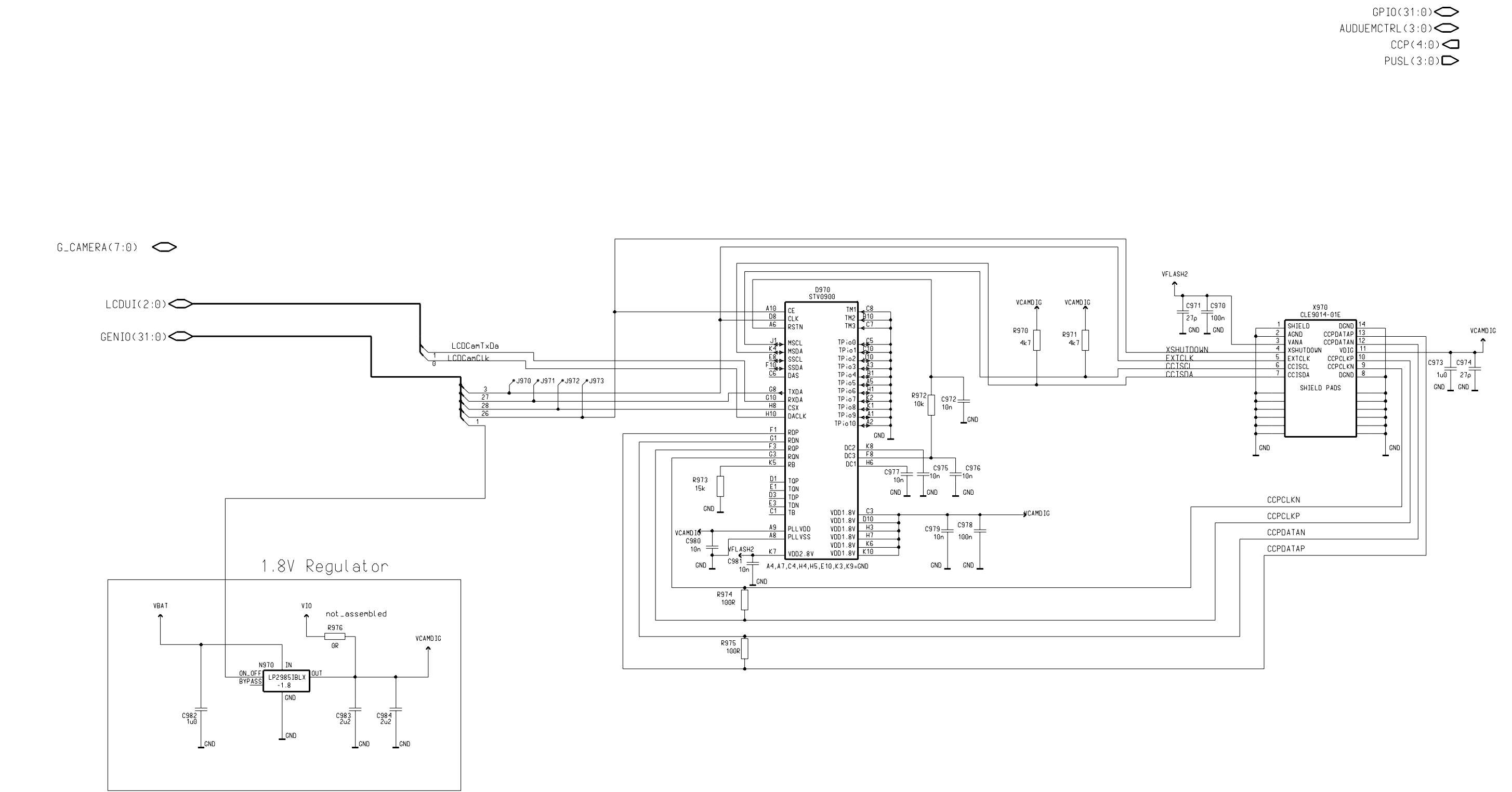
DC/DC Converter



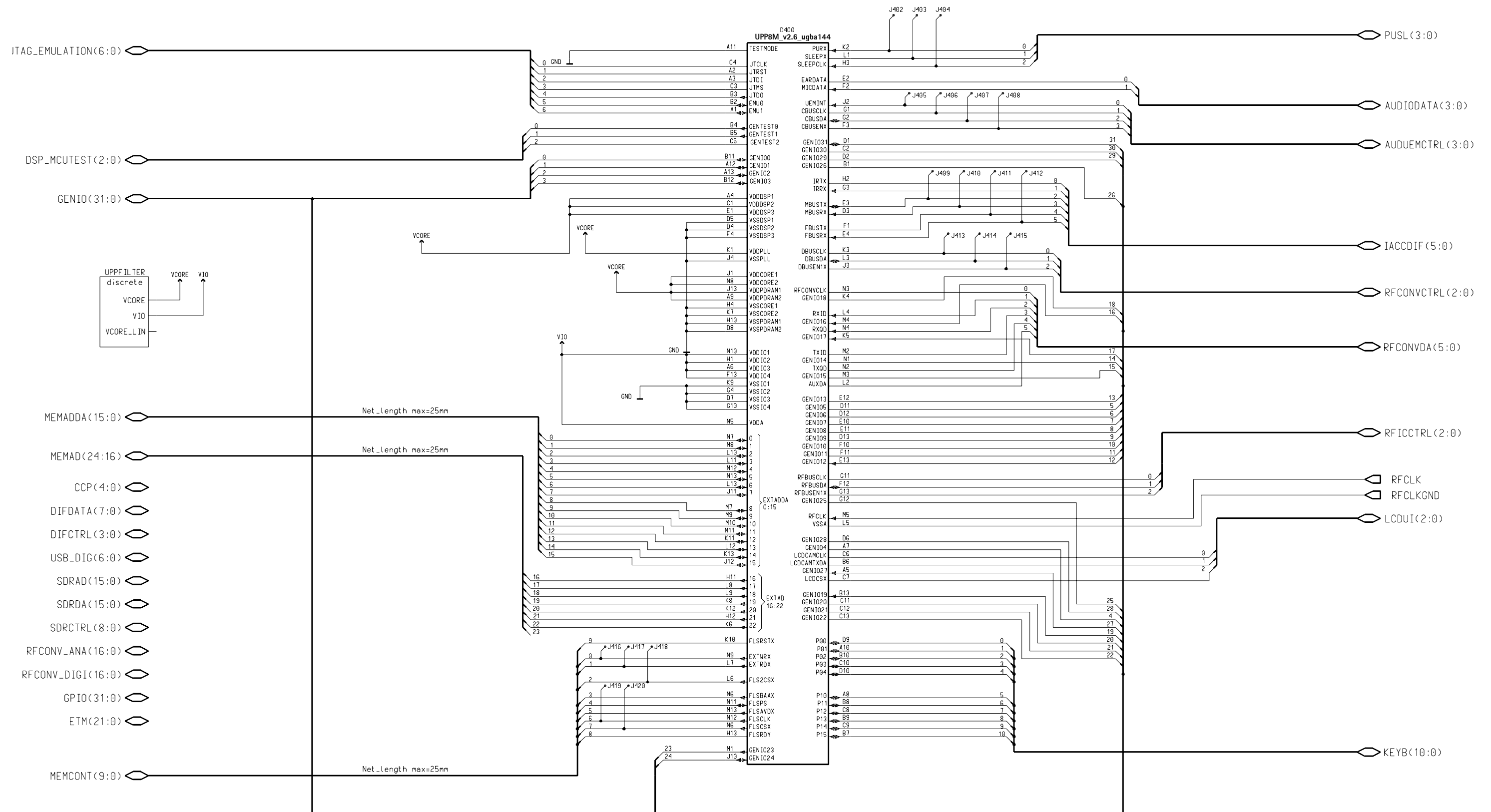
Old Power Discrete



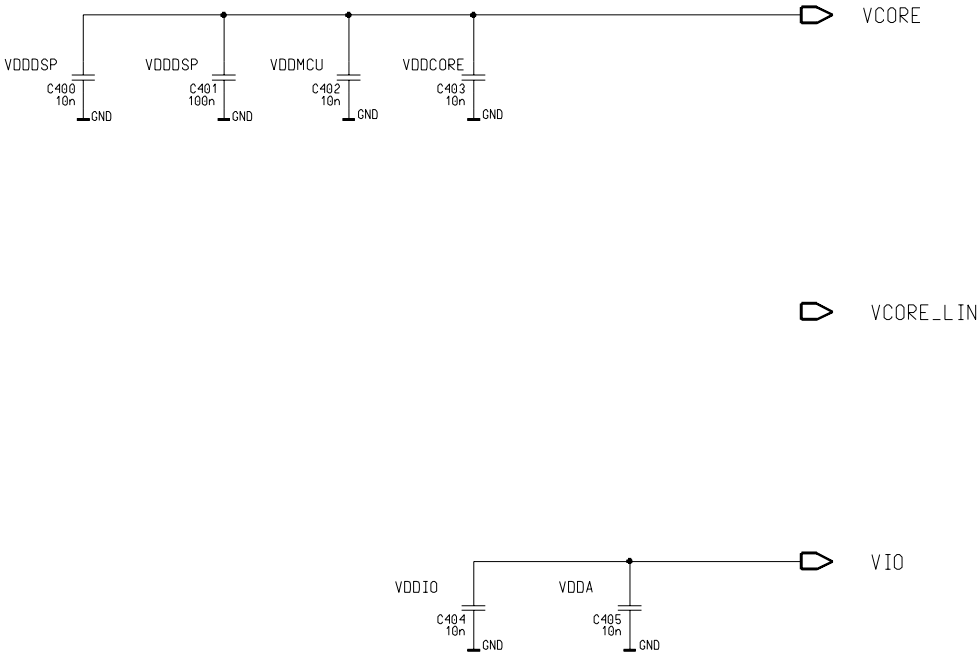
Camera



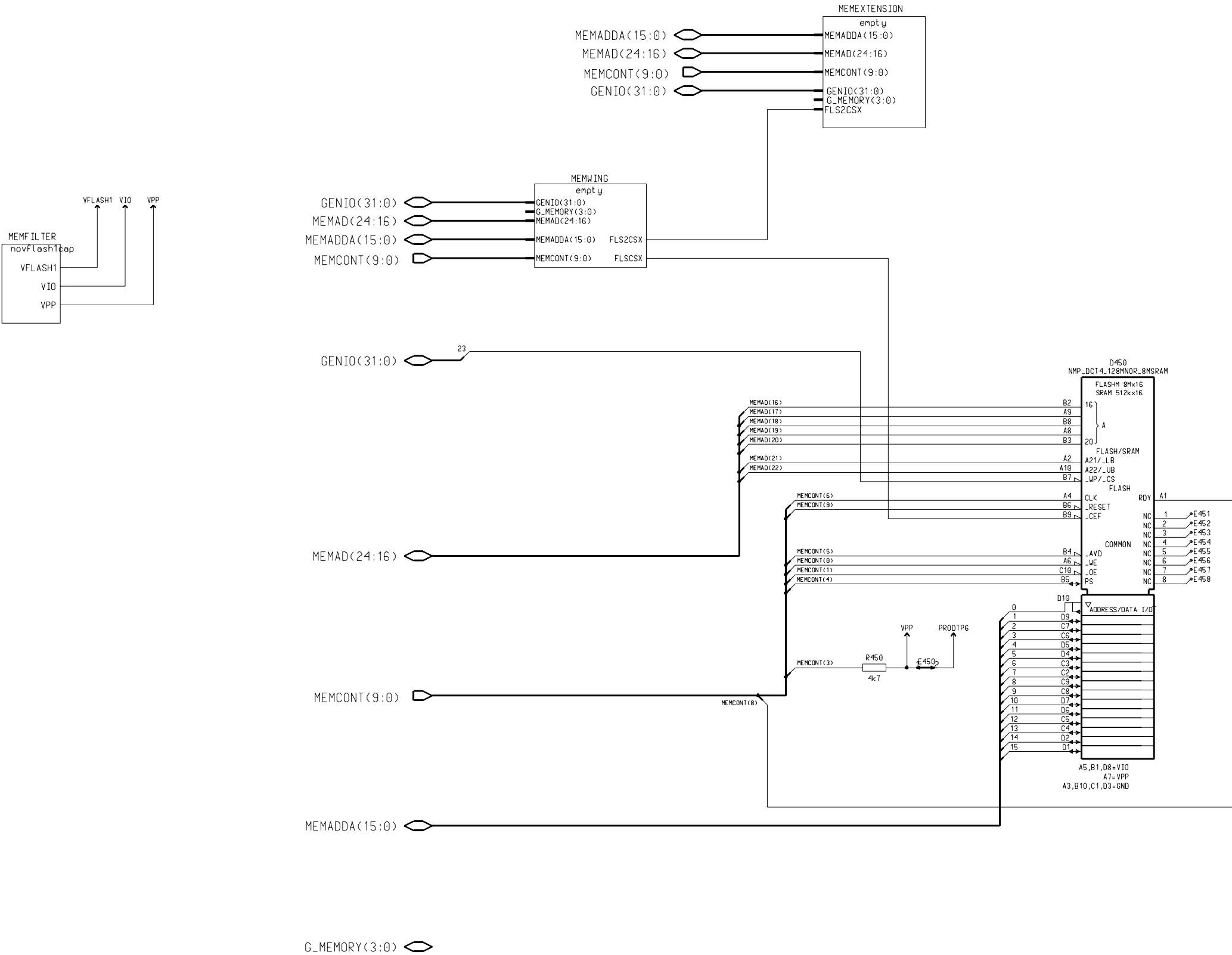
A-12



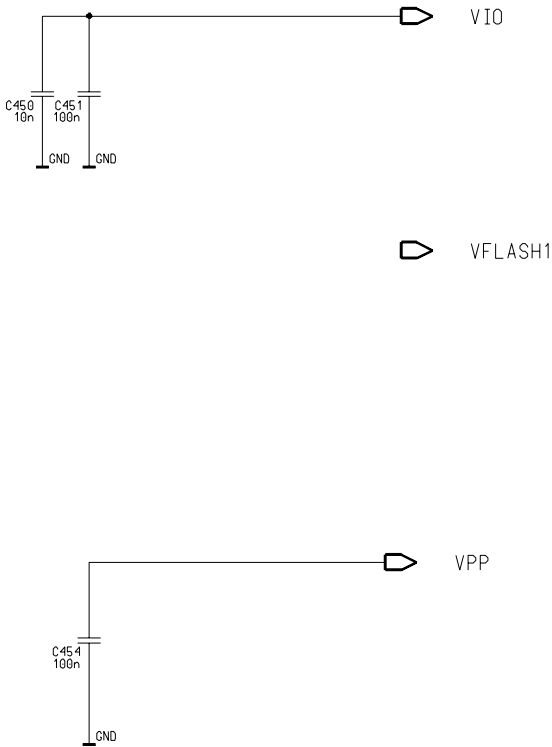
Decoupling Capacitors for UPP



Combo Memory

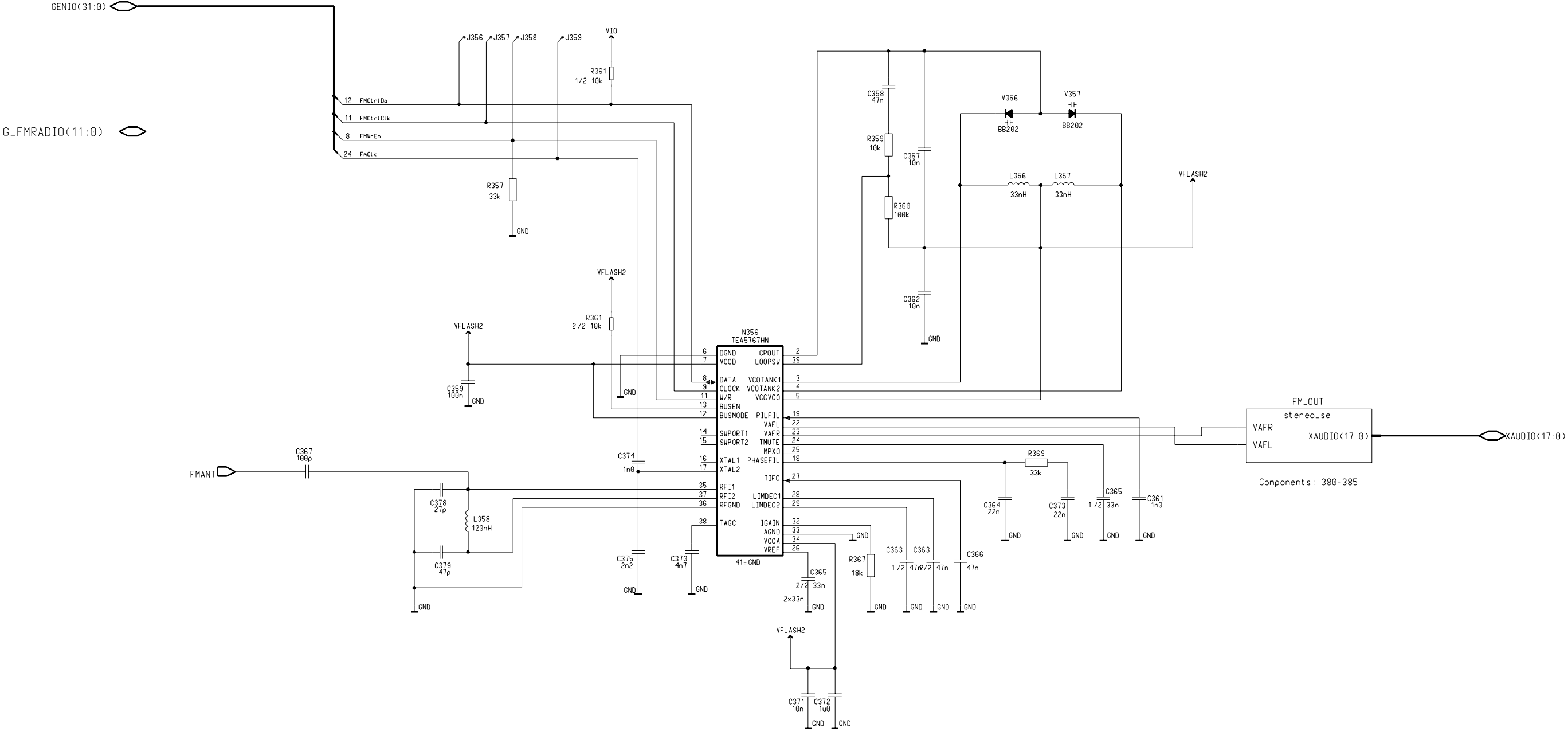


Memory Without VFLASH1



FM Radio

FMCLK = GENIO(24)

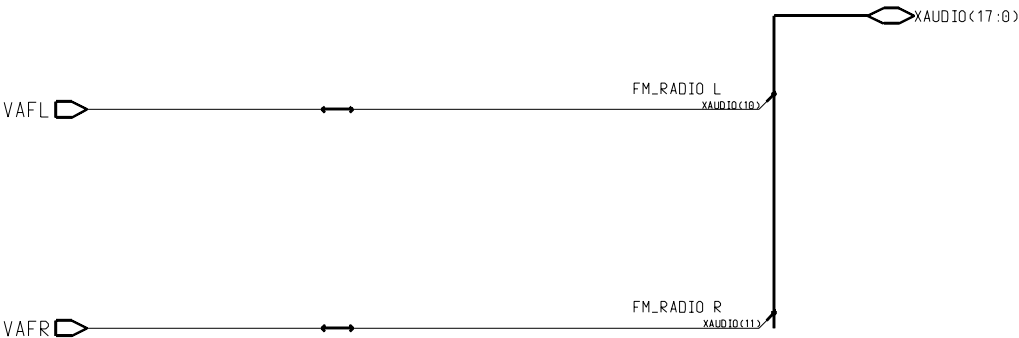


AUDUEMCTRL(3:0)

PUSL(3:0)

Notice:
C374 (1n0) and C375 (2n2) are configured for 32kHz reference clock
If reference clock is 6.5MHz, use C374 (3p9) and C375 (10p)

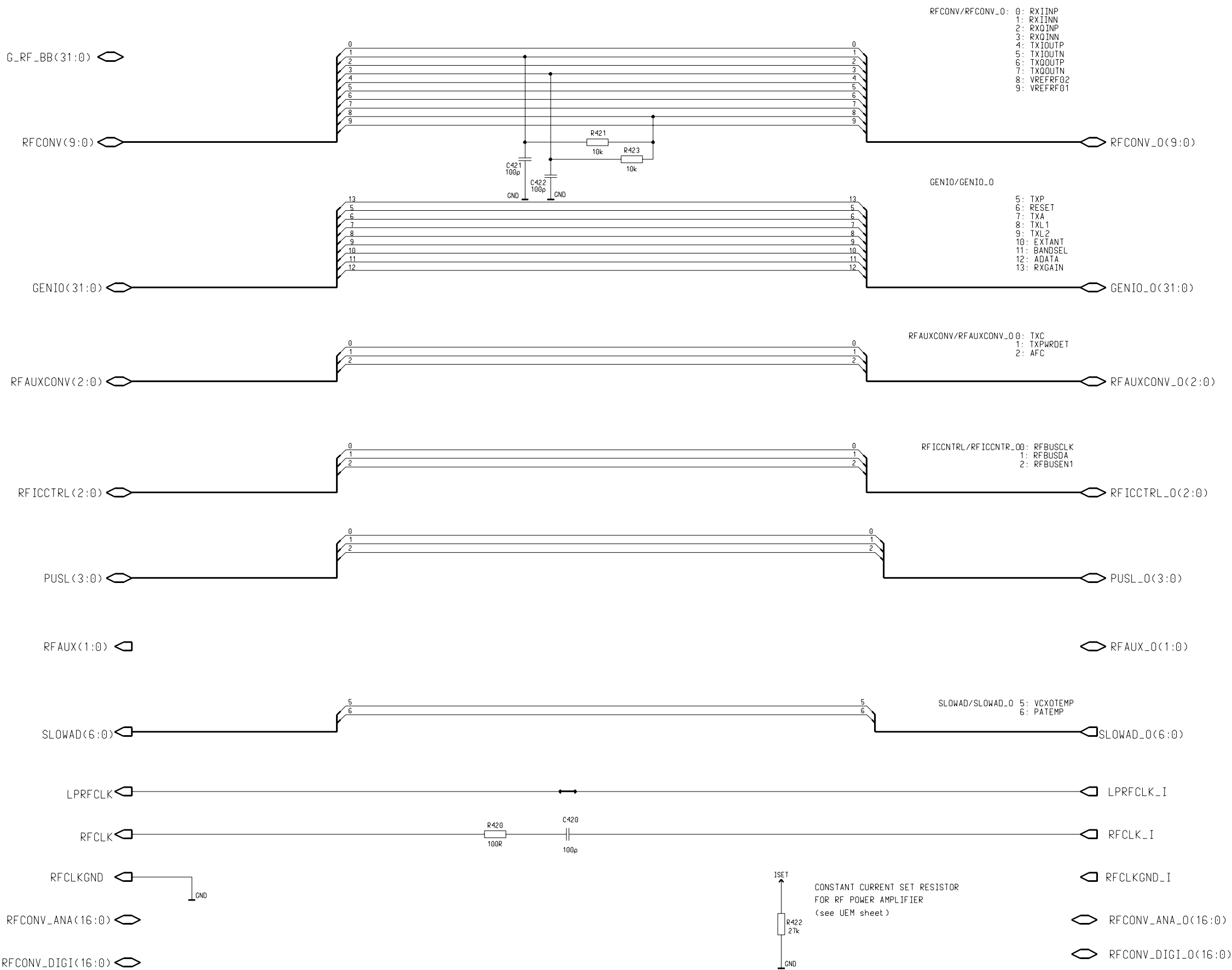
FM Radio Out



Test Pattern



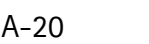
RF BB Interface

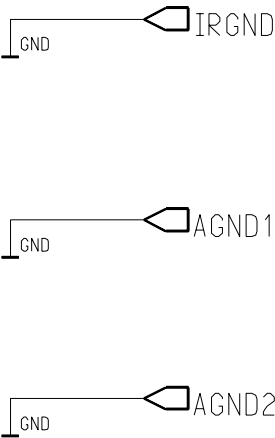


IPA1 AND IPA2 ARE USED IN RF, THE TOLERANCE OF R422 IS 1% (0402, 1430873)

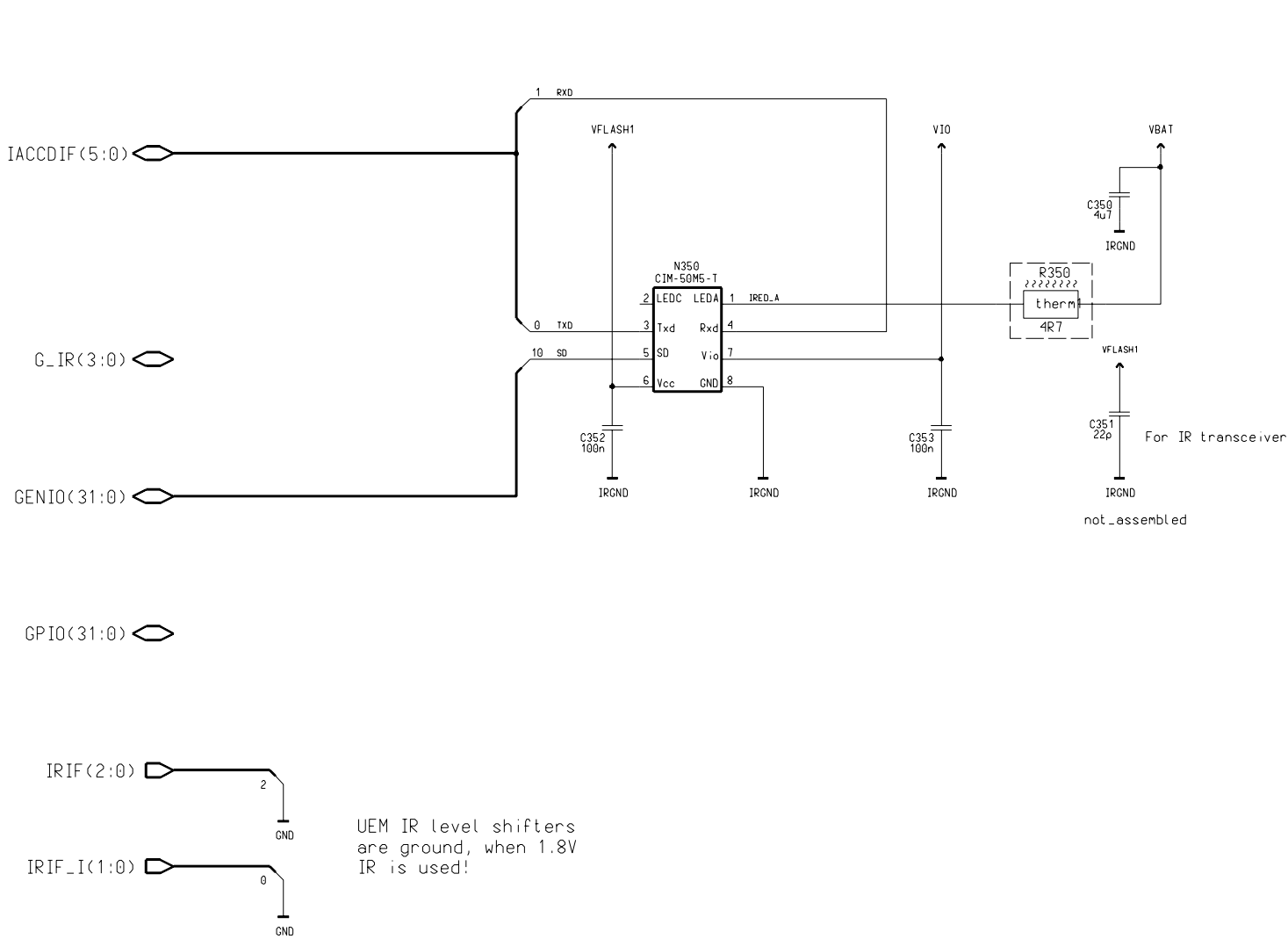
 G_AUDIO(11:0)
 SLOWAD(6:0)
 AUDUEMCTRL(3:0)
 UIDRV(5:0)
 HOOKINT
 PUSL(3:0)

 MCBSP2(5:0)
 APE_AUDIO(3:0)
 APE_GPIO(15:0)



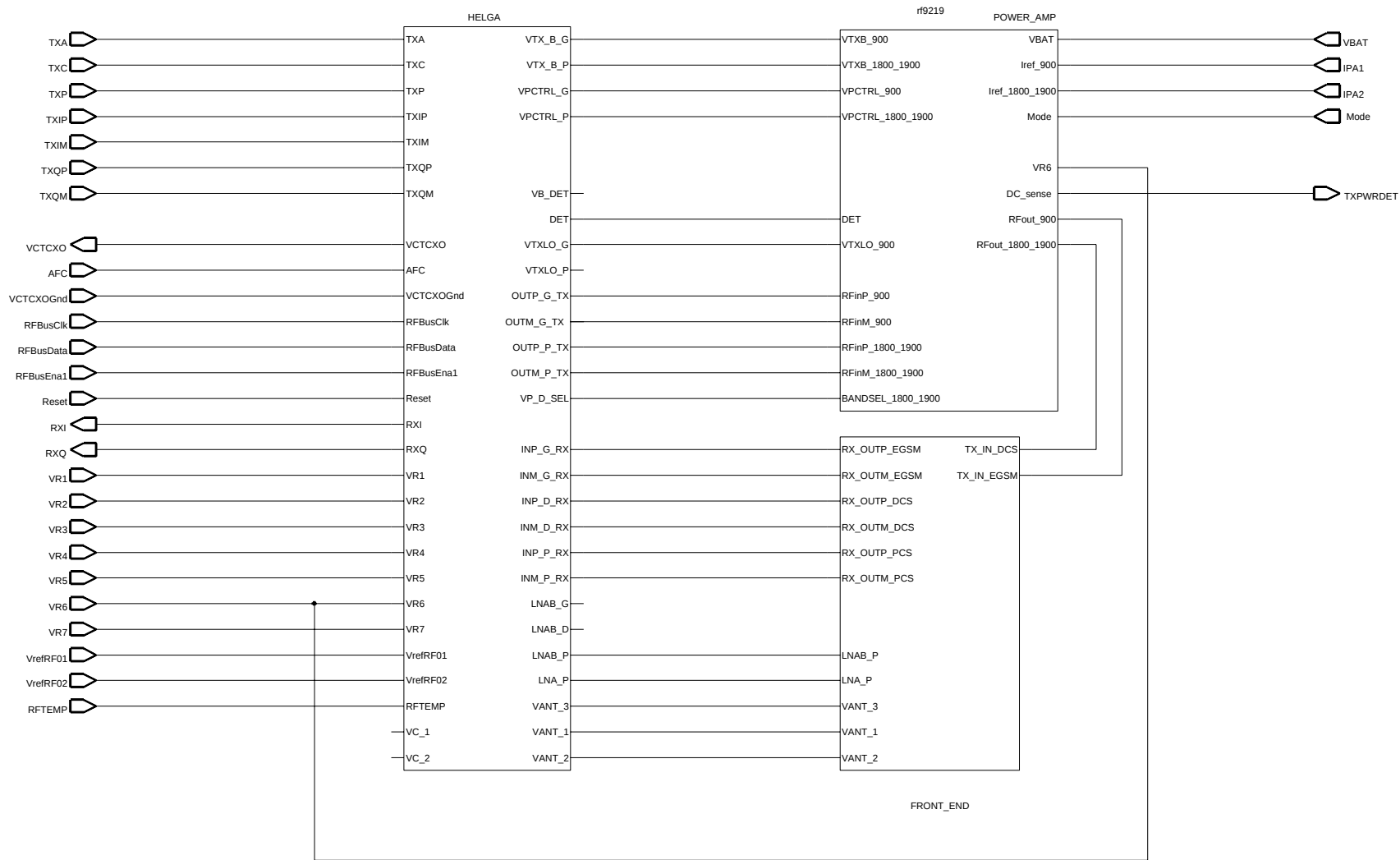


IR Module

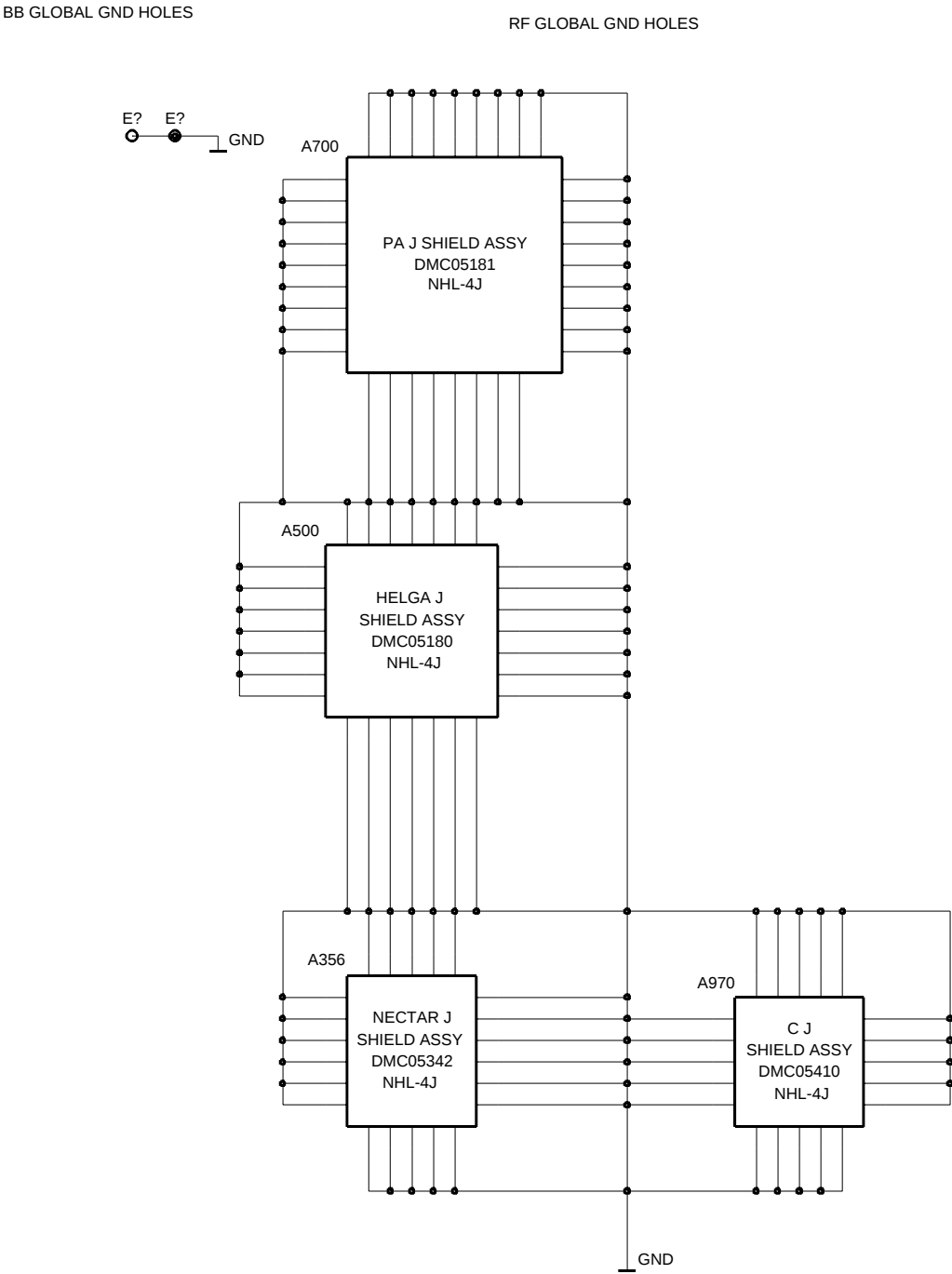


Used referenses

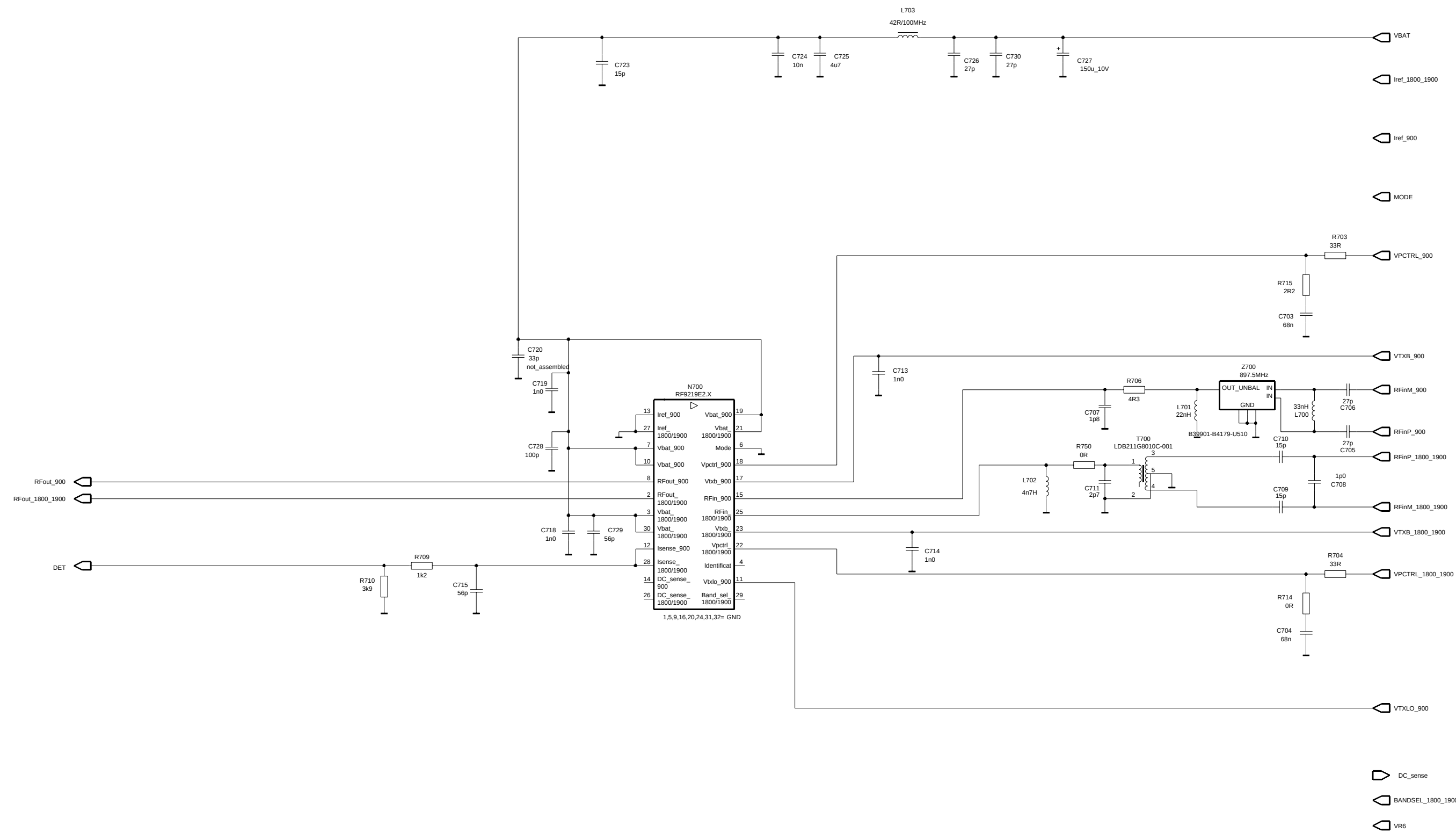
- C 350 - 353
- N 350
- R 350



RF Shields and Vias



Power Amplifier and Detection



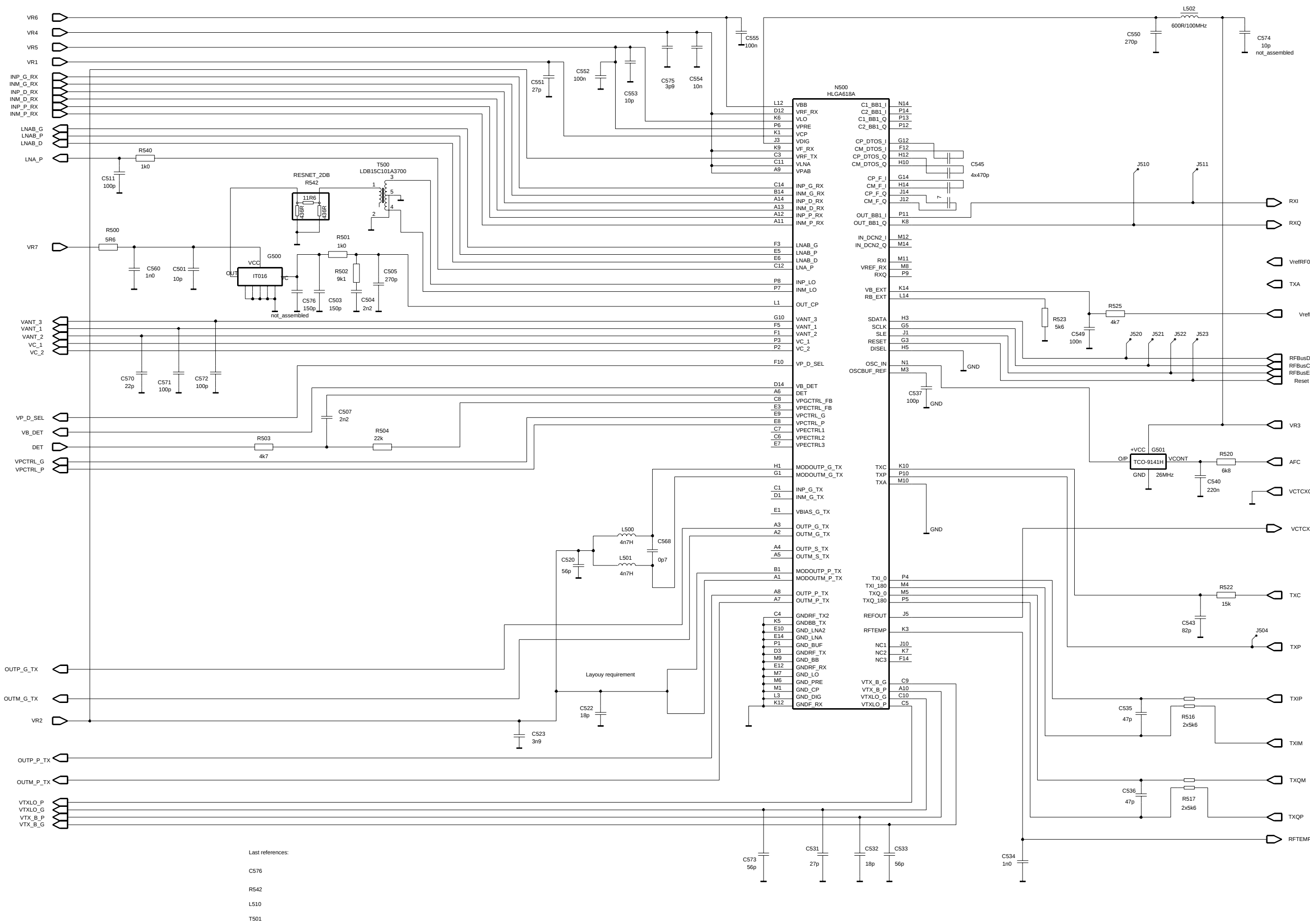
Last references:

C729

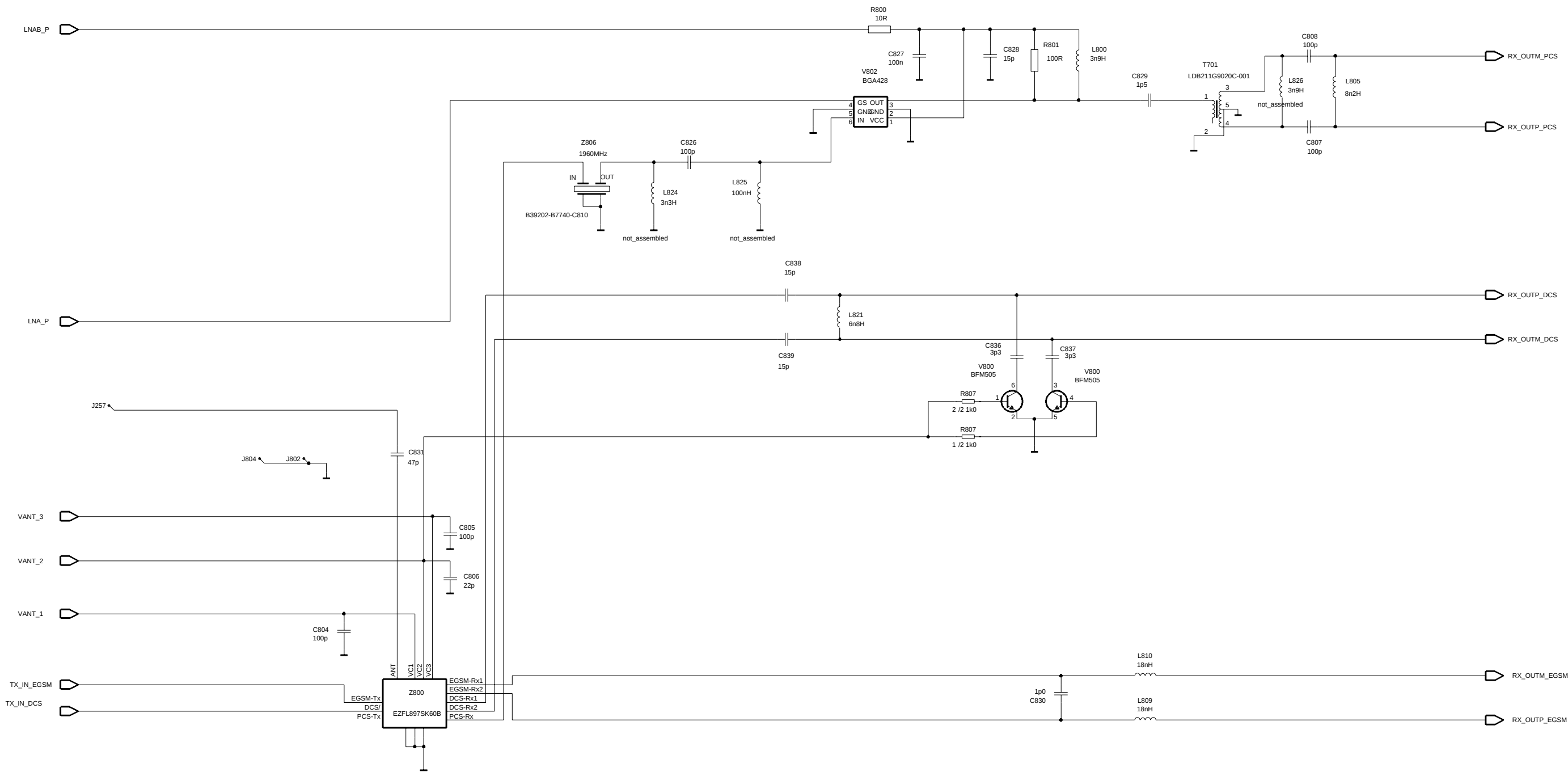
R715

L704

HELGA



RX Front End and Antenna Switch



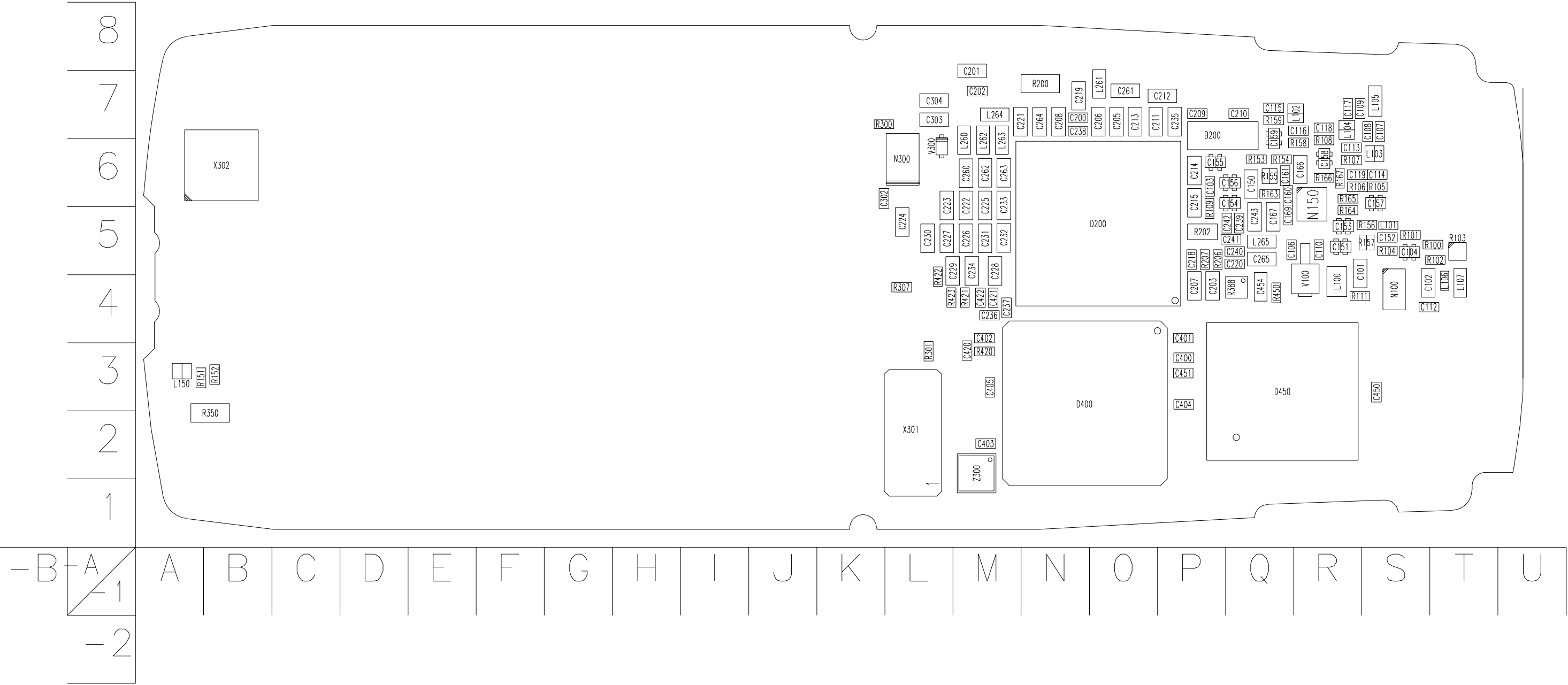
Last references:

C835

R807

L825

Assembly Values Diagram Top



[This page intentionally blank]