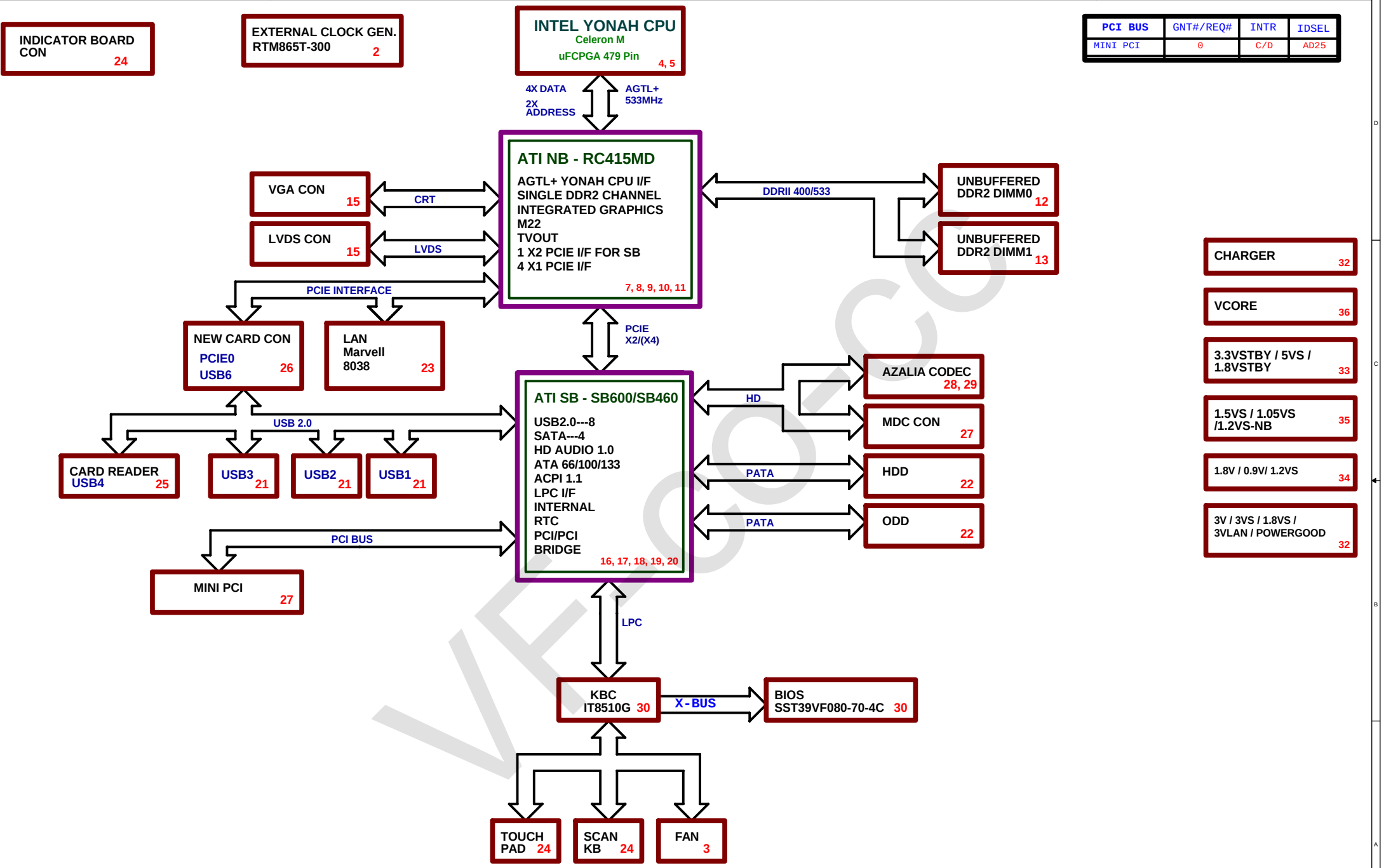




YONAH Celeron M Processor + RC415MD + SB460



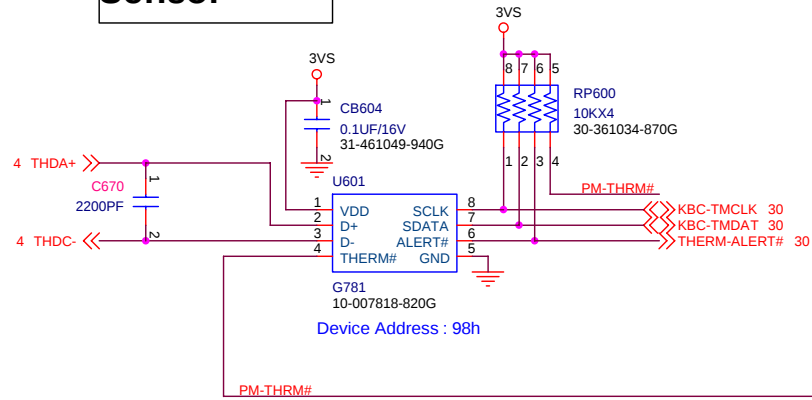
EXT CLK Frequency Select Table

LAYOUT NOTE:

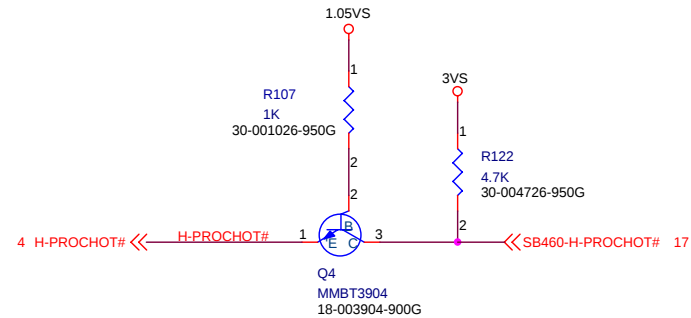
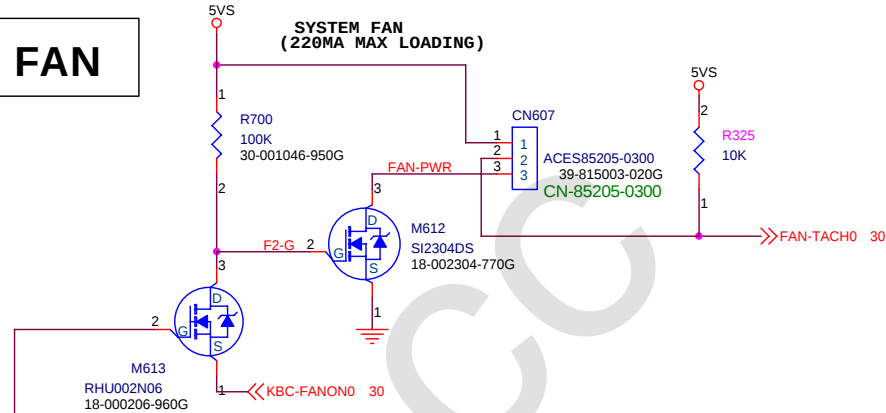
1. Place Series termination resistor close to CLK GEN.
2. Route CPUCLK+/-, NBCLK+/-, ITPCLK+/-, SCR+/- as different pair rule.
3. Put decoupling CAPS close to CLK GEN power pin.

ARIMA COMPUTER CORP.			
Project Name : W340UI		Title : CLK GEN-ICS951461	
Size :	Document Number : 40GAB1230-B100		Rev : B
Customer Date: Wednesday, June 21, 2006		Sheet : 2 of 39	

CPU Thermal Sensor



FAN

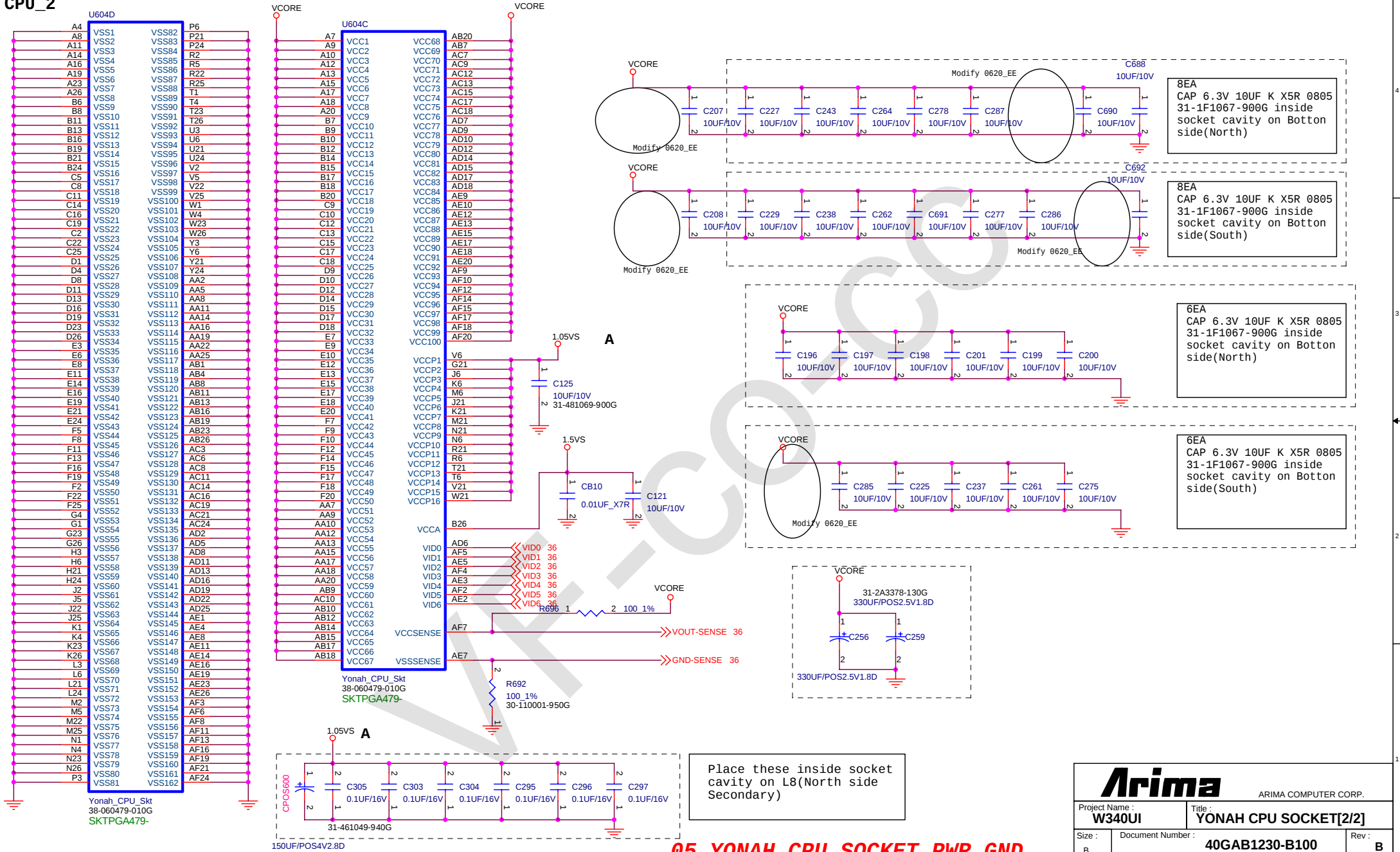


03_CPU THERMAL SENSIOR_FAN

Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : CPU THERM SENSOR/FAN	
Size : Custom	Document Number : 40GAB1230-B100	Rev : B	
Date: Wednesday, June 21, 2006		Sheet : 3	of 39

VF-CO-CC

CPU_2



05_YONAH CPU SOCKET_PWR_GND

Arima

ARIMA COMPUTER CORP.

Project Name :
W340UI

Title : **YONAH CPU SOCKET[2/2]**

Size :
B

Document Number :	
-------------------	--

40GAB1230-B100

ev : **B**

Date: Wednesday, June 21, 2006

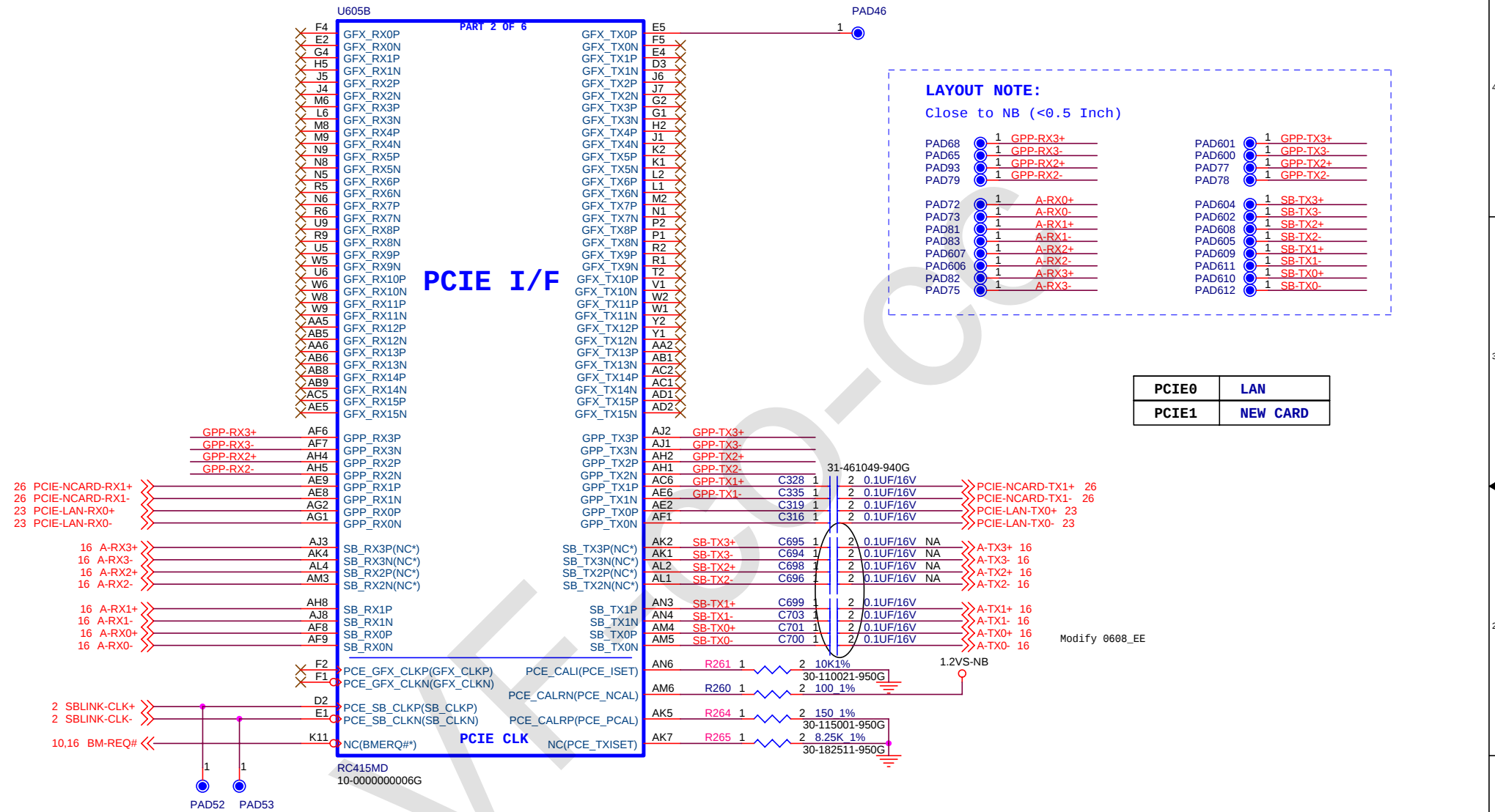
Sheet : 5 of 39

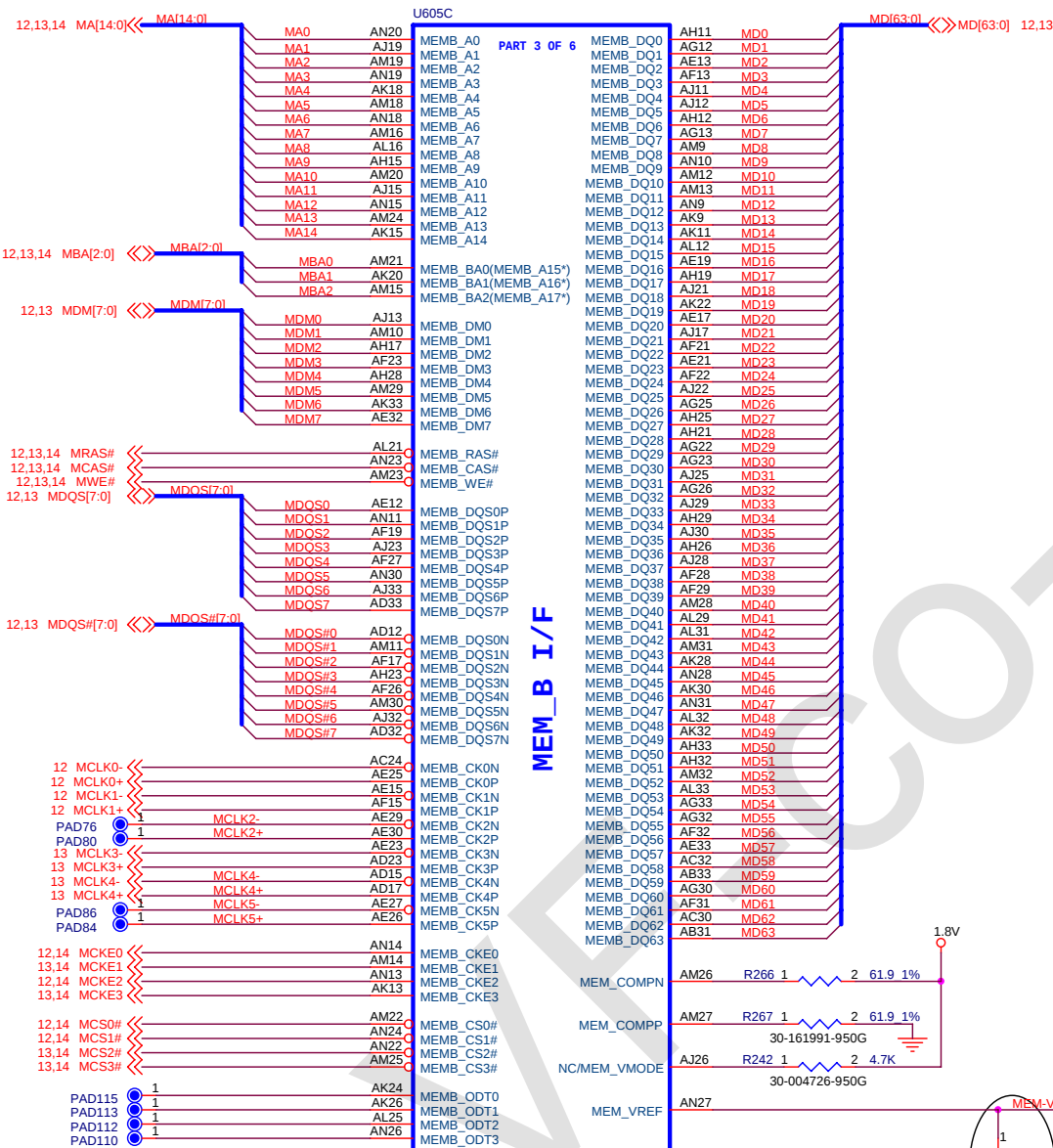
VF-CO-CC

Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : BLANK	
Size : C	Document Number : 40GAB1230-B100		Rev : B
Date: Wednesday, June 21, 2006		Sheet : 6 of 39	



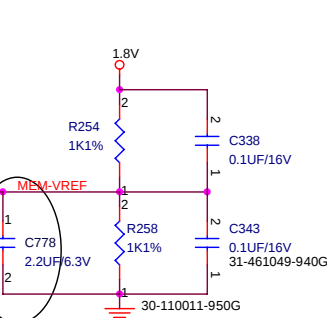
				ARIMA COMPUTER CORP.			
Project Name : W340UI				Title : RC415ME(1/5)_AGTL+			
Size : Custom		Document Number : 40GAB1230-B100				Rev : B	
Date : Wednesday, June 21, 2006				Sheet : 7 of 39			





RC415MD
10-0000000006G

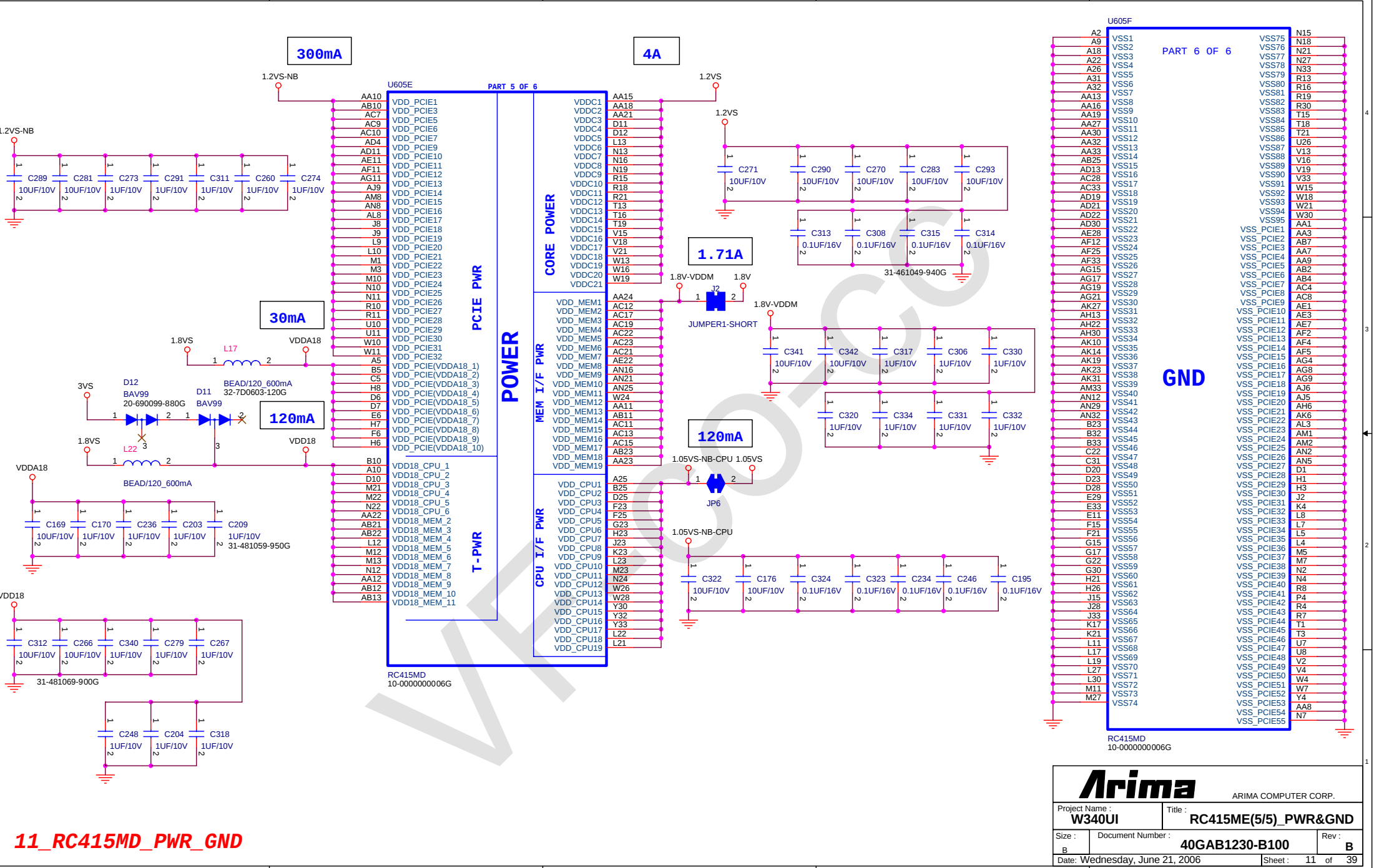
LAYOUT NOTE:
Resistors divider close to NB
and MEM-VREF should be 20mil



5/10 Add for ATI request.

09_RC415MD_MEMORY

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI		Title : RC415ME(3/5)_MEMORY
Size :	Document Number :	40GAB1230-B100	Rev : B
Date :	Wednesday, June 21, 2006		Sheet : 9 of 39

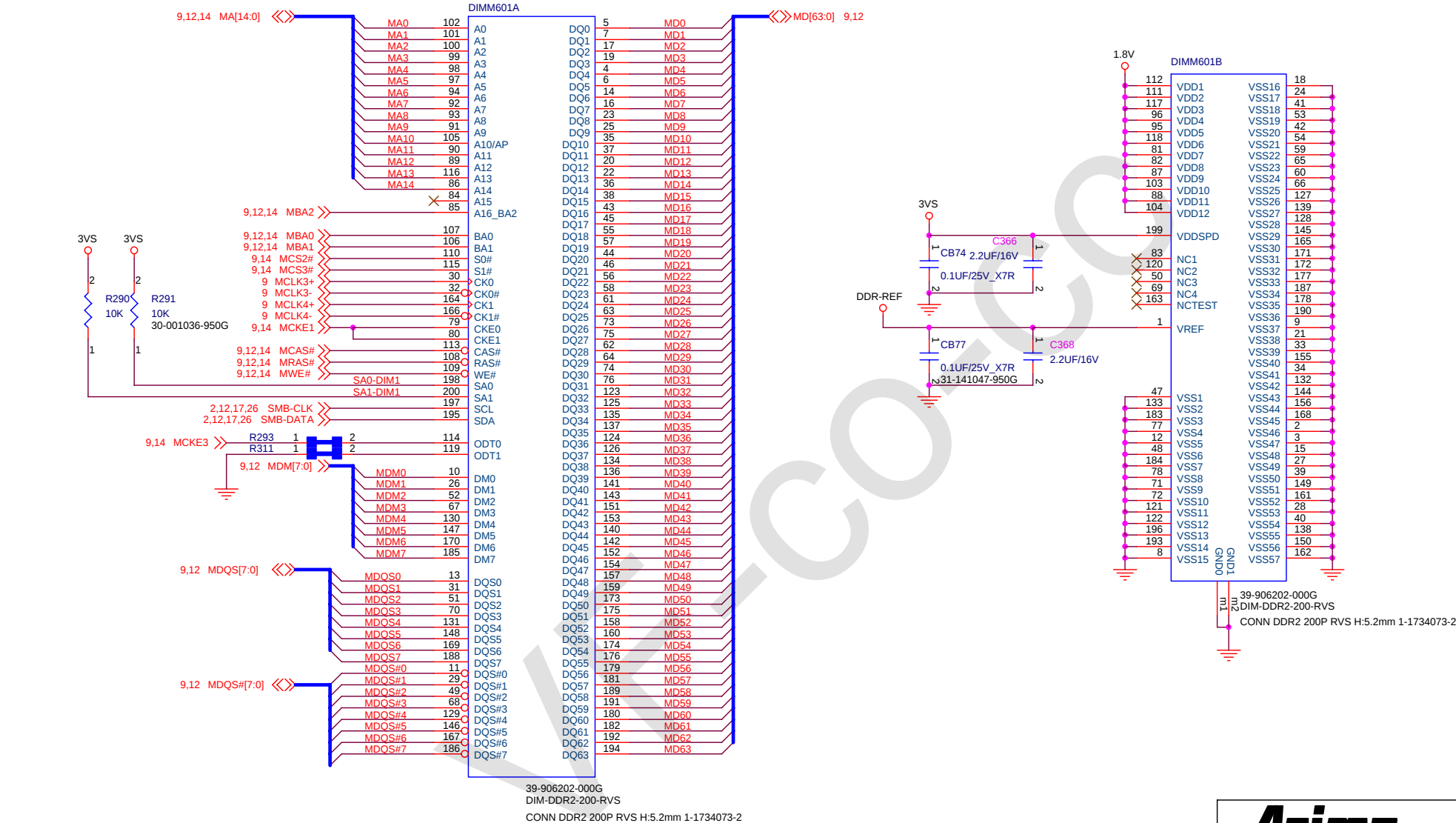


11_RC415MD_PWR_GND

Arima

ARIMA COMPUTER CORP.

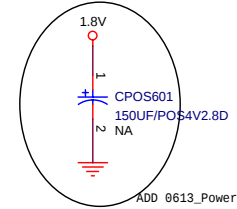
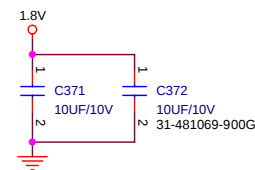
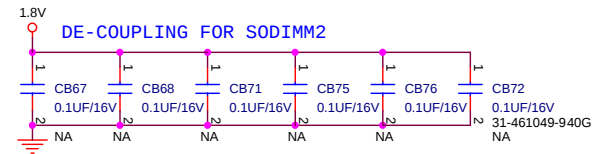
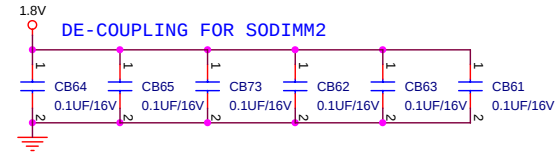
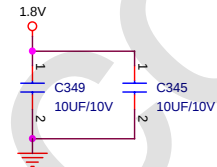
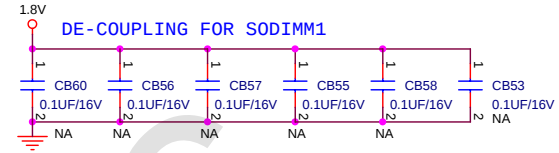
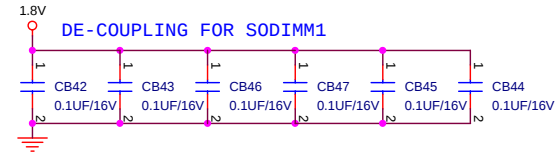
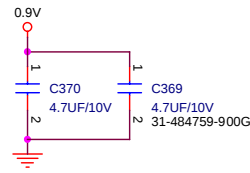
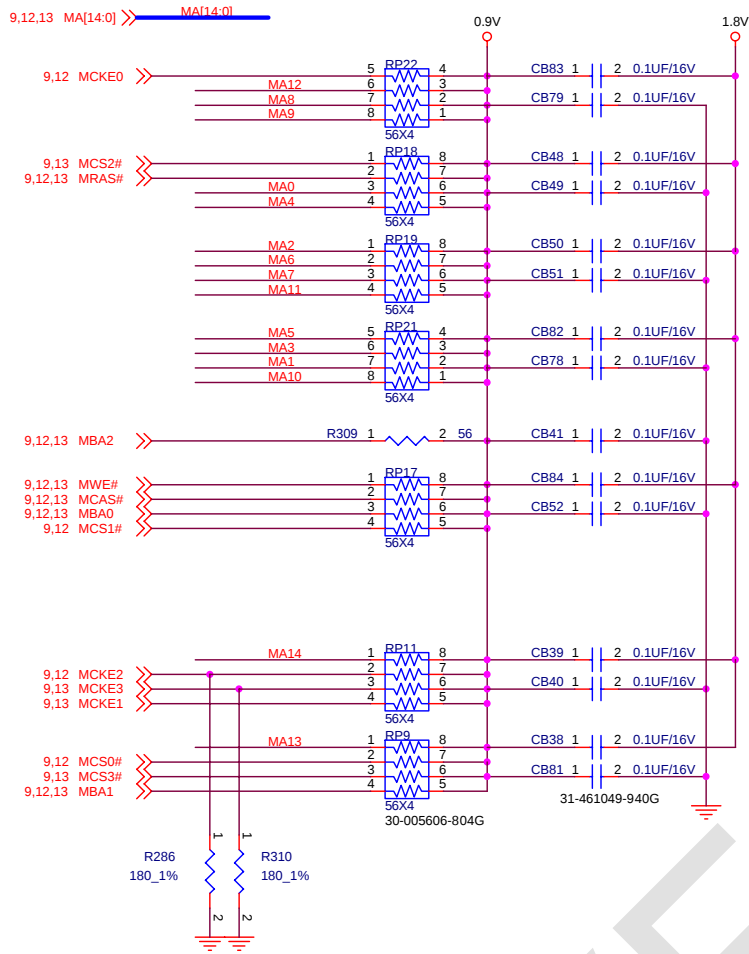
Project Name : W340UI		Title : RC415ME(5/5)_PWR&GND	
Size : B	Document Number : 40GAB1230-B100	Rev : B	
Date: Wednesday, June 21, 2006		Sheet: 11 of 39	



13_DDR2 SO-DIMM2

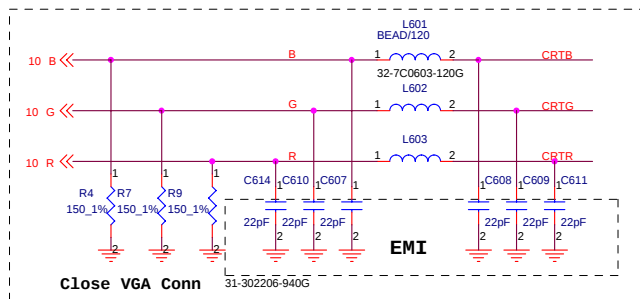
Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : DDR2 SO-DIMM1	
Size : Custom	Document Number : 40GAB1230-B100	Rev : B	
Date: Wednesday, June 21, 2006		Sheet : 13 of 39	

14_DDR2 TERMINATION

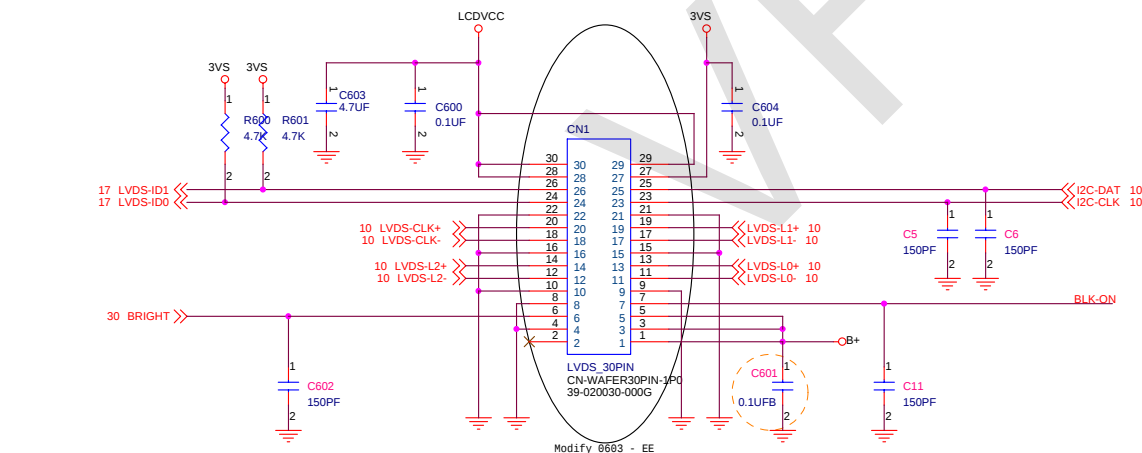
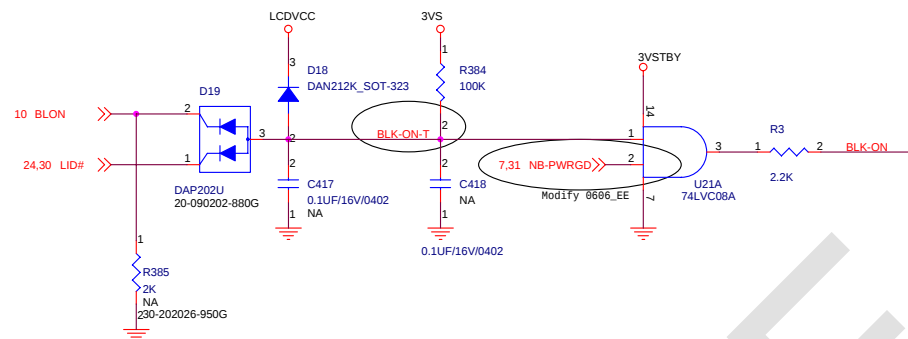


Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	DDR2 TERMINATION
Size :	Document Number :	Rev :	
B	40GAB1230-B100	B	
Date: Wednesday, June 21, 2006		Sheet : 14 of 39	

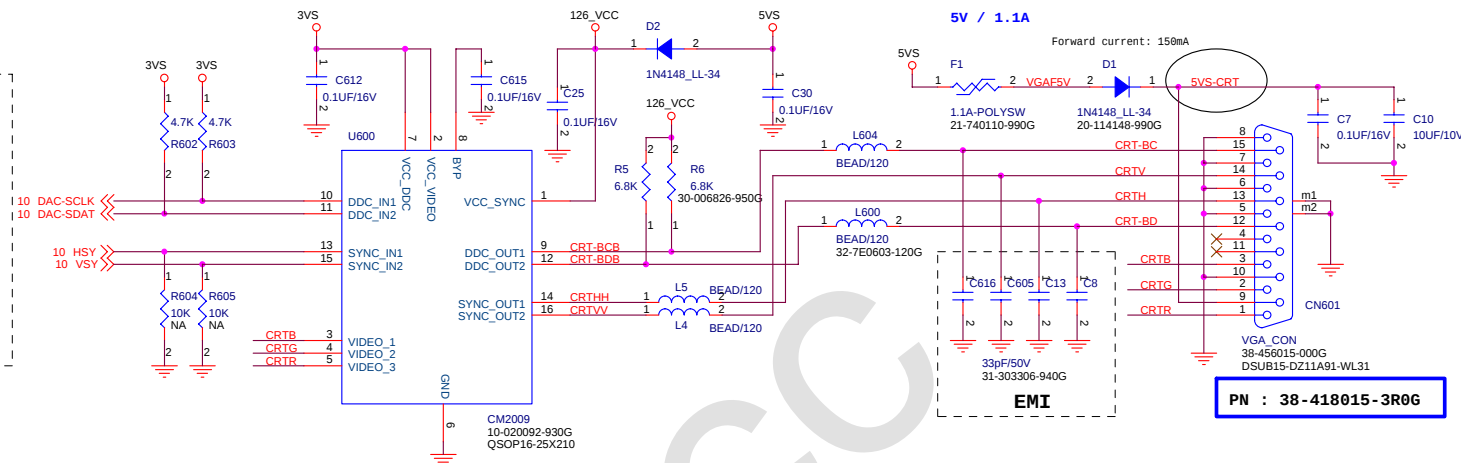
VGA CONNECTOR LCD CONNECTOR INVERTER CONN.



Layout Notice
Net R,G,B trace 5mil
5mi spacing1,10mil GND trace.



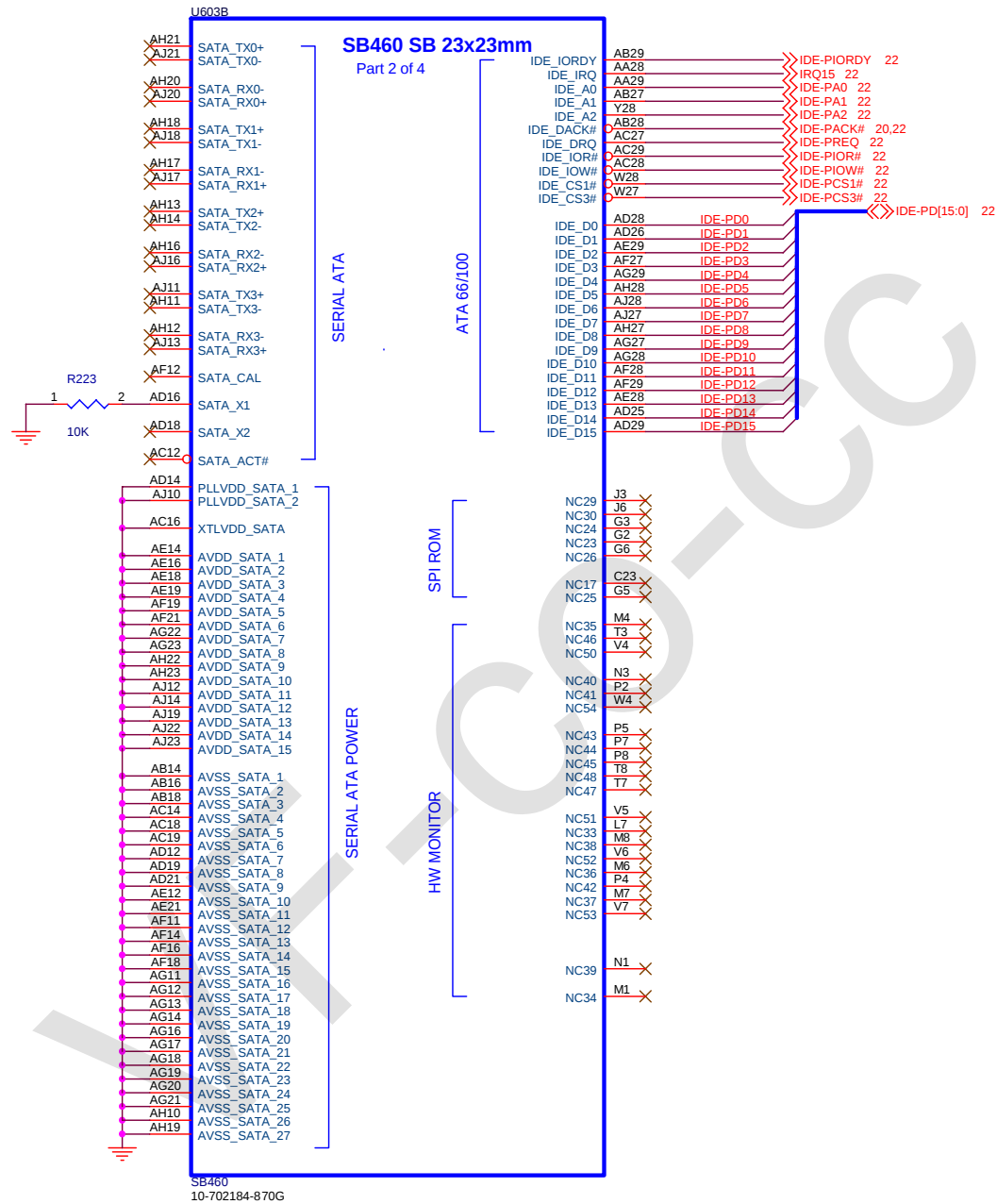
15_LVDS_CRT

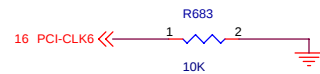
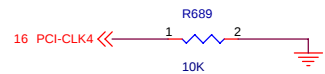
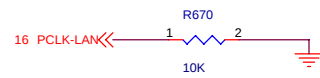
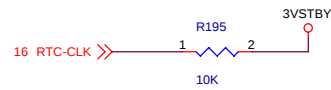
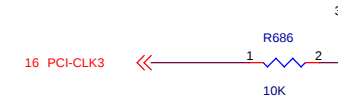
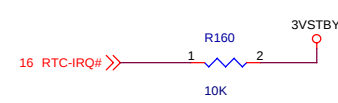
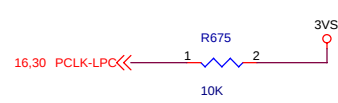
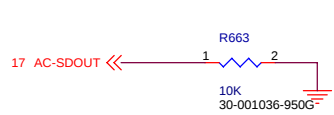


PN : 38-418015-3R0G

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI		Title : LVDS_CRT
Size :	Document Number :	40GAB1230-B100	Rev : B
Custom :			
Date : Wednesday, June 21, 2006	Sheet : 15		of 39







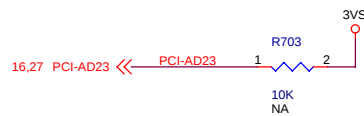
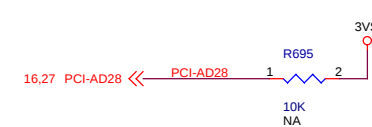
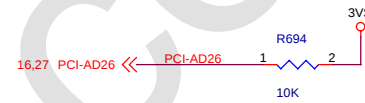
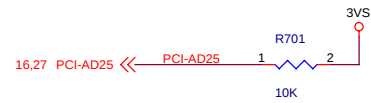
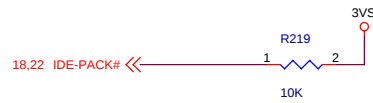
REQUIRED STRAPS

	AC-SDOUT	RTC_CLK	PCI_CLK4	PCI_CLK6	PCLK-LPC PCI_CLK0	PCLK-LAN PCI_CLK1
PULL HIGH	USE DEBUG STRAPS	INTERNAL RTC DEFAULT	USE INT. PLL48	CPU I/F = K8	ROM TYPE H,H = PCI ROM H,L = LPC I ROM	DEFAULT
PULL LOW	IGNORE DEBUG STRAPS DEFAULT	EXTERNAL RTC	USE EXT. 48MHz DEFAULT	CPU I/F = P4 DEFAULT	L,H = LPC II ROM L,L = FWH ROM	

PCI_CLK0
PCI_CLK1
PCI_CLK2
PCI_CLK3
PCI_CLK4
PCI_CLK5
PCI_CLK6
PCI_CLK7
PCI_CLK8

PCLK-LPC
PCLK-LAN
NOT USED
NOT USED
NOT USED
CLK-MINI
NOT USED
NOT USED
NOT USED

	RTC-IRQ# ACPWRON	SPDIF_OUT	PCI_CLK2	PCI_CLK3	CLK-MINI PCI_CLK5	LPC-FRAME#
PULL HIGH	MANUAL PWR ON DEFAULT	SIO 24MHz	XTAL MODE NOT SUPPORTED	USB PHY PWRDOWN DISABLE DEFAULT	PCIE_CM_SET LOW DEFAULT	ENABLE THERMALTRIP# DEFAULT
PULL LOW	AUTO PWR ON	SIO 48MHz DEFAULT	48MHz OSC MODE DEFAULT	USB PHY PWRDOWN ENABLE	PCIE_CM_SET HIGH	DISABLE THERMALTRIP#



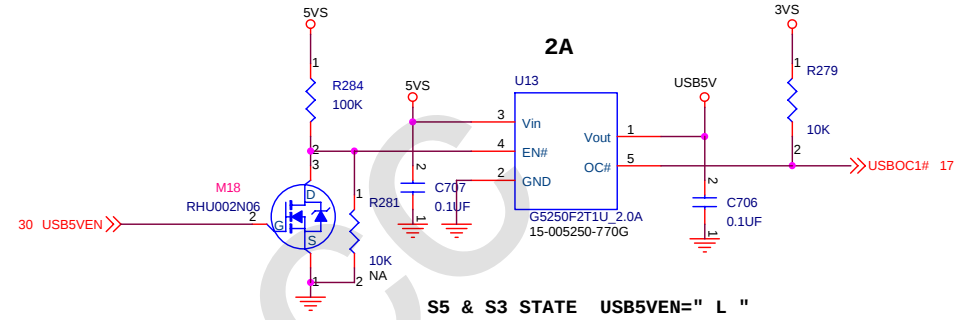
DEBUG STRAPS

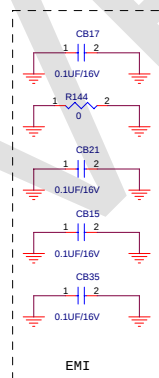
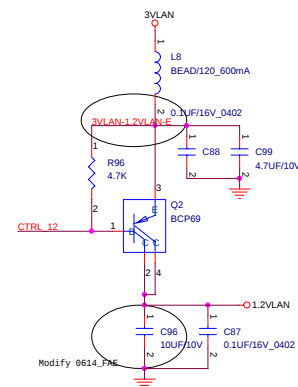
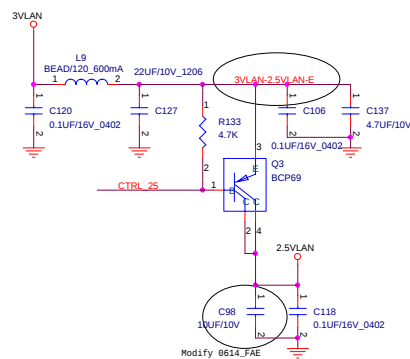
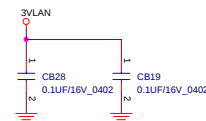
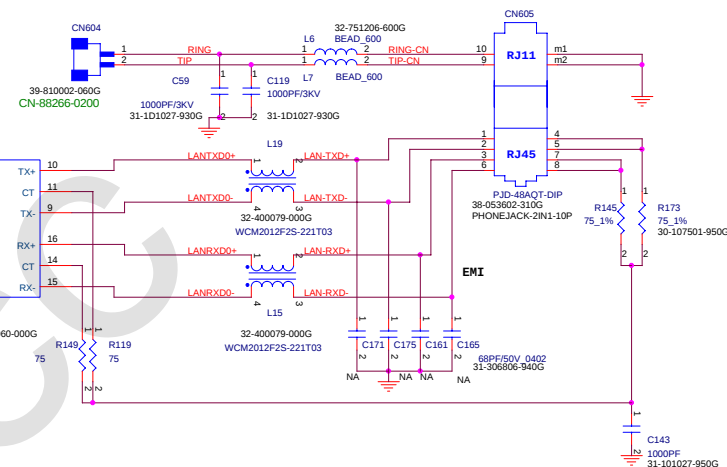
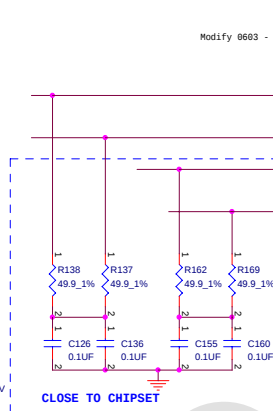
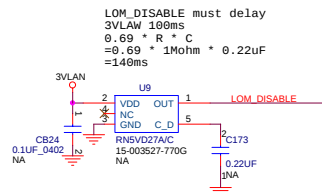
	IDE-PACK#	PCI-AD28	PCI-AD27	PCI-AD26	PCI-AD25	PCI-AD24	PCI-AD23
PULL HIGH	USE LONG RESET DEFAULT	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	BOOTFAIL TIMER DISABLED DEFAULT
PULL LOW	USE SHORT RESET	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	BOOTFAIL TIMER ENABLE

20_SB460 STRAPS

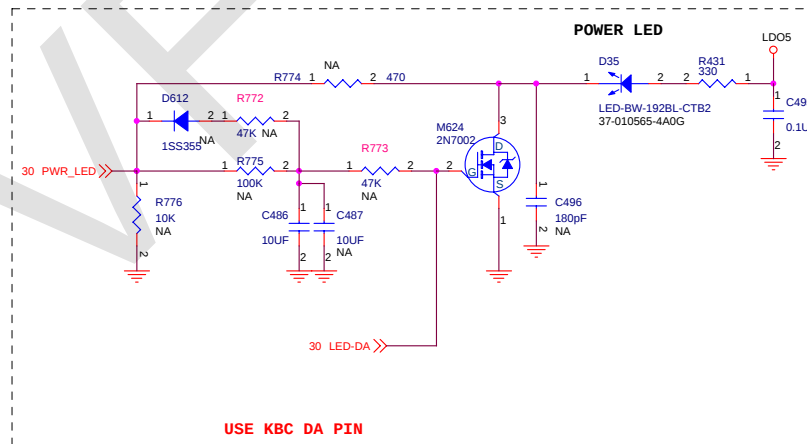
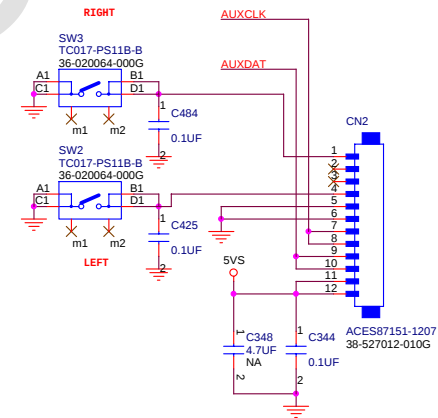
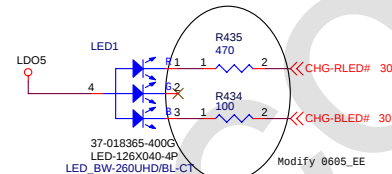
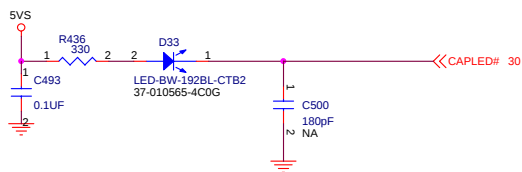
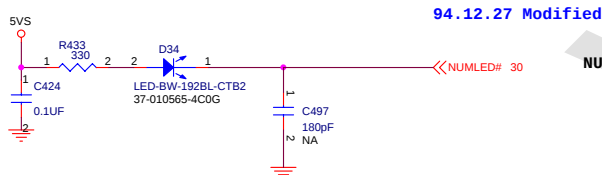
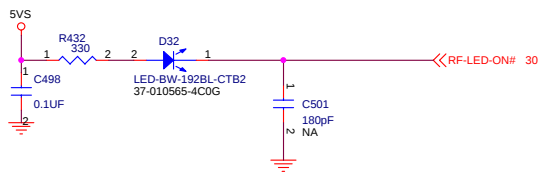
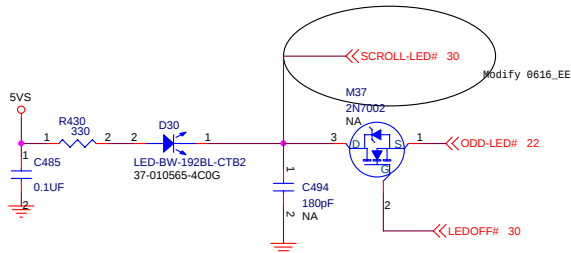
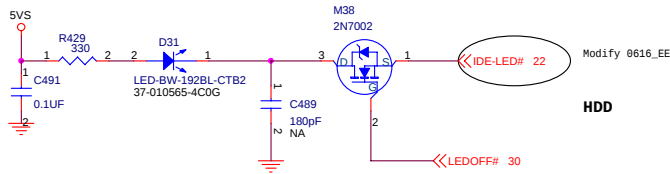
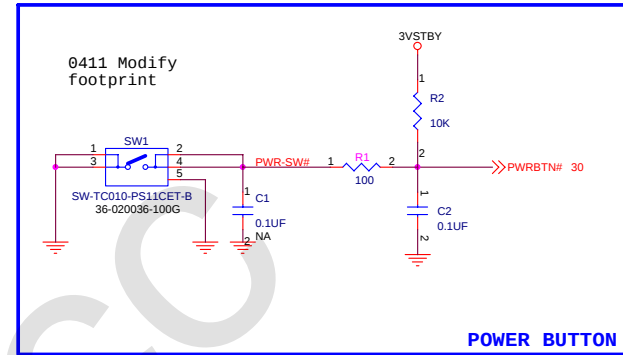
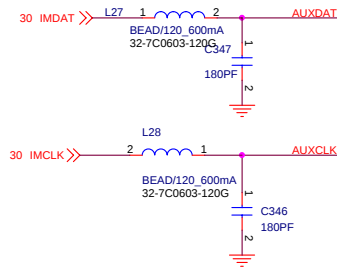
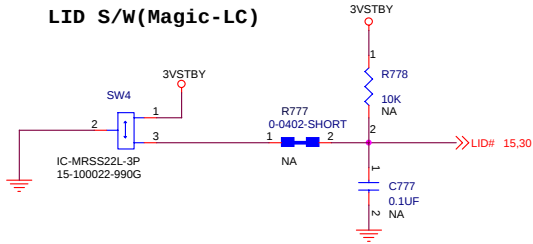
Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : SB460[5/5]	
Size : Custom	Document Number : 40GAB1230-B100		Rev : B
Date: Wednesday, June 21, 2006		Sheet : 20 of 39	

21_USB 2.0





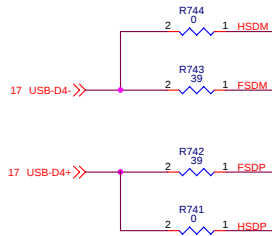
LID S/W(Magic-LC)



24_INDICATOR / LID / LED / TP

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	INDICATOR / LID / LED / TP
Size :	Document Number :	40GAB1230-B100	Rev : B
Custom :			
Date : Wednesday, June 21, 2006		Sheet : 24	of 39

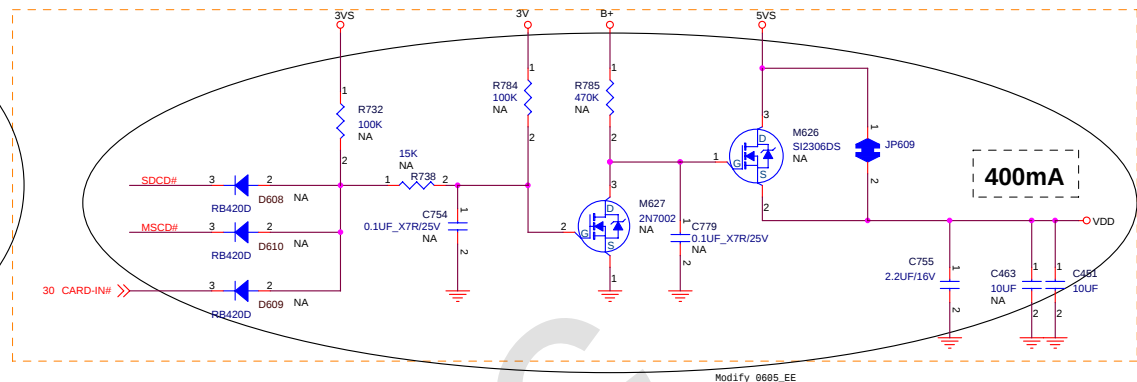
USB2.0 Interface



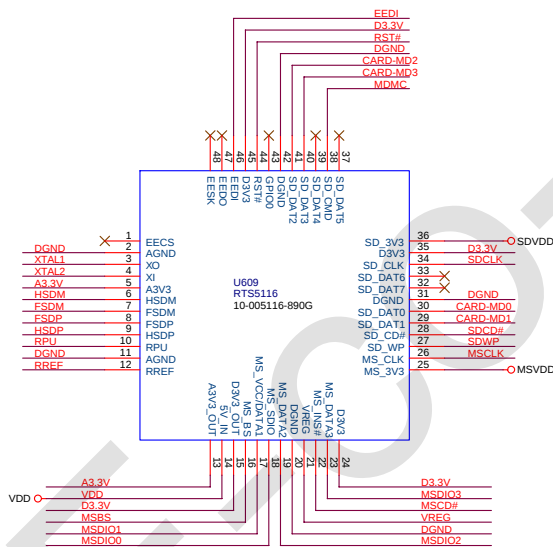
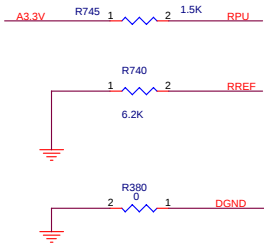
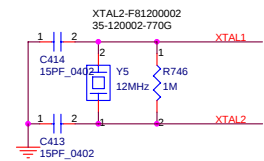
If you do not want to use EEPROM,
Please as follows.

Reserved for configurations	R733	R736
SD/MMC only	OPEN	OPEN
SD/MMC/MS/MS-Pro Combo Don't Support CPU C3 State	4.7K	OPEN

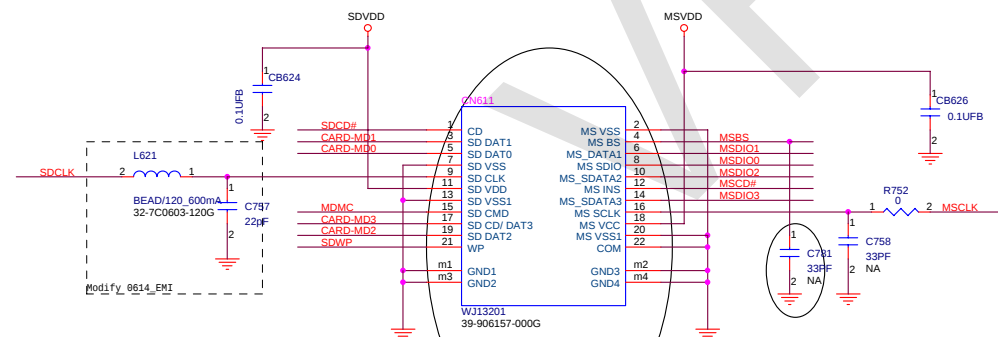
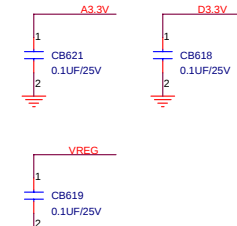
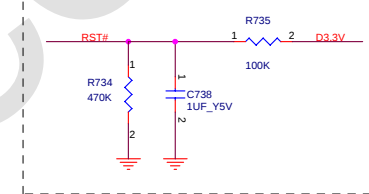
Modify 0612_EE



Modify 0605_EE



System reset

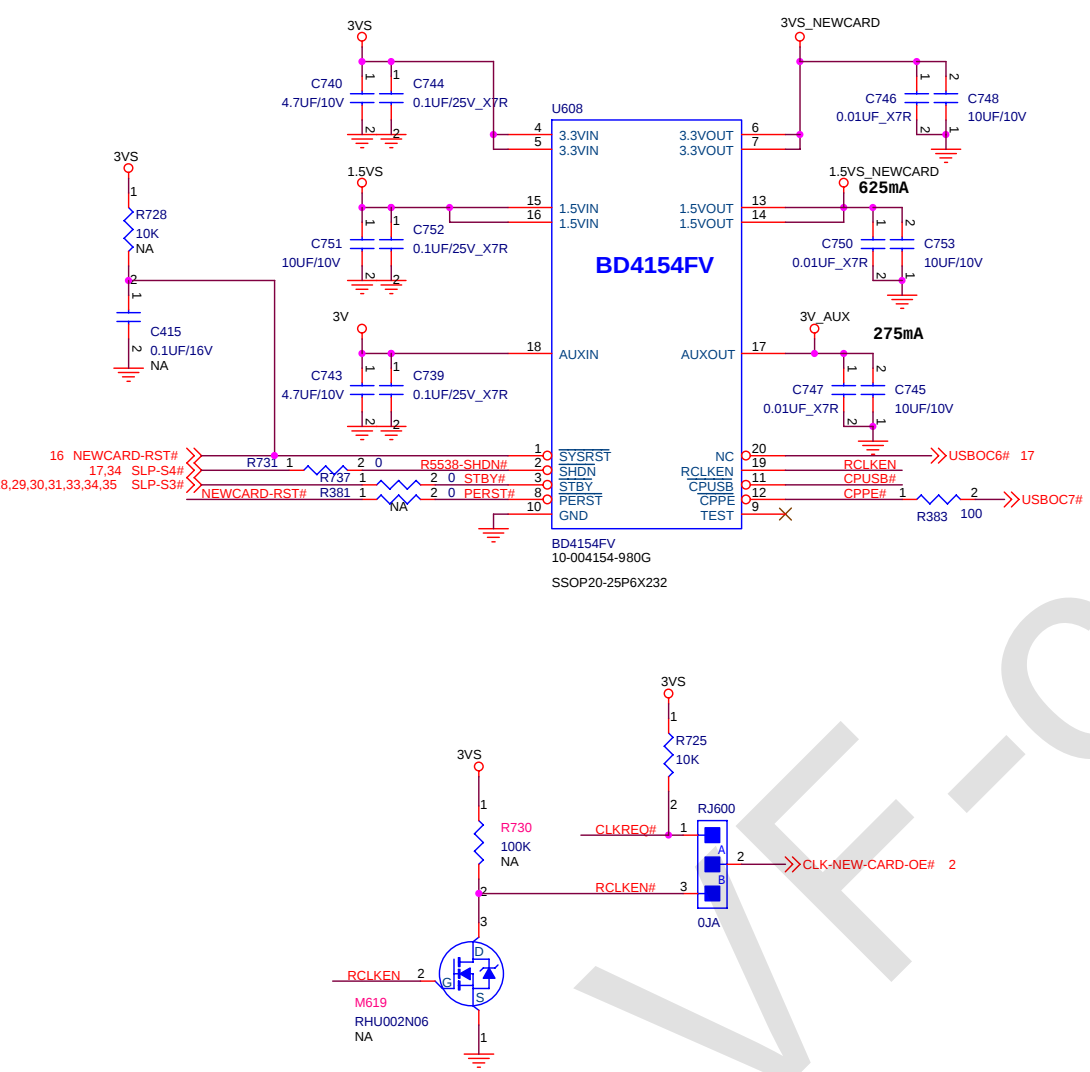


25_CARD READER CONTROL

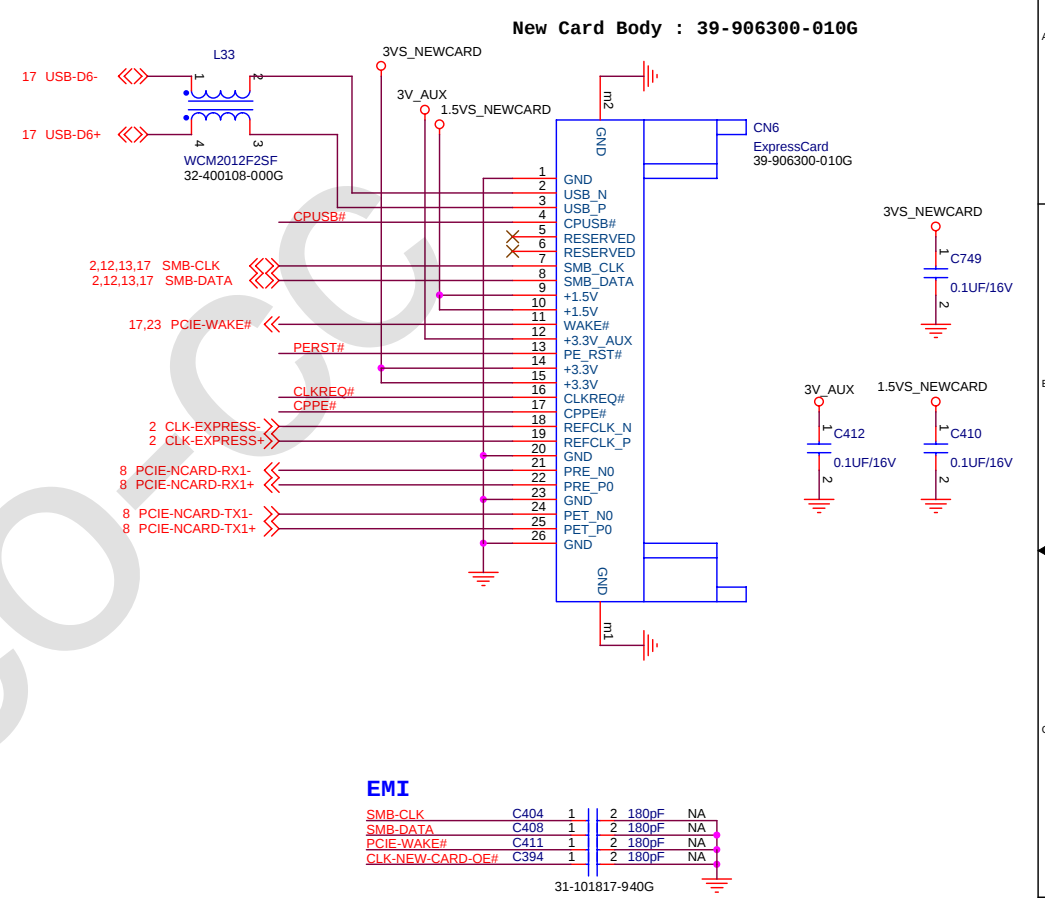
Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	CARD READER CONTROL
Size :	Document Number :	40GAB1230-B100	Rev : B
Custom	Date : Wednesday, June 21, 2006	Sheet :	25 of 39

NewCardSlot

26_NewCard Slot



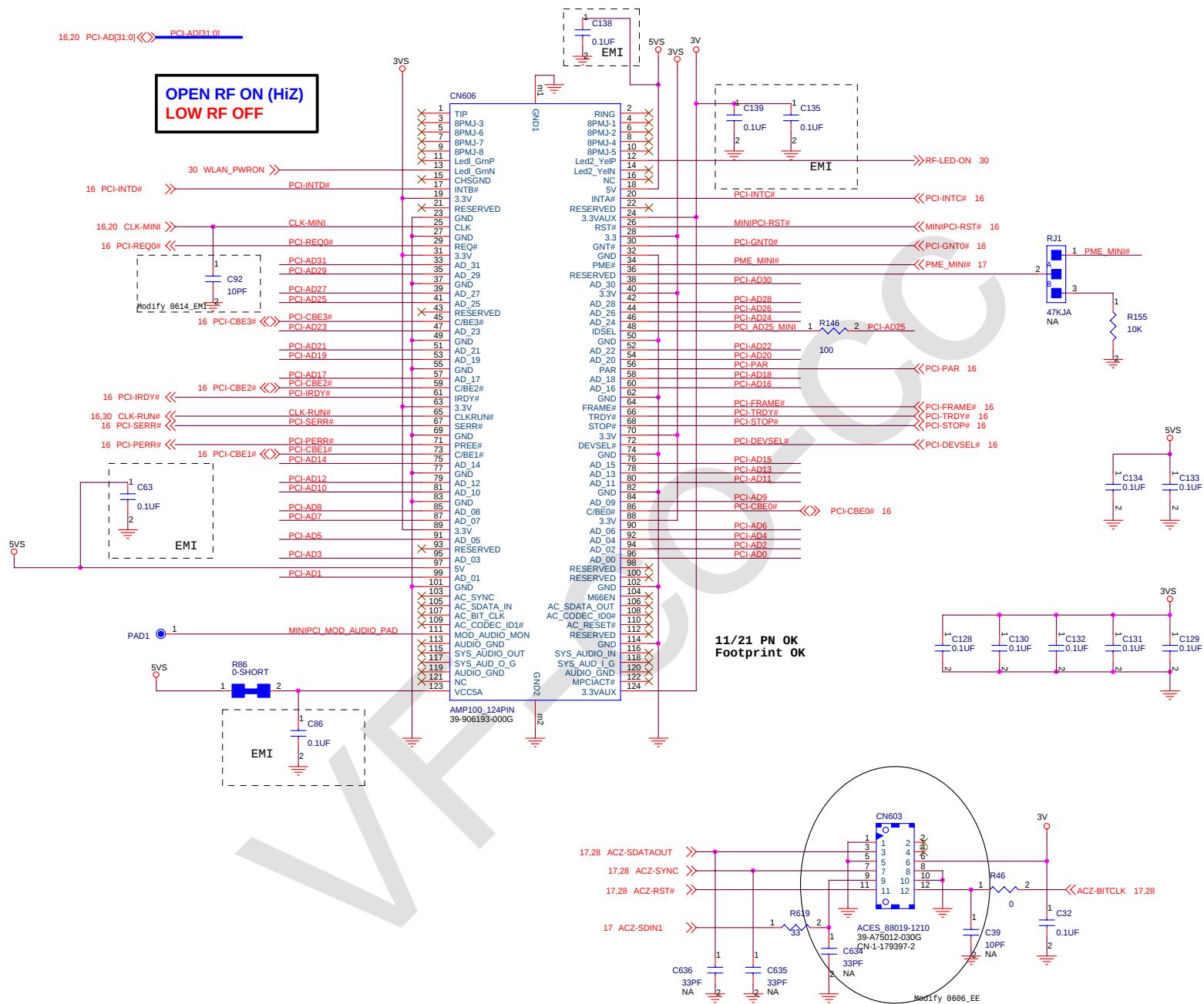
Express Slot(New Card)



Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : NEW CARD SLOT	
Size : B	Document Number : 40GAB1230-B100		Rev : B
Date : Wednesday, June 21, 2006		Sheet : 26 of 39	

16,20 PCI-AD[31:0] << PCI-AD[31:0]

OPEN RF ON (HiZ)
LOW RF OFF



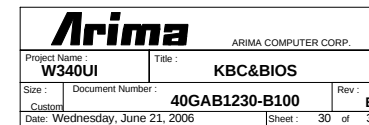
11/21 PN OK
Footprint OK

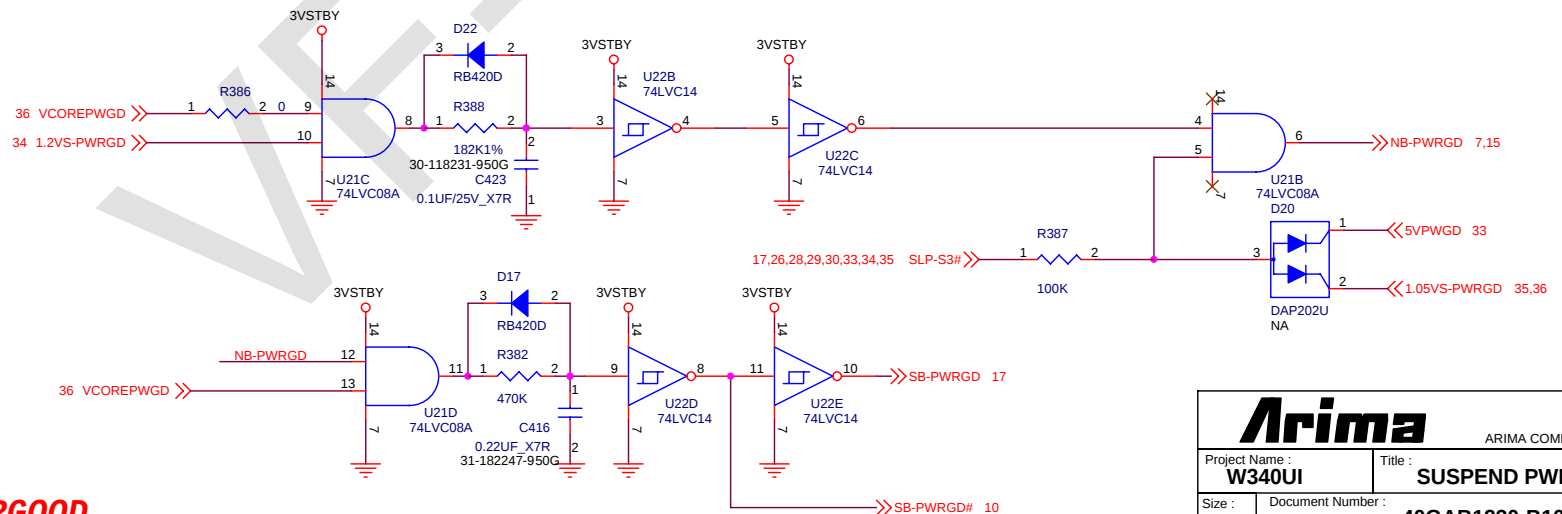
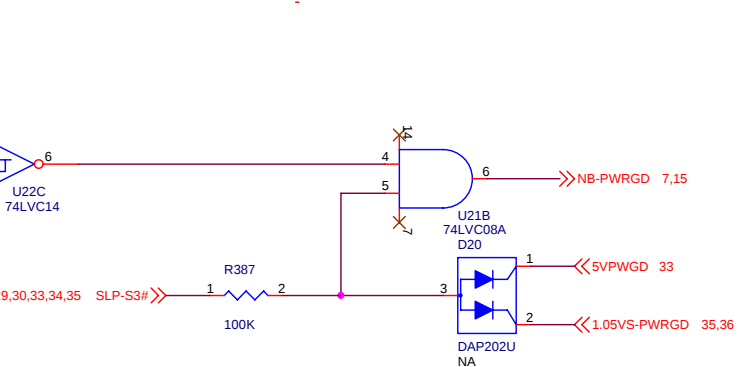
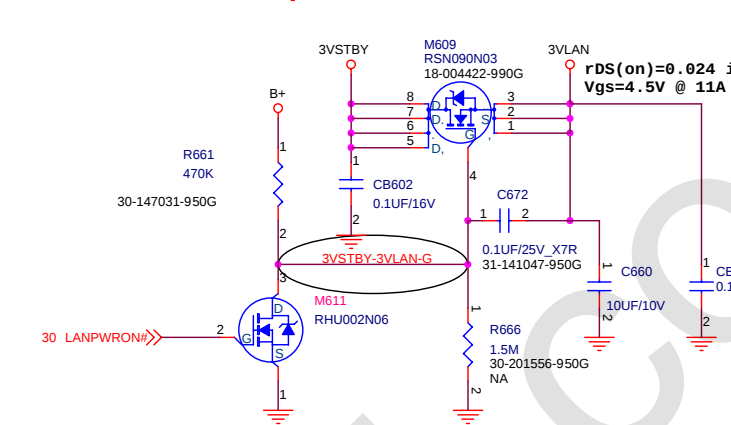
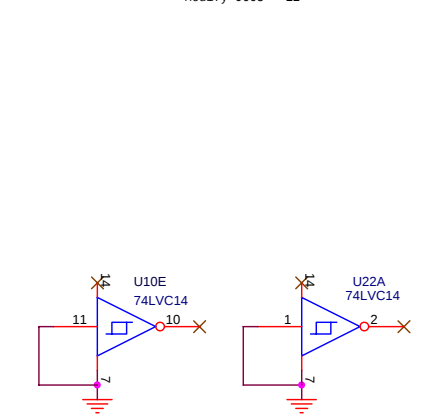
27_MINI PCI / MDC

Arima Proprietary

Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : MINI PCI / MDC	
Size : Custom	Document Number : 40GAB1230-B100	Rev : B	
Date: Wednesday, June 21, 2006		Sheet: 27 of 39	





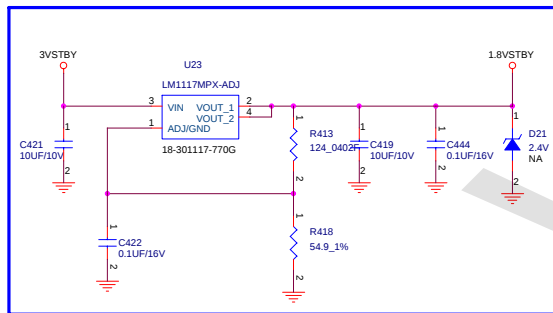


				ARIMA COMPUTER CORP.			
Project Name : W340UI			Title : SUSPEND PWR_PWRGD				
Size :	Document Number : 40GAB1230-B100				Rev : B		
Custom	Date : Wednesday, June 21, 2006				Sheet : 31 of 39		

3VSTBY/3Amax

5V/5Amax

1.8VSTBY



When the 3.3V main output voltage is above the 2.91V, an internal 1.5ohm P-channel MOSFET switch connects OUT3 to LDO3, while simultaneously shutting down the LDO3 linear regulator.

-SKIP connect to:
Floating for ultrasonic mode
VCC for PWM mode
GND for normal idle-mode

Arima		ARIMA COMPUTER CORP.	
Project Name :	W340UI	Title :	3VSTBY_5V
Size :	Document Number :	40GAB1230-B100	Rev : B
Customer :	Date : Wednesday, June 21, 2006	Sheet :	33 of 39

33_3VSTBY_5VS_1.8VS

Modify 0609_POWER

1.2V/5A max

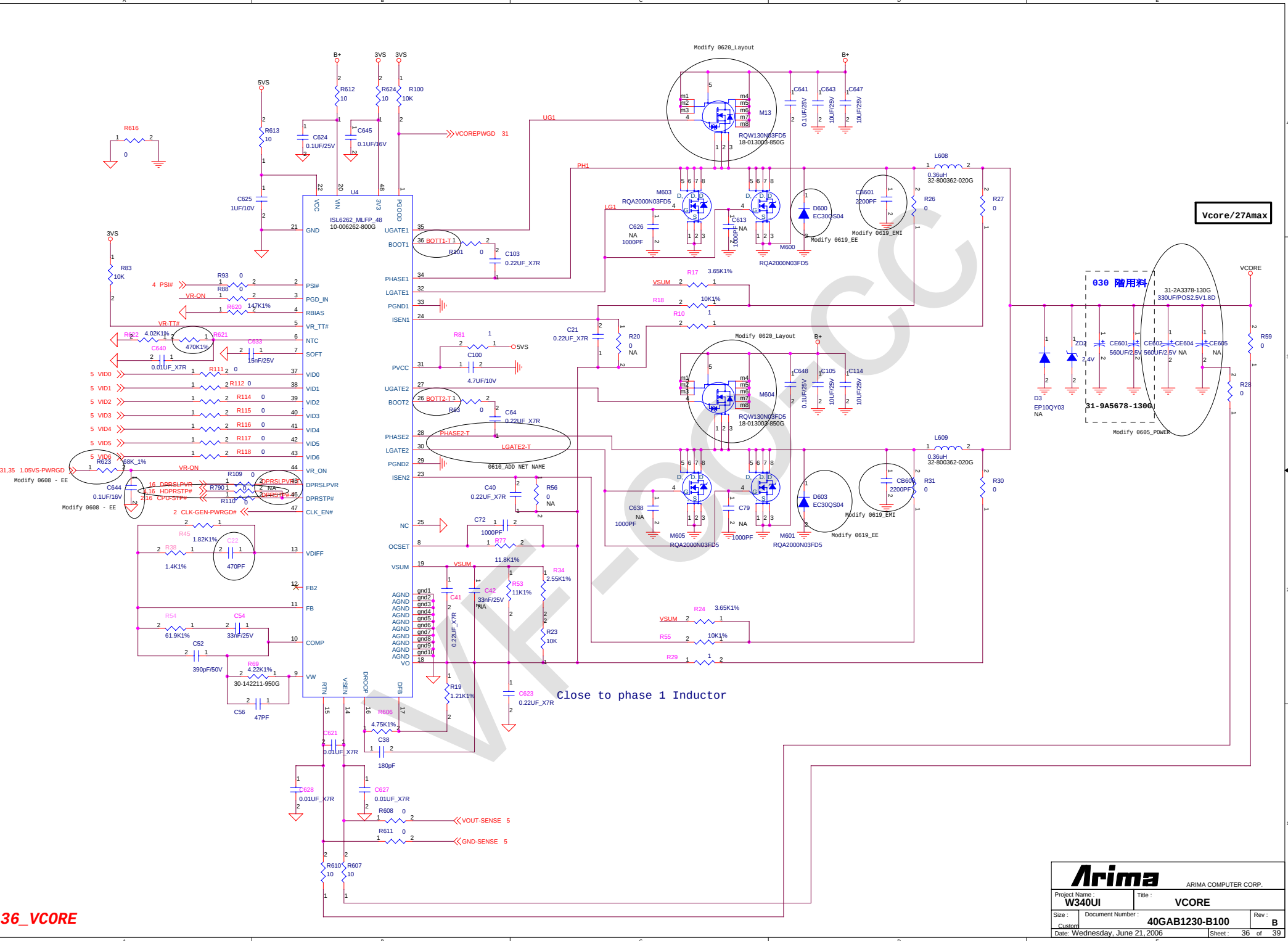
1.8V/8Amax

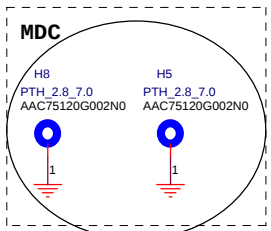
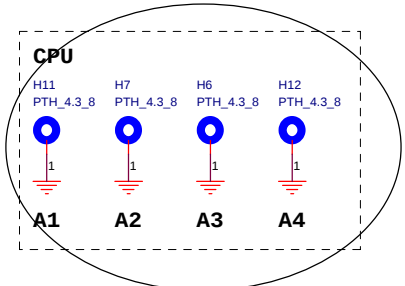
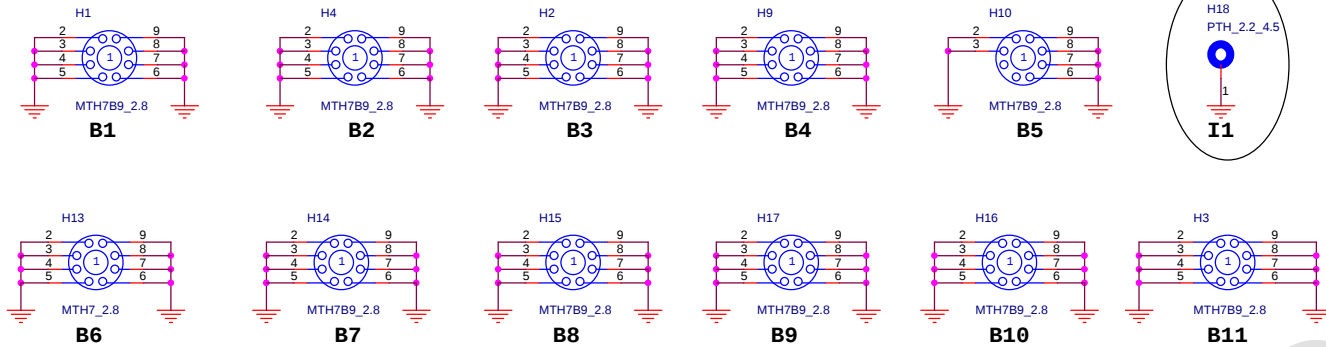
0.9V/2Amax

Connect from DDR DIM

Close to 1.8V

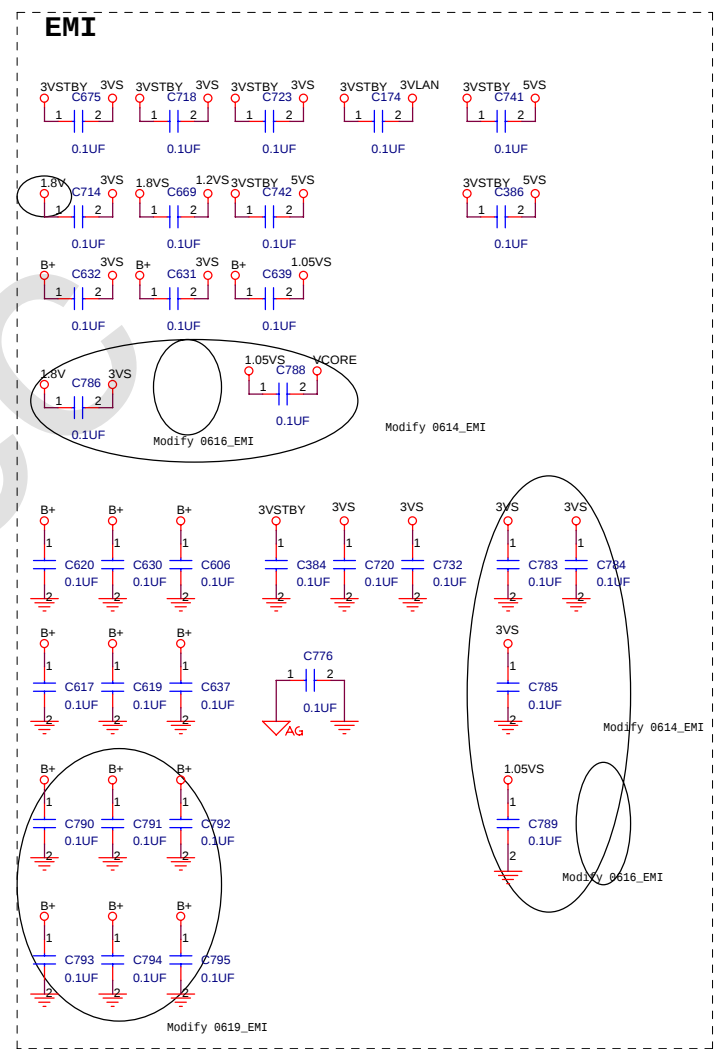
Change from 2N7002
TO RHU002N06





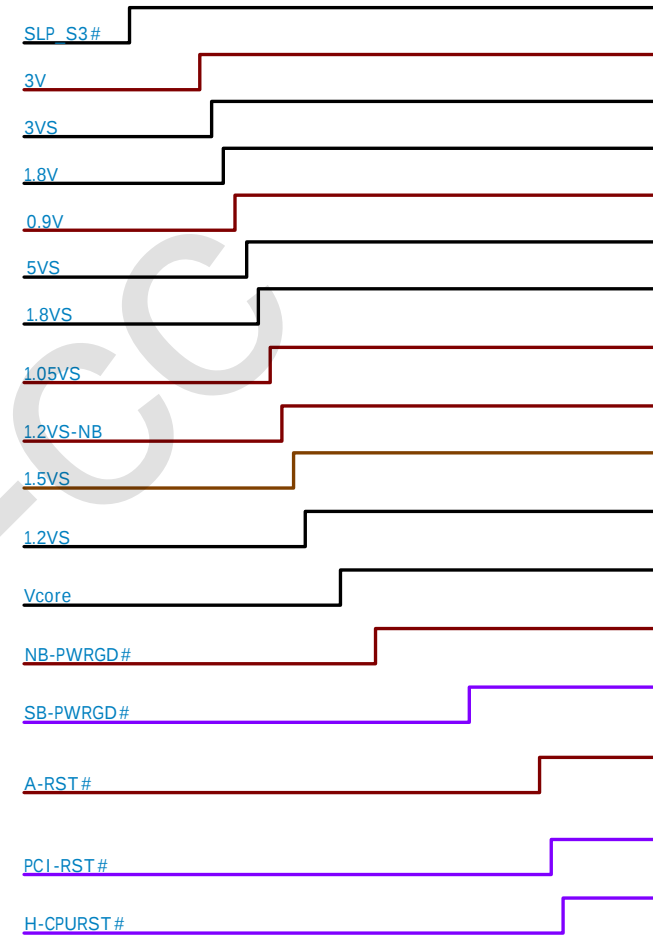
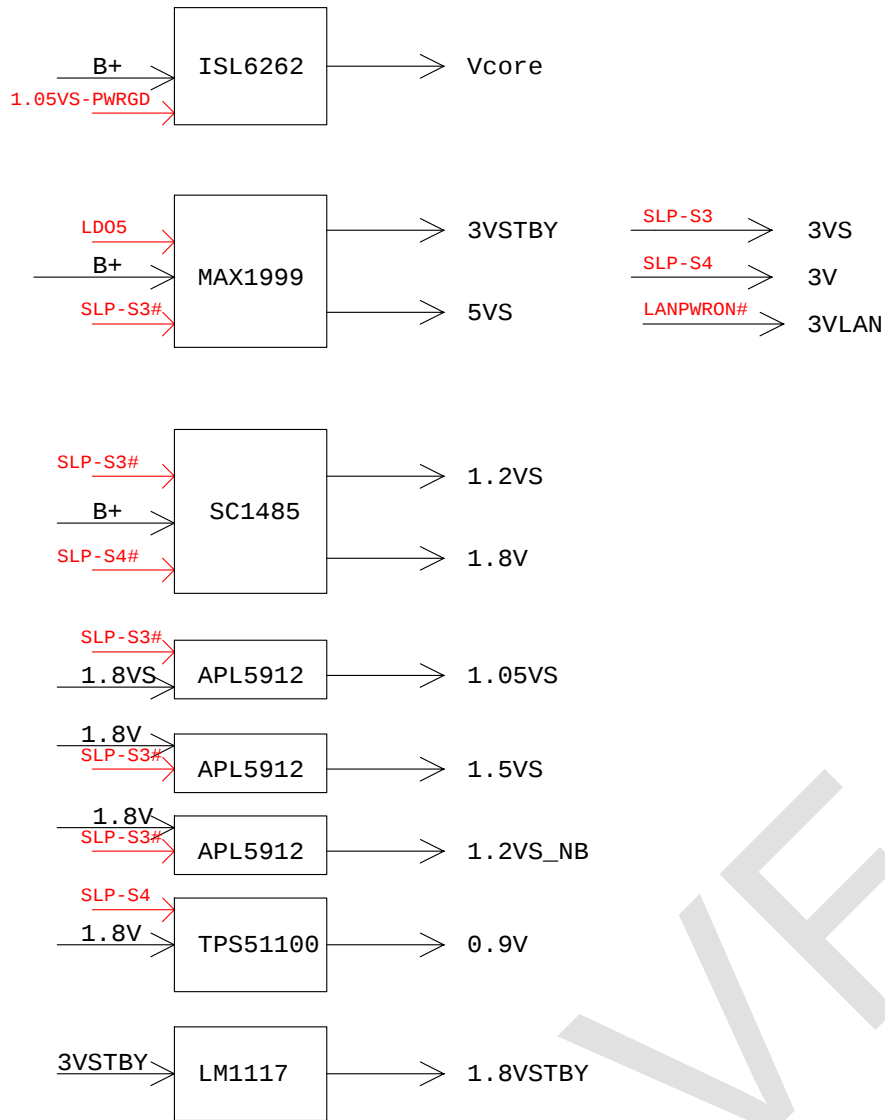
FID7 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID603 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID1 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID600 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用
FID3 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID601 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID5 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用	FID602 FIDUCIAL 本PIN不可接任何信號或電源 CAD-016 FIDUCIAL MARK 光學定位用

Top Layer



94.12.28 Modified

Arima		ARIMA COMPUTER CORP.	
Project Name : W340UI		Title : MOUNTING HOLE	
Size : Custom	Document Number : 40GAB1230-B100	Rev : B	
Date : Wednesday, June 21, 2006		Sheet : 37 of 39	



Rev.A to Rev.B MODIFY LIST A: ADD D:DEL M:Modify #:DEL ITEM

2006/05/18	01.Page 07	M	R648	0603 change to 0402
	02.Page 07	M	R643	0402 change to 0603
	03.Page 37	M	H11, H7, H6, H12	PTH9_5 change to PTH_4.3_8
	04.Page 37	M	H8,H5	PTH_6.1.2 change to PTH_2.8_7.0
	05.Page 37	A	H18	PTH_2.2_4.5
2006/06/05	02.Page 07	M	R269	300Kohm change to 240Kohm
	05.Page 09	A	C778	2.2uF
	06.Page 10	A	R779	4.7Kohm Pull Low
	07.Page 10	D	Q8, R189	NA
	08.Page 10	M	Q7 (BJT)	change to M625 (2N7002)
	09.Page 15	M	CN1	Modify direction for Conn
	12.Page 16	A	R780	20Mohm
	13.Page 16	M	Y600	35-327682-722G change to 35-327682-700G
	15.Page 16	A	R781	0ohm
	16.Page 16	A	R787	10Kohm - Delay SB-STPCLK#
	17.Page 16	A	D613	DIODE 1SS355 - Delay SB-STPCLK#
	18.Page 16	A	C780	330PF/50V_NPO - Delay SB-STPCLK#
	19.Page 16	A	Q603	MMBT3904 - Delay SB-STPCLK#
	20.Page 16	A	R786	10Kohm Pull 3VS - Delay SB-STPCLK#
	21.Page 16	A	M628	RHU002N06 - Delay SB-STPCLK#
	22.Page 17	D	RP601	Pin6 (MD-ID1)
	23.Page 17	D	RP2	Pin6 (MD-ID0)
	25.Page 17	A	RJ601	10Kohm_A - MB Revision
	26.Page 17	A	RJ602	10Kohm_B - MB Revision
	27.Page 17	A	RJ603	10Kohm_A - LOGO Select
	28.Page 17	A	PAD616	
	30.Page 22	M	CN608	Pin34 connect to CN612 Pin32 (PDIAGR#)
	31.Page 22	M	R706	10Kohm NA
	33.Page 23	D	CB13 & CB16	
	34.Page 23	M	TT600	Pin6, Pin3 connect to 2.5VLAN
	35.Page 24	M	R435	560ohm change to 470ohm
	36.Page 24	M	R434	560ohm change to 100ohm
	37.Page 25	M	D608, D609, D610	DIODE NA
	38.Page 25	M	R732	NA
	39.Page 25	A	R764	100Kohm NA
	40.Page 25	A	M627	2N7002 NA
	41.Page 25	A	R765	470Kohm NA
	42.Page 25	A	C779	0.1uF NA
	43.Page 25	A	M626	MOSFET N-CH Si2306DS NA
	44.Page 25	A	JP609	
	45.Page 28	M	R421	Pin1 connect to SLP-S3#
	46.Page 31	M	M621	Pin2 connect to SLP-S4
	47.Page 31	M	R747	Pin1 connect to 3V
	48.Page 31	M	R374	300Kohm change to 180Kohm
	49.Page 31	M	R371	300Kohm change to 430Kohm
	50.Page 31	M	R713	300Kohm change to 150Kohm
	51.Page 32	M	R78	10Kohm change to 25.5Kohm
	52.Page 33	M	R768	20Kohm change to 62Kohm
	53.Page 33	M	C773	100PF change to 0.1uF
	54.Page 34	A	RJ525	0ohm_A (5VS)
	55.Page 34	M	D601	Pin2, C17 Pin1, C104 Pin1 connect to RJ525 Pin2
	57.Page 34	M	C34	0.1uF Mount
	61.Page 34	M	VTTSRC	modify to 0.9VSR
	62.Page 35	M	R669	150Kohm change to 68Kohm
	63.Page 35	M	C680	0.1uF NA
	64.Page 35	M	R331	47Kohm change to 82Kohm
	65.Page 35	M	R715	47Kohm change to 68Kohm
	67.Page 36	M	CE604, CE605	31-9A5678-130G change to 31-2A3378-130G

2006/06/06	01.Page 07	M	RJ604	Pin2 connect to SB-STPCLK#-R , A: SB-STPCLK#
	24.Page 17	A	U603	Pin E23 connect to RP601 Pin5 (3VS)
	01.Page 15	M	U21	Pin2 connect to NB-PWRGD
	02.Page 27	M	CN603	39-A75012-020G change to 39-A75012-030G
	03.Page 08	M	C699, C703, C701, C700	0.1uF
	04.Page 08	M	C695, C694, C698, C696	0.1uF
	29.Page 17	M	R172	11.8Kohm change to 10Kohm
	56.Page 34	M	R58	120Kohm change to 160Kohm
	66.Page 36	A	R790	0ohm HDRPSTP # connect to U4 Pin46
	01.Page 36	M	C644	100PF change to 0.1uF
	02.Page 28	M	M32	2N7002 NA
	03.Page 28	M	R402	100Kohm NA
	04.Page 28	M	C432	0.1uF change to 0.01uF
	05.Page 28	A	R791	100Kohm M32 Pin 2 & M32 Pin3
	06.Page 29	M	U512	10-009750-800G change to 15-00100000001G (Max9750C)
	07.Page 29	M	R392	0ohm NA
	08.Page 29	M	R393	Mount - 30-000006-990G
	09.Page 29	M	R395	NA
	10.Page 29	M	R394	Mount - 30-000006-990G
	11.Page 29	M	R398	22Kohm change to 0ohm
	12.Page 29	M	R409	470Kohm change to 100Kohm
	13.Page 29	M	C427	0.1UF (CAP0402) change to 0.47UF (CAP0805)
	14.Page 15	M	R8	1Mohm change to 470Kohm
	15.Page 17	M	R638	NA
2006/06/09	54.Page 34	#	RJ525	0ohm_A (5VS)
	55.Page 34	#	D601	Pin2, C17 Pin1, C104 Pin1 connect to RJ525 Pin2
	58.Page 34	M	R98	1Kohm change to 47Kohm
	59.Page 34	M	C69	2200PF change to 47PF
	60.Page 34	M	C94	0.22UF change to 4700PF
	06.Page 29	M	U24	10-009750-800G change to 15-00100000001G (Max9750C)
2006/06/13	01.Page 14	D	CE611	220uF
	02.Page 25	M	R733	4.7Kohm mount
	03.Page 25	M	R736	4.7Kohm NA
	04.Page 25	A	C781	33PF NA
	05.Page 25	M	CN611	Footprint CN-WJ13201 change to CN-2WX1301-11
	06.Page 30	M	JP10	Footprint JUMPER change to JUMPER2-SHORT
	07.Page 30	M	JP7	Footprint JUMPER change to JUMPER2-SHORT
	08.Page 22	D	CPOS601 & CPOS2	
	09.Page 22	A	CB612	31-611078-130G
	10.Page 22	A	CT601	31-A11078-130G
	11.Page 14	A	CPOS601	150uF
2006/06/14	10.Page 16	M	C233, C239, C218, C232	0.1uF
	11.Page 16	M	C210, C206, C194, C205	0.1uF
	01.Page 16	M	C687	Mount - EMI
	02.Page 16	M	C689	Mount - EMI
	03.Page 25	M	R750	0ohm (DEL) change to L621 BEAD 120ohm - EMI
	04.Page 25	M	C757	33pF change to 22pF - EMI
	05.Page 27	M	C92	33pF change to 10pF - EMI
	06.Page 30	A	C782	10pF - EMI
	07.Page 30	M	CP1 & CP2	180pFX4 Mount - EMI
	08.Page 30	M	C350, C351, C352, C353	100pF Mount - EMI
	09.Page 37	A	C783, C784, C785	0.1uf (Pull 3VS) - EMI
	10.Page 23	M	C98, C96	4.7uF change to 10uF
	11.Page 24	A	R792	Pin1 connect U18 Pin70
	12.Page 24	A	D30	Pin1 connect to R792 Pin2 (ODD-ELED#)
	13.Page 24	M	M37	2N7002 NA
	14.Page 37	A	C789	0.1uf (Pull 1.05VS) - EMI
	16.Page 37	A	C788	0.1uf between 1.05VS and VCore - EMI
	18.Page 37	A	C786	0.1uf between 1.8V and 3VS - EMI
	19.Page 37	M	C714	Pin1 net name Change to 1.8V from 1.8VS - EMI
	2006/06/16	54.Page 37	#	C790 0.1uf (Pull 1.8V) - EMI
	17.Page 37	#	C787	0.1uf between 1.8V and 1.2VS - EMI
	32.Page 22	M	R789	Footprint RES0402 change to RES0402-SHORT05
	01.Page 22	M	D29	Pin 1 connect to M38 Pin1 (IDE-LED#)
	02.Page 24	M	D30	Pin 1 connect to U18 Pin168 (SCROLL-LED#)

2006/06/19	01.Page 36	M	CB600, CB601	Mount - EMI
	02.Page 37	A	C790, C791, C792	0.1uF between B+ and GND - EMI
	03.Page 37	A	C793, C794, C795	0.1uF between B+ and GND - EMI
	04.Page 35	M	JP8, JP605	Footprint TESTPAD change to JUMPER1-SHORT
	05.Page 35	M	JP602	Footprint TESTPAD change to JUMPER1-SHORT
	06.Page 36	M	D600, D603	20-320340-870G change to 20-020050-870G
	07.Page 32	M	J1	Footprint TESTPAD change to JUMPER1-SHORT
2006/06/20	08.Page 05	D	C301, C298, C302, C193	
	01.Page 35	D	JP605	- POWER
	02.Page 30	M	R201, R637	NA
	03.Page 30	M	M13, M604	Footprint MOSFET-8P-1P27 change to MOSFET-8P-1P27-M
	04.Page 32	M	C622	NA
2006/06/21	01.Page 32	M	C70	NA
	02.Page 21	D	CE607	
	03.Page 21	A	C796, C797	10uF NA