

Project Name :A14IEXX Rev : A0

Platform : PineView -D + TigerPoint + DDR3

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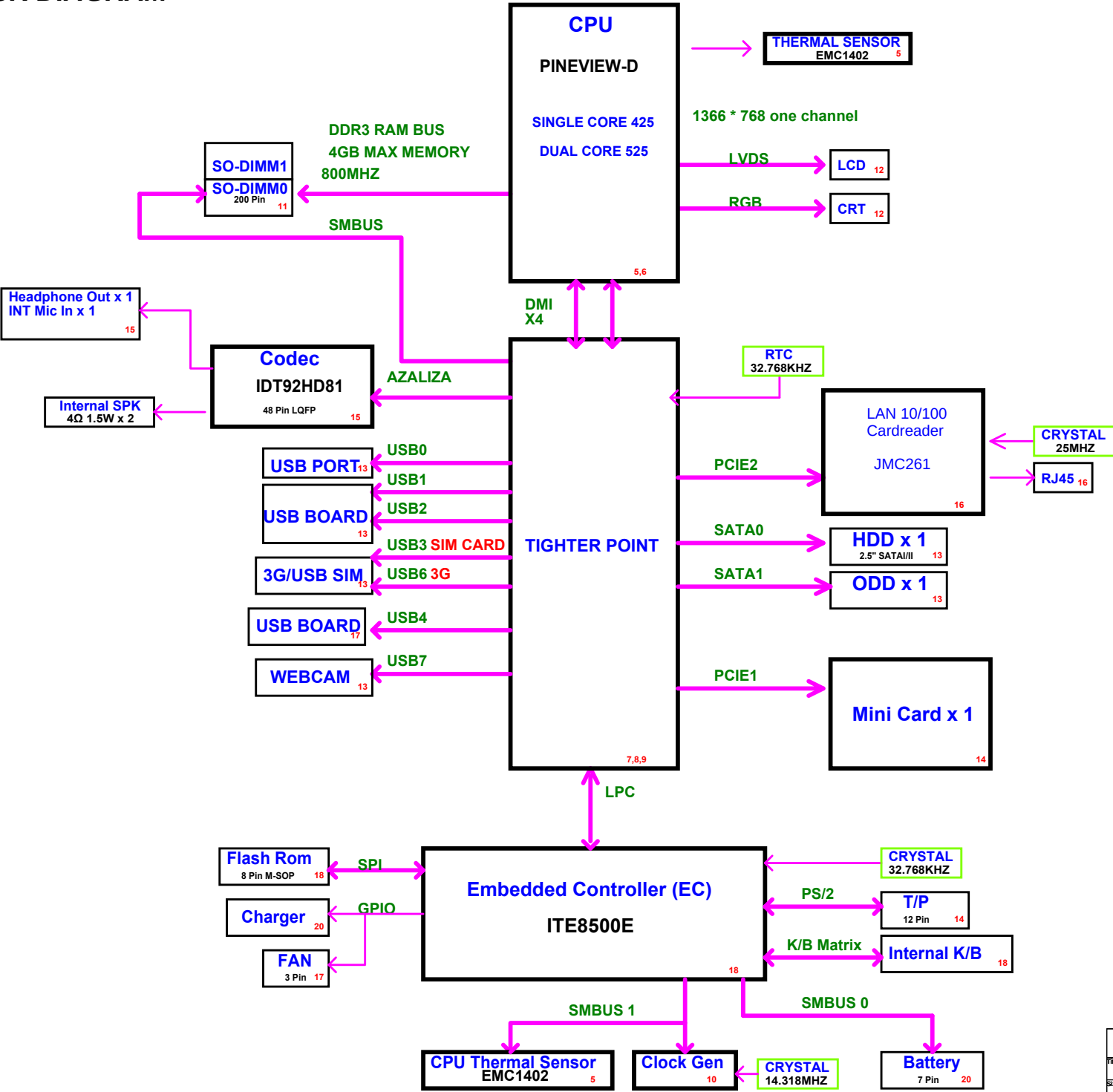
M/B Schematic Version Change List

Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note
					Main
					Main
					2ND

Daughter Board Schematic Version Change List

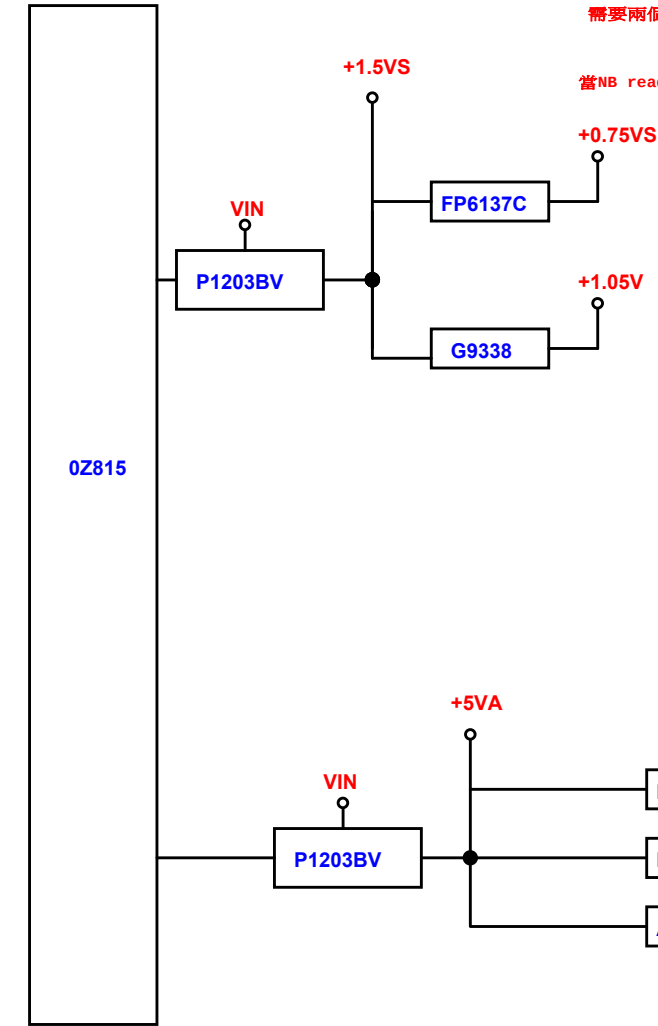
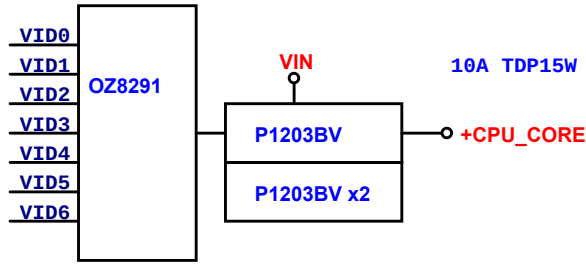
Release Date	Version	PCB P/N	PCB Description	PCBA P/N	Note

SYSTEM BLOCK DIAGRAM



POWER BLOCK DIAGRAM

System Poewr On Sequence



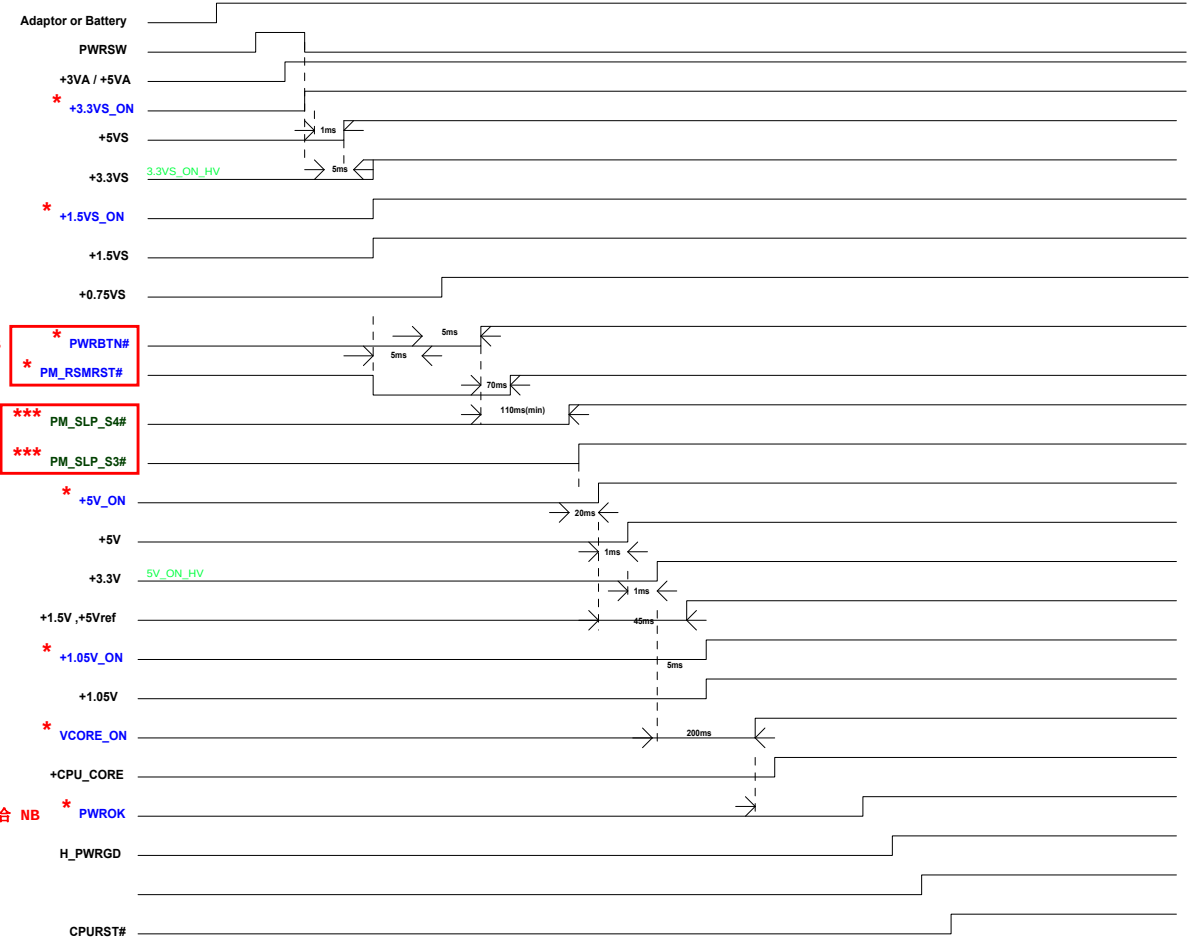
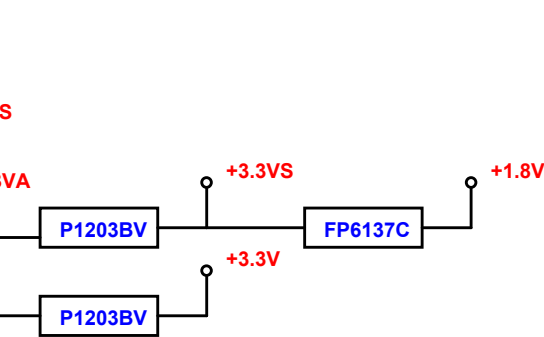
EC發給NB
需要兩個訊號收到後NB才會WORK

- * PWRBTN#
- * PM_RSMRST#
- *** PM_SLP_S4#
- *** PM_SLP_S3#

當NB ready 時 SUSB SUSC 為HI

EC 發給 NB

- * EC Control Pin (O/P)
- ** EC Control Pin (I/P)
- *** NB Control Pin (O/P)



TPT	
GPIO	
GPI00	AUD_DETECT
GPI01	4.7K UP TO +5V
GPI02	PIRQE-
GPI03	PIRQF-
GPI04	PIRQG-
GPI05	PIRQH-
GPI06	NC
GPI07	NC
GPI08	NC
GPI09	NC
GPI010	EC_EXTSMI#
GPI011	ICH_SMBALERT-
GPI012	10K UP TO +3.3VS
GPI013	EC_EXTSCI#
GPI014	NC
GPI015	10K UP TO +3.3VS
GPI017	FLASH_STRAP2
GPI018	
GPI019	
GPI020	
GPI021	
GPI022	4.7K UP TO +5V
GPI023	LDRQ1#
GPI024	GXT_ON#
GPI025	5K DOWN TO GND
GPI026	NC
GPI027	NC
GPI028	NC
GPI029	USB_OC#4
GPI030	USB_OC#6
GPI031	USB_OC#6
GPI032	
GPI033	NC
GPI034	NC
GPI035	
GPI036	4.7K UP TO +3.3V
GPI037	
GPI038	NC
GPI039	NC
GPI040	
GPI041	
GPI042	
GPI043	
GPI048	FLASH_STRAP1
GPI049	CPUPWRGD

ITE8500E		Default
GPIO		Pull/Mode
GPA0	RF_LED_ON#	UP / GPI
GPA1	EC_BSEL0	UP / GPI
GPA2	BT_L_BEEP	UP / GPI
GPA3	WLAN_PWR#	UP / GPI
GPA4	P_ID0	UP / GPI
GPA5	P_ID1	UP / GPI
GPA6	PM_RSMRST#	UP / GPI
GPA7	EC_BL_PWM	UP / GPI
GPB0	PM_SLP_S4#	UP / GPI
GPB1	PM_SLP_S3#	UP / GPI
GPB2	+1.05V_ON	Dn / GPI
GPB3	BAT_SMBCLK	/ GPI
GPB4	BAT_SMBDAT	/ GPI
GPB5	H_A20GATE	/ GP0
GPB6	H_RCIN#	UP / Func1
GPB7	PWR_USB#	Dn / GPI
GPC0	+1.5V_ON	Dn / GPI
GPC1	SMBCLK_EC	/ GPI
GPC2	SMBDAT_EC	/ GPI
GPC3	KEY_OUT16	Dn / GPI
GPC4	SAVE_Pwr	Dn / GPI
GPC5	KEY_OUT17	Dn / GPI
GPC6	WLAN_ON	Dn / GPI
GPC7	EC_VID0	UP / GPI
GPD0	ADAP_IN	UP / GPI
GPD1	PWRBTN#	UP / GPI
GPD2	PLT_RST#	UP / Func1
GPD3	EC_EXTSCI#	UP / GPI
GPD4	EC_EXTSMI#	UP / GPI
GPD5	USB_LED#	UP / GPI
GPD6	+5V_ON	Dn / GPI
GPD7	SET_V	Dn / GPI
GPE0	LID#	Dn / GPI
GPE1	Fastcharge_EN	Dn / GPI
GPE2	PwR0K	Dn / GPI
GPE3	Vcore_ON	Dn / GPI
GPE4	PWRSW	UP / GPI
GPE5	+1.VS_ON	Dn / GPI
GPE6	3G_PWR#	Dn / GPI
GPE7	AMP_MUTE#	UP / GPI
GPF0	EC_VID4	UP / GPI
GPF1	3G_ON	UP / GPI
GPF2	EC_VID3	UP / GPI
GPF3	CHG_ON#	UP / GPI
GPF4	TP_CLK	UP / GPI
GPF5	TP_DATA	UP / GPI
GPF6	EC_VID1	UP / GPI
GPF7	EC_VID2	UP / GPI
GP00	100K UP TO +3.3VA	Dn/GP0/TM
GP01	+3.3VS_ON	Dn/GP0/ID7
GP02	FLFRAME#	
GP06	WEBCAN_ON	
GPH0	SAFTY_PROTECT	Dn/GPI/ID0
GPH1	+1.8VS_ON	Dn/GPI/ID1
GPH2	SENBAT_V	Dn/GPI/ID2
GPH3	CHG_B_LED	Dn/GPI/ID3
GPH4	CHG_R_LED	Dn/GPI/ID4
GPH5	BATTOFF	Dn/GPI/ID5
GPH6	PWR_LED	Dn/GPI/ID6

ITE8500E		Default
GPIO		Pull/Mode
GPI0	NC	/GPI
GPI1	LCDSW0	/GPI
GPI2	LCDSW1	/GPI
GPI3	NC	/GPI
GPI4	BAT_I	/GPI/ADC
GPI5	BATT_TEMP	/GPI/ADC
GPI6	ADAPTER_I	/GPI/ADC
GPI7	BAT_V	/GPI/ADC
GPJ0	EC_BL_EN	/GPI/DAC
GPJ1	EC_PROCHOT#	/GPI/DAC
GPJ2	FAN_CTRL0	/GPI/DAC
GPJ3	CHG_REF	/GPI/DAC
GPJ4	CHG_I	/GPI/DAC
GPJ5	CRT_DETECT	/GPI/DAC

PINEVIEW-D					
	CPU	CORE(V)	ICC(A)	W	TEMP(°C)
IMVP-6+	1.2	3	3.3	90	

TPT					
	CPU	CORE(V)	ICC(A)	W	TEMP(°C)
IMVP-6+	1.2	3	3.3	90	

ITE8500E			
VCC	ICC(mA)	mW	TEMP(°C)
+3.3V	100	330	70

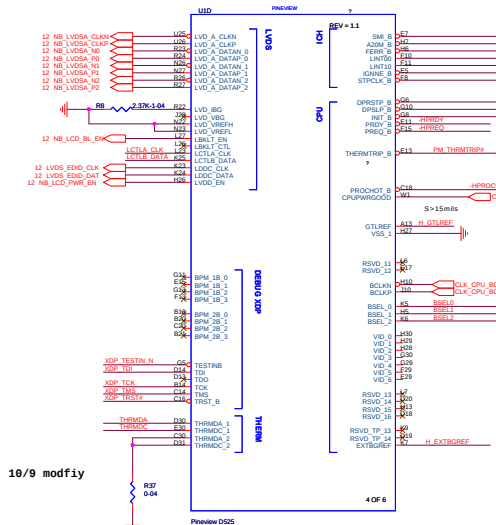
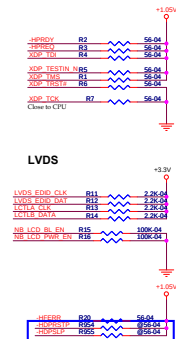
CLOCK GENERATOR			
VCC	ICC(mA)	mW	TEMP(°C)
+3.3V	250	825	70

EMC1402			
VCC	ICC	mW	TEMP(°C)
+3.3V	170uA	0.56	150

CODEC			
VCC	ICC(mA)	mW	TEMP(°C)
+3.3V	250	825	70

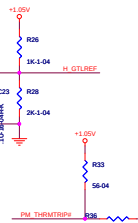
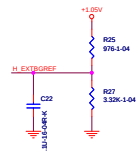
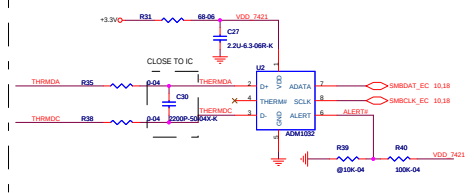
JMC261			
VCC	ICC(mA)	mW	TEMP(°C)
+3.3V	250	825	70

SHUTTLE			
GPIO & Power Consumption			
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IC	SETTING	R35	R38	R39
EMC1402		0R	0R	@10K
ADT7421		100R_1	100R_1	@10K
LM95245		0R	0R	10K

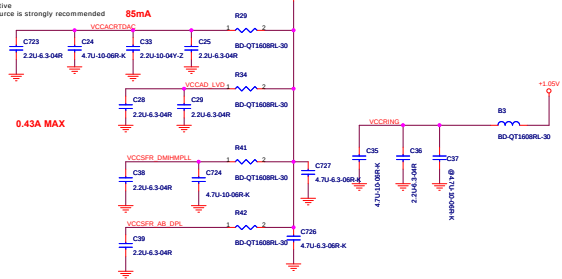
CPU Thermal Sensor

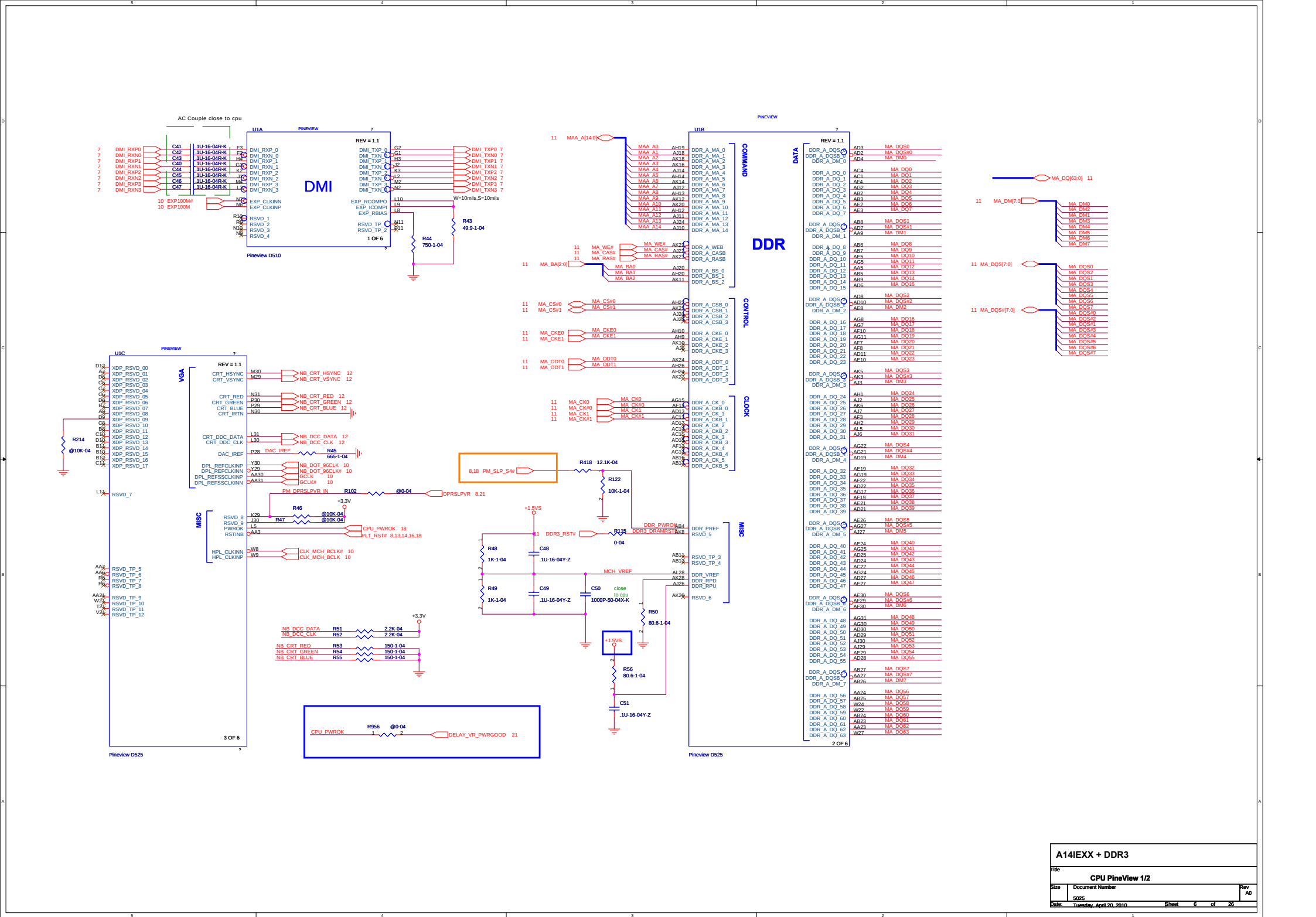


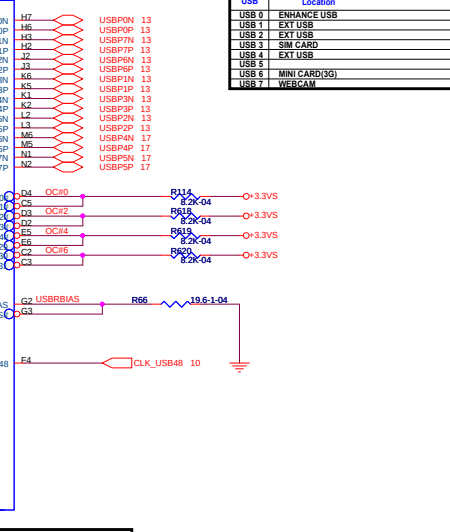
V_1P8_PLSFR INCLUDE
1.VCCSR_AB_DPL
2.VCCSR_DMHPML
3.VCCSR_DMHPML
4.VCCSR_DMHPML
5.VCCSR_DMHPML

The V1_05_core Linear Voltage Regulator will source to Tiger Point core Voltage and Preview-D core voltage. V_1P8_PLSFR will then need to be turned off before V_1P05_CORE start to ramp down.

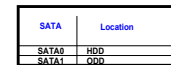
10.4.2.6.1
VCCACRTAC are very sensitive
use of a linear VR voltage source is strongly recommended







PCIE	Location
PCIE1	EXT VGA
PCIE2	LAN(JMC261)
PCIE3	MINI CARD(WLAN)
PCIE4	



DEL IN B

+1.05V

~HAZ0M R77 @1K-04

~HCPUSLP R79 @1K-04

~HIGNNE R80 @1K-04

~HNMT N R82 @1K-04

~HNMT R83 @1K-04

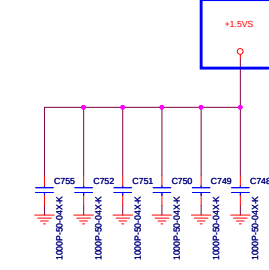
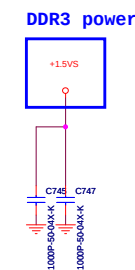
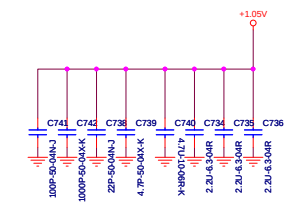
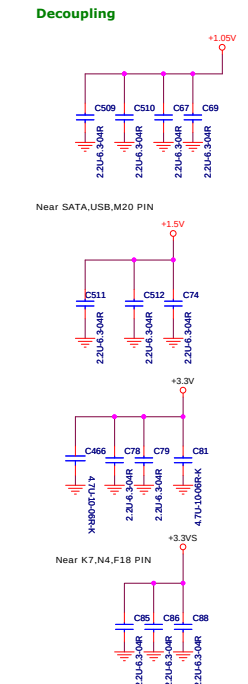
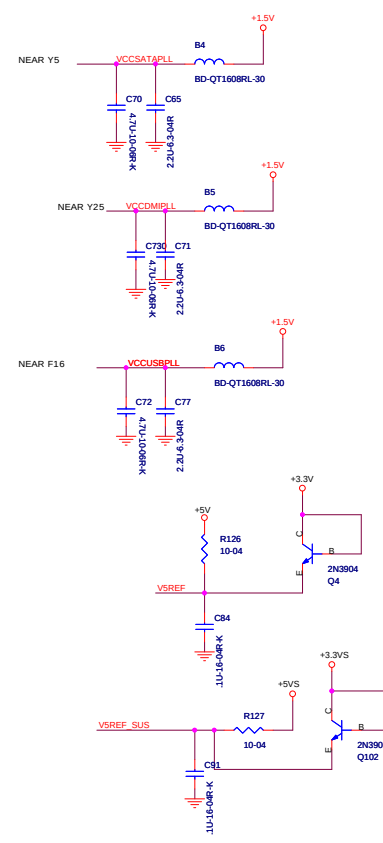
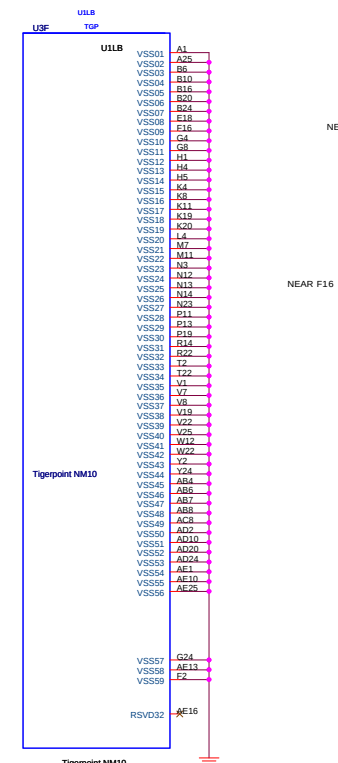
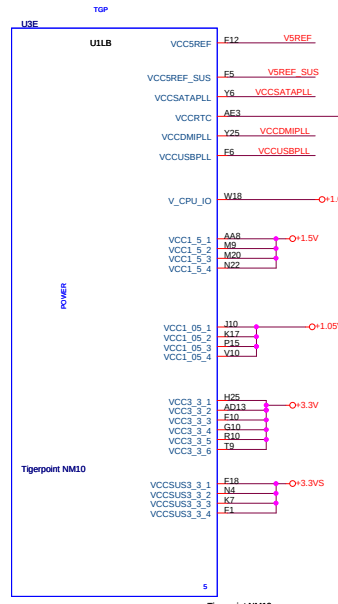
~HNMT R84 @1K-04

~HSMI R85 @1K-04

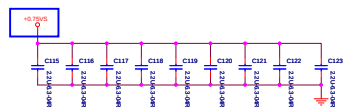
~HSTPCLK R86 @1K-04

WHEN NOT IN USED, PU WITH 1KOHM DEFENSIVE SITE

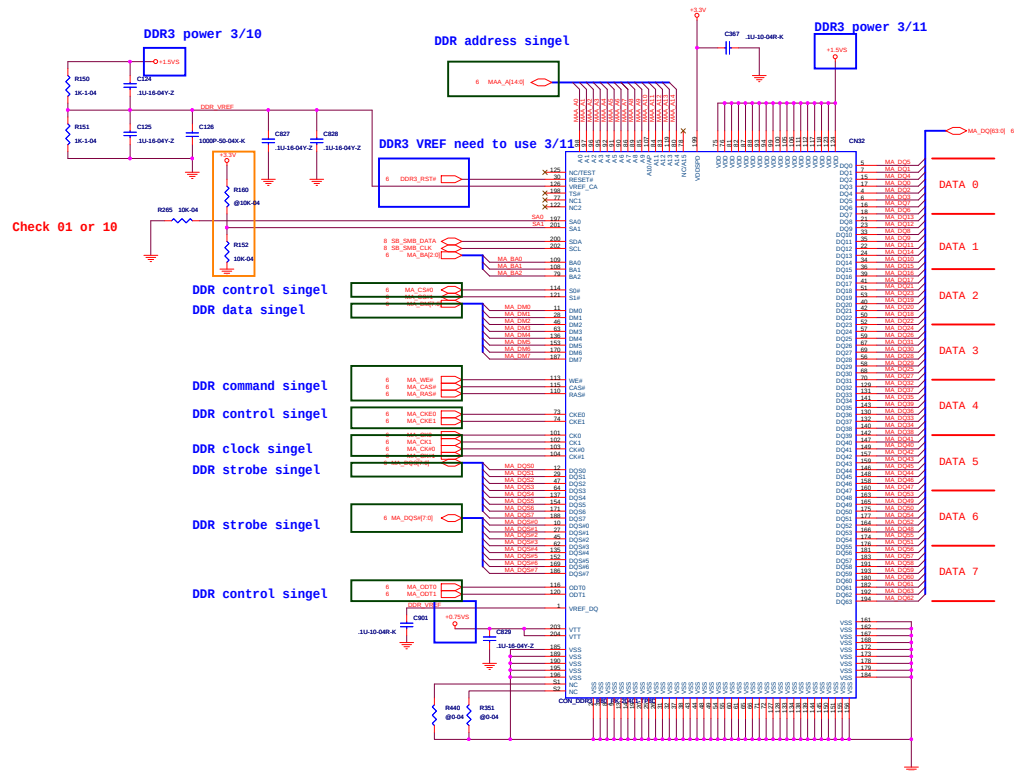
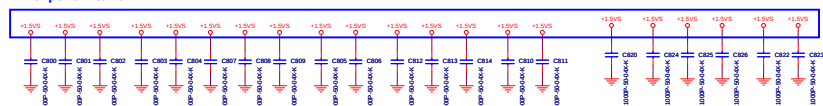
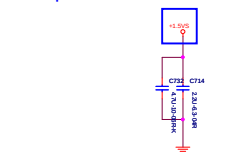
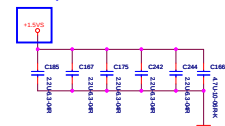
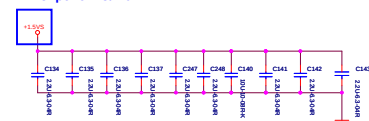
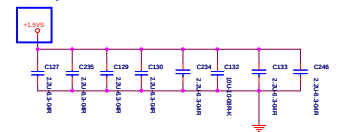
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07 TPT - PCI&USB&HOST			
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A14IEXX + DDR3			
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TPT Power			
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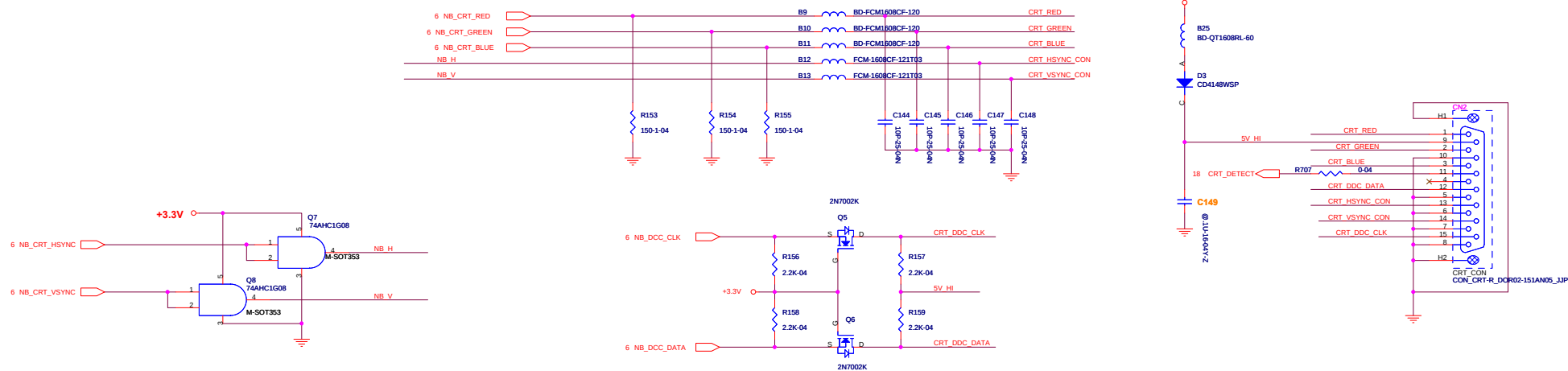


DDR3 power 3/10



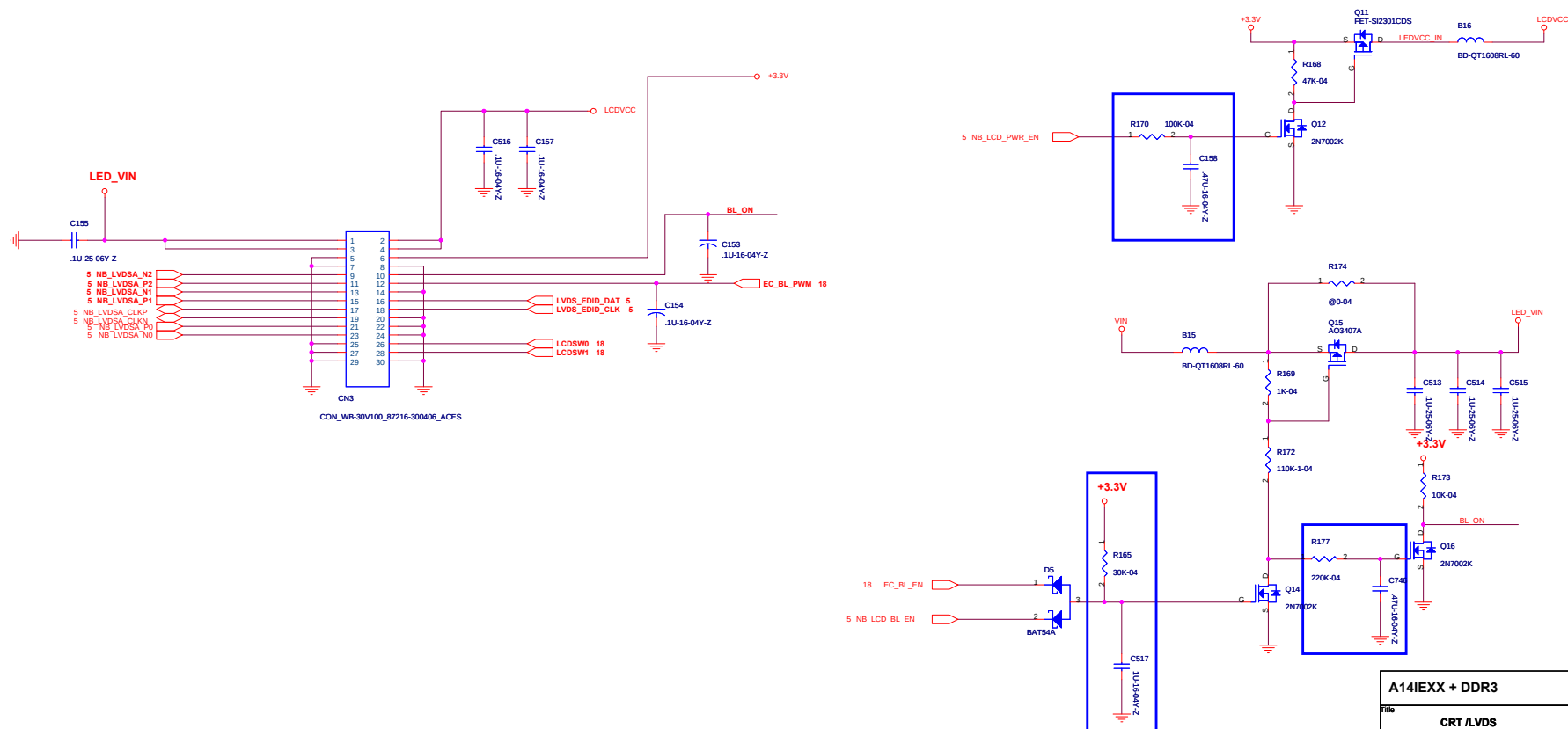
A14IEXX + DDR3				
Title				
DDR2 SODIMM				
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	5005			
Author	Typed/checked	Drawn	10	2010
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VGA Conn.



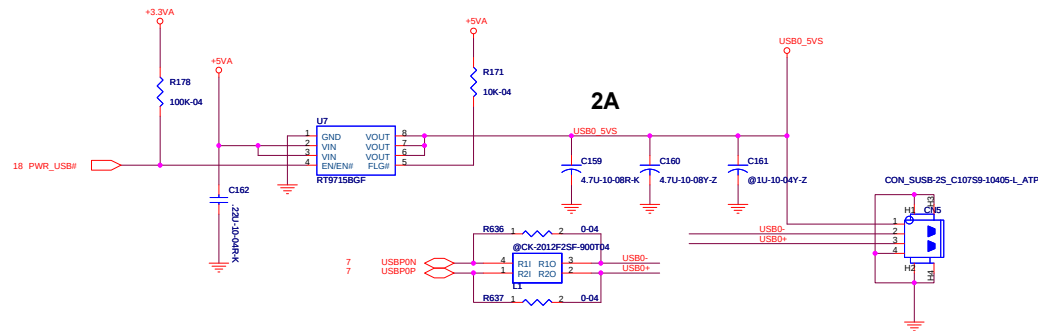
LVDS

LED option

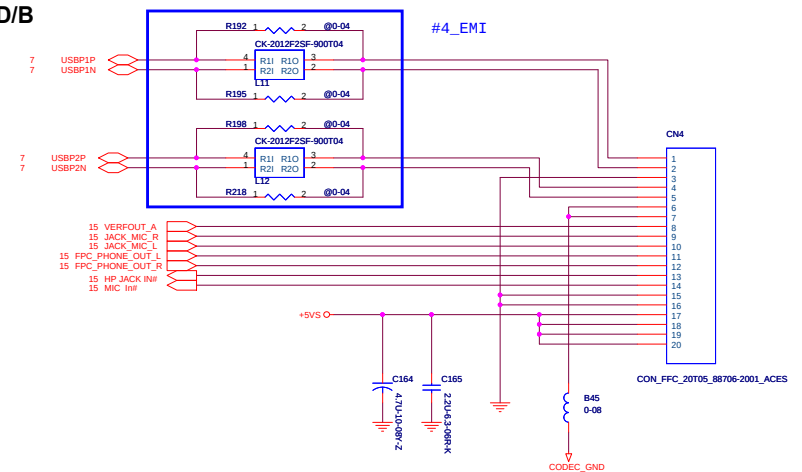


A14IEXX + DDR3		
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CRT /LVDS		
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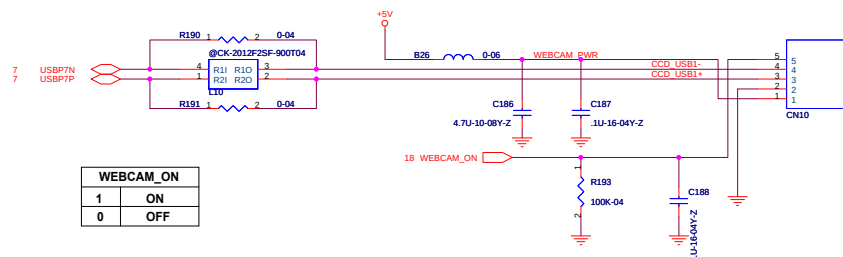
ENHANCE USB



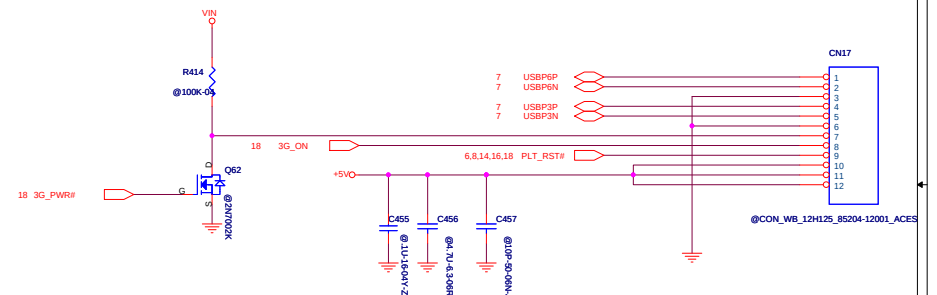
USB D/B



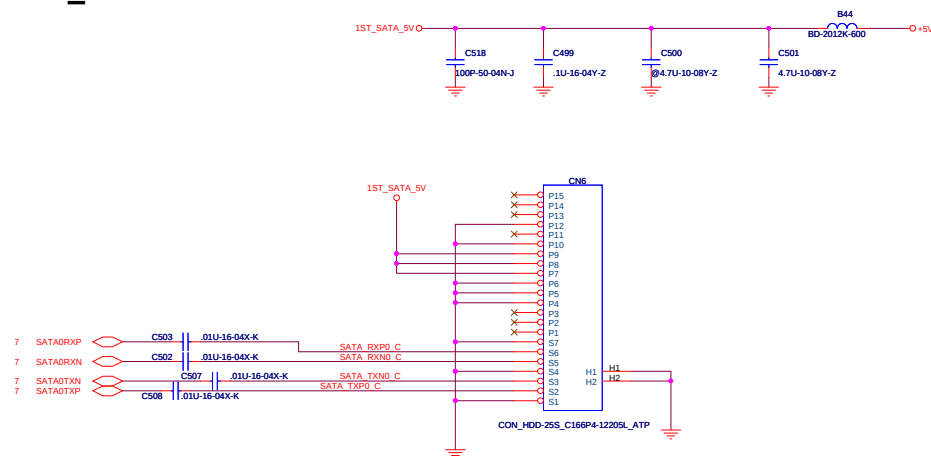
WEBCAM



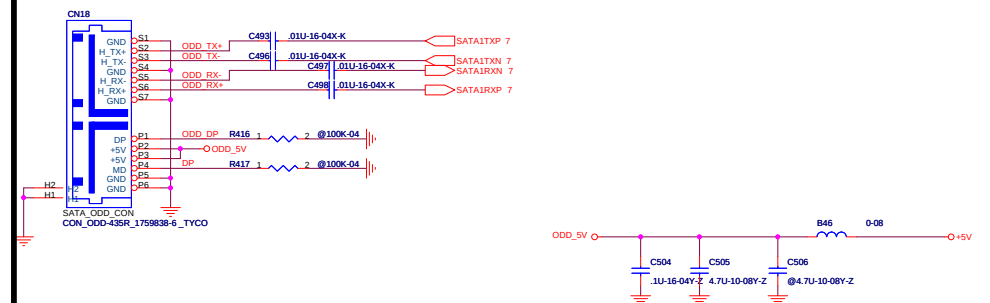
3G_D/B



SATA_HDD



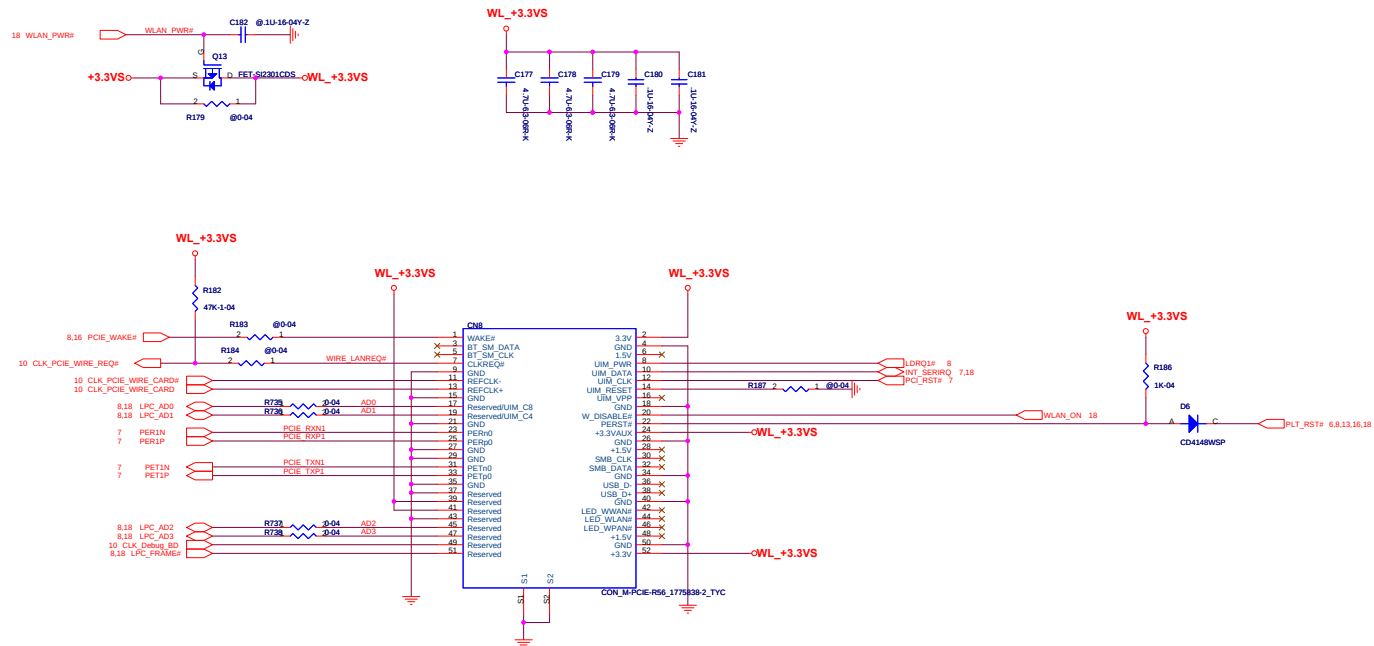
SATA_ODD



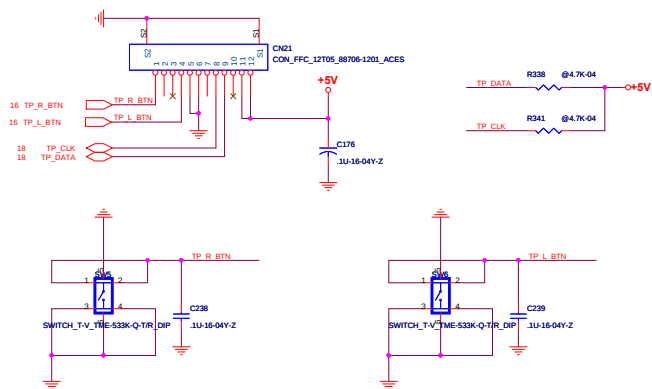
A14IEXX + DDR3

13 USB / WEBCAM / SATA / BT		
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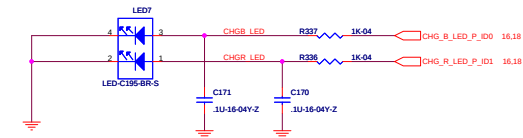
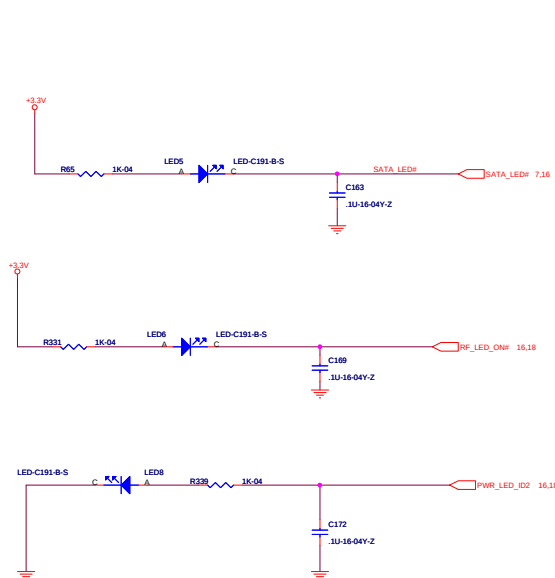
MINI CARD Socket



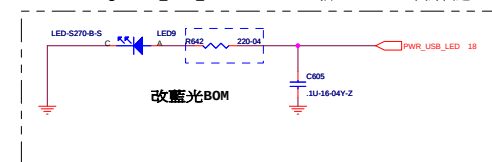
Touch Pad Button



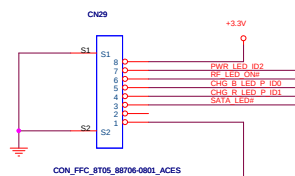
LED



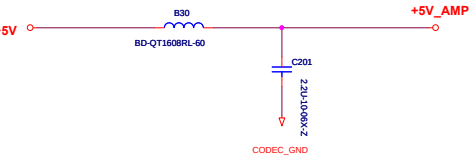
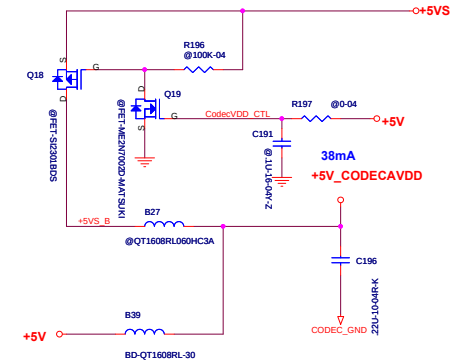
Change PWR_USB_LED sch to solve 插ADP or BAT 閃爍問題



LED BD

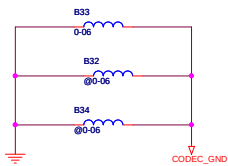


AMP VDD

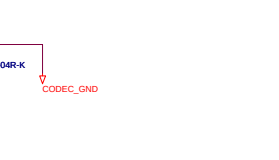
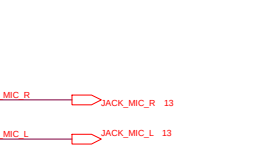
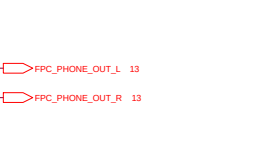
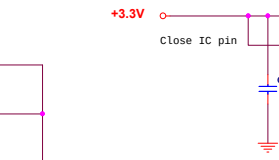
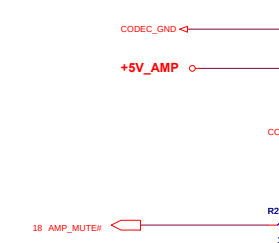
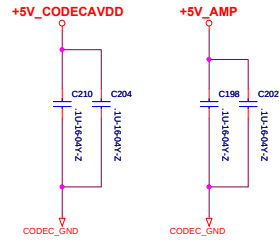
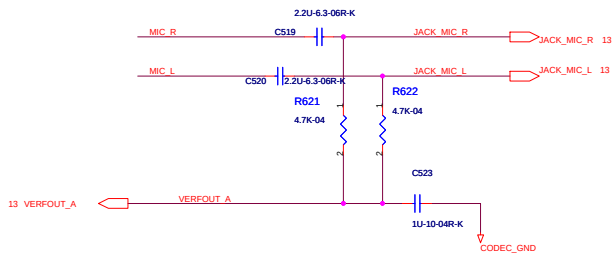
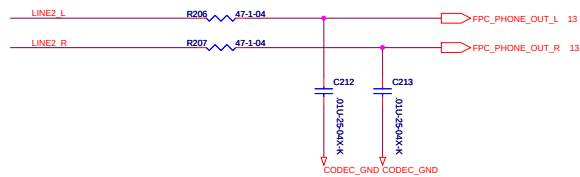


USE 80 MILLS WIDE TRACE
LOCATE UNDER CODEC.

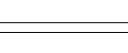
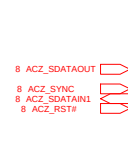
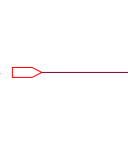
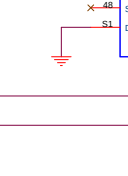
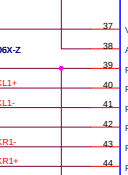
BRIDGING AGND AND DGND PLANES



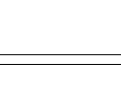
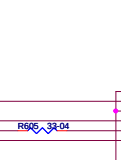
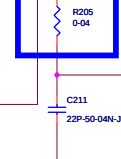
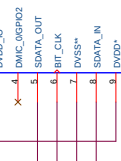
HEADPHONE



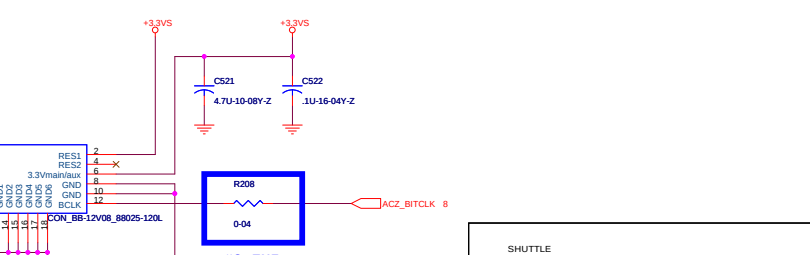
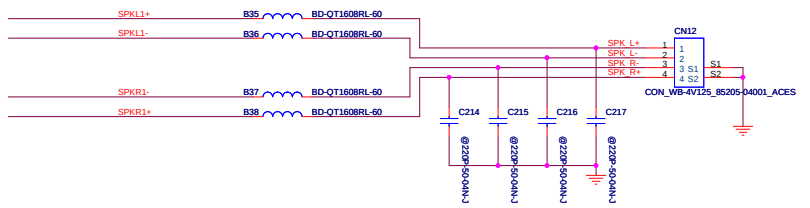
+5V_CODECAVDD



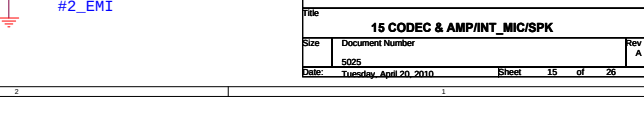
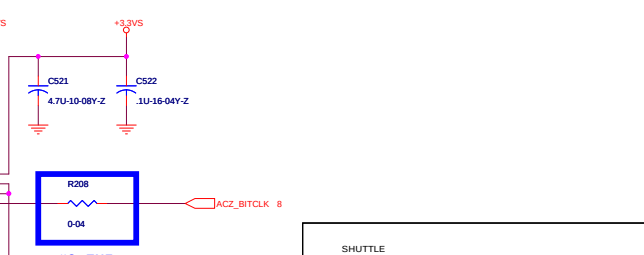
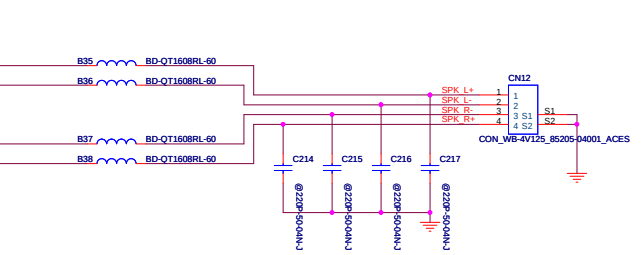
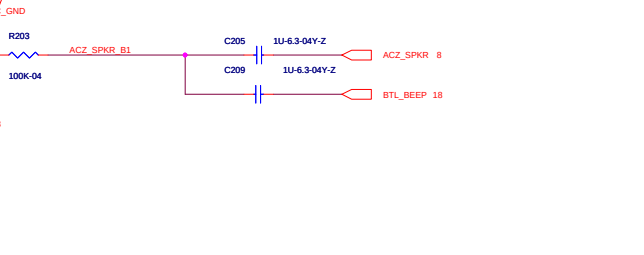
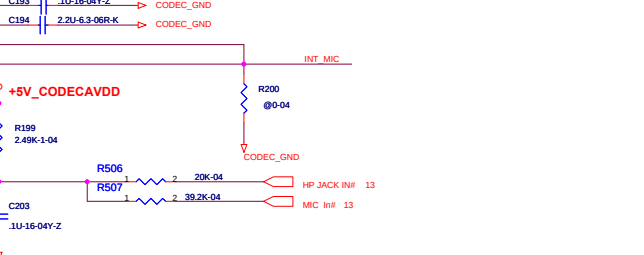
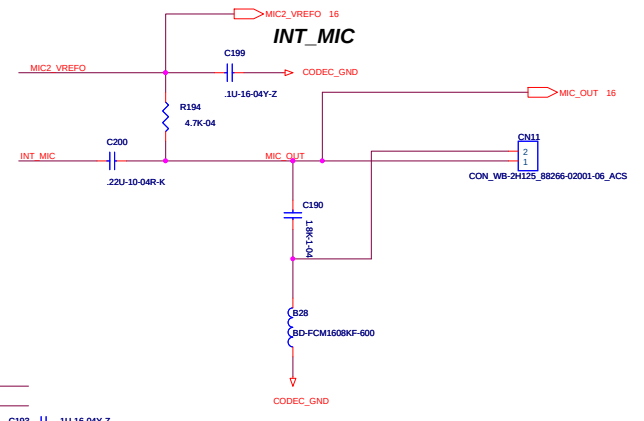
92HD81-YD



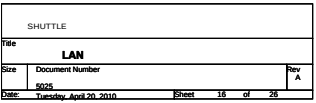
SPEAKER



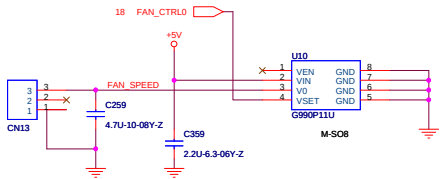
INT_MIC



SHUTTLE		
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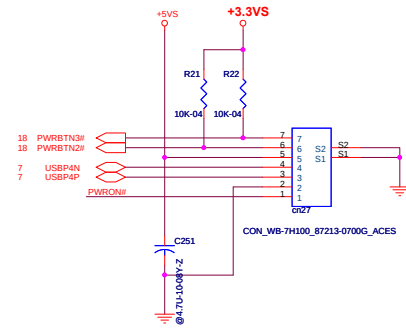


FAN



CON_WB-3H125_85204-03001_ACES

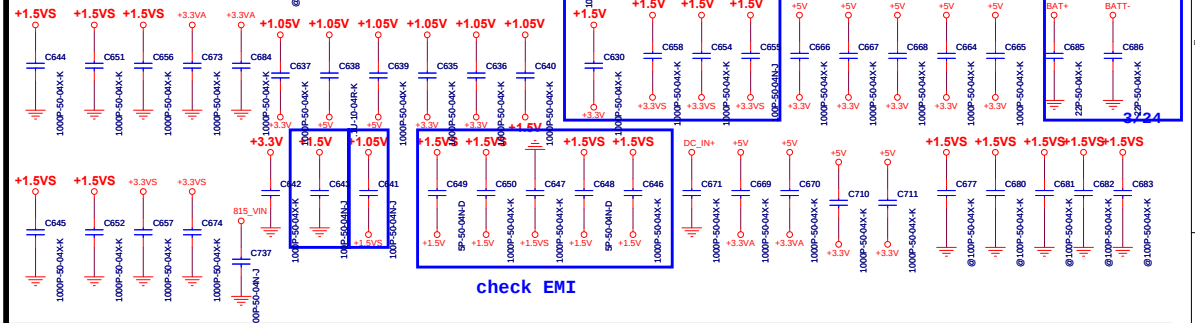
USB SWITCH



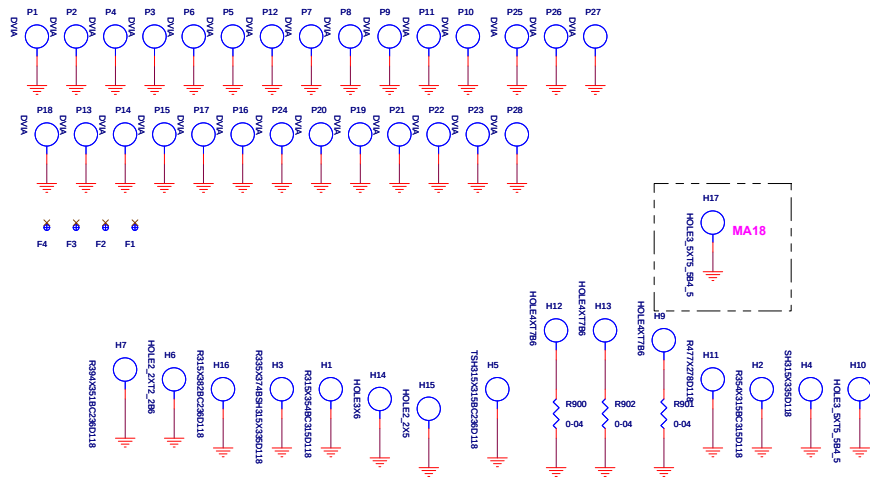
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AC-CAP

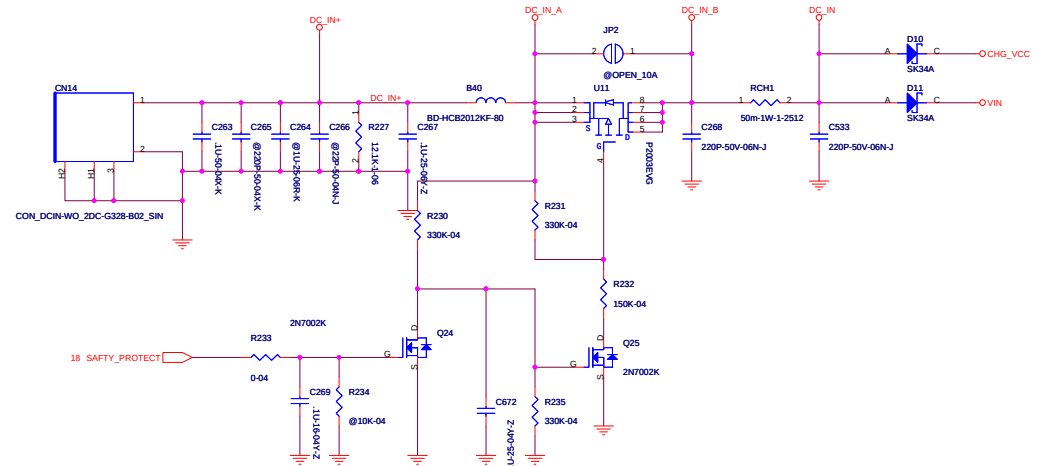
CHECK EMI #5_EMI



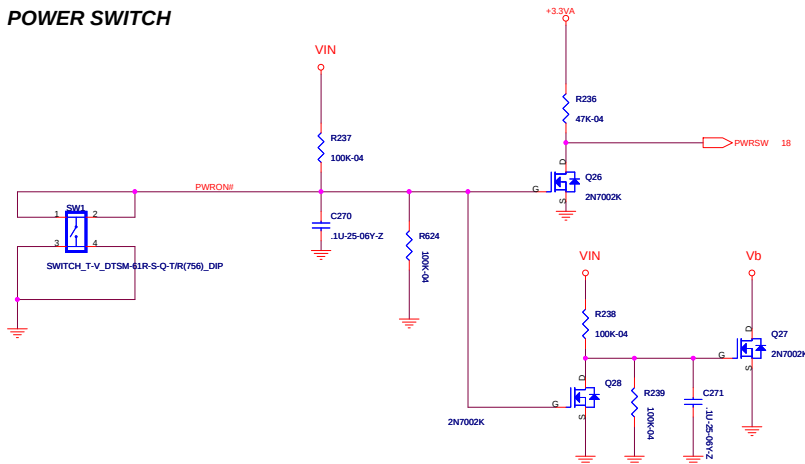
check EMI



DC-IN



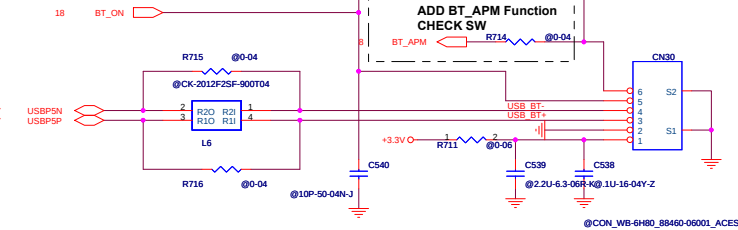
POWER SWITCH



BT CONN

ADD BT CONN CN21

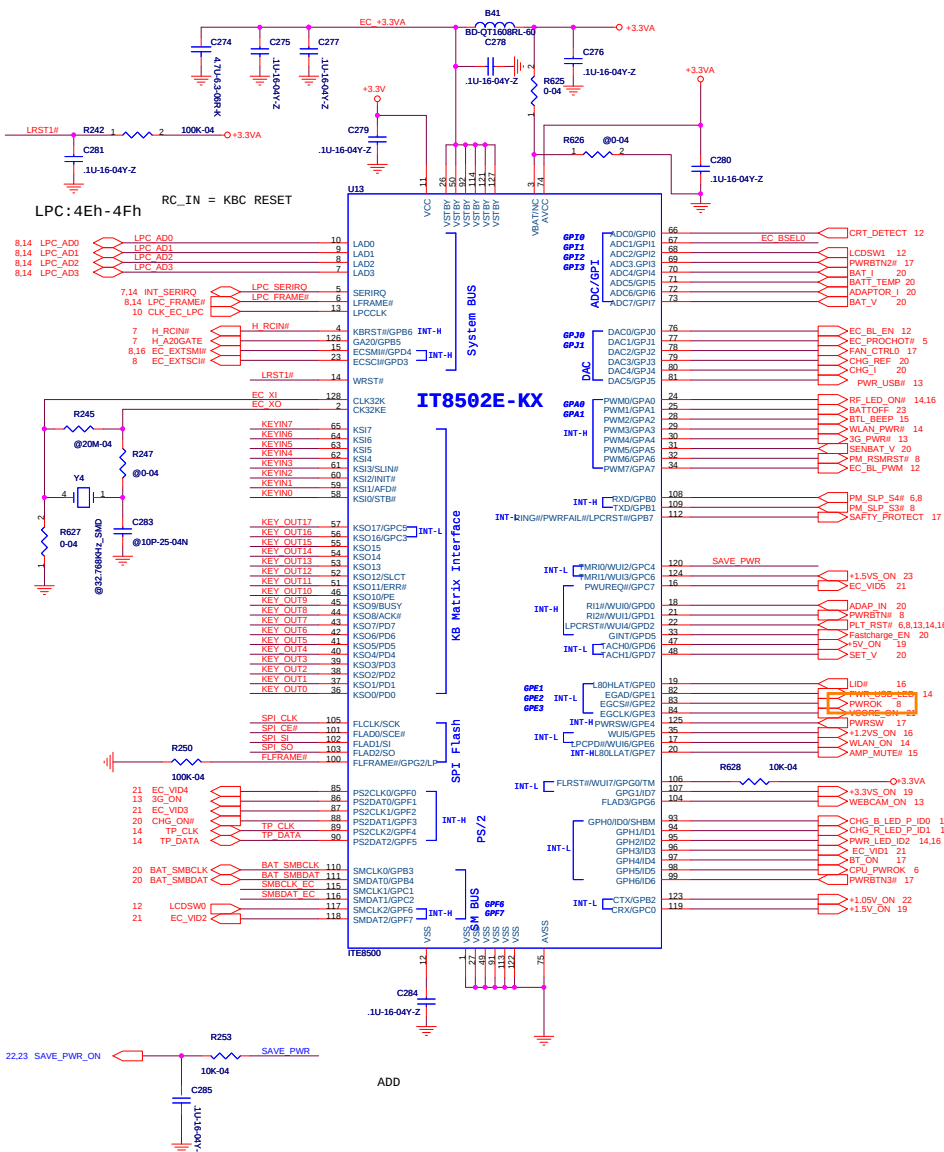
MA13



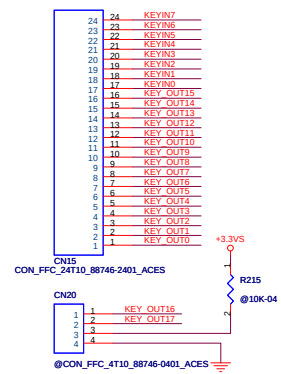
modify 9/28

CHECK BT TYPE AND Life power function

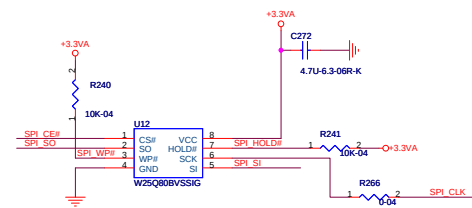
File		
PWR SW/FAN/HSCRP CAP/DC IN		
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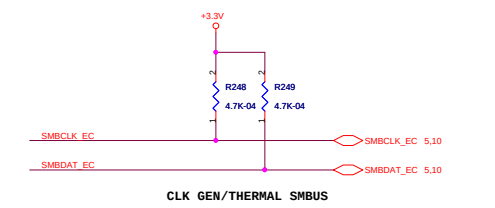
KEYBOARD CON



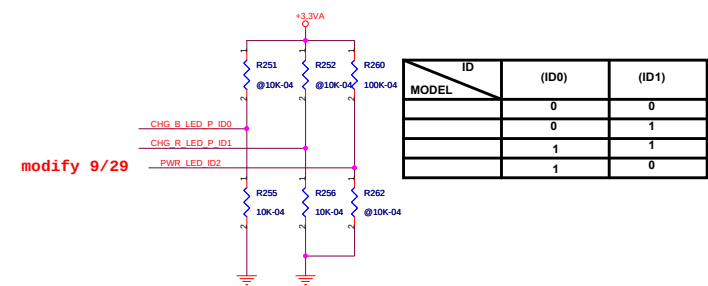
FLASH ROM(SPI)



SMBUS LEVEL SHIFT

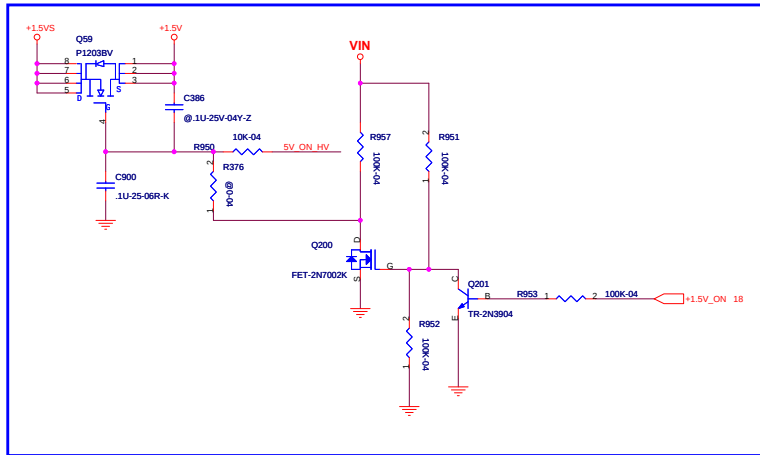
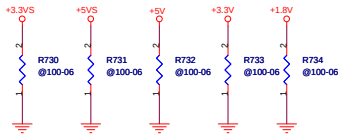
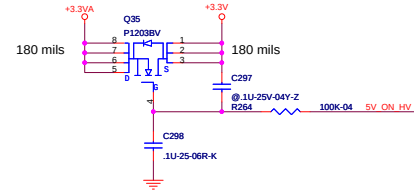
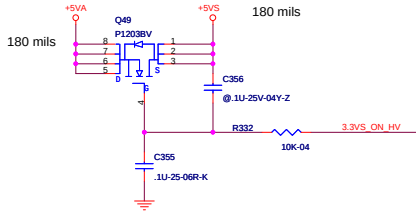
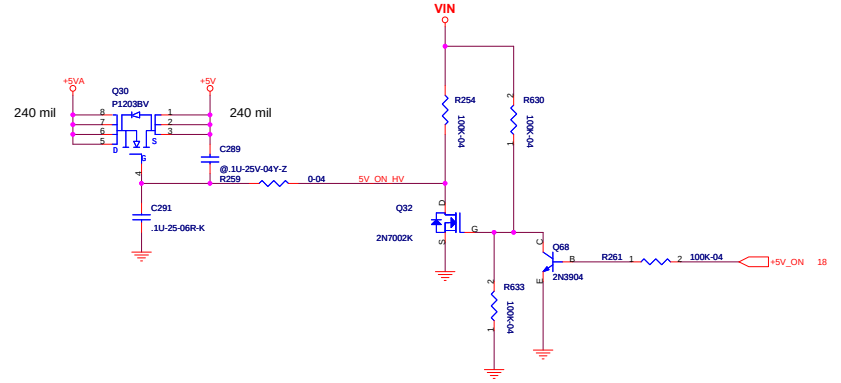
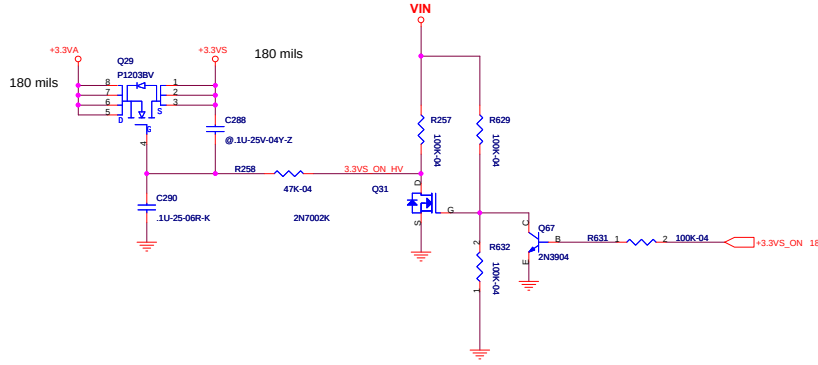


Platform ID

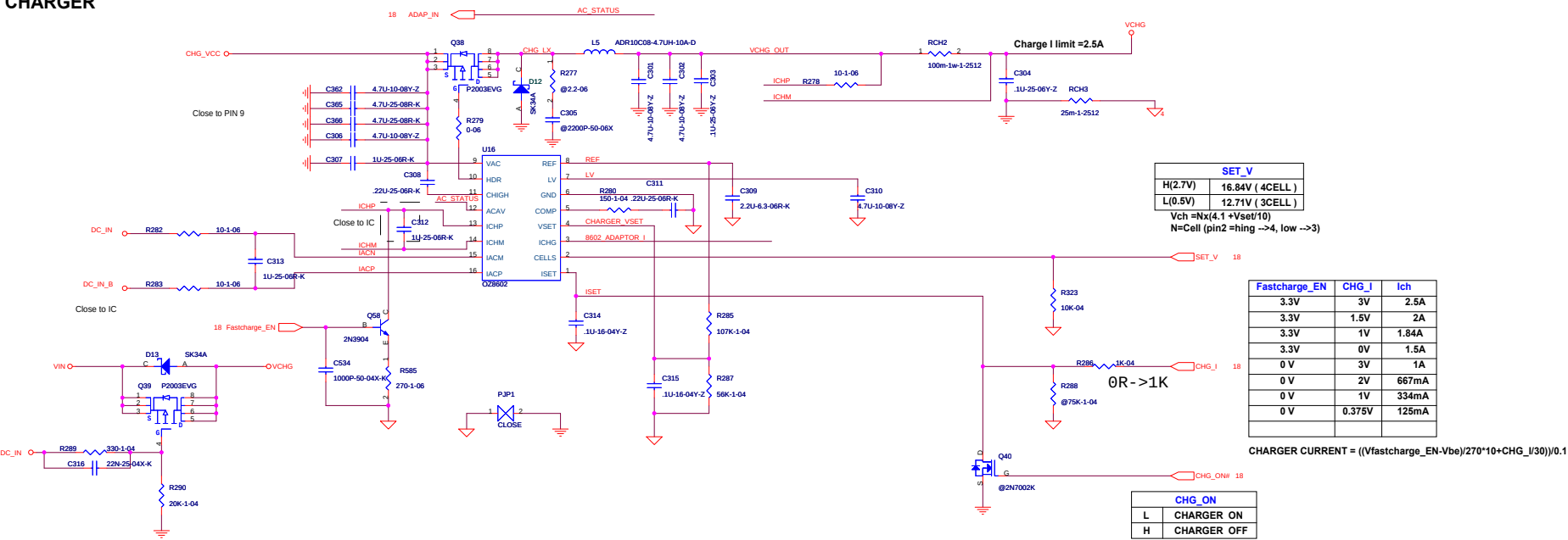


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PWR SW/FAN/HSCRP CAP/DC IN			
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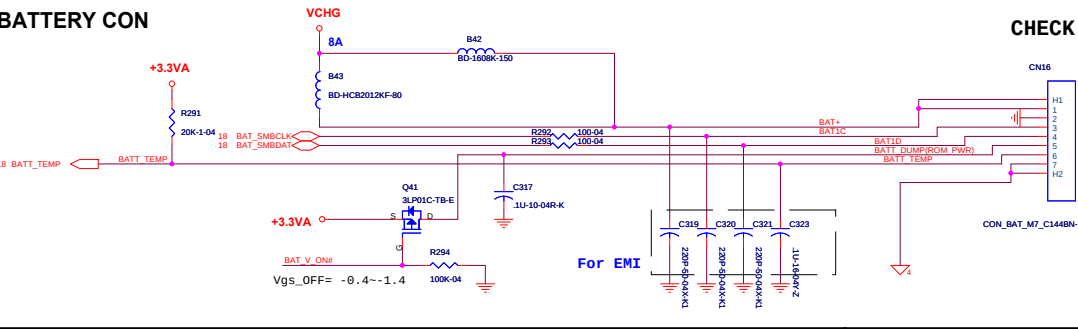
VCC SW



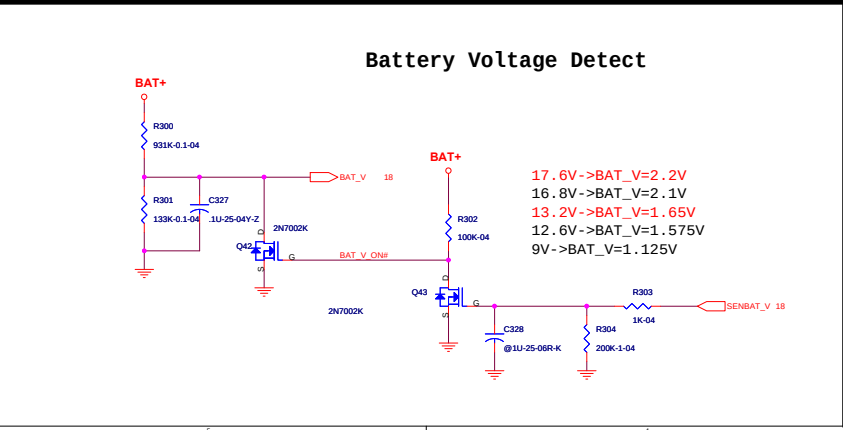
CHARGER



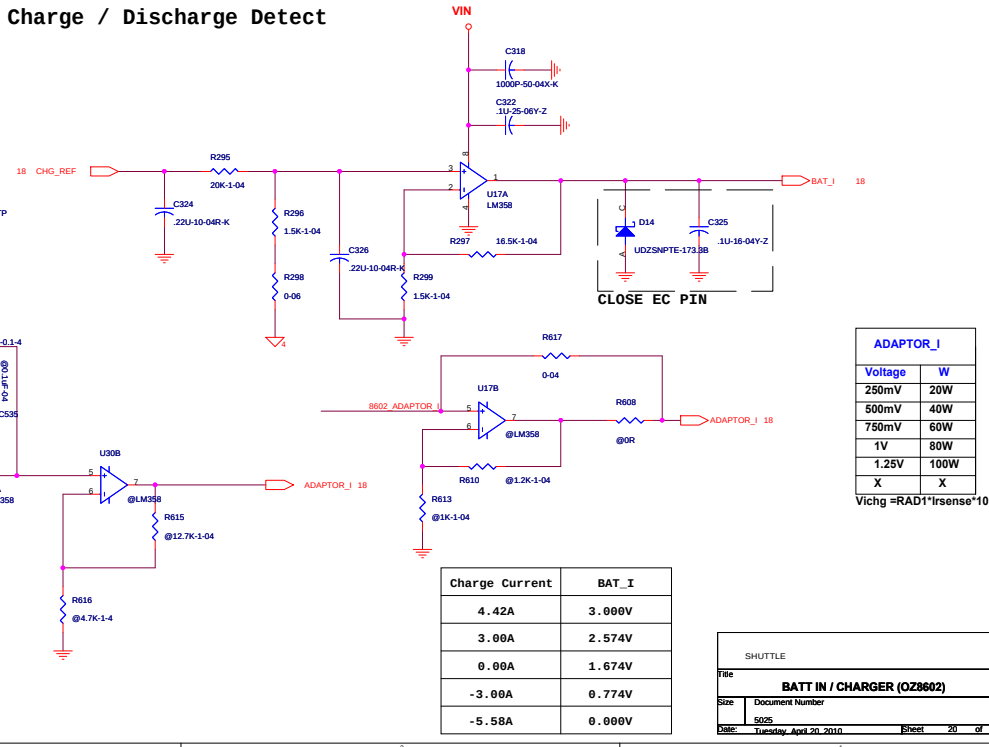
BATTERY CON

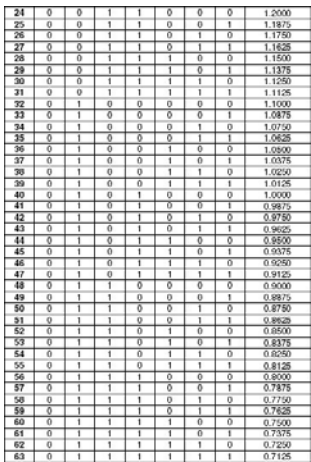


Battery Voltage Detect

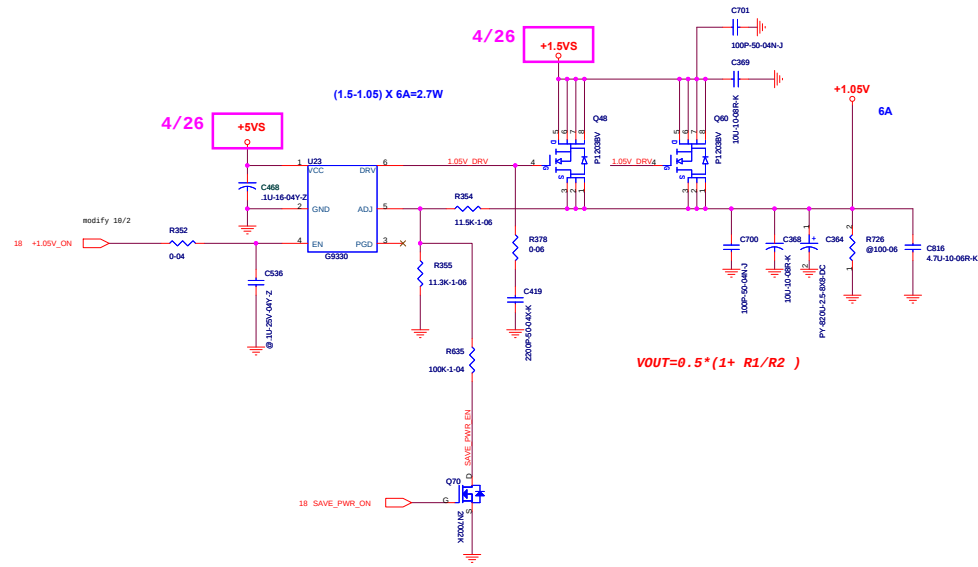


Charge / Discharge Detect





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File CPU CORE (OZ8291)			
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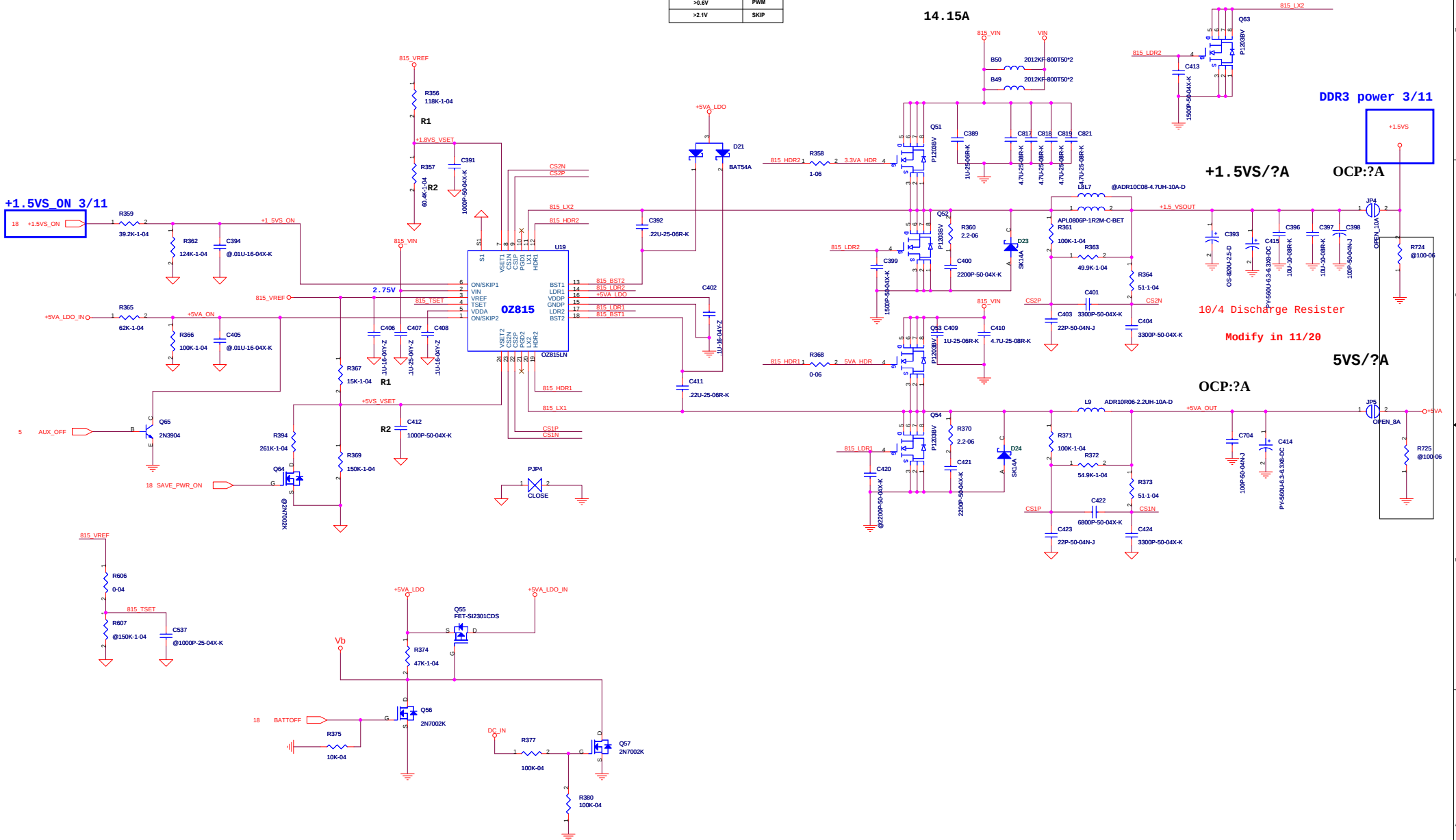


+1.8V/+5V_ON Voltage	Mode
<0.4V	OFF
>0.6V	PWM
>2.1V	SKIP

$$\text{Output Voltage} = \left[\text{Vref} \times \frac{R2}{R1+R2} \right] \times 2$$

14. 15A

+1.5VS_ON 3/11



SHUTTLE		
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23 +3.3VA/+5VS (TPS51125)		
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RB change list:

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RC change list:

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T10IL1 Power on Sequence Diagram

