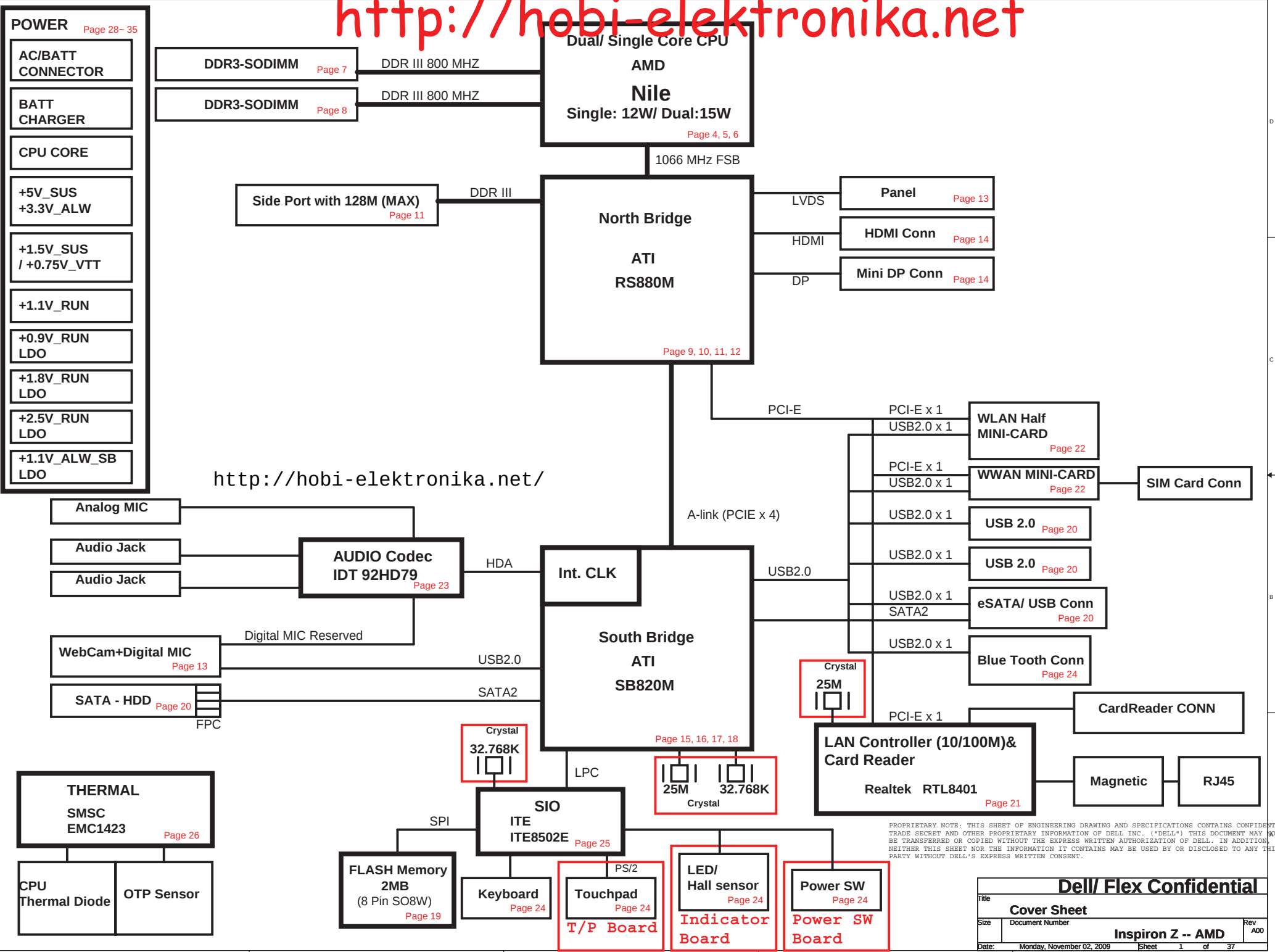
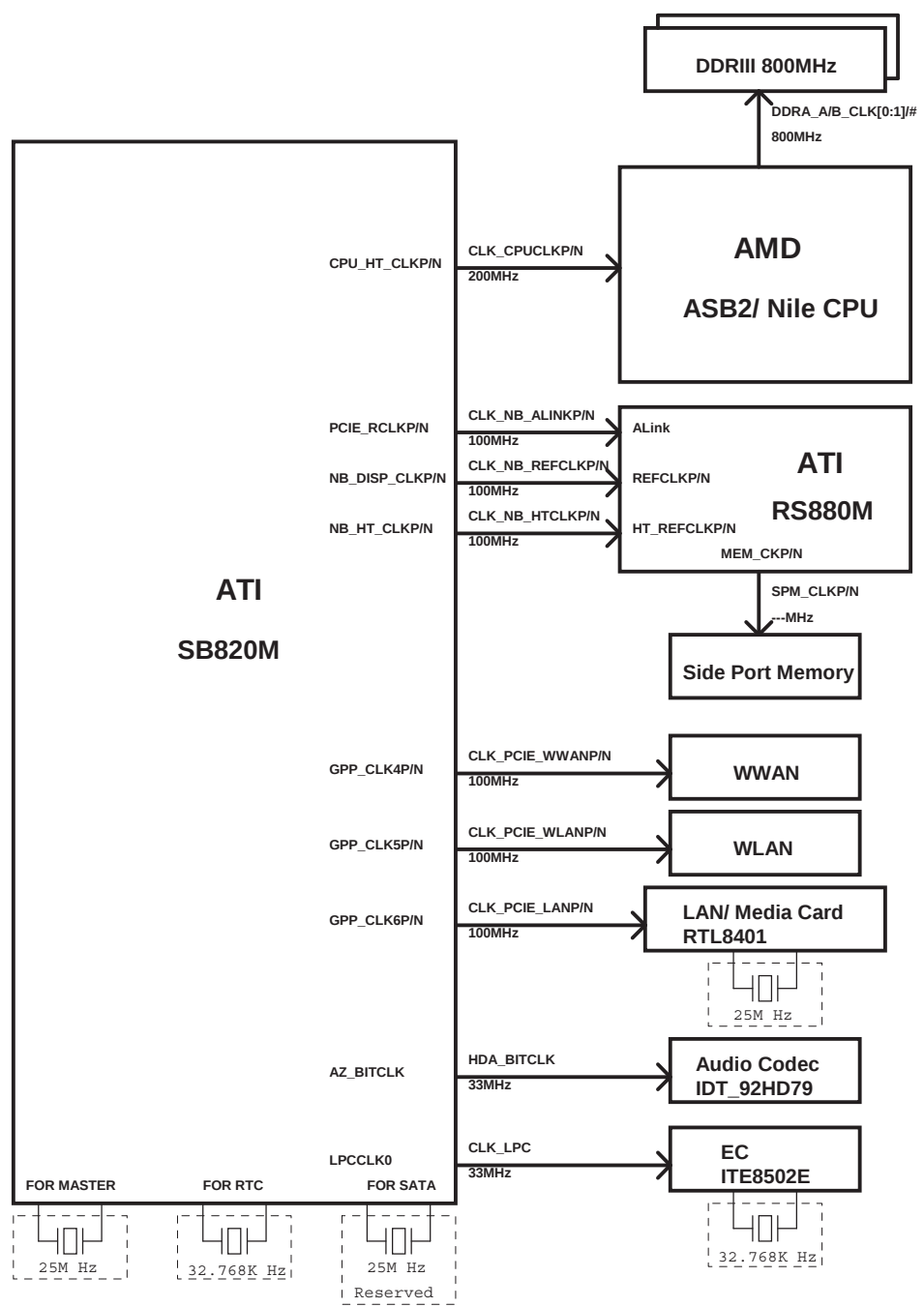




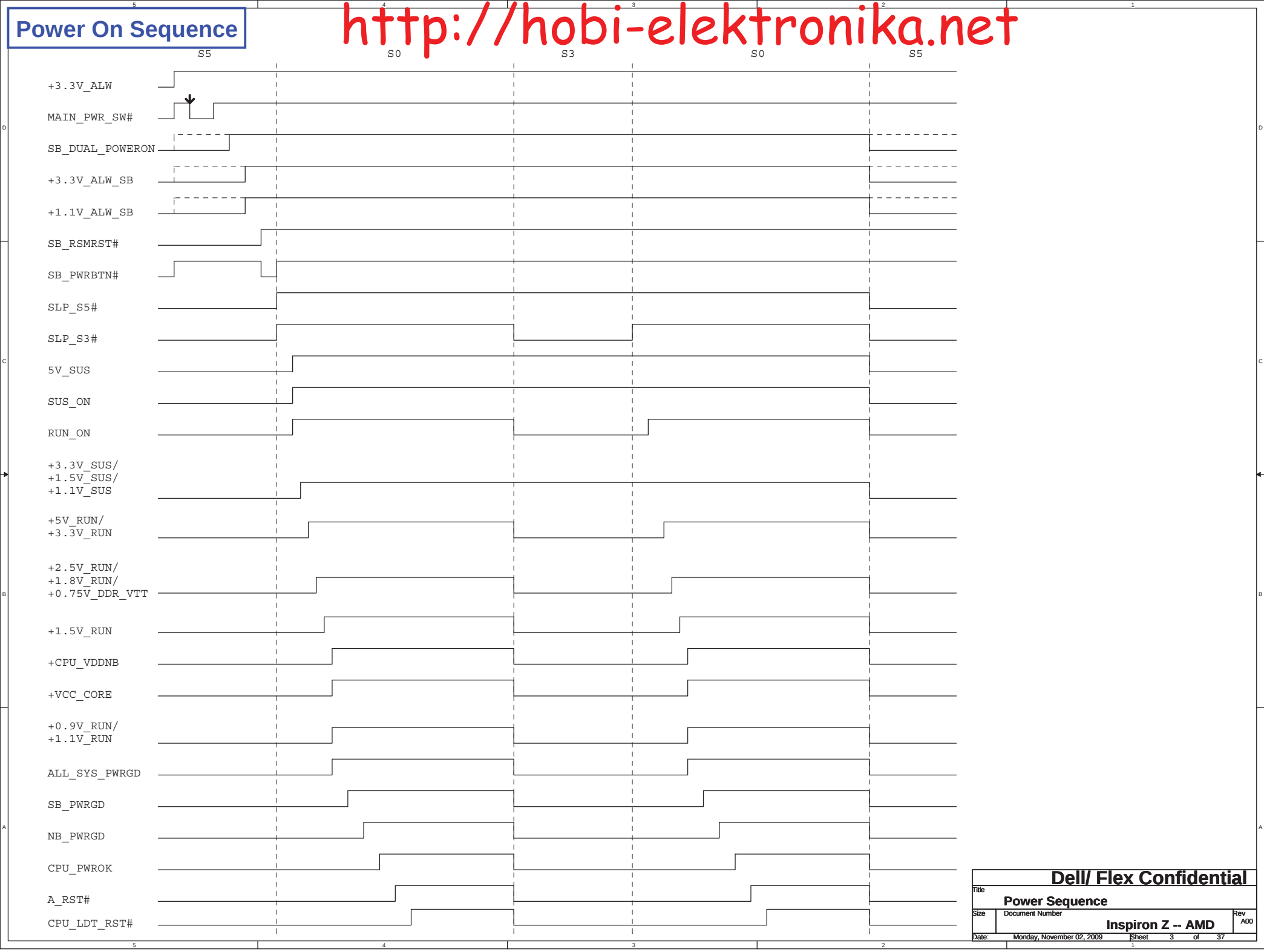
PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION, NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.



Power States							
Power Rail	Control Signal	S0	S3	S4	S5	G3/ AC-in	G3/ Battery Only
+PWR_SRC	N/A	V	V	V	V		
+0.75V_DDR_VTT	RUN_ON	V					
+0.9V_RUN	RUN_ON	V					
+1.1V_ALW_SB	SB_DUAL_PWRON	V	V	V	V		
+1.1V_SUS	SUS_ON	V	V				
+1.1V_RUN	RUN_ON	V					
+1.5V_SUS	SUS_ON	V	V				
+1.5V_RUN	RUN_ON	V					
+1.8V_RUN	RUN_ON	V					
+2.5V_RUN	RUN_ON	V					
+3.3V_ALW	+3.3V_EN2	V	V	V	V		
+3.3V_ALW_SB	SB_DUAL_PWRON	V	V	V			
+3.3V_SUS	SUS_ON	V	V				
+3.3V_RUN	RUN_ON	V					
+3.3V_KBVCC	N/A	V	V	V	V		
+5V_LDO	N/A	V	V	V	V		
+5V_SUS	5V_SUS_ON	V	V				
+5V_RUN	RUN_ON	V					
+GFX_PWR_SRC	N/A	V					
+LCDVCC	LCD_DIGON	V					
+CPU_VDDNB	2.5V_PWRGD	V					
+VCC_CORE	2.5V_PWRGD	V					
+RTC_CELL	N/A	V	V	V	V	V	V
+USB_RIGHT_PWR	USB_EN#	V	V				
+USB_LEFT_PWR	USB_EN#	V	V				

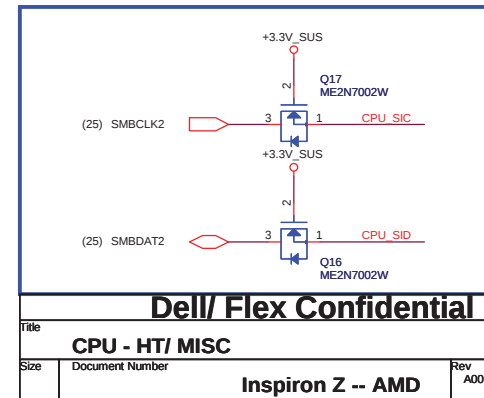
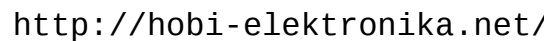
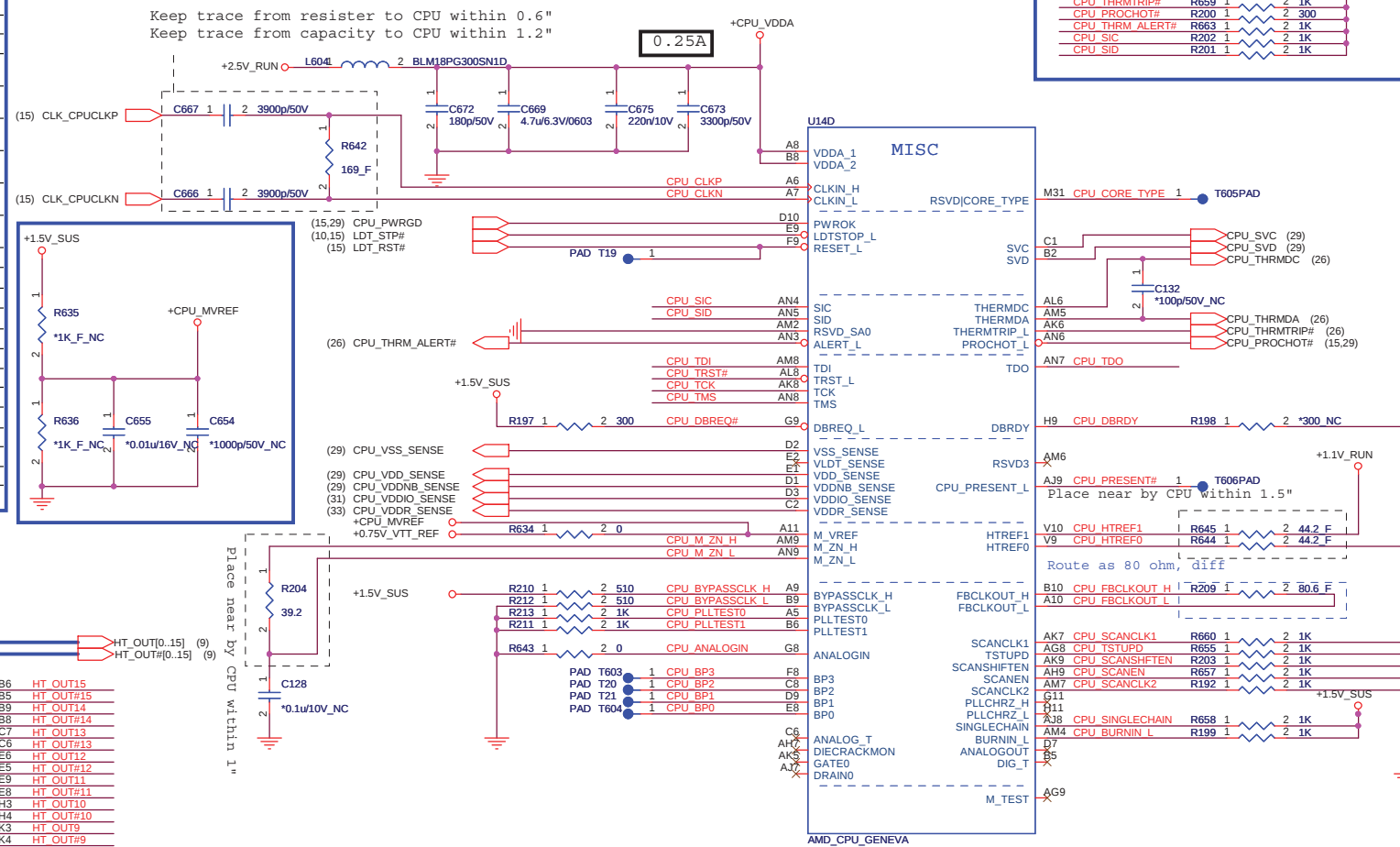
Power On Sequence

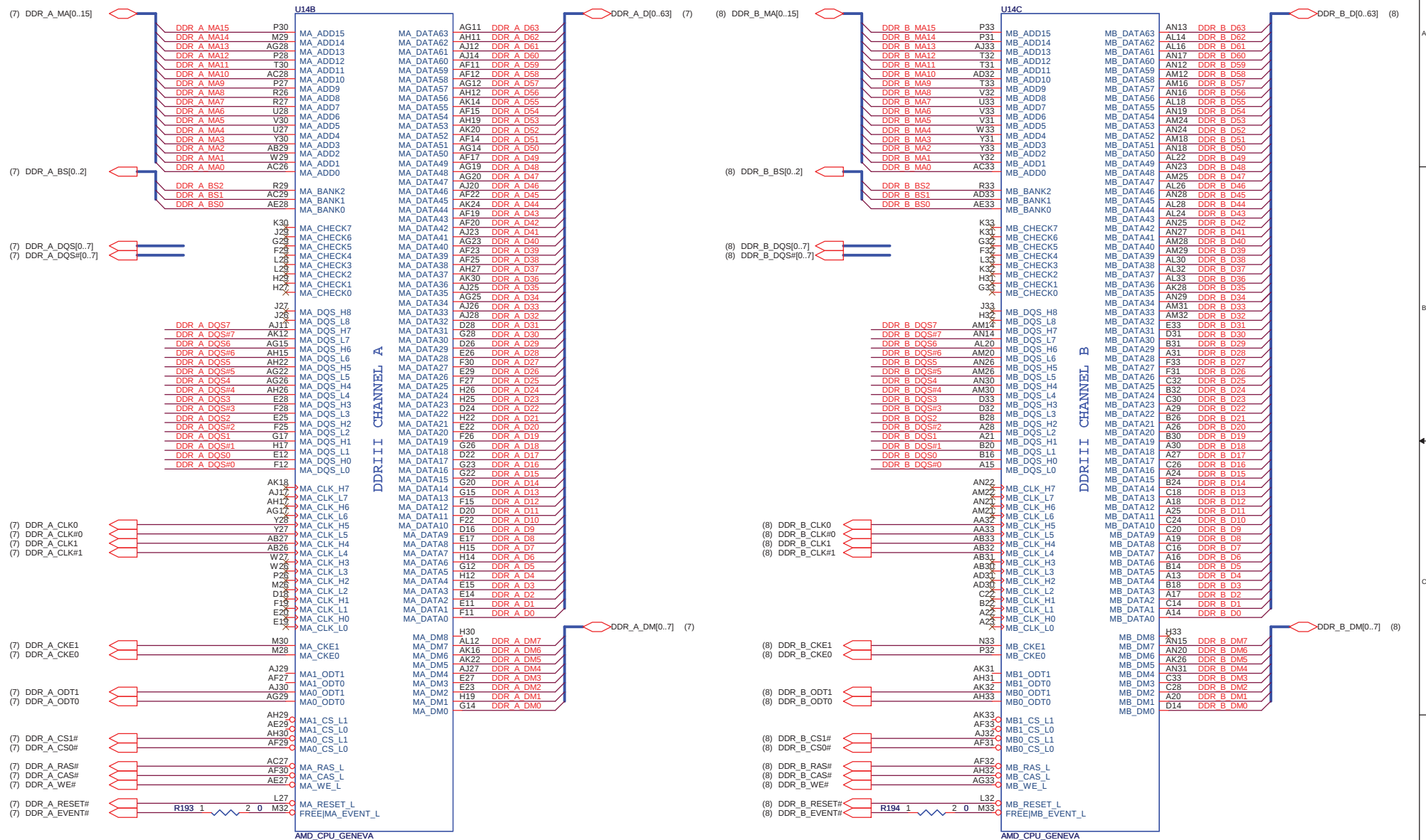
http://hobi-elektronika.net

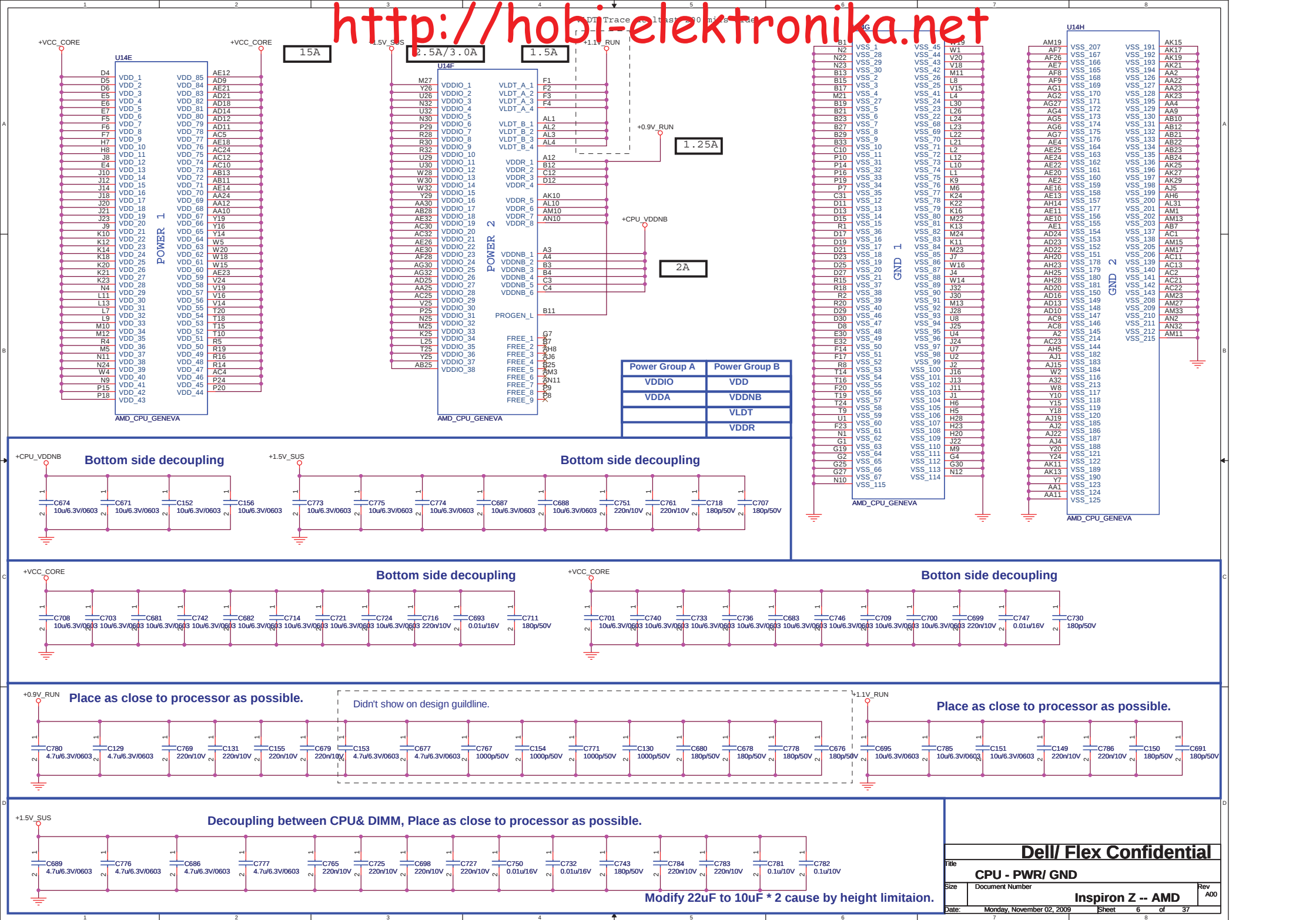


<http://hobi-elektronika.net>

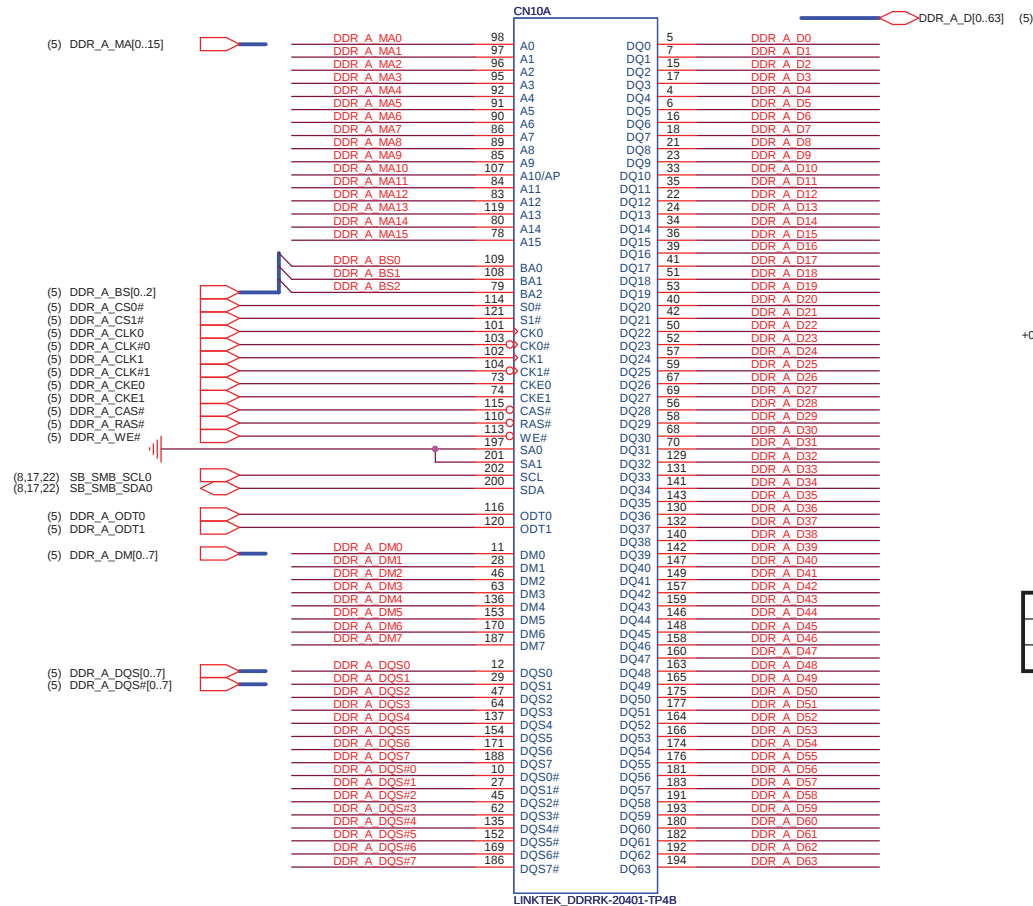
Model	CPU	Storage	RAM	GPU	Display	OS	Weight	Price
DELH-40GAB4900-X000	Athlon Single Core 1.7G	AMK125DBV13GM	1C	CPU AMK125DBV13GM	1G7 BGA-812	Windows 7	1.2kg	15000
DELH-40GAB4900-X001	Athlon Dual Core 1.3G	AMK325DBV23GM	2C	CPU AMK325DBV23GM	1G3 BGA-812	Windows 7	1.2kg	18000
DELH-40GAB4900-X002	Turion Dual Core 1.5G	TMK625DBV23GM	2C	CPU TMK625DBV23GM	1G5 BGA-812	Windows 7	1.2kg	20000



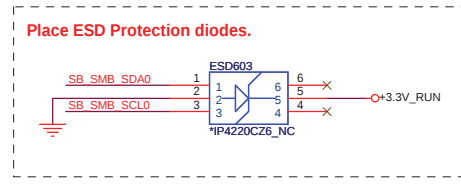
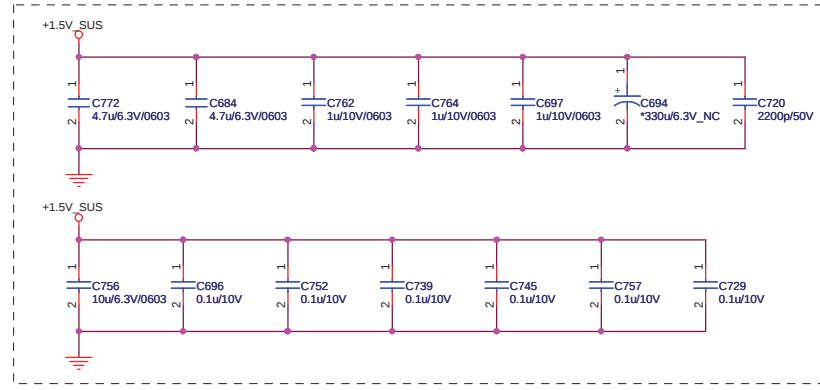
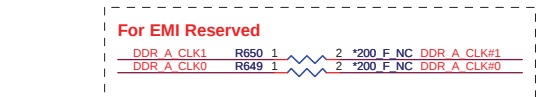




DDR3 DIMM A

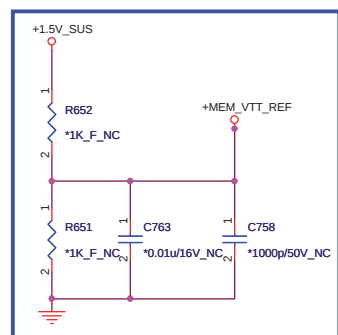
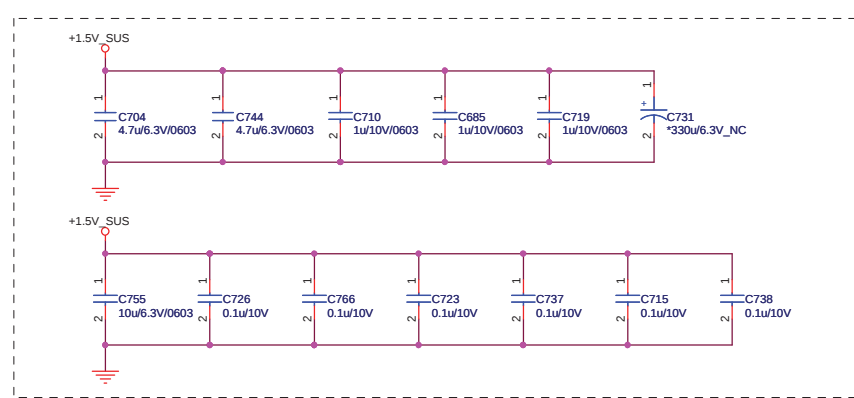
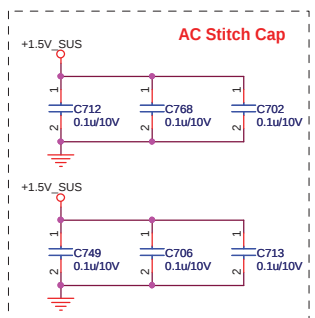
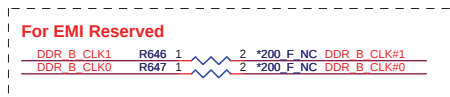
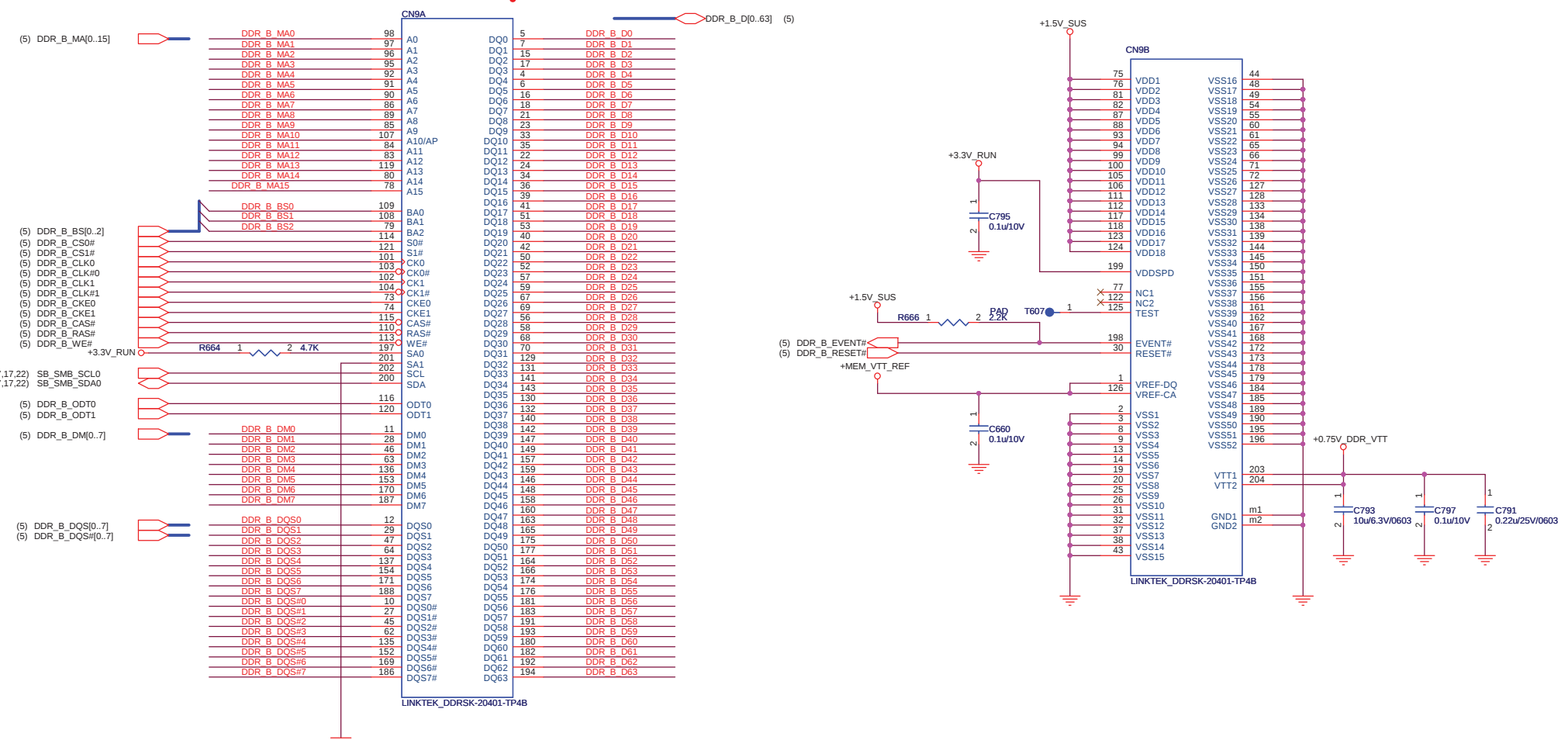


SM_MEM BUS ADDRESS	
SO-DIMM0	1010 000
SO-DIMM1	1010 001



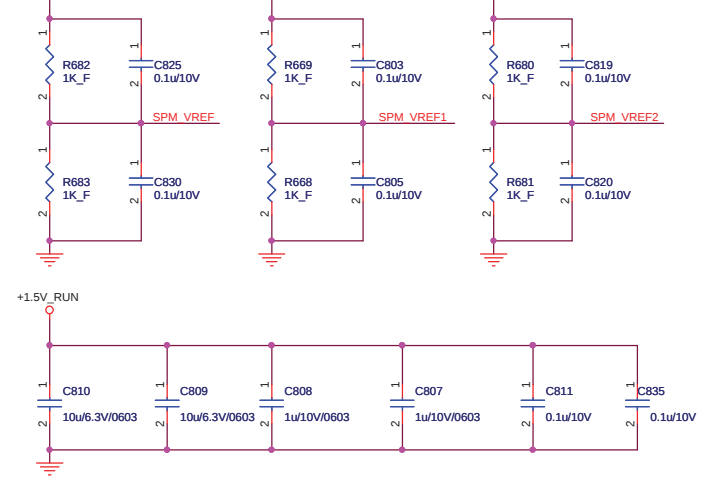
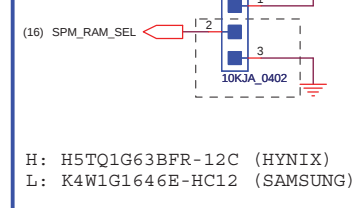
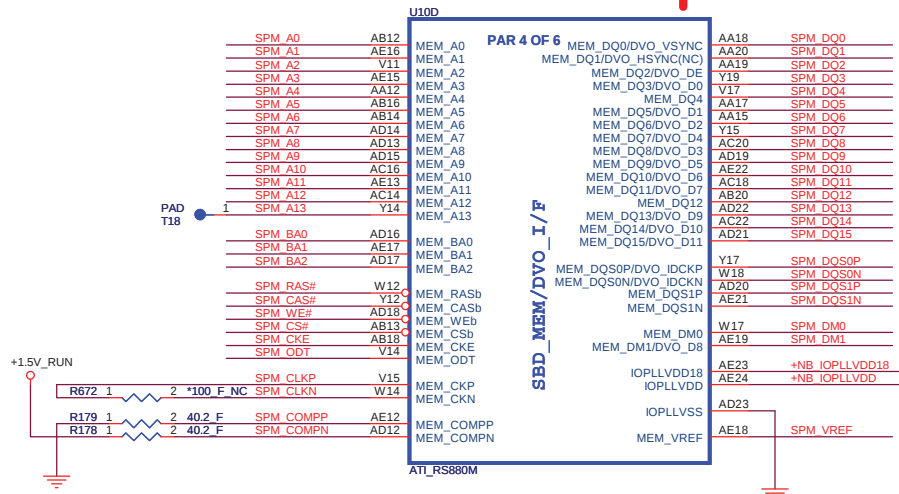
DDRIII DIMM B

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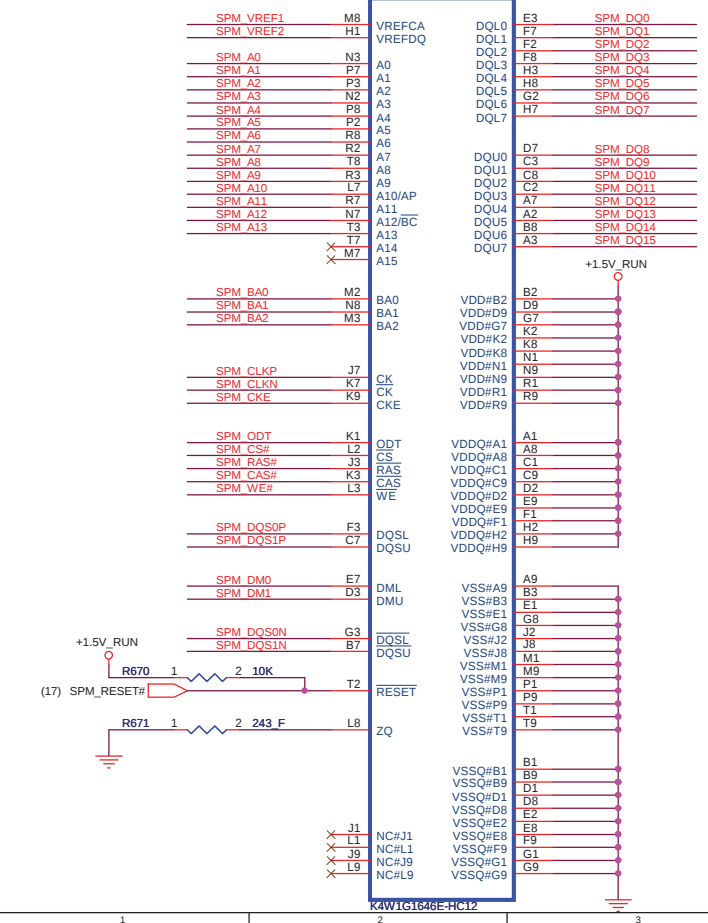




Side Port/ Strapping



2'nd source:
DELH-14D00A0000003G -- IC DDR3 H5TQ1G63BFR-12C 1Gbits FBGA-96



RS880M H/W STRAPS

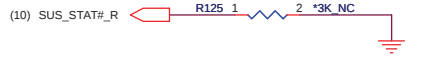
STRAP_DEBUG_BUS_GPIO_ENABLEb

Enables the Test Debug Bus using GPIO.
DAC_VSYNC (RS880.Pin B11)
1 = Disable (default)
0 = Enable



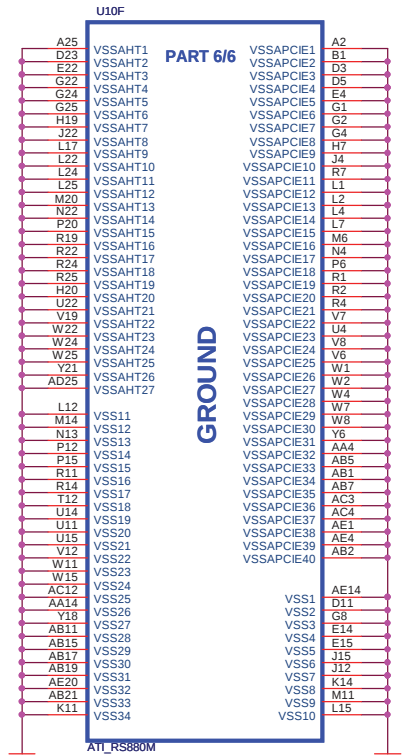
DFT_GPIO1: LOAD_EEPROM_STRAPS

Selects Loading of STRAPS from EPROM.
SUS_STAT# (RS880.Pin D12)
1 = Bypass the loading of EEPROM straps and use Hardware Default Values
0 = I2C Master can load strap values from EEPROM if connected, or use default values if not connected

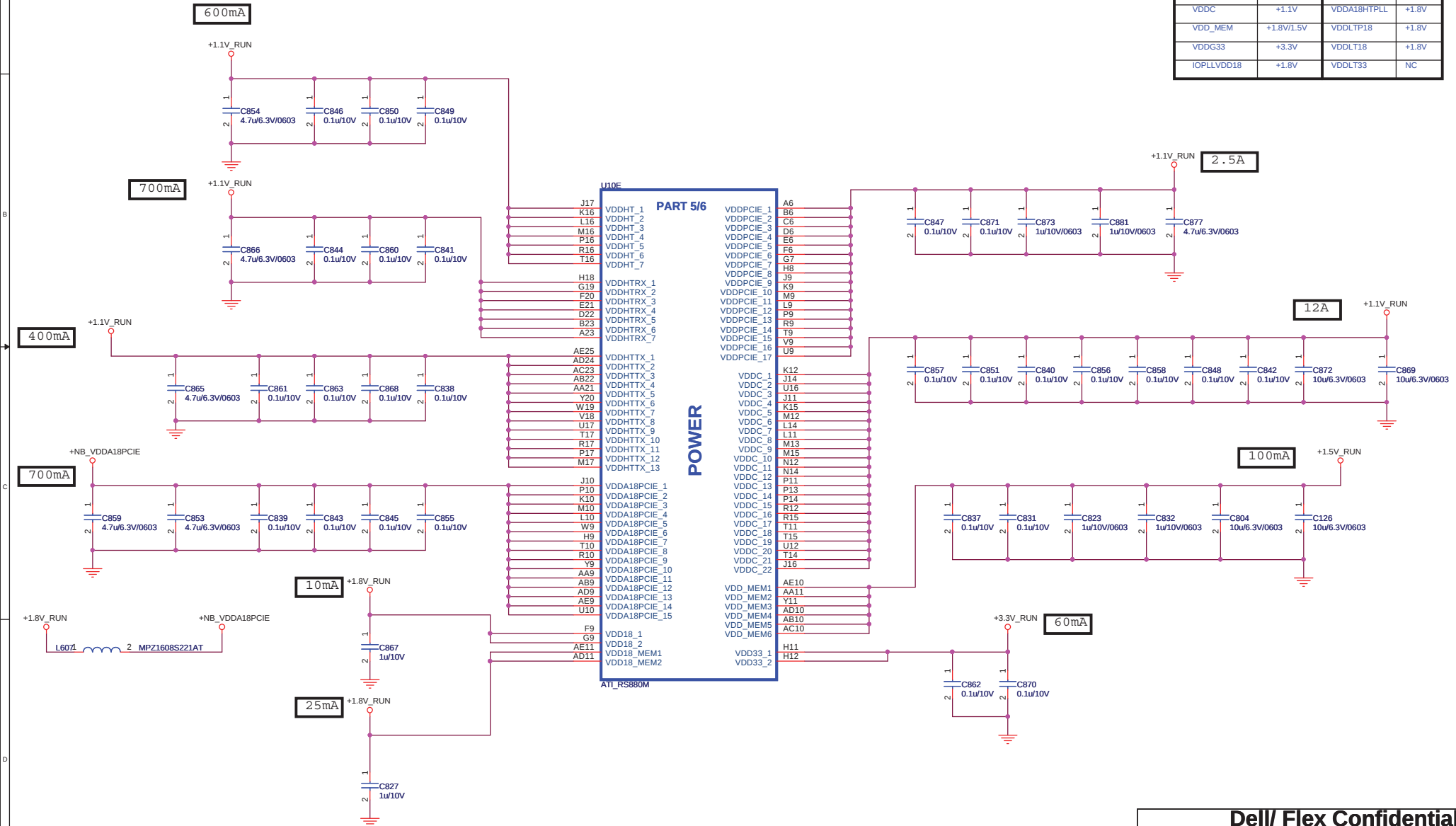


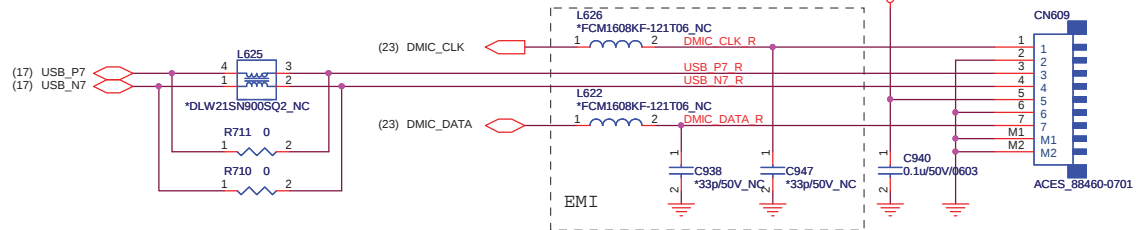
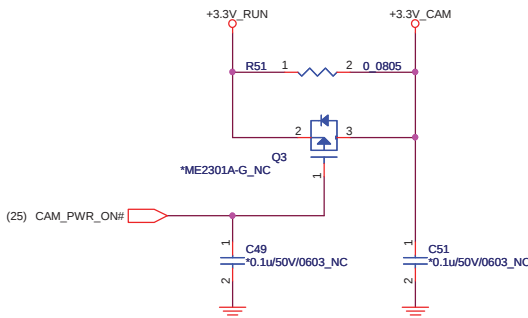
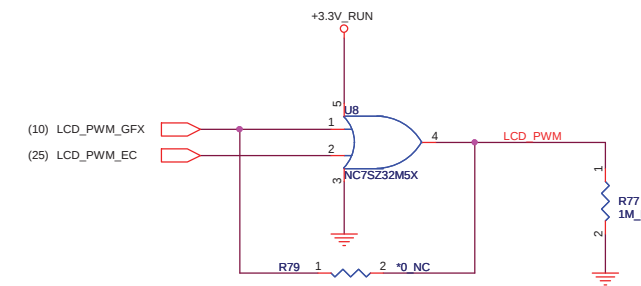
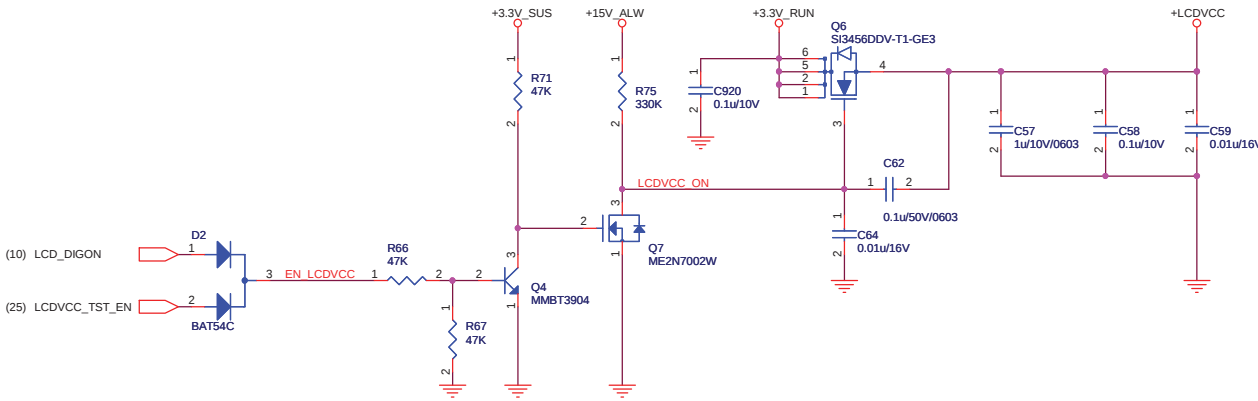
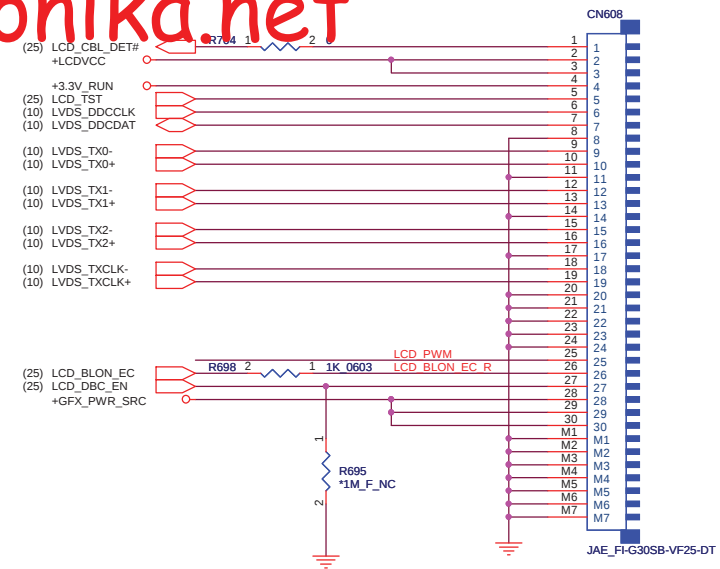
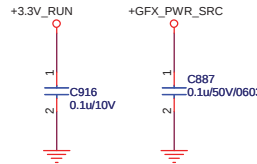
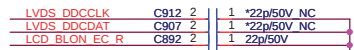
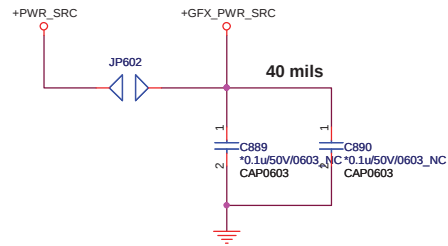
RS880: Enable Side Port Memory

Selects if Memory SIDE PORT is available or not
DAC_HSYNC (RS880.Pin A11)
1 : Disable (default)
0 : Enable
Register Readback of strap: NB_CLKCFG:CLK_TOP_SPARE_D[1]

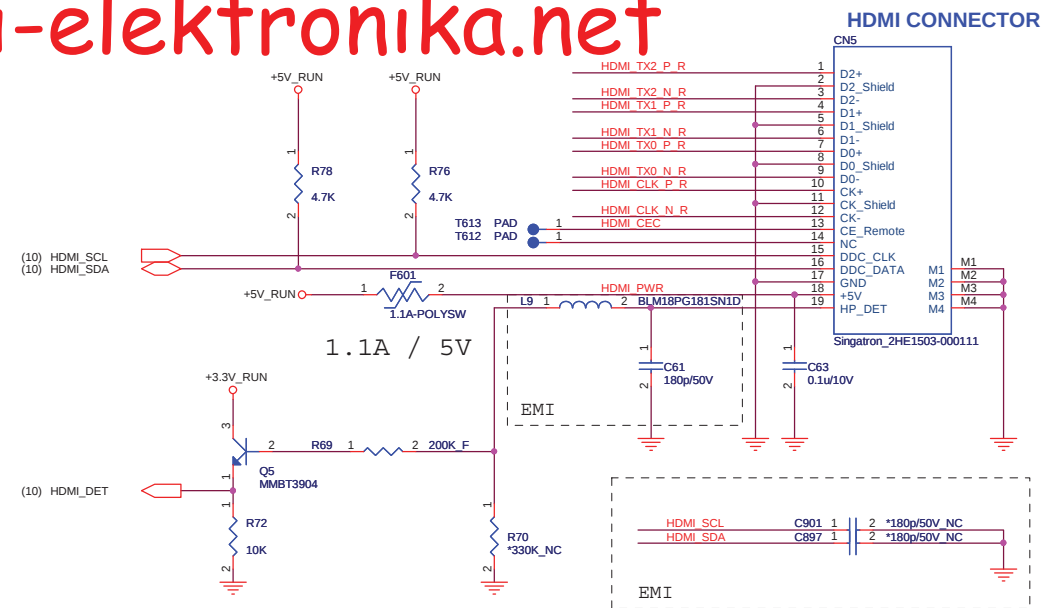
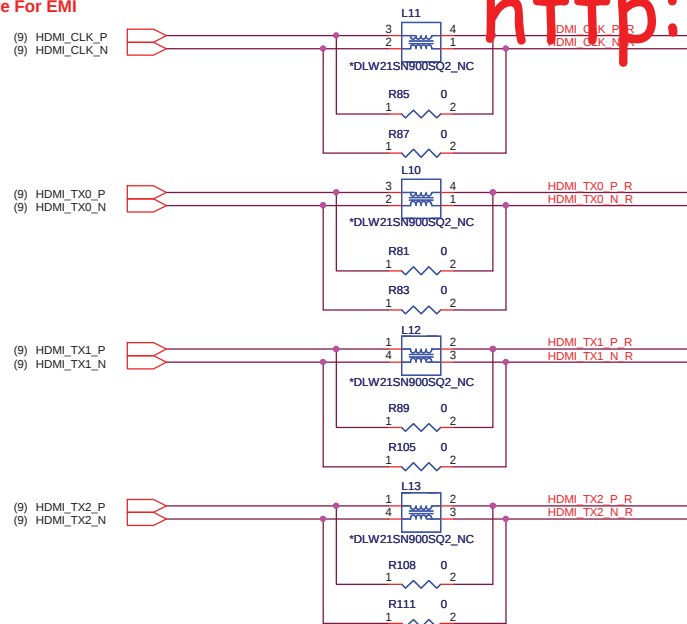


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Title NB - PWR			
Size	Document Number		Rev A00
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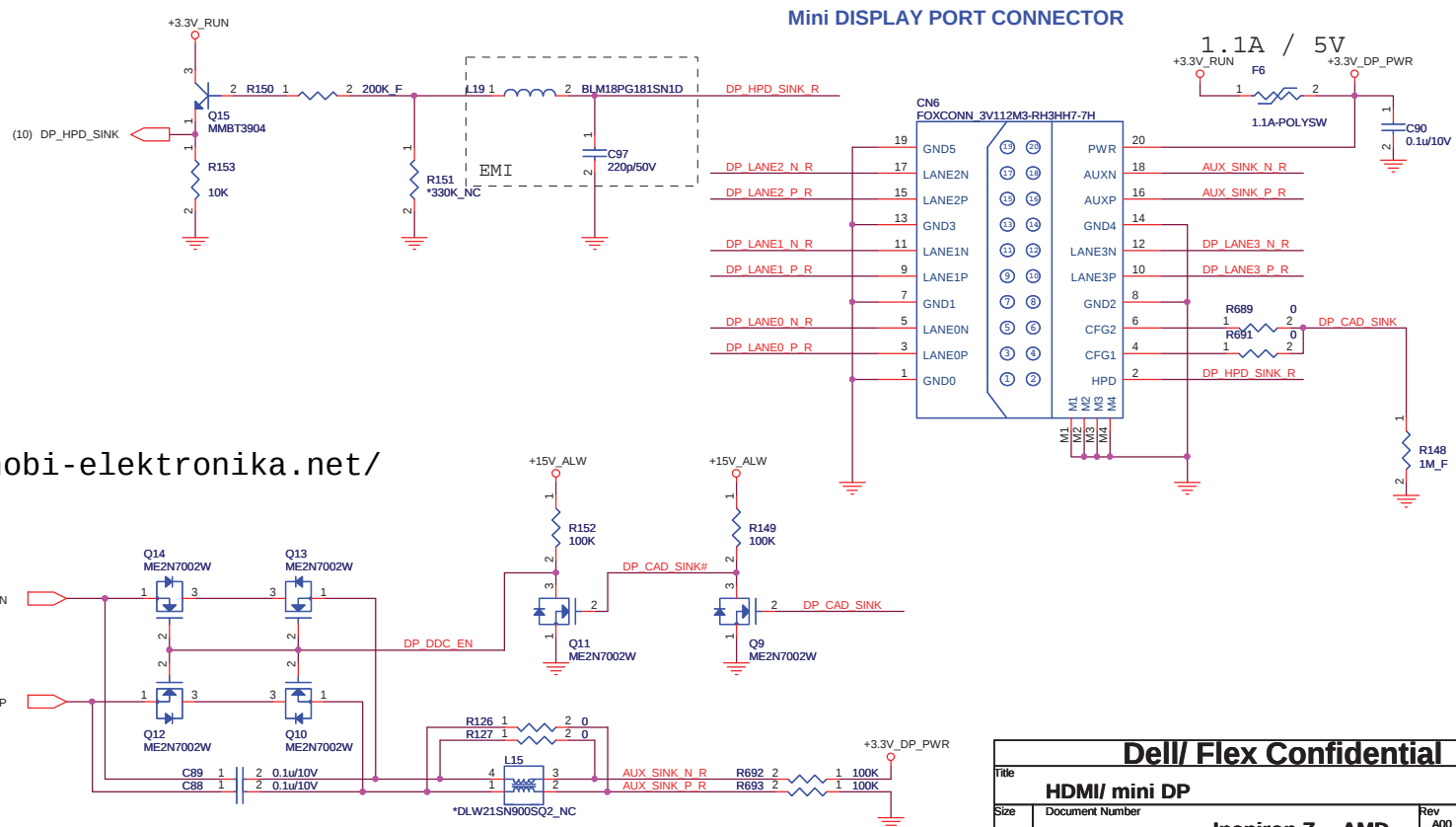
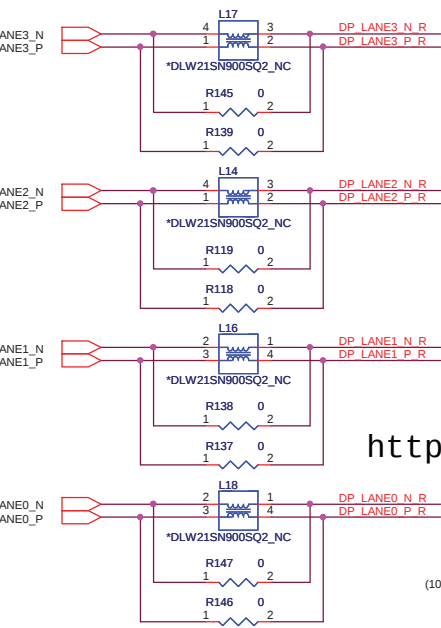


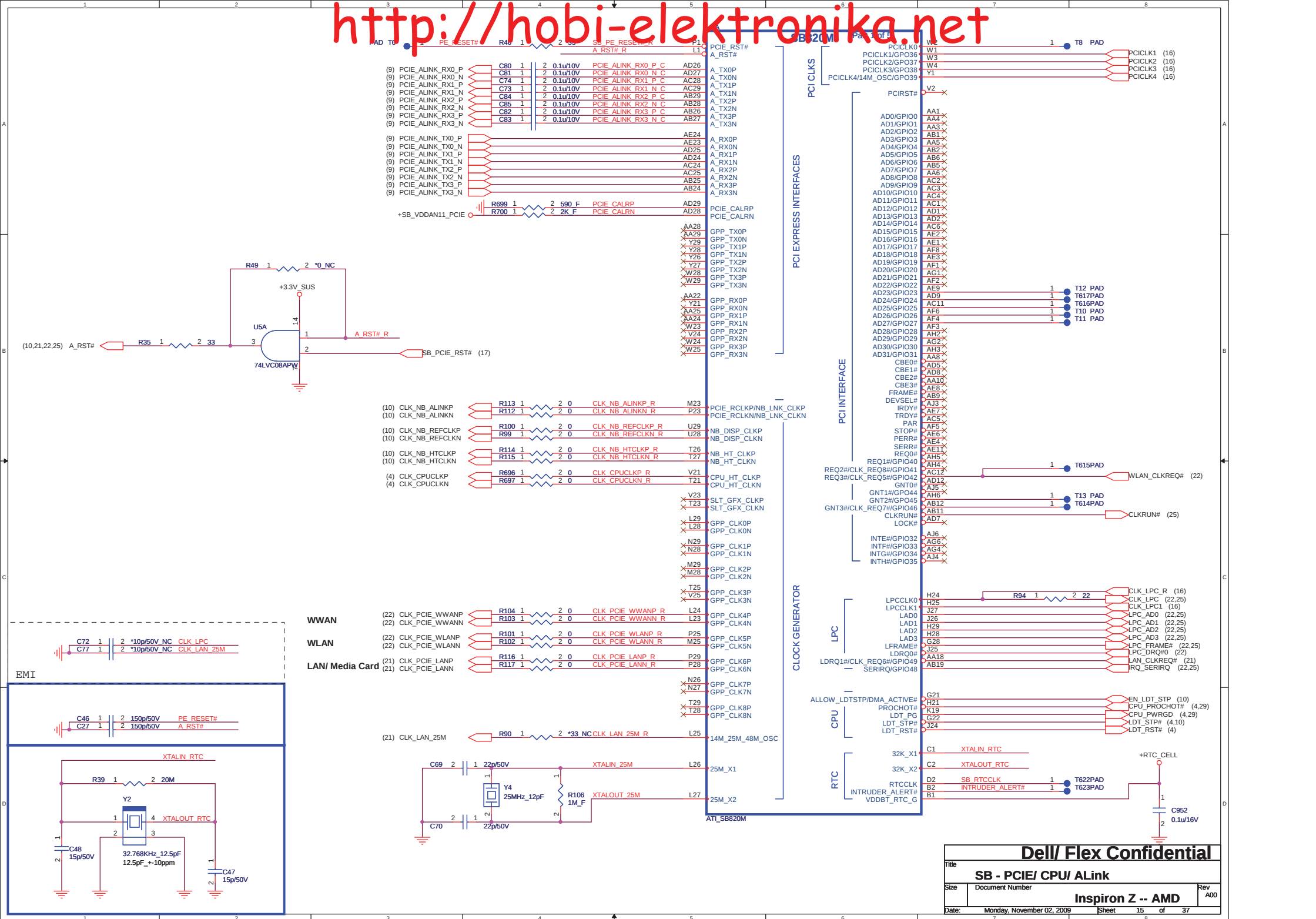


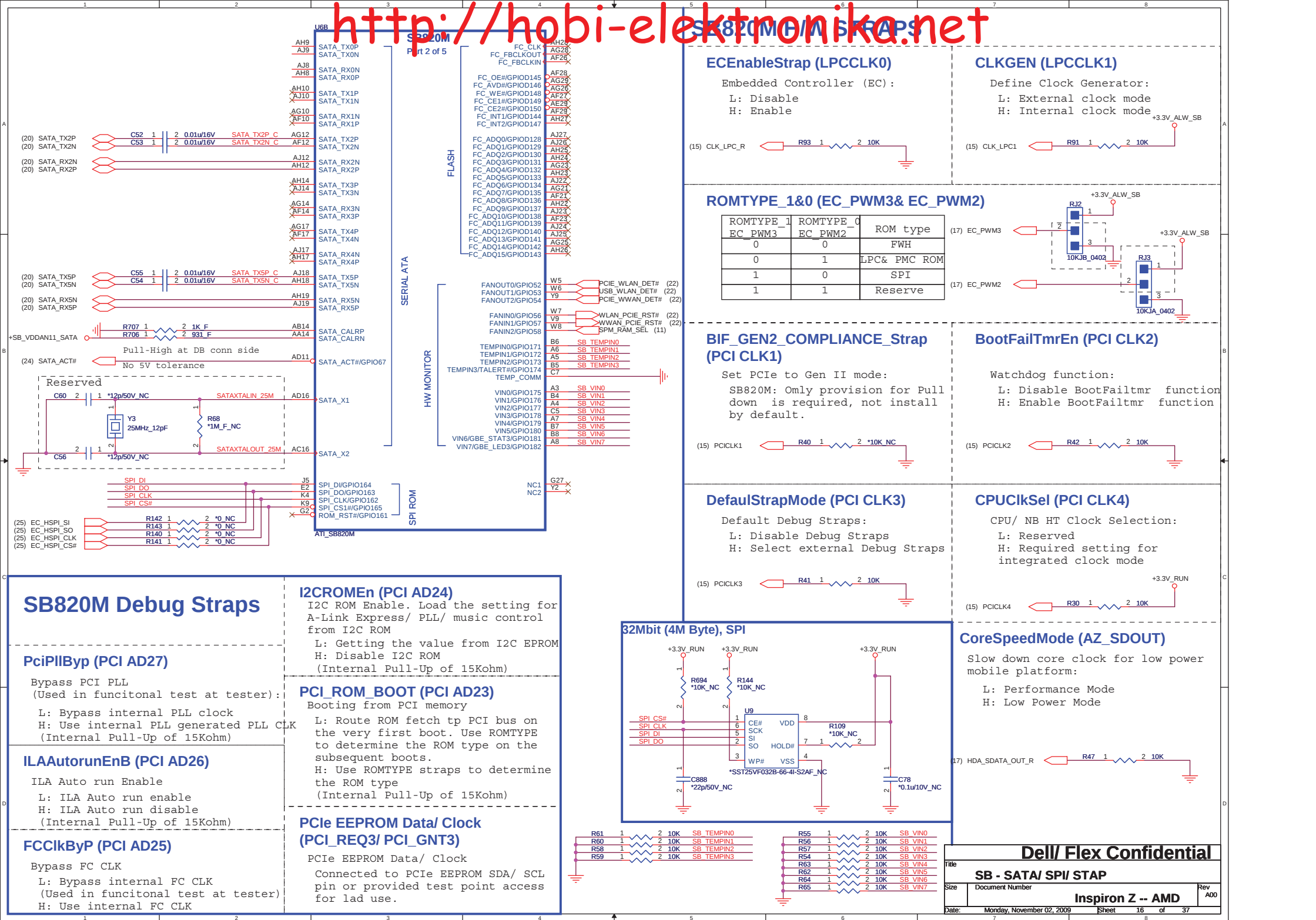
Reserve For EMI



Reserve For EMI

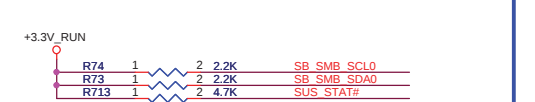
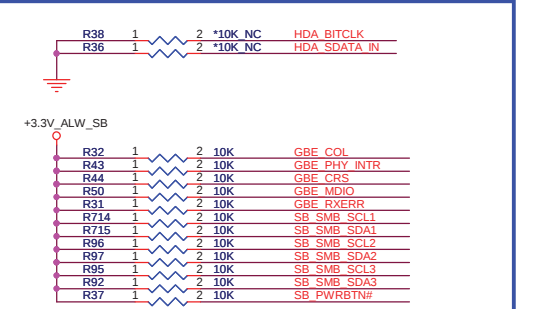
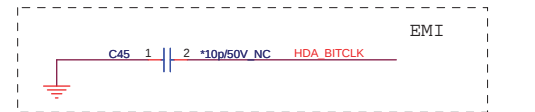
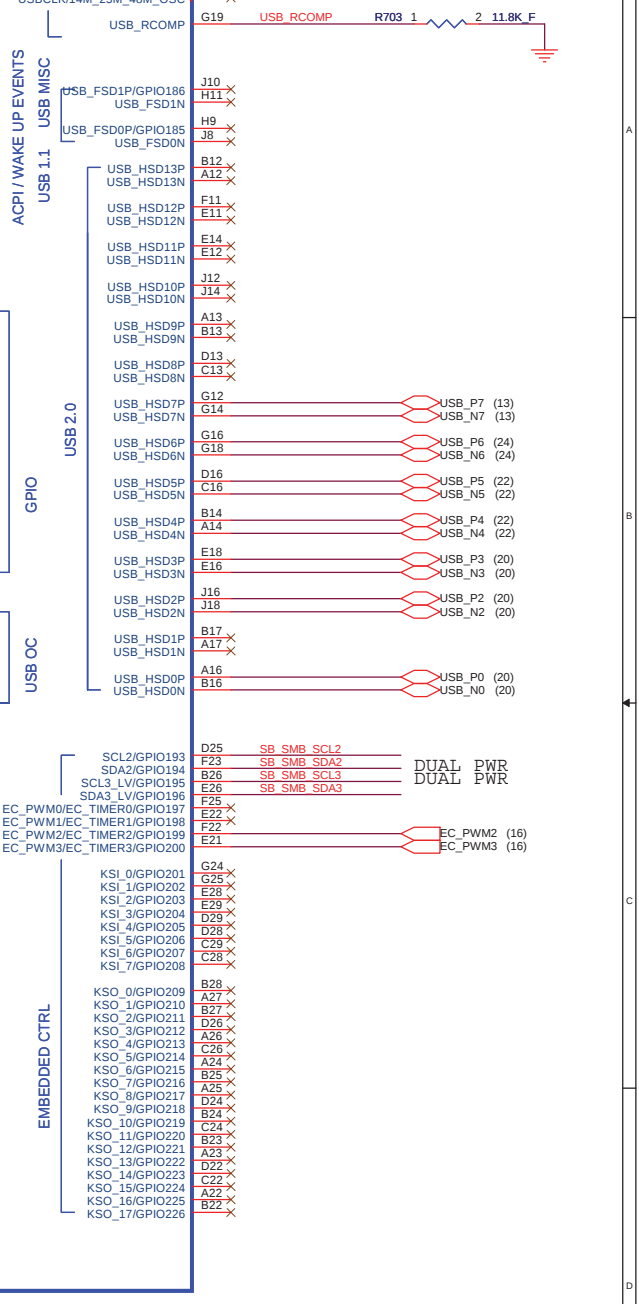
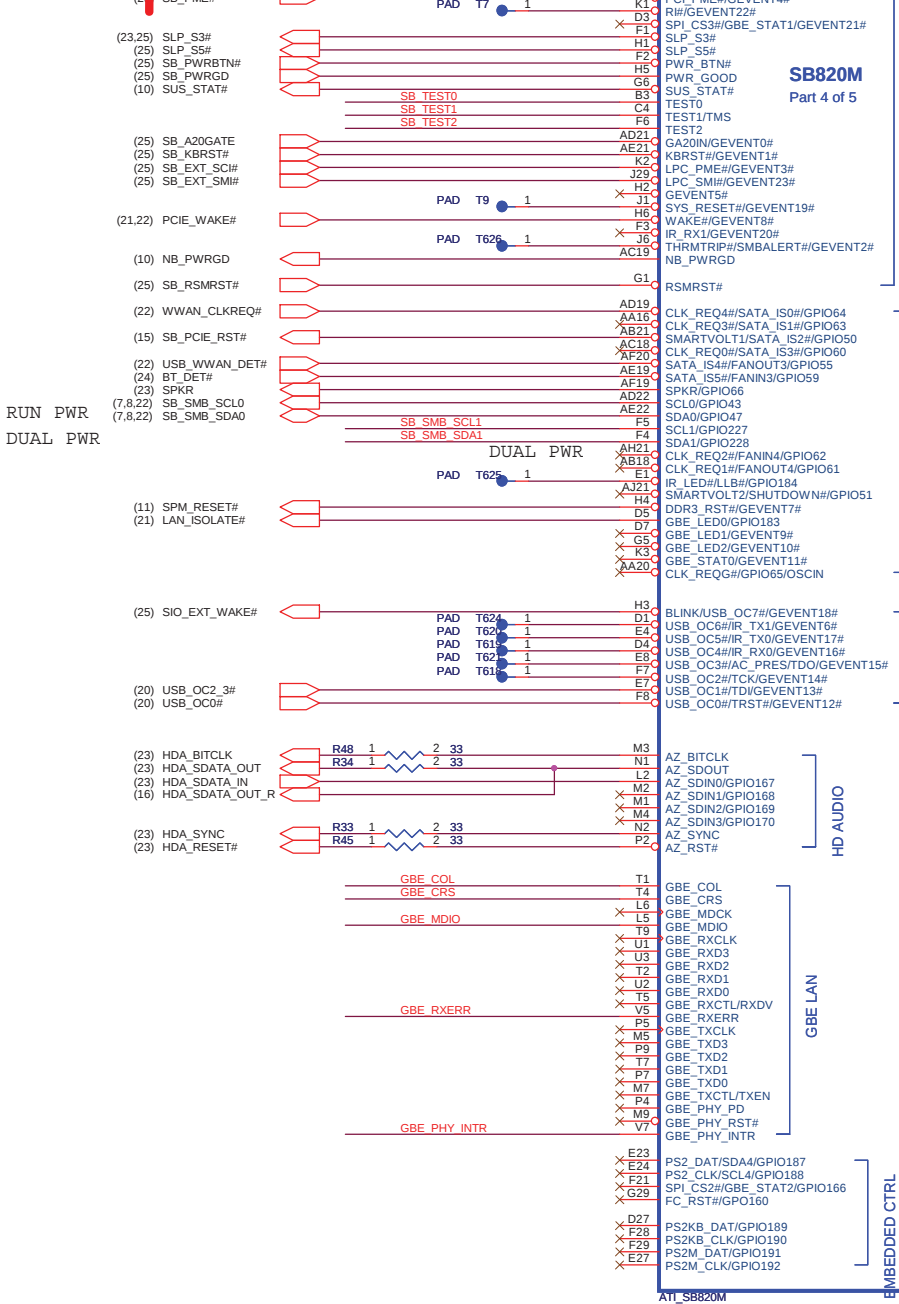
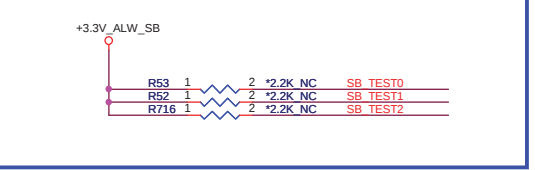


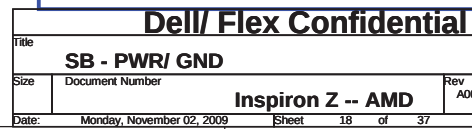




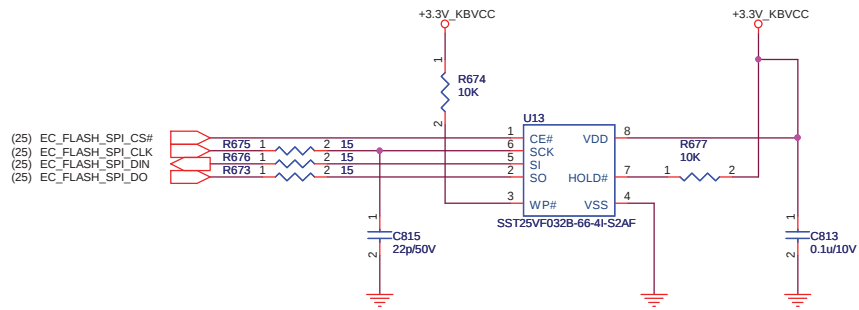
SB820M Debug Straps

<http://hobi-elektronika.net>

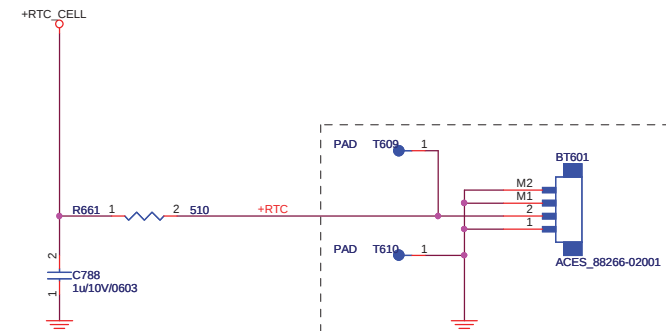




32Mbit (4M Byte), SPI



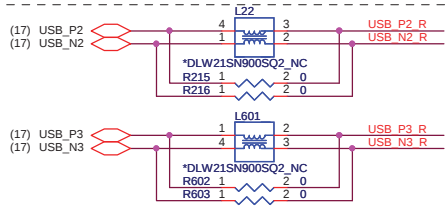
RTC BATTERY



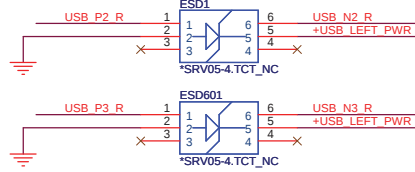
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Title	RTC/ SPI		
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USB Jack x 2

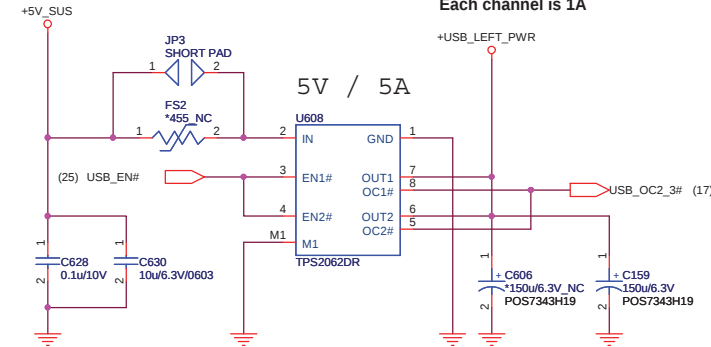


Platforms should put in PADS for the USB chokes if they have the room.
Chokes should be NOPOP.



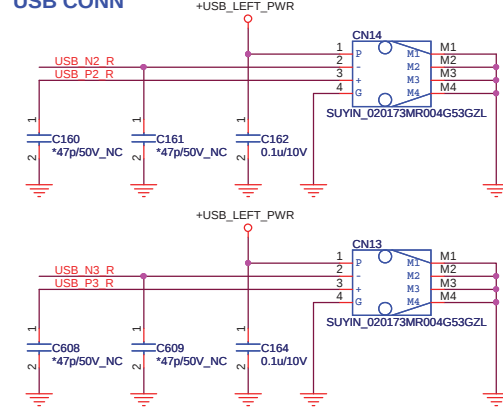
Place ESD diodes as close as USB connector.

USB POWER SW



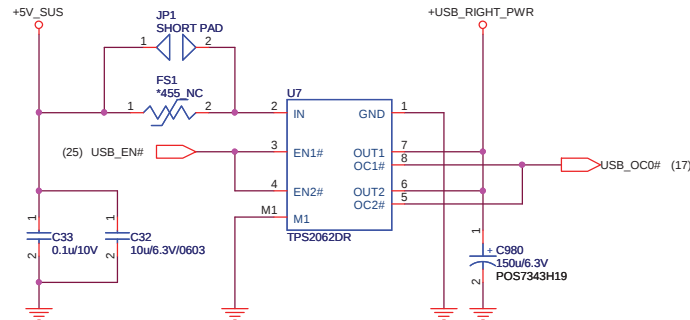
Place one 150uF cap by each USB connector.

USB CONN



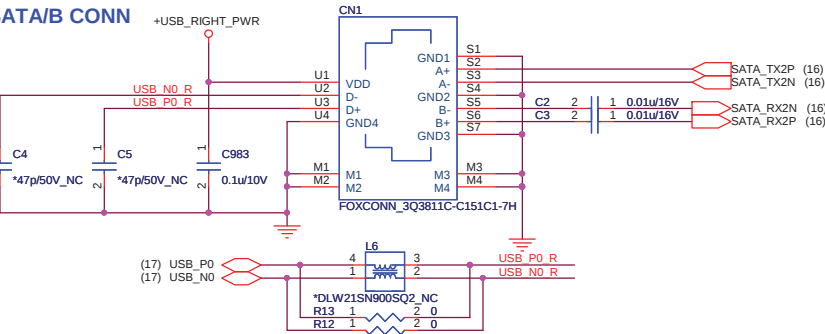
eSATA Connector

USB POWER SW

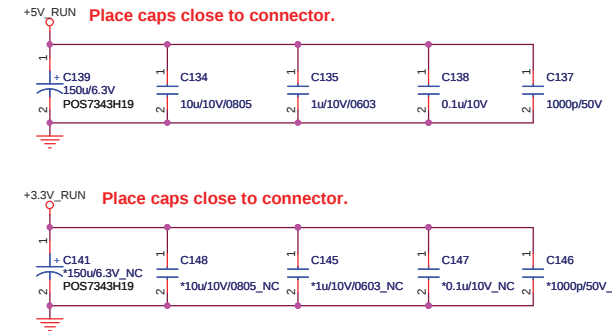
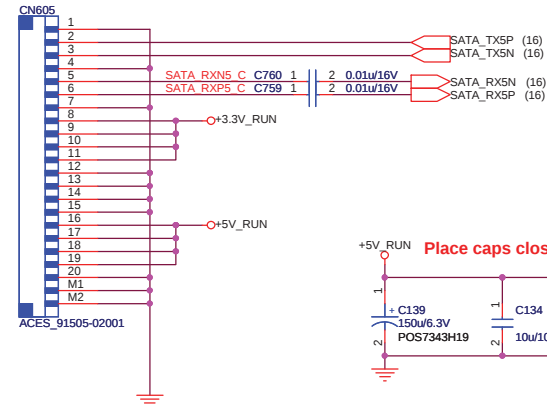


Place one 150uF cap by each USB connector.

ESATA/B CONN

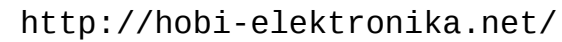


SATA HDD Connector

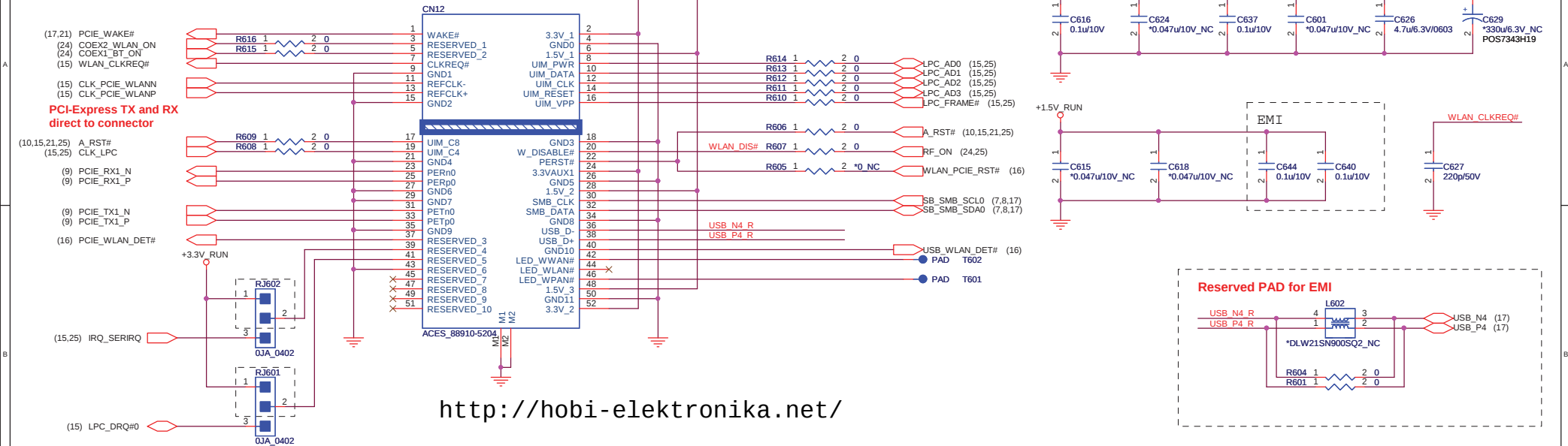


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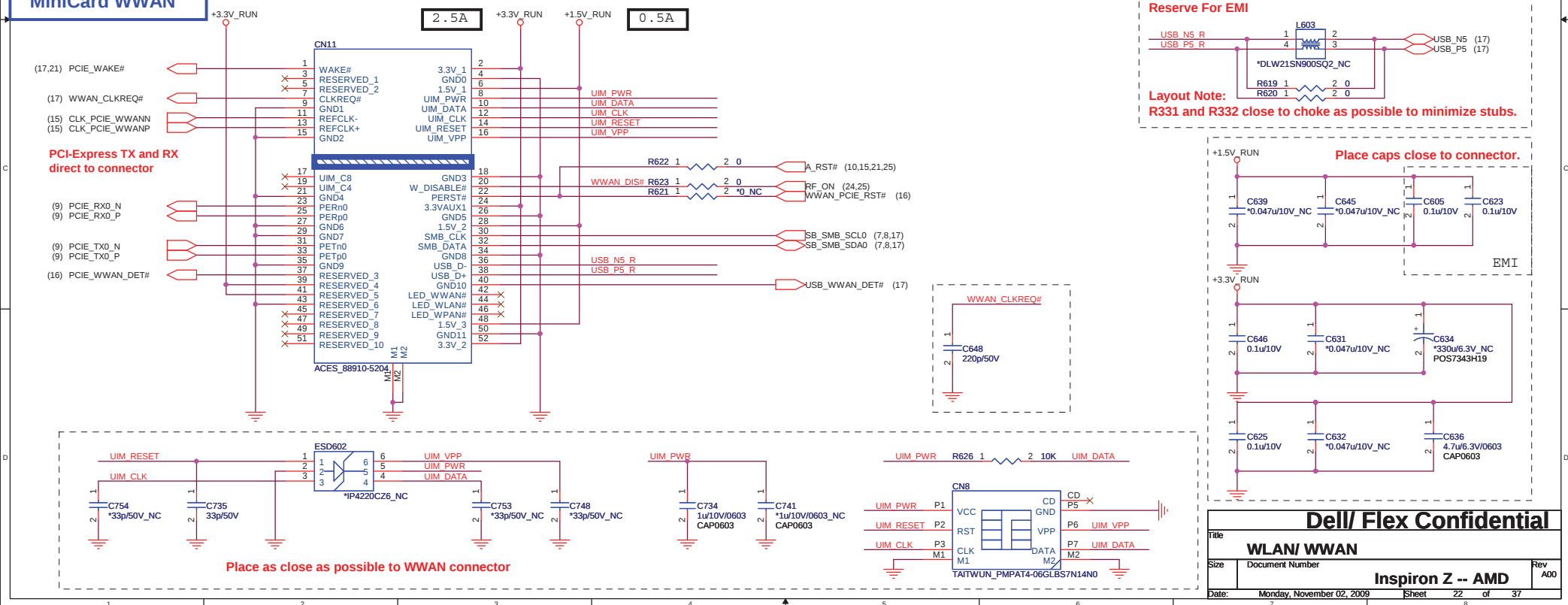
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USB/ eSATA/ HDD			
Size	Document Number		Rev
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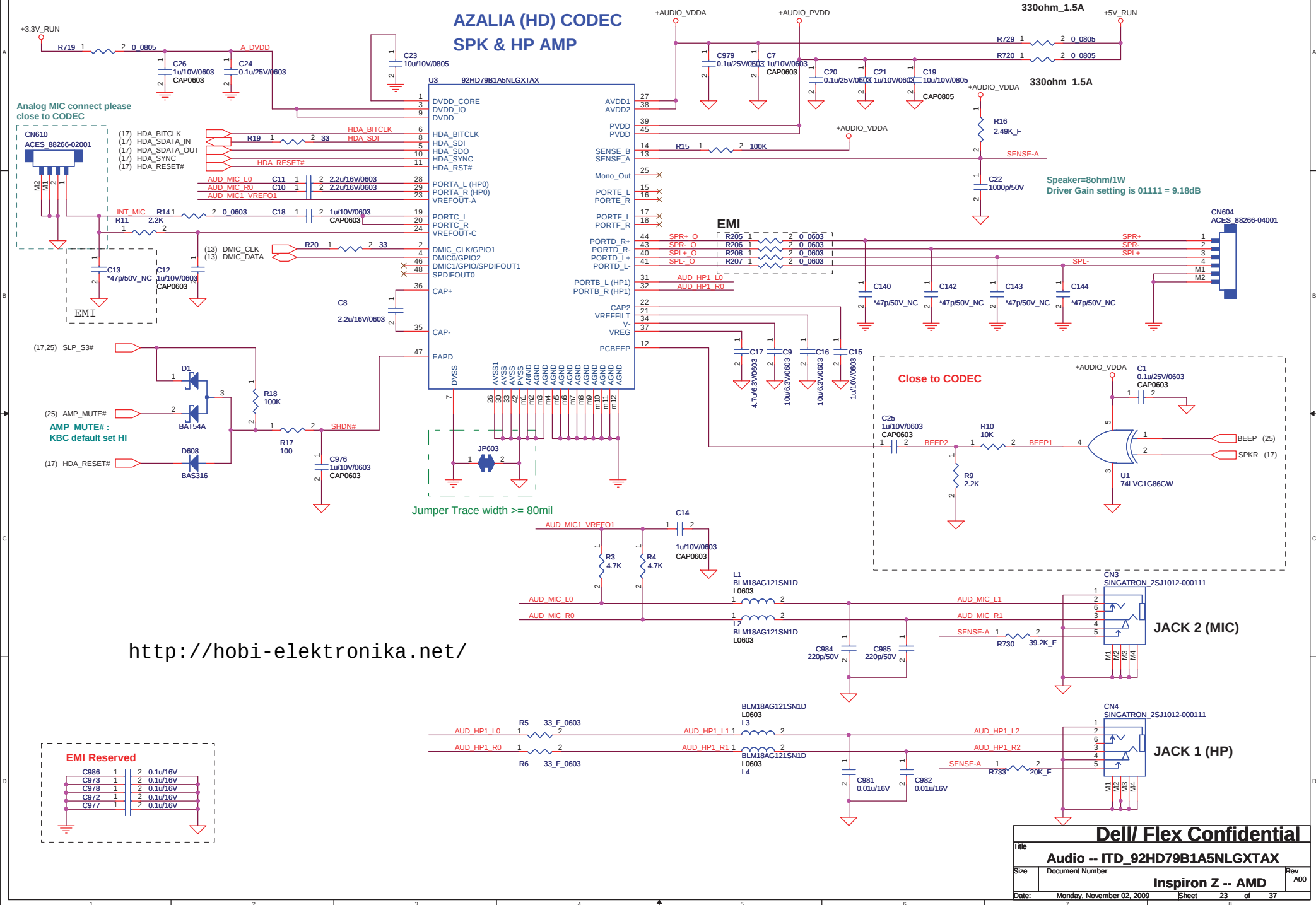


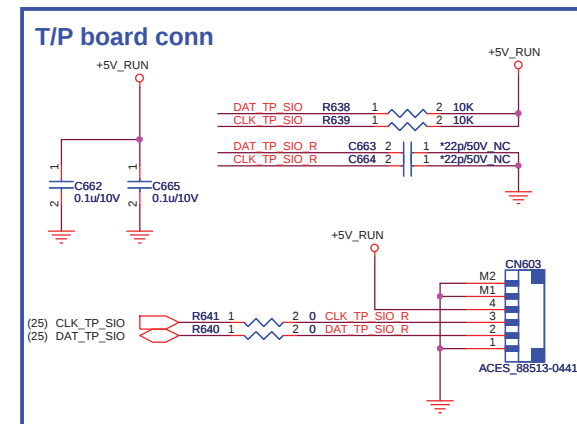
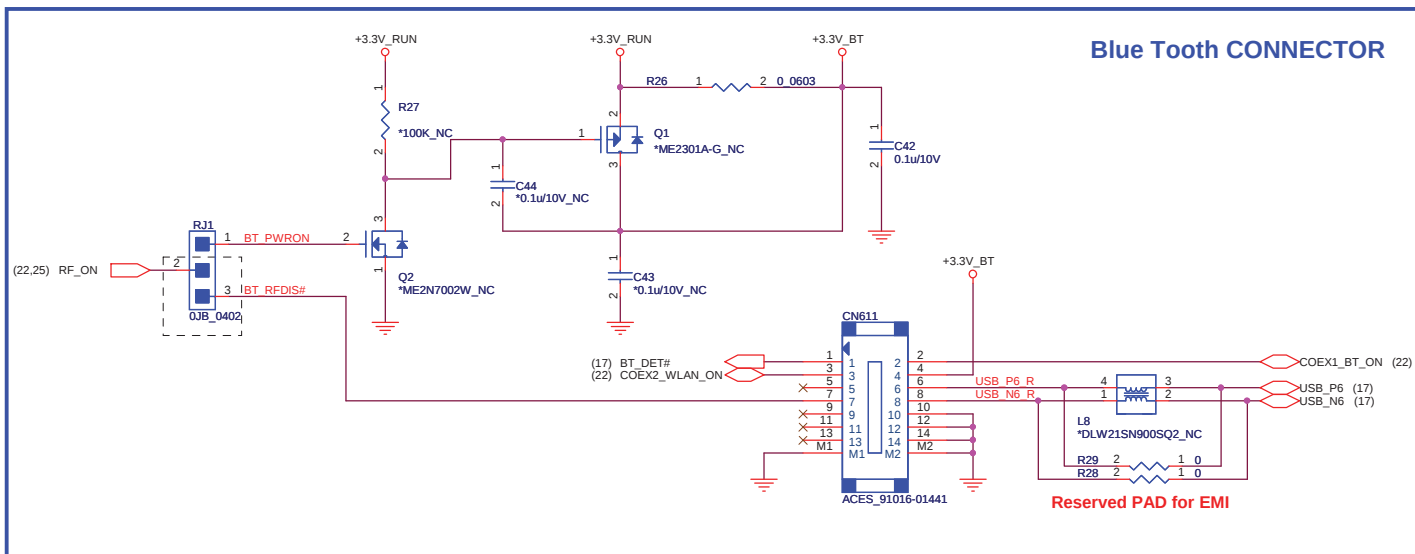
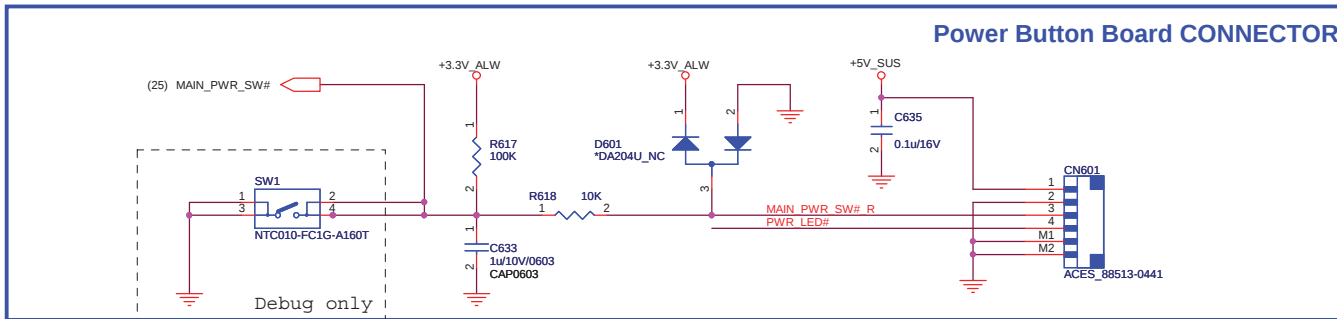
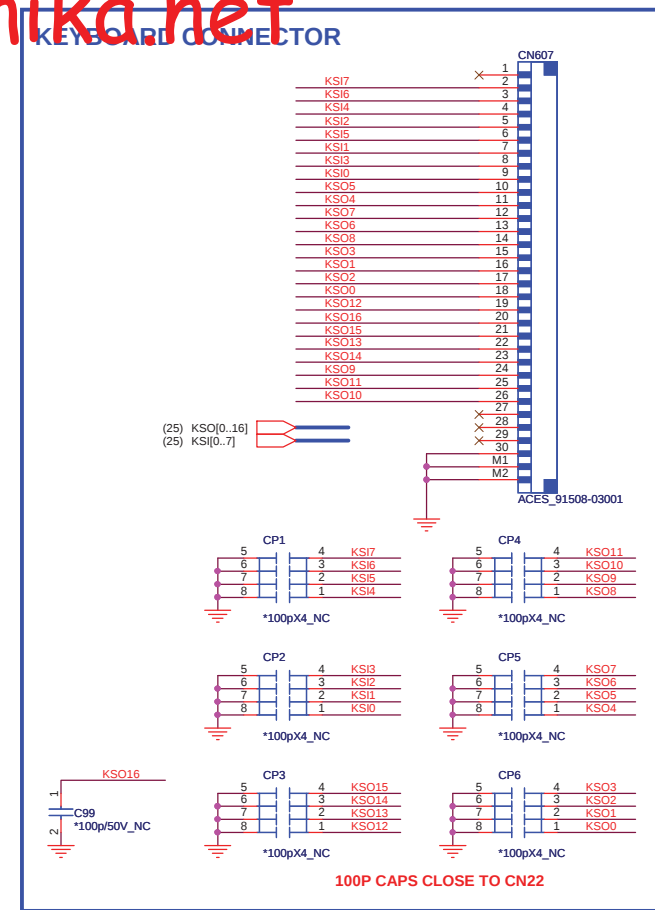
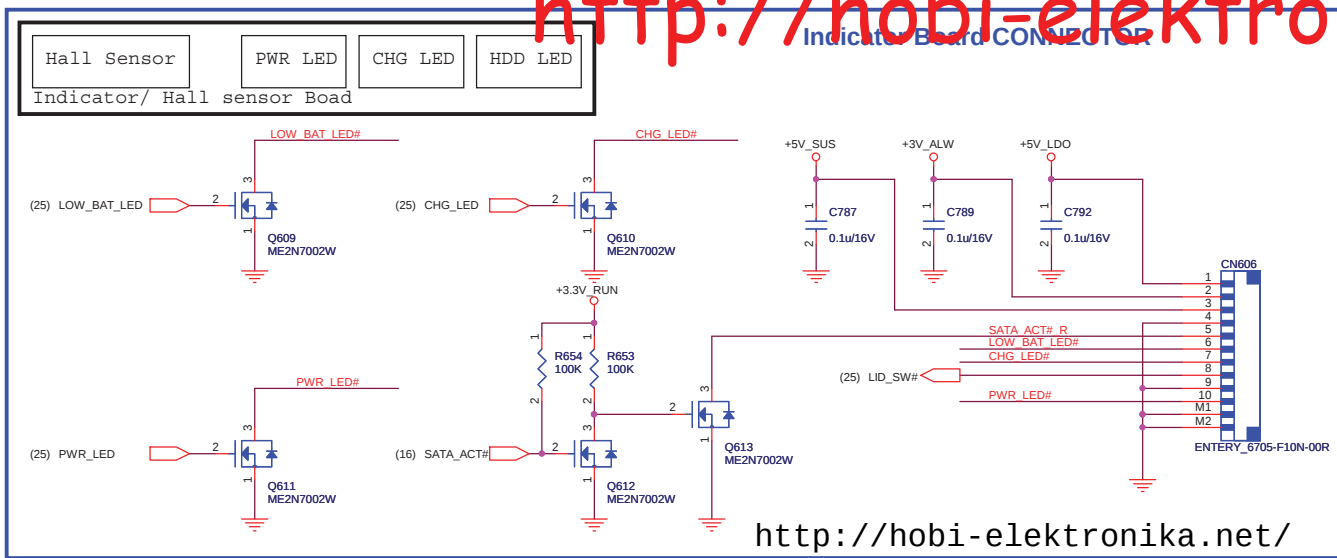
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LAN/ Card Reader -- RL8401			
Size	Document Number	Rev	
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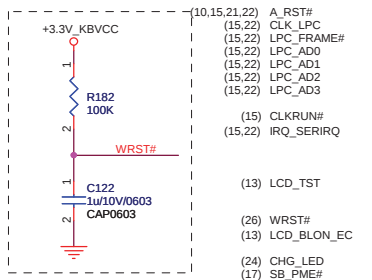
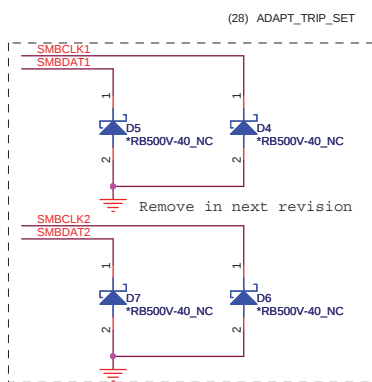
MiniCard WWAN







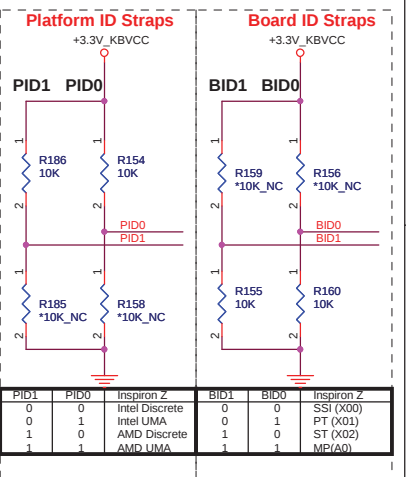
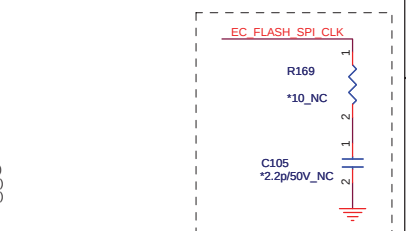
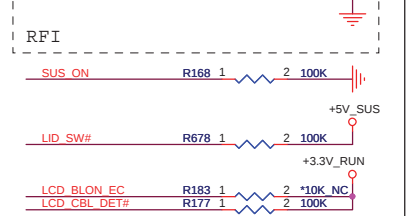
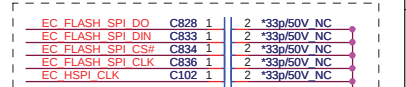
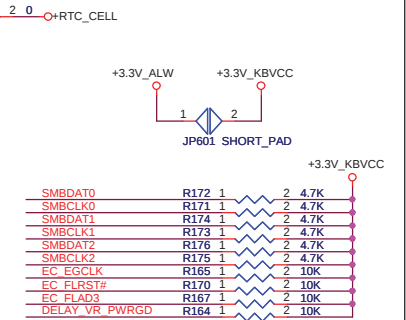
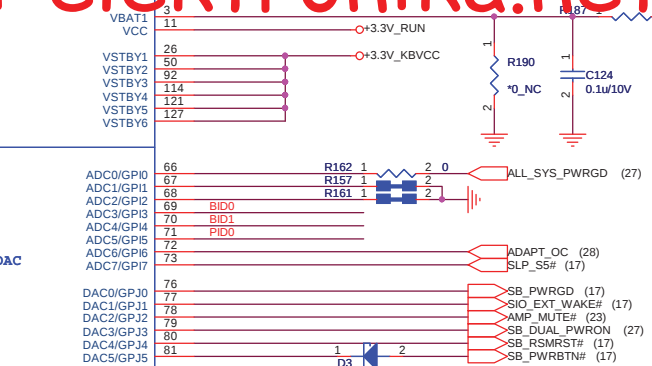
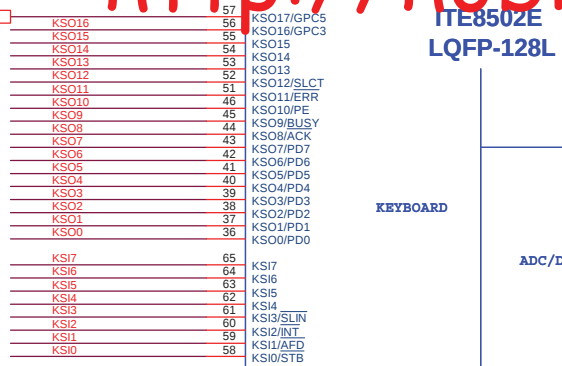
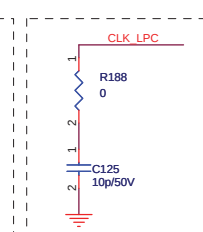
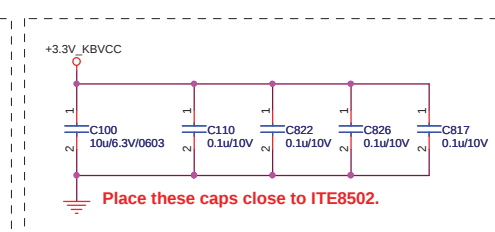
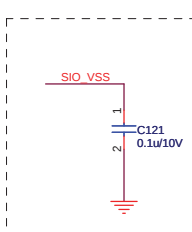
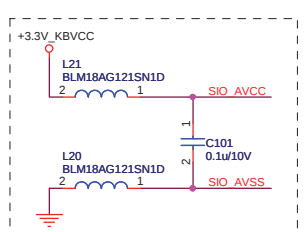
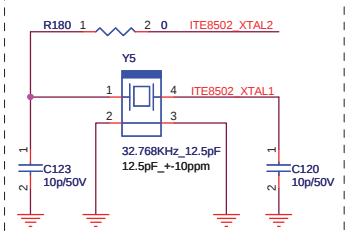
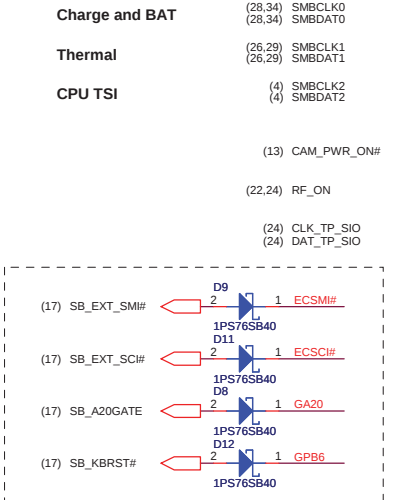
(24) KSO[0..16]
(24) KSI[0..7]

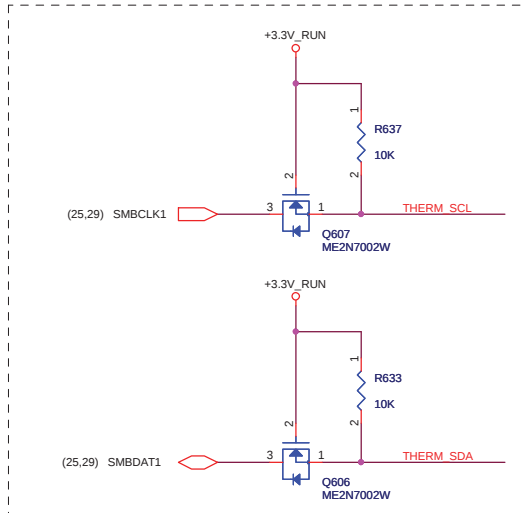
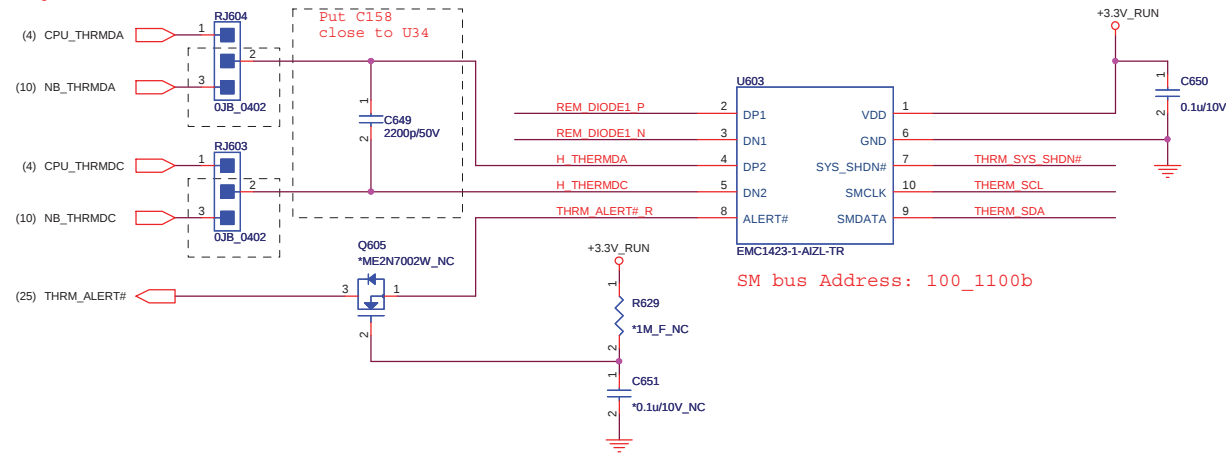
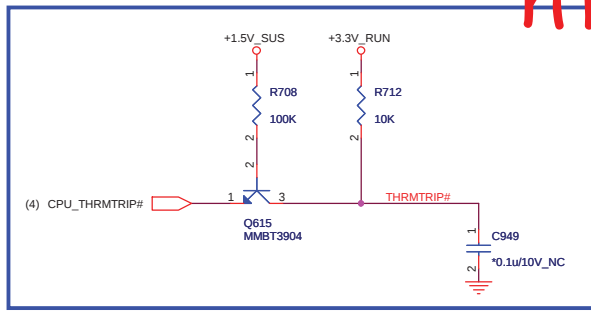


Charge and BAT

Thermal

CPU TSI

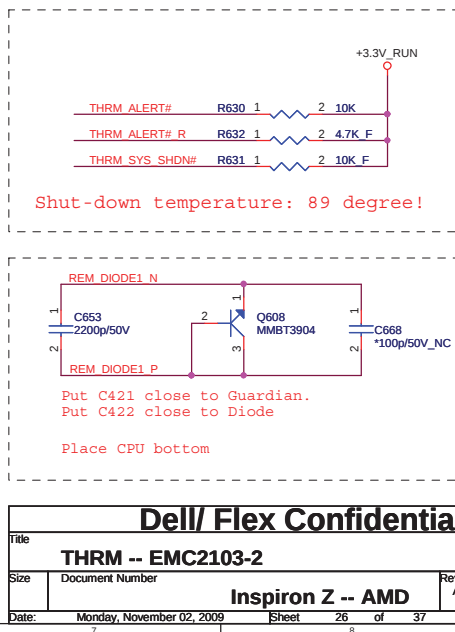
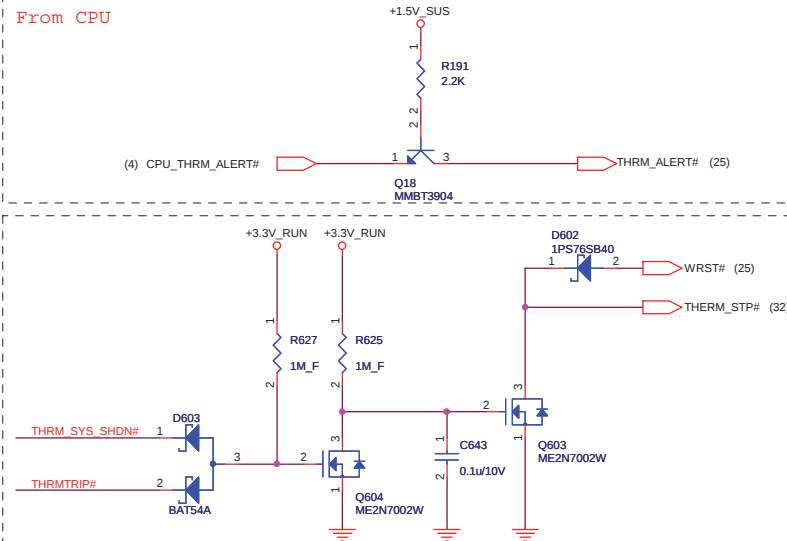
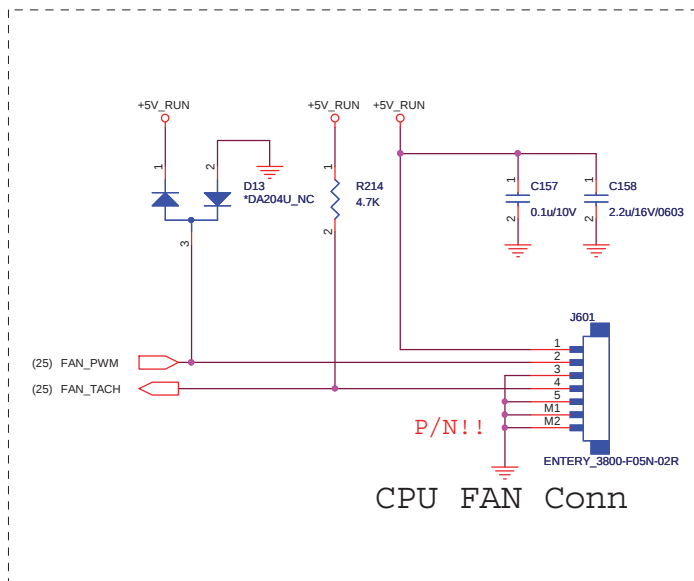


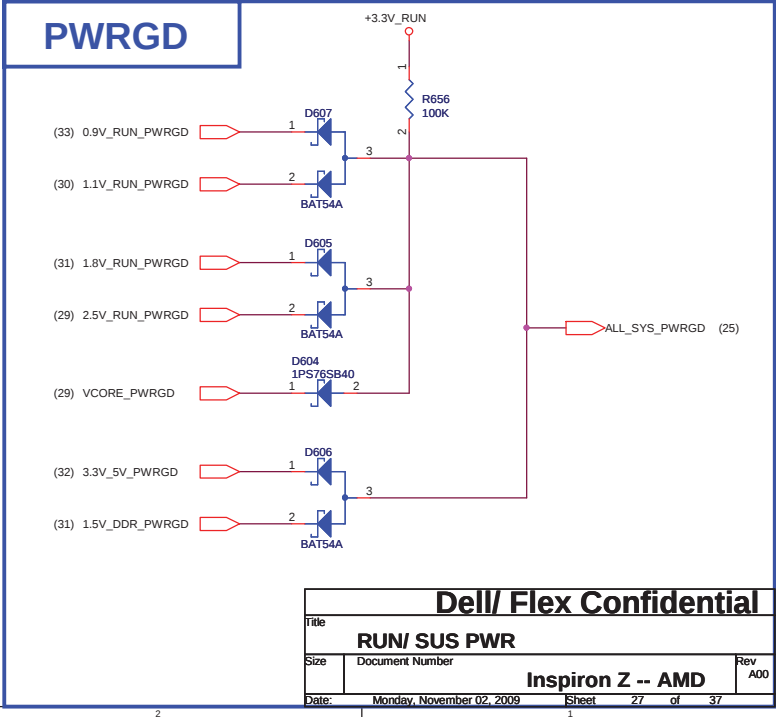


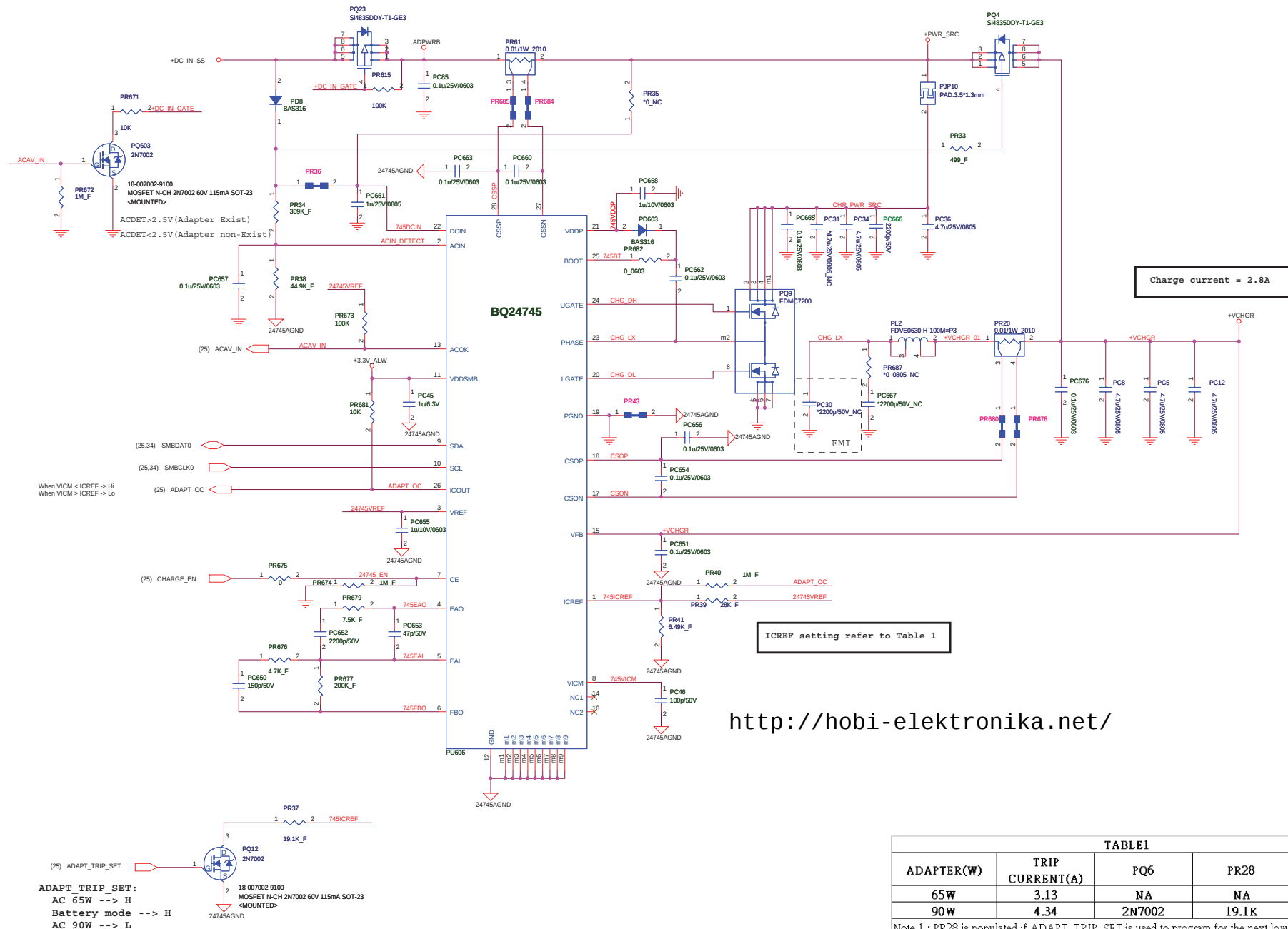
http://hobi-elektronika.net/

Table 5.2 SYS_SHDN Threshold Temperature

SYS_SHD PULL-UP ALERT PULL-UP	4.7K OHM ±10%	6.8K OHM ±10%	10K OHM ±10%	15K OHM ±10%	22K OHM ±10%	33K OHM ±10%
4.7K OHM ±10%	77°C	83°C	89°C	95°C	101°C	107°C
6.8K OHM ±10%	78°C	84°C	90°C	96°C	102°C	108°C
10K OHM ±10%	79°C	85°C	91°C	97°C	103°C	109°C
15K OHM ±10%	80°C	86°C	92°C	98°C	104°C	110°C
22K OHM ±10%	81°C	87°C	93°C	99°C	105°C	111°C
33K OHM ±10%	82°C	88°C	94°C	100°C	106°C	112°C



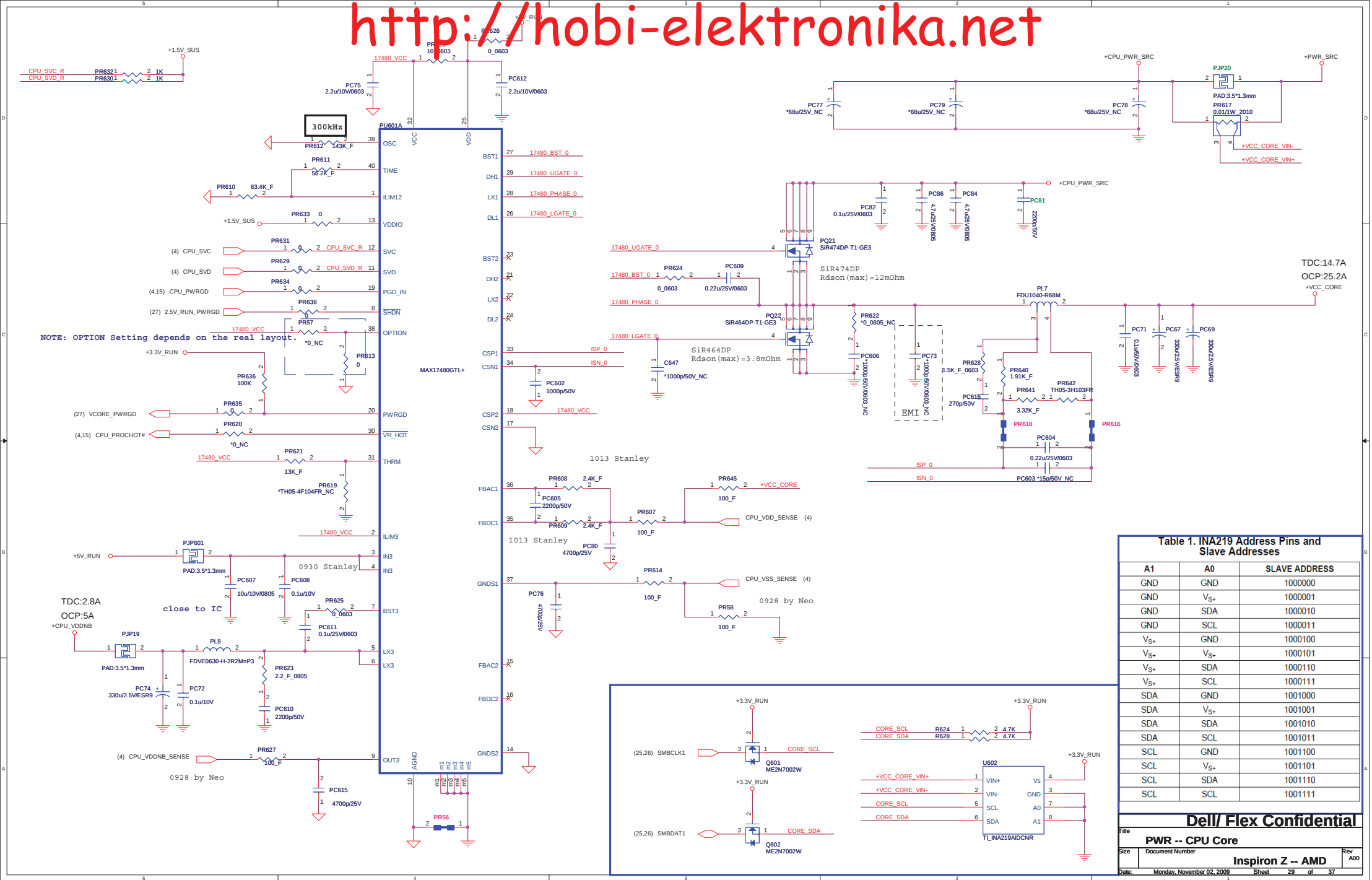




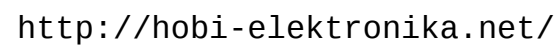
http://hobi-elektronika.net/

TABLE1				
ADAPTER(W)	TRIP CURRENT(A)	PQ6	PR28	PR25
65W	3.13	NA	NA	6.49K
90W	4.34	2N7002	19.1K	10K

Note 1 : PR28 is populated if ADAPT_TRIP_SET is used to program for the next lower adapter.
ADAPT_TRIP_SET is floating for the higher adapter , grounded for the lower adapter.



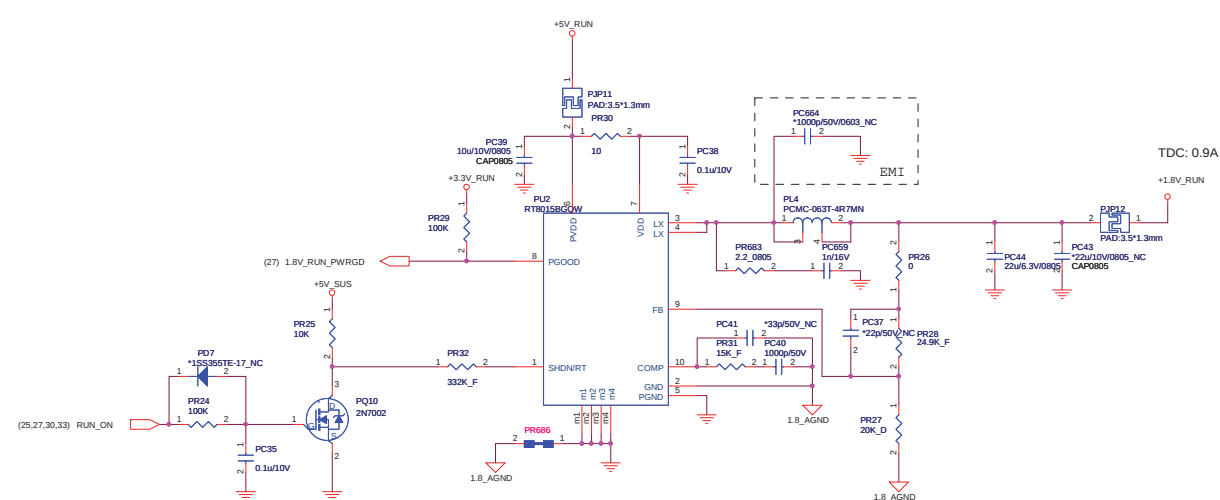
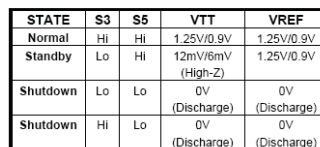
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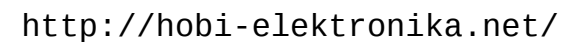


Title			
PWR -- 1.1V_RUN			
Size	Document Number		Rev
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1.8VS

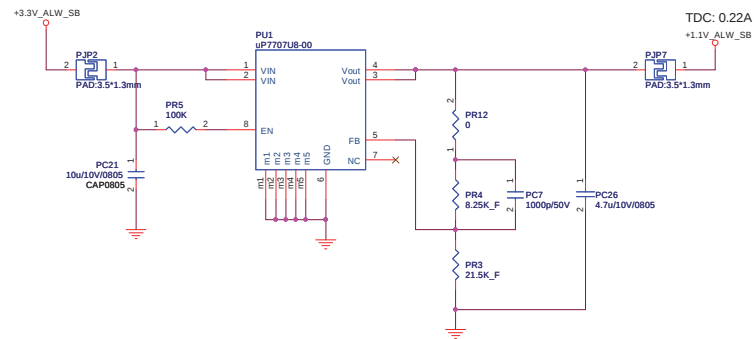




TON	Frequency
GND	OUT1@400K , OUT2@500K
VCC	OUT1@200K , OUT2@300K
OPEN	OUT1@400K , OUT2@300K

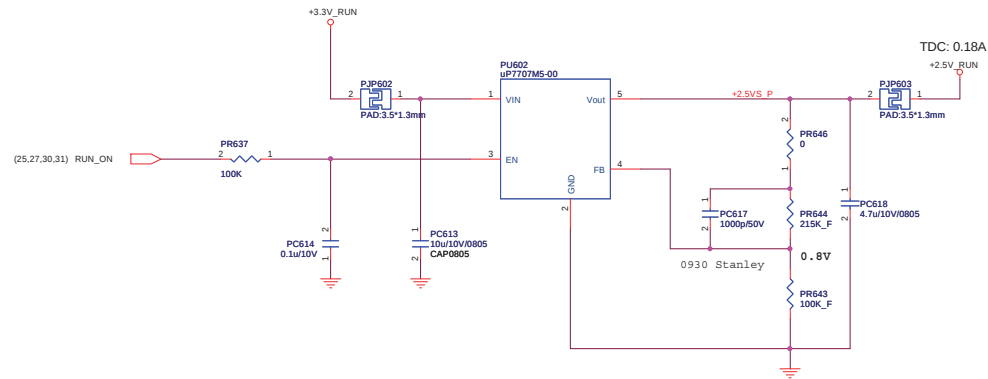
Del// Flex Confidential			
Title PWR -- SYS 5VI 3V			
Size	Document Number		Rev A00
Inspiron Z -- AMD			
Date:	Monday, November 02, 2009	Sheet 32 of 37	

+1.1V_ALW_SB

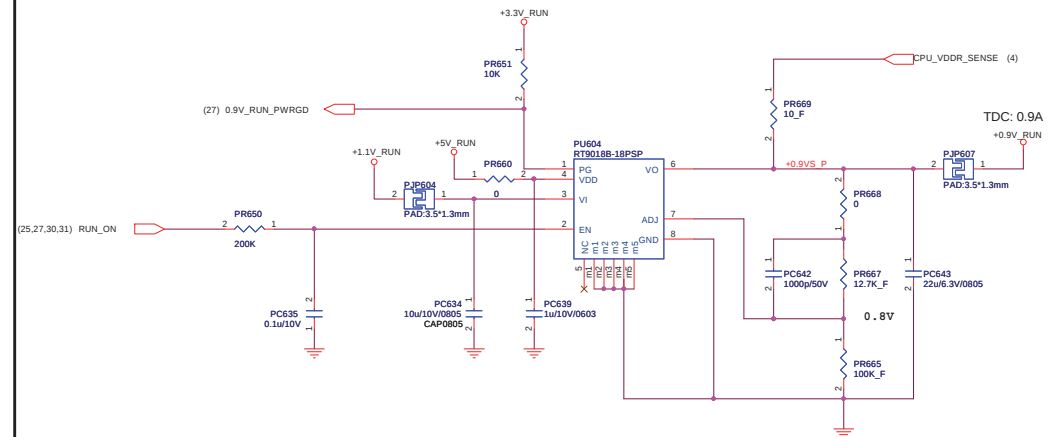


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+2.5V_RUN



+0.9V_RUN



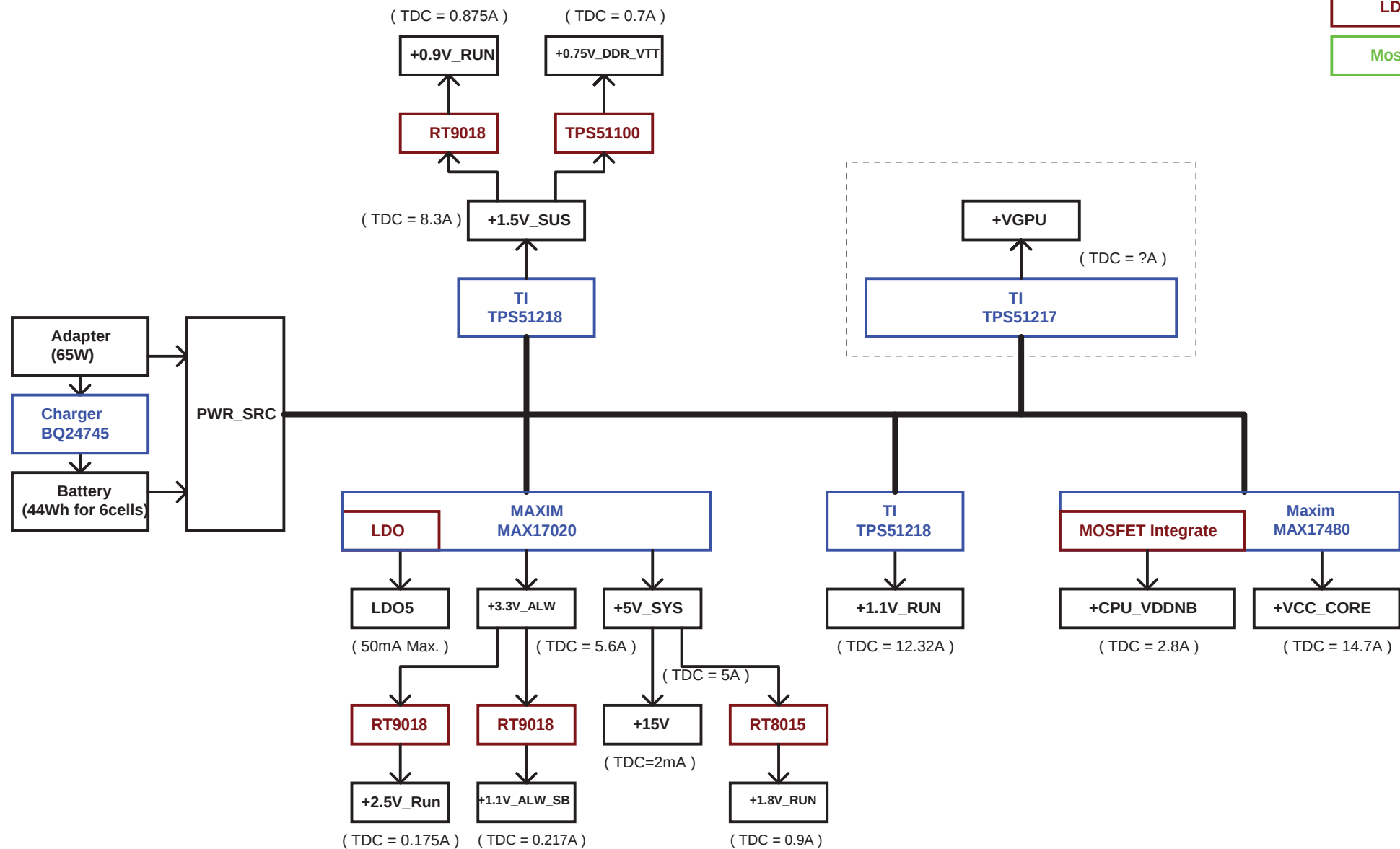
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PWM Switching

LDO

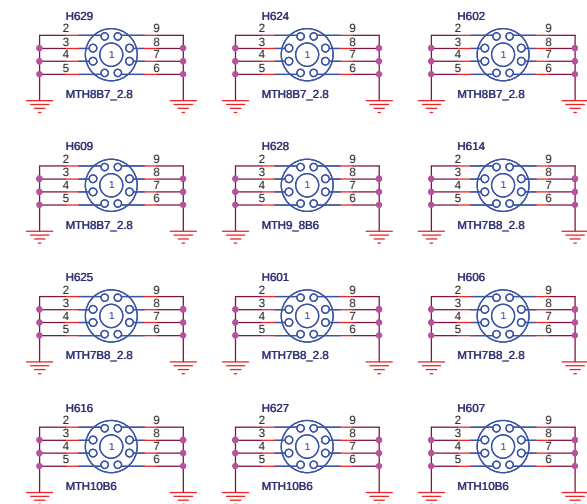
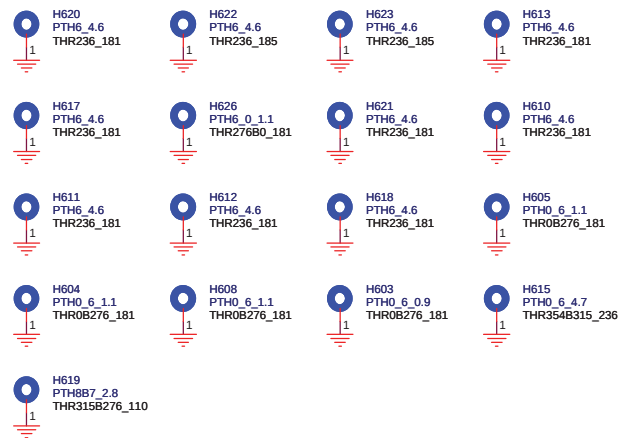
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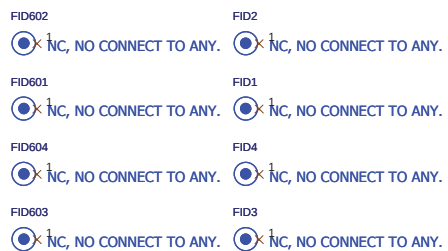
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Cover Sheet			
Size	Document Number		Rev
	M11z		A00
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Screw Hole

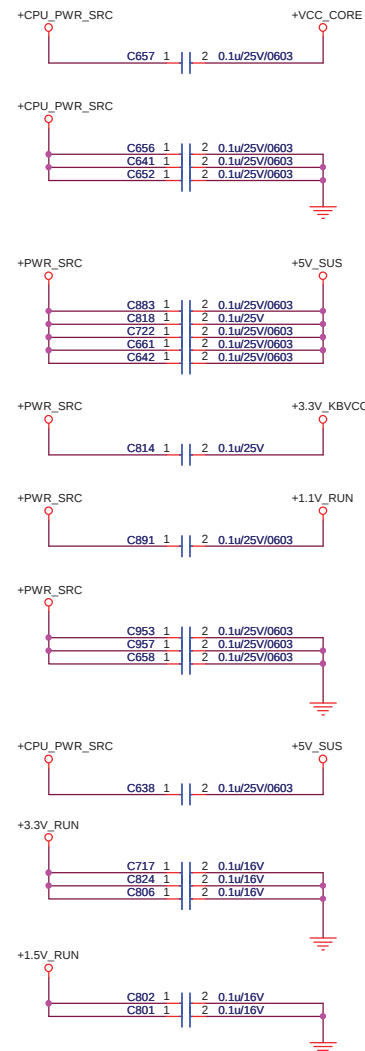
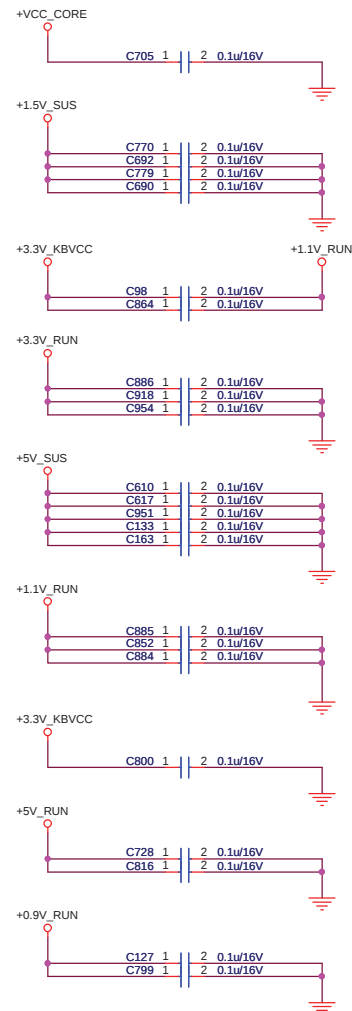
<http://hobi-elektronika.net>



FID



EMI Solution



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Title			
Screw Hole/ EMI/ Unused Gates			
Size	Document Number	Inspiron Z -- AMD	Rev A00
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Item	Fixed Issue	Reason for Change	Rev	PG#	Modify List	B Ver#	Phase
1			X00		X00 Schematic Official Release	X00	SSI

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Title		
History - 1		
Size	Document Number	Rev
	Inspiron Z -- AMD	A00
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