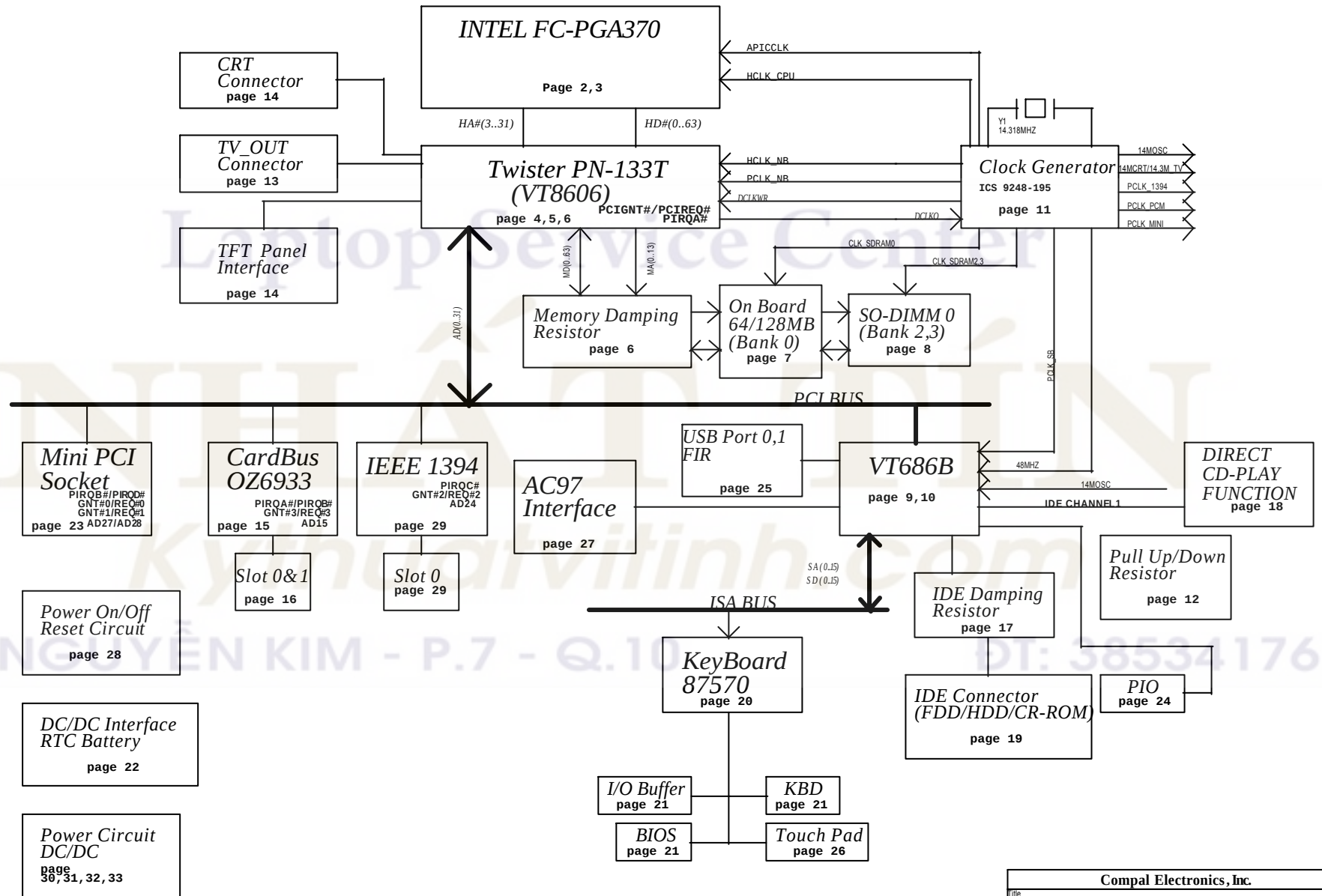


Model Name : CY23 LA-1281 Rev:2.0

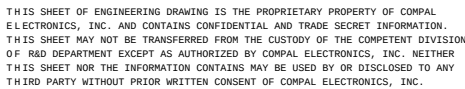


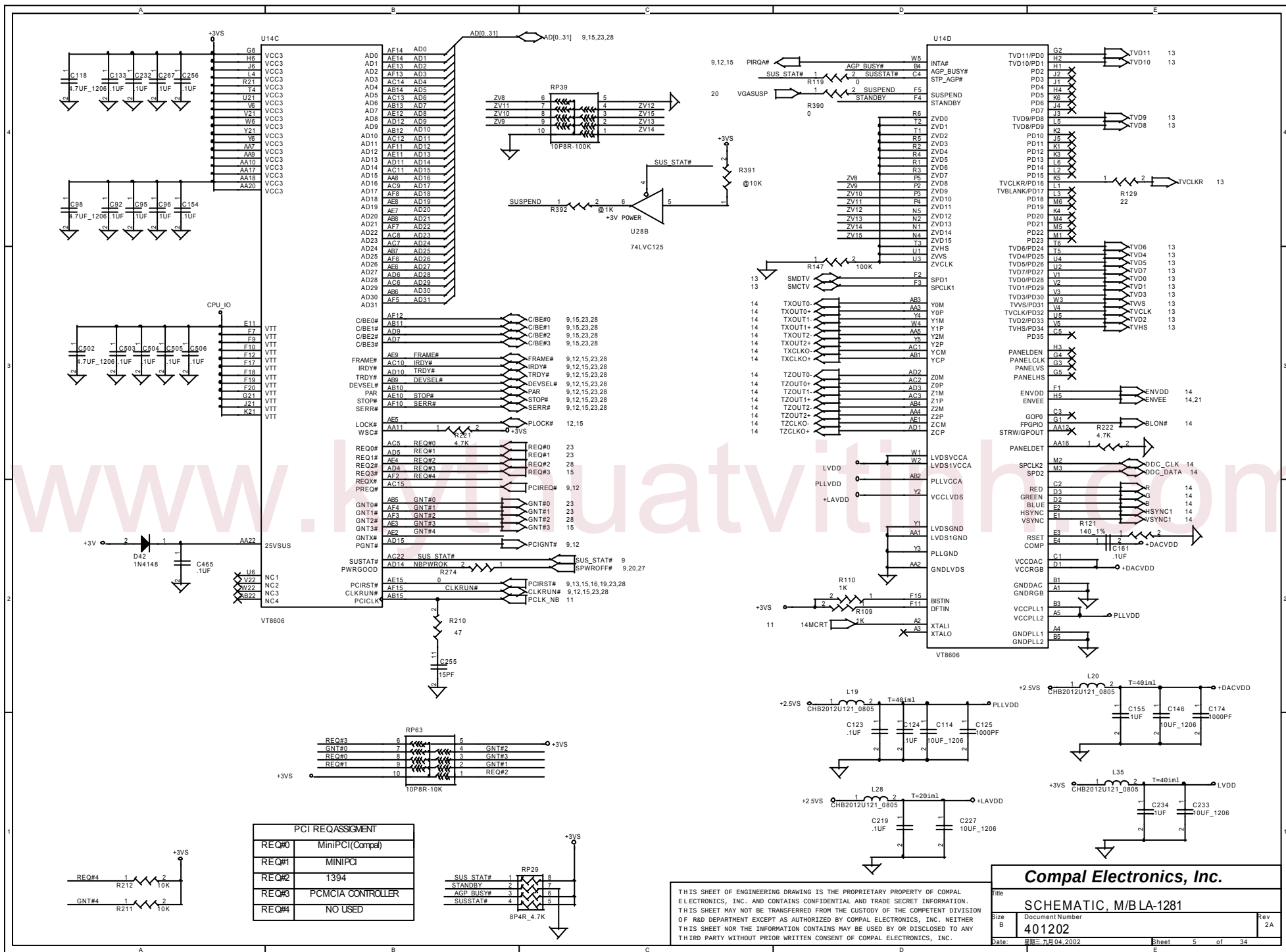
PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

| Compal Electronics, Inc. |                 |       |         |
|--------------------------|-----------------|-------|---------|
| Title                    |                 |       |         |
| SCHEMATIC, M/BLA-1281    |                 |       |         |
| Size                     | Document Number | Rev   |         |
| B                        | 401202          | 2A    |         |
| Date                     | 2002.04.04      | Sheet | 1 of 34 |

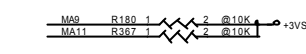
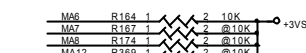
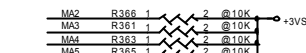
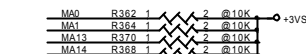
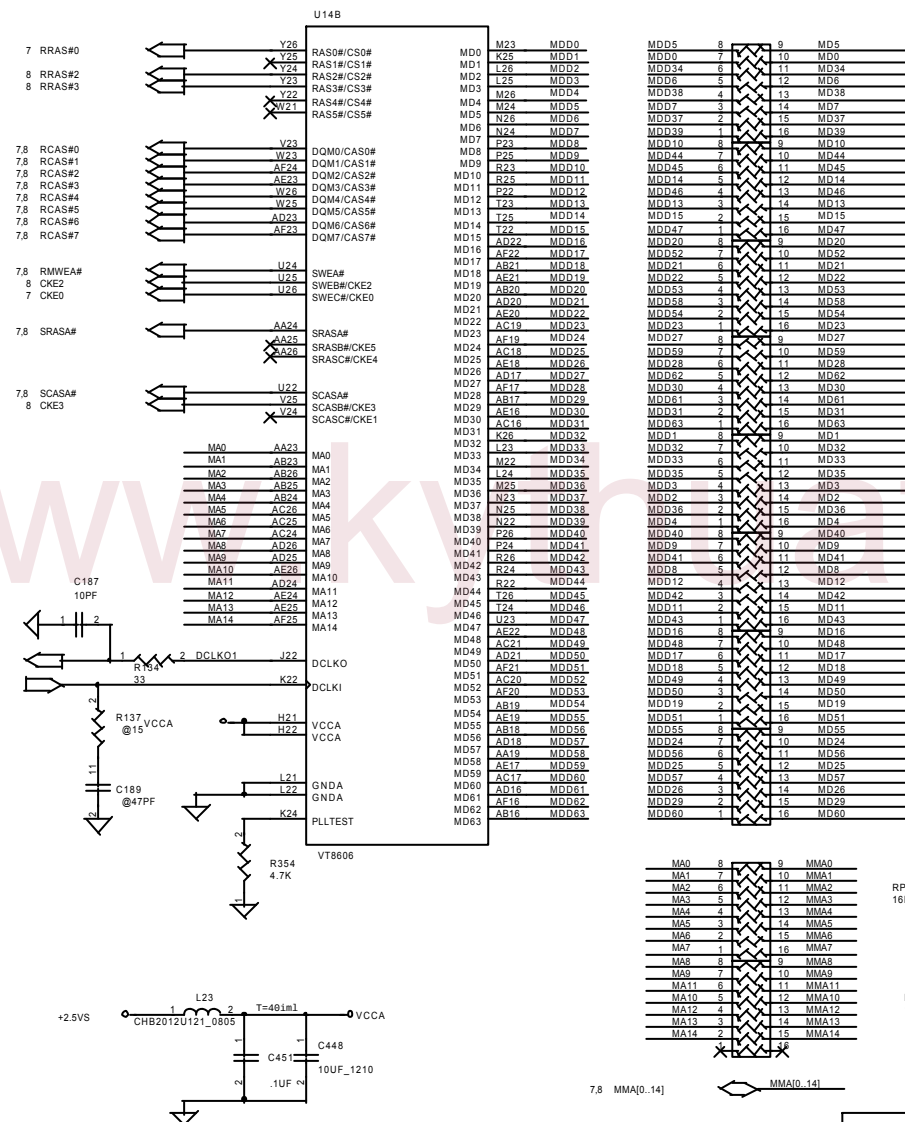








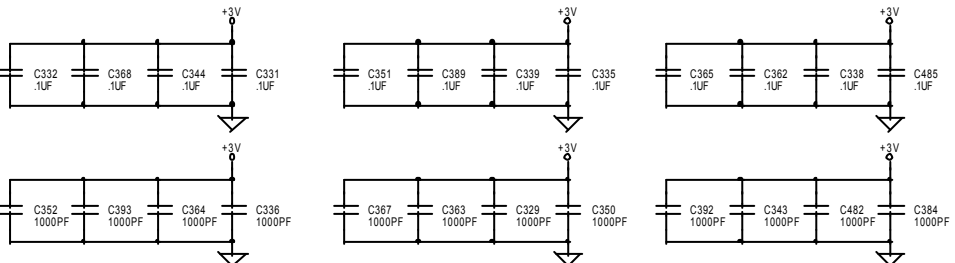
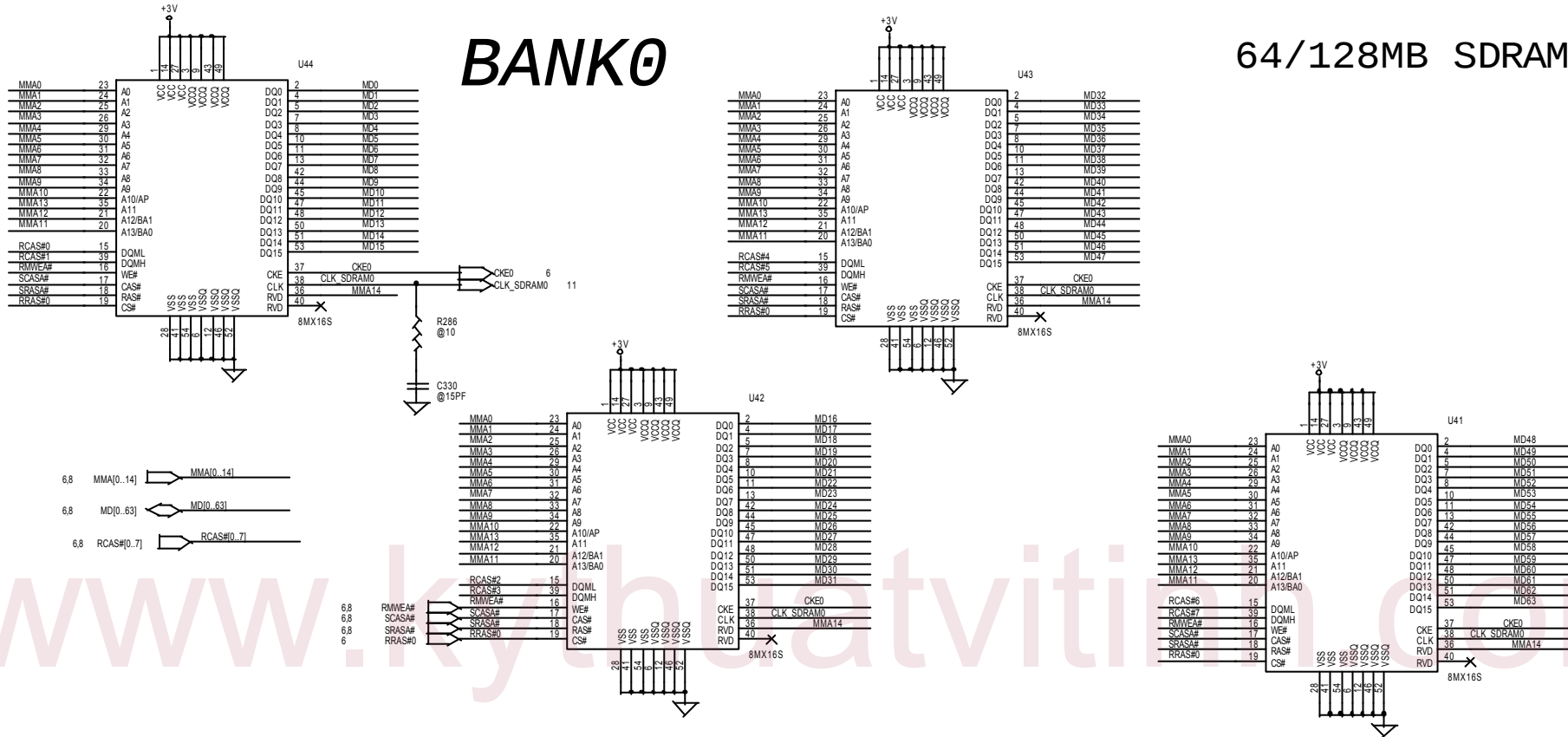
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



| Strap          | Description               | Setting                                      |
|----------------|---------------------------|----------------------------------------------|
| MA12, 8        | CPU Clock Frequency       | 0=100MHz<br>1=133MHz<br>2=150MHz<br>3=166MHz |
| MA2            | PCI Base Address Mapping  | 0=Map0<br>1=Map1                             |
| MA3            | Graphic IO Enable/Disable | 0=Enable<br>1=Disable                        |
| MA4            | PCI Interrupt             | 0=Enable<br>1=Disable                        |
| MA7            | Graphic Test Mode         | 0=Normal<br>1=Test                           |
| MA9            | VGA Clock Select          | 0=PLL<br>1= External                         |
| MA11           | IOQ Level                 | 0=Level0<br>1=Level1                         |
| MA0, 1, 13, 14 | Panel Type                |                                              |

# BANK0

## 64/128MB SDRAM

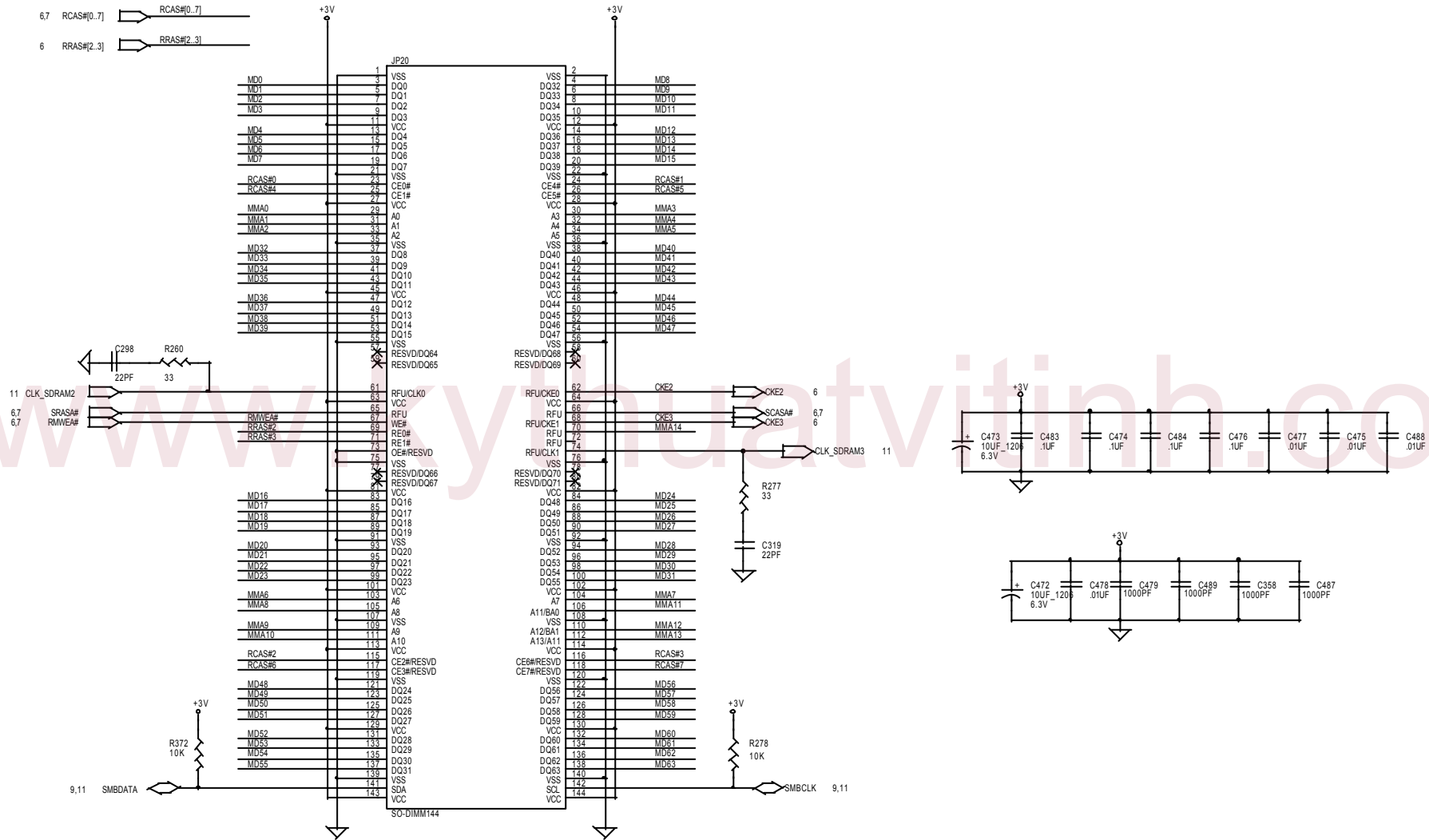
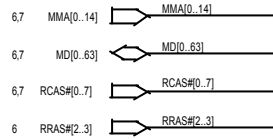


PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|                          |            |        |
|--------------------------|------------|--------|
| Compal Electronics, Inc. |            |        |
| SCHEMATIC, M/B LA-1281   |            |        |
| Doc Number               | 401202     |        |
| Date                     | 2002.04.04 | Rev 2A |
| Sheet                    | 7          | of 34  |

SO-DIM 144 PINS  
RAM MODULE CONN.

**BANK2/3**



**DIMM1**

PROPRIETARY NOTE

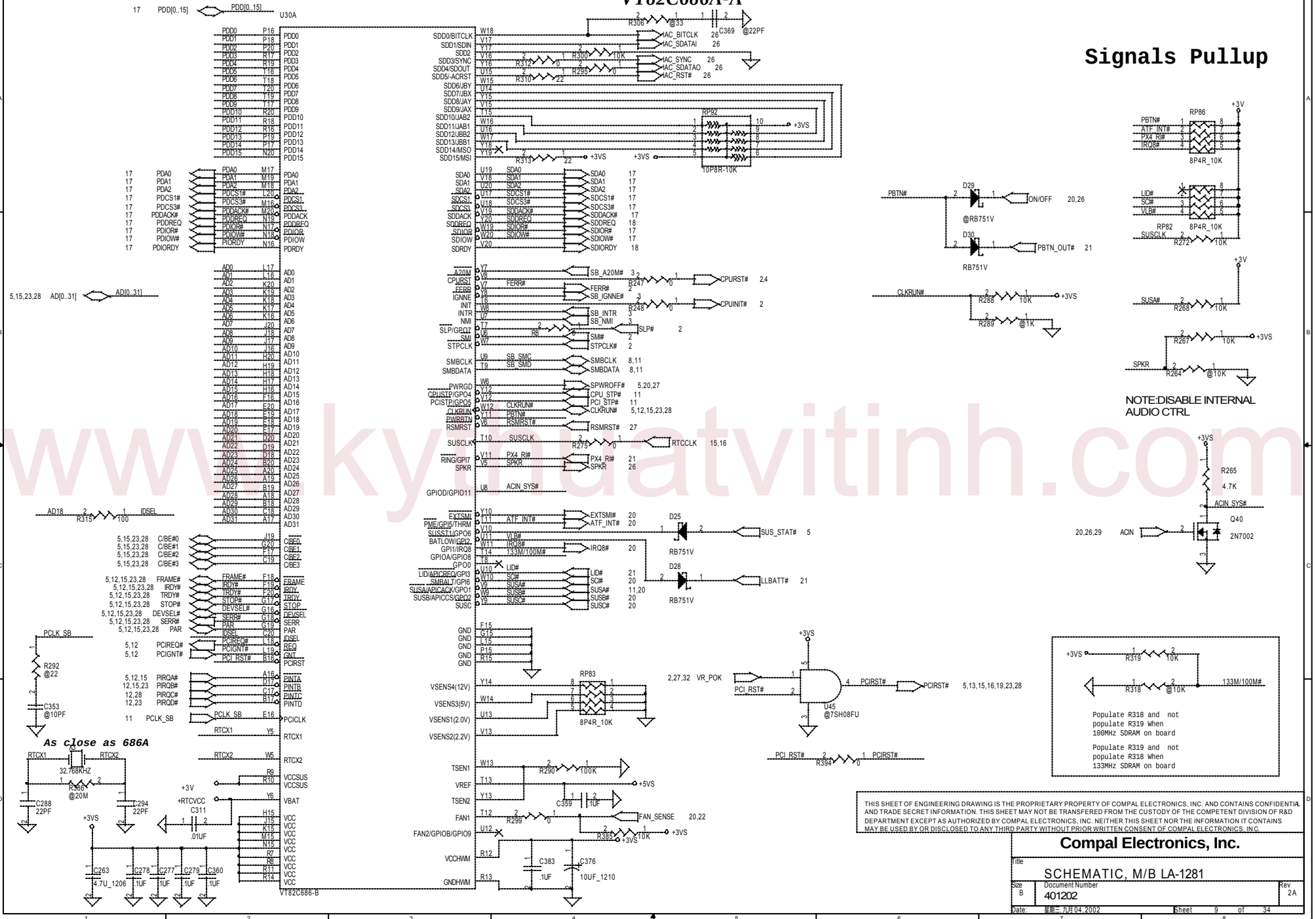
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

**Compal Electronics, Inc.**

SCHEMATIC, M/B LA-1281

| Rev                   | Document Number | Rev |
|-----------------------|-----------------|-----|
| B                     | 401202          | 2A  |
| Date: 星期三, 九月04, 2002 | Sheet 8 of 34   |     |

## Signals Pullup



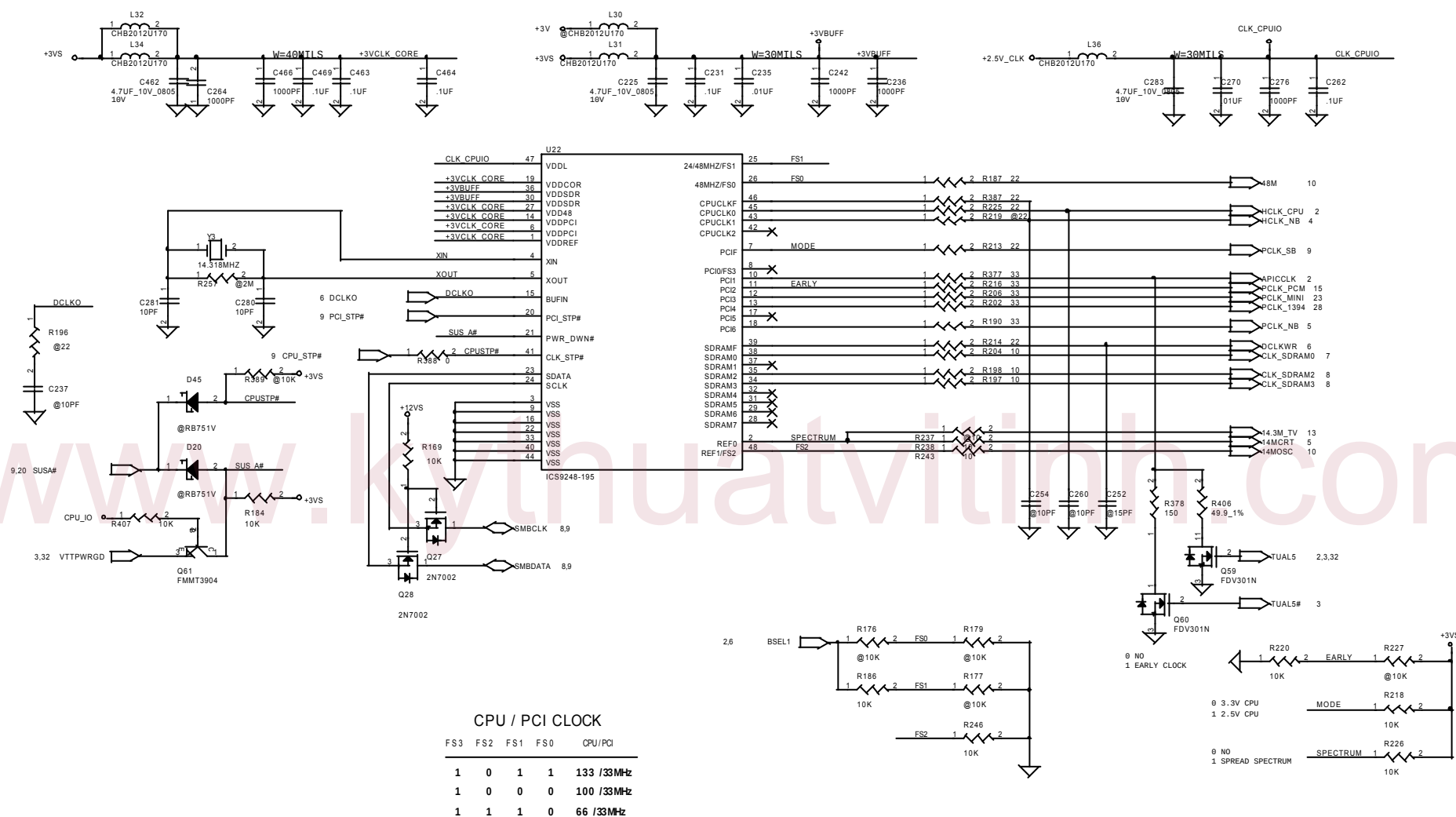
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

**Compal Electronics, Inc.**

|                          |                           |                        |  |
|--------------------------|---------------------------|------------------------|--|
| Title                    |                           | SCHEMATIC, M/B LA-1281 |  |
| Size<br>B                | Document Number<br>401202 | Rev<br>2A              |  |
| Date<br>星期二, 九月 04, 2002 | Sheet<br>9 of 34          |                        |  |



## CLOCK GENERATOR & BUFFER



**Compal Electronics, Inc.**

|       |                        |
|-------|------------------------|
| Title | SCHEMATIC, M/B LA-1281 |
|-------|------------------------|

|      |                 |
|------|-----------------|
| Size | Document Number |
|      | 401202          |

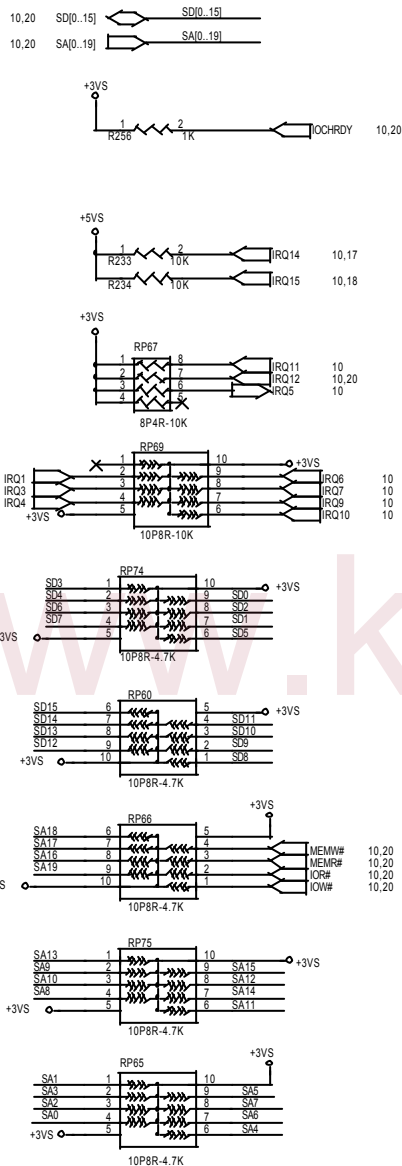
Date: 星期三, 九月 04, 2002

Rev  
2A

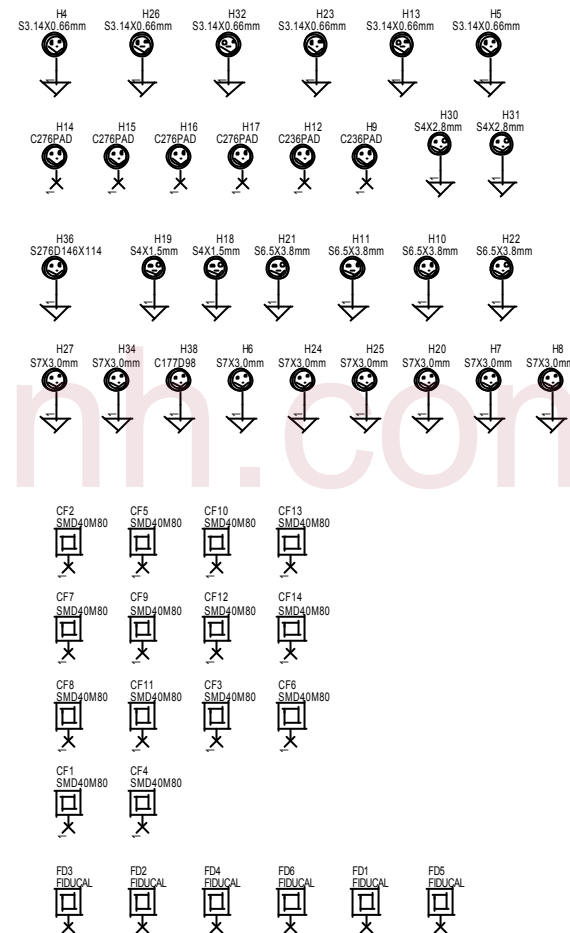
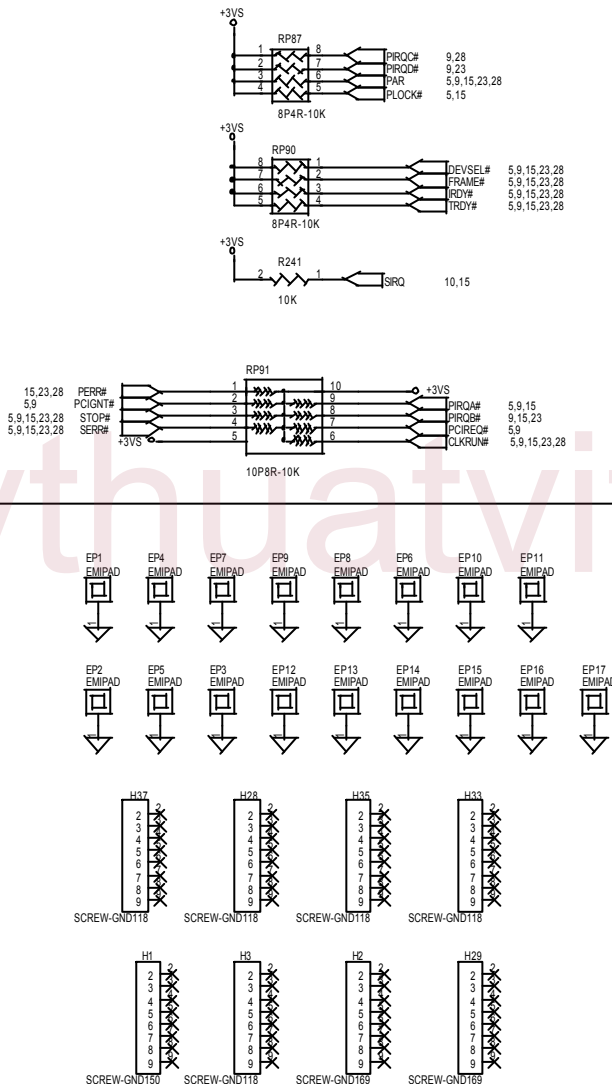
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|       |                  |       |    |    |    |
|-------|------------------|-------|----|----|----|
| Date: | 星期三, 九月 04, 2002 | Sheet | 11 | of | 34 |
|-------|------------------|-------|----|----|----|

## ISA BUS Pullup



## PCI BUS Pullup



PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

SCHEMATIC, M/B LA-1281

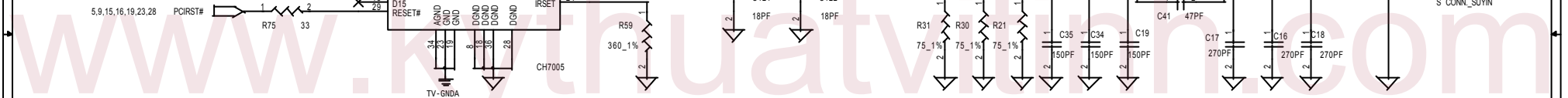
Document Number

401202

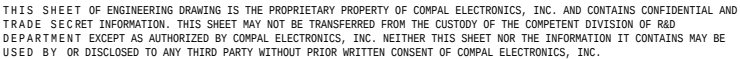
Date: 星期三, 2002年04月04日

Sheet 12 of 34

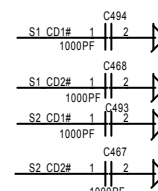
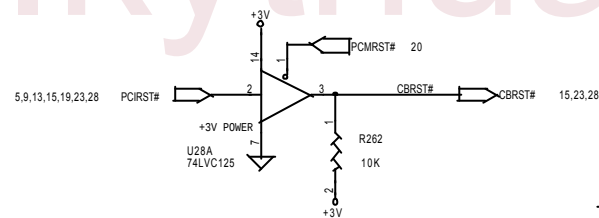
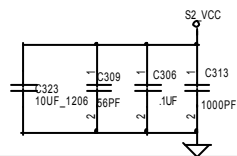
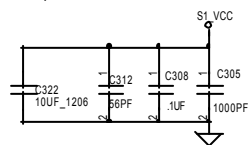
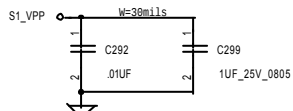
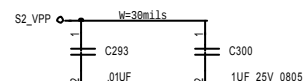
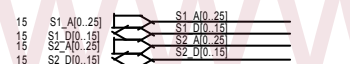
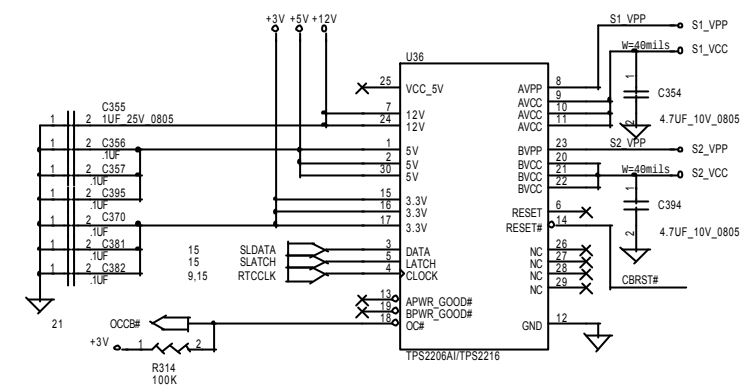
Rev 2A



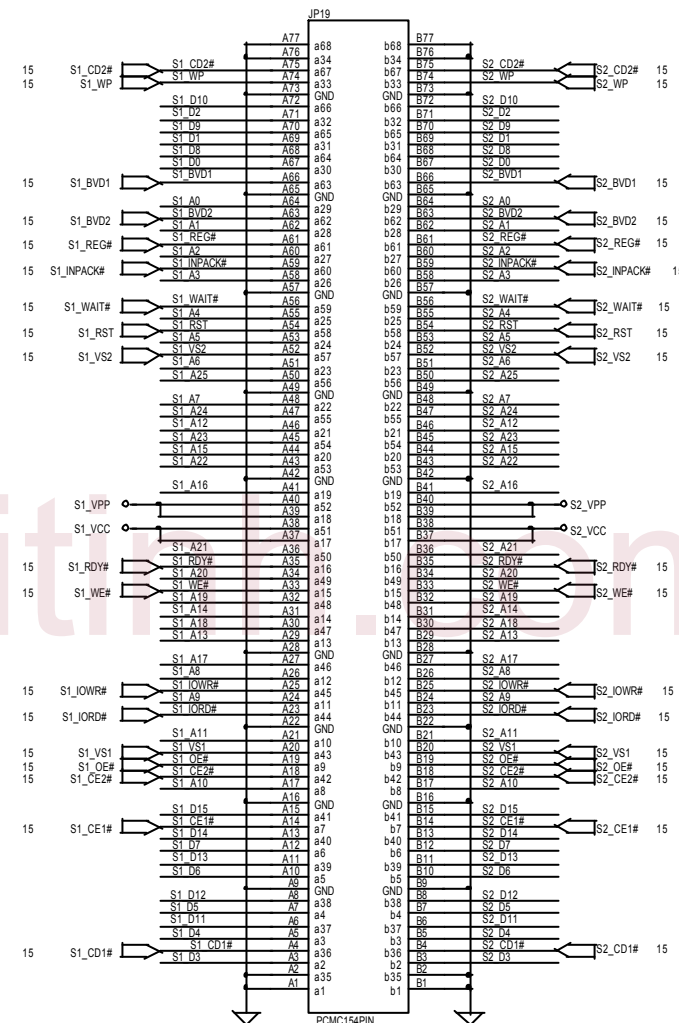




PCMCIA POWER CTRL.    Wire ZV PORT to Slot A



## CARDBUS SOCKET



**Compal Electronics, Inc.**

SCHEMATIC, M/B LA-1281

Document Number  
401202

星期三, 九月 04, 2002

---

Sheet 16 of 34

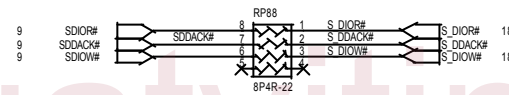
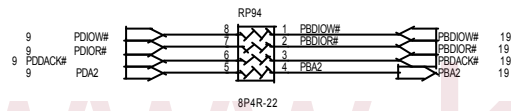
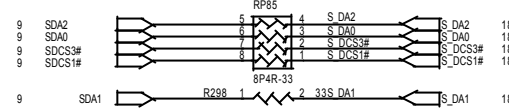
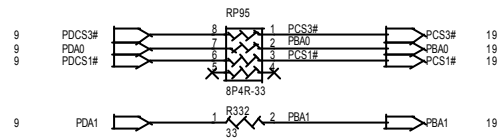
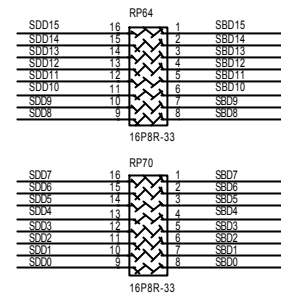
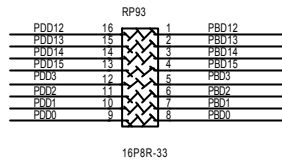
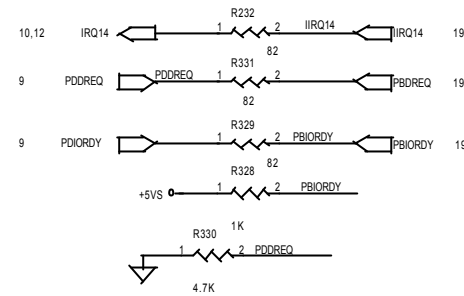
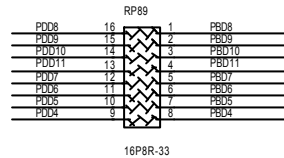
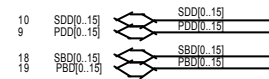
REV  
2A

|  |  |
|--|--|
|  |  |
|  |  |

---

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

**IDE Series Resistor**  
**Place them close SB**



www.kythuatvitinh.com

**Compal Electronics, Inc.**

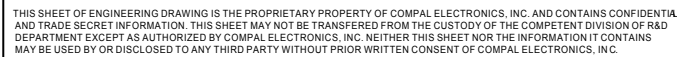
SCHEMATIC, M/B LA-1281

Document Number  
**401202**

Rev  
2A

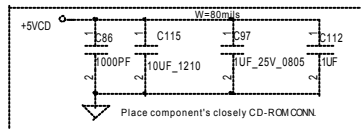
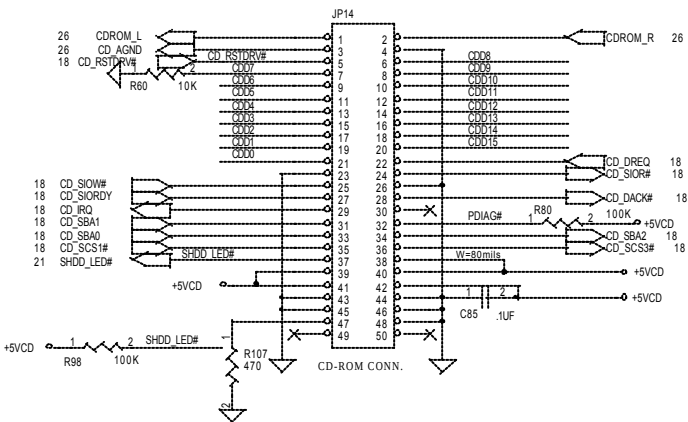
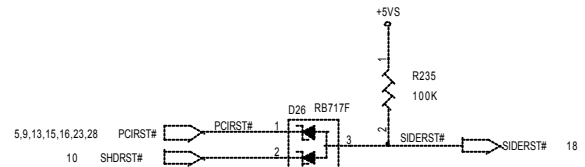
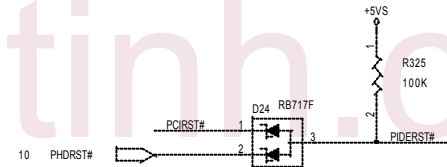
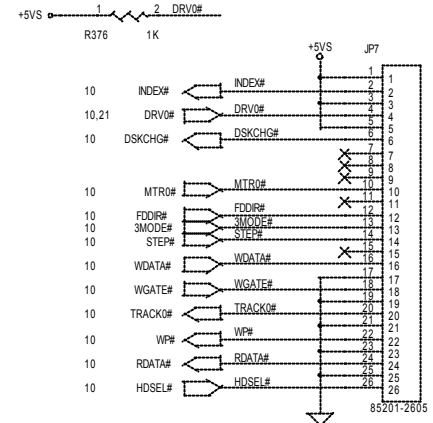
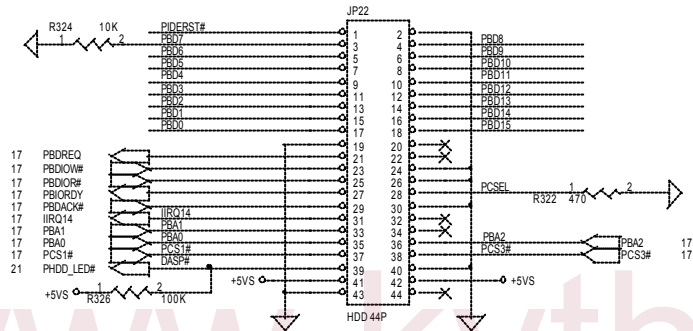
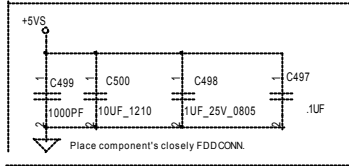
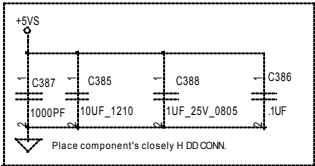
Date: 2002.04.04 Sheet 17 of 34

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



|  |  |
|--|--|
|  |  |
|  |  |

IDE,CD-ROM & FDD Module CONN.



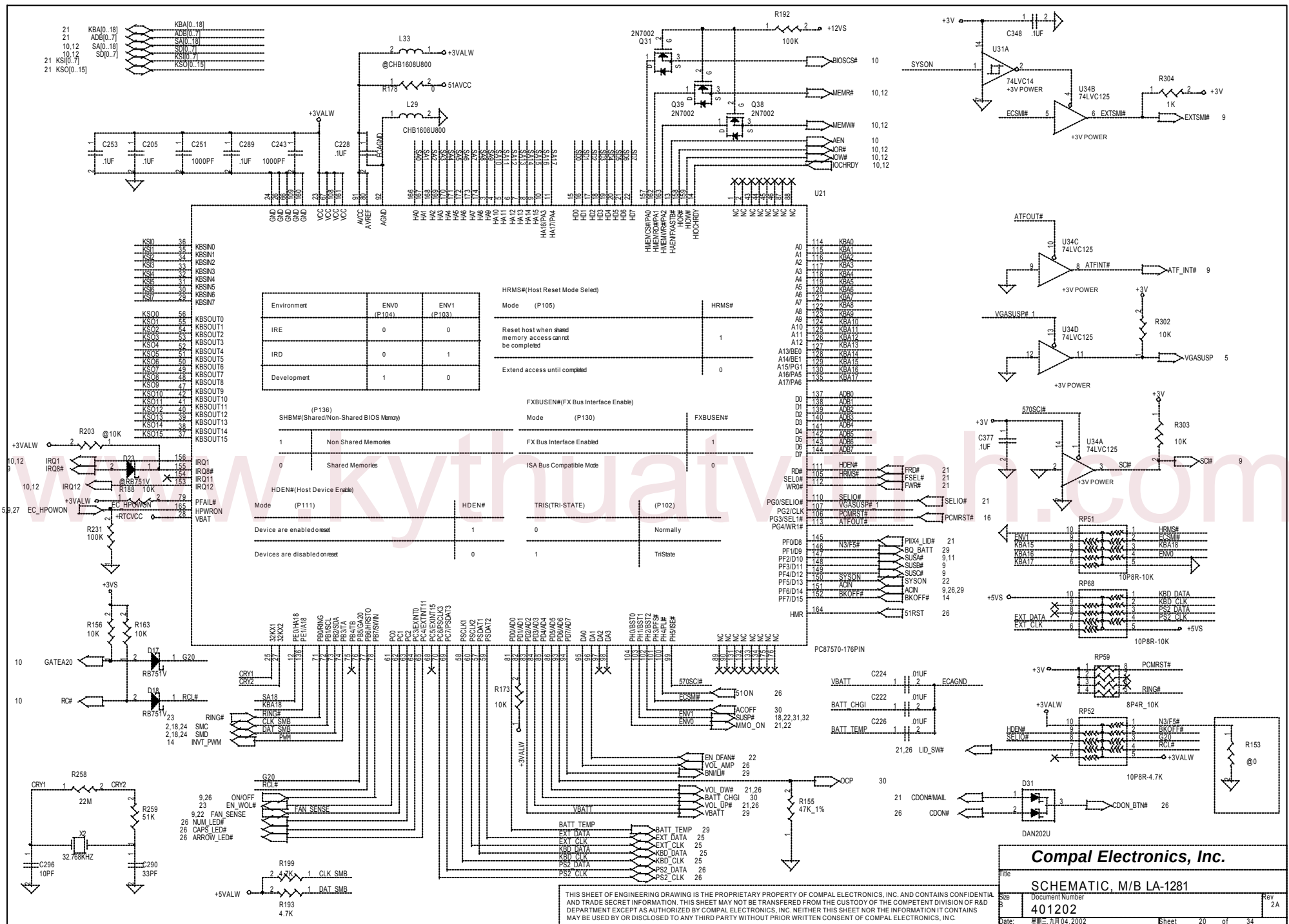
**Compal Electronics, Inc.**

SCHEMATIC, M/B LA-1281

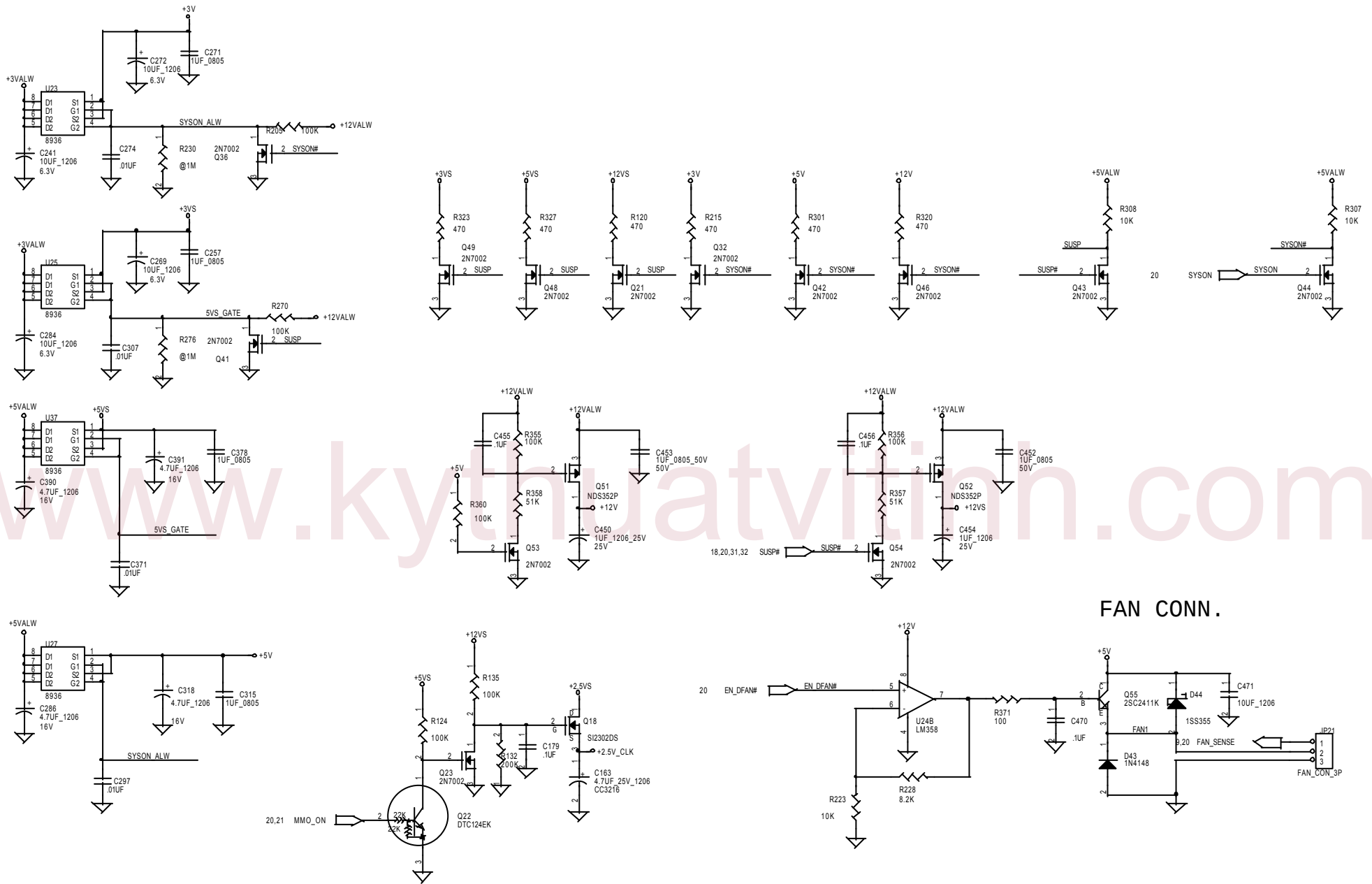
Document Number  
401202

星期三, 九月 04, 2002 Sheet 19 of 34

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.







# FAN CONN.

Compal Electronics, Inc.

SCHEMATIC, M/B LA-1281

Document Number

401202

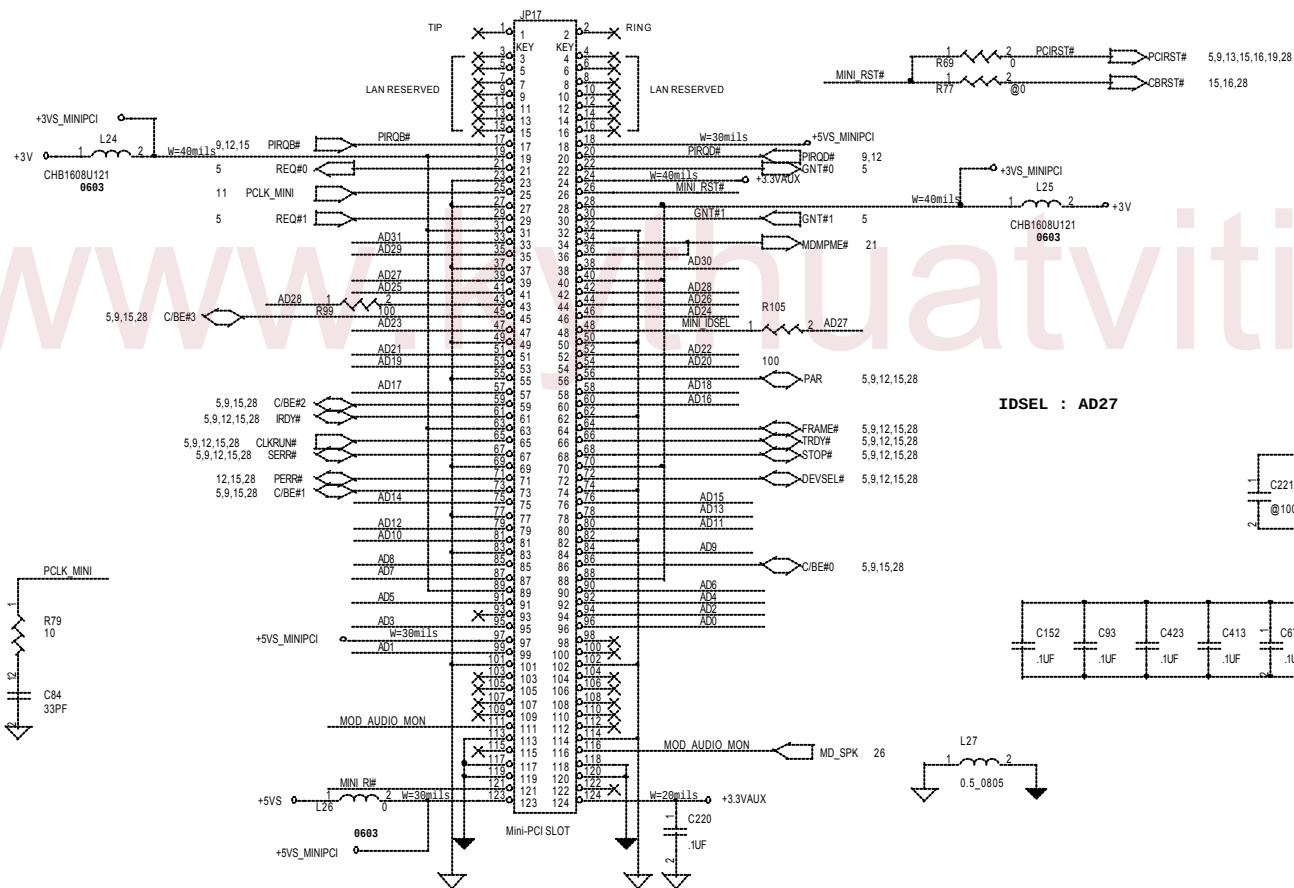
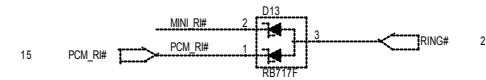
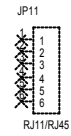
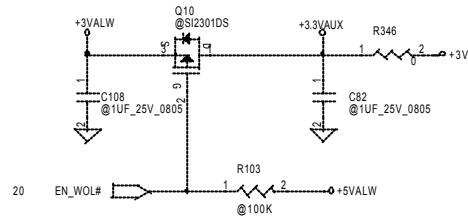
Rev

2A

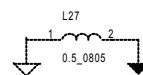
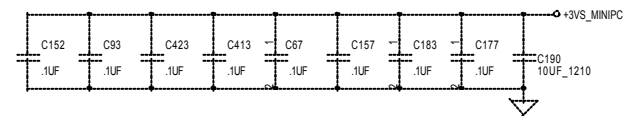
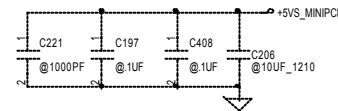
Date: 2002.04.04

Sheet 22 of 34

PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



IDSEL : AD27



Compal Electronics, Inc.

SCHEMATIC, M/B LA-1281

Document Number

401202

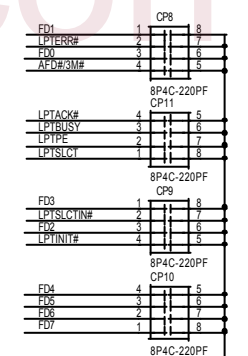
Date: 2002.04.04

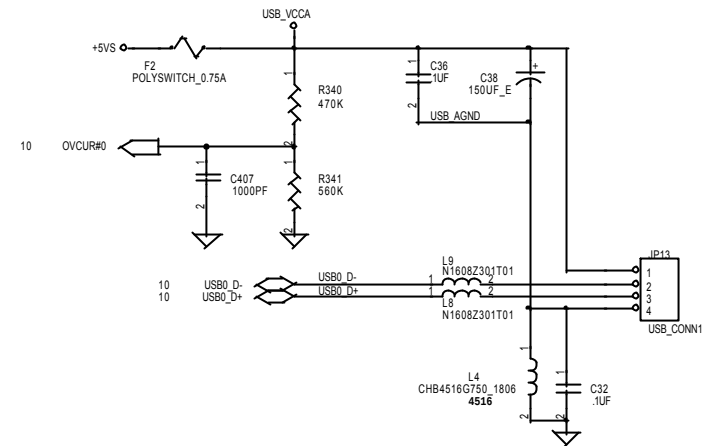
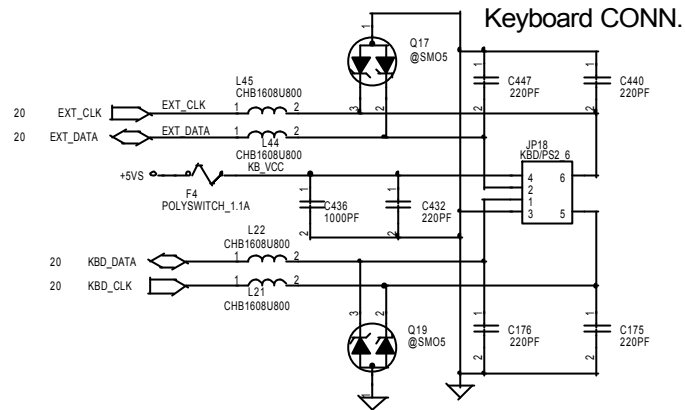
Sheet 23 of 34

Rev 2A

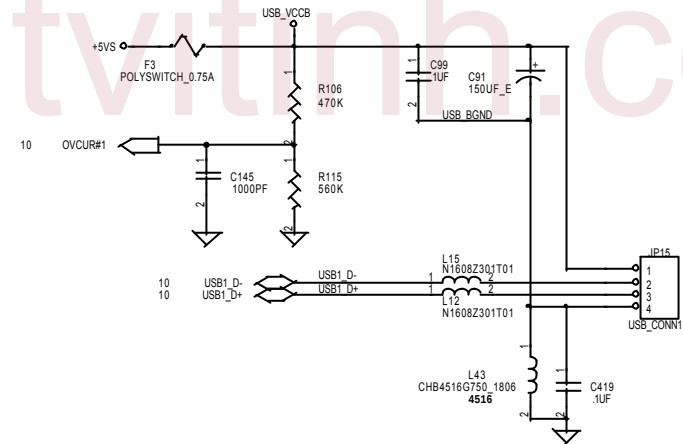
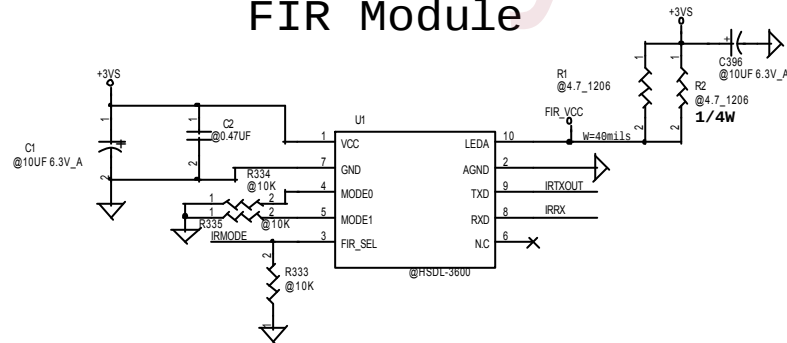
PROPRIETARY NOTE

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.





## FIR Module



**Compal Electronics, Inc.**

**SCHEMATIC, M/B LA-1281**

Document Number

401202

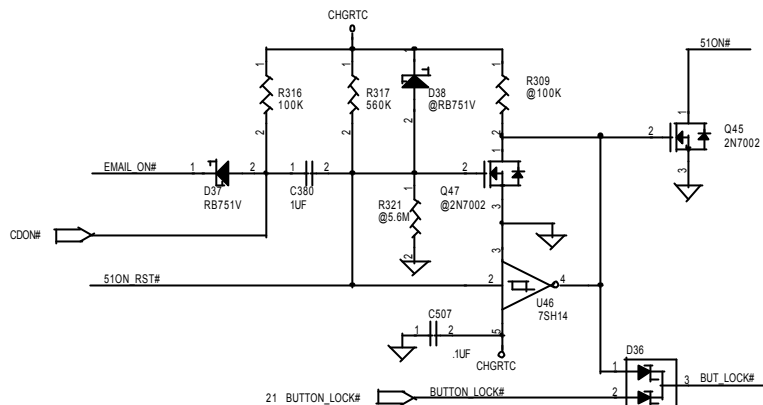
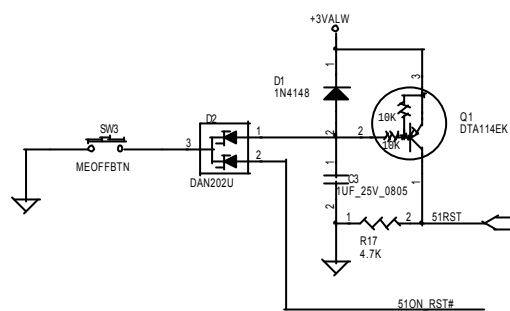
Date: 2002.04.04

Sheet 25 of 34

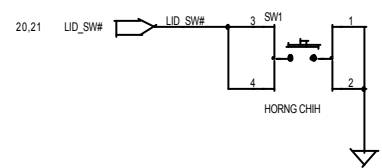
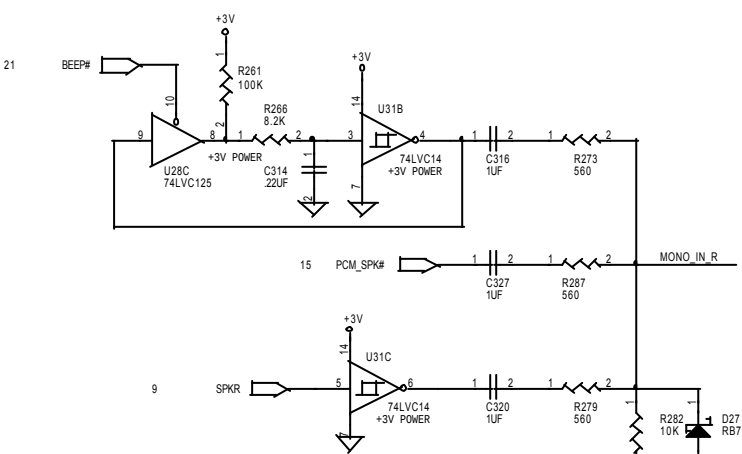
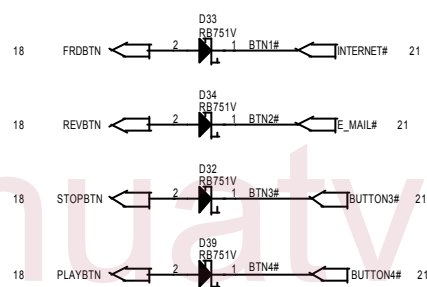
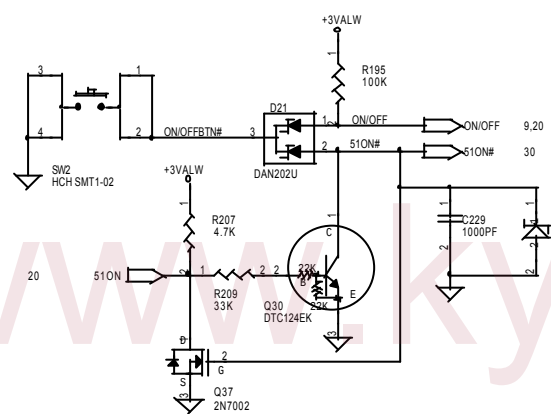
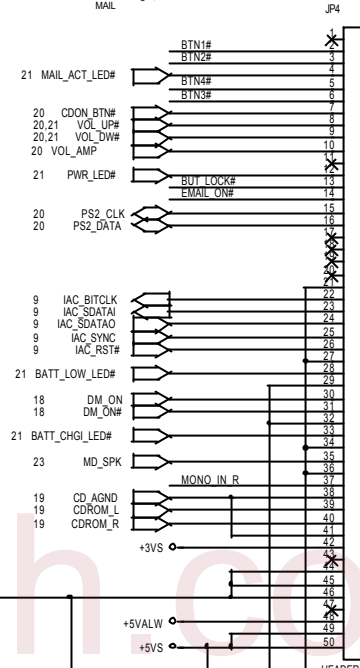
Rev 2A

PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

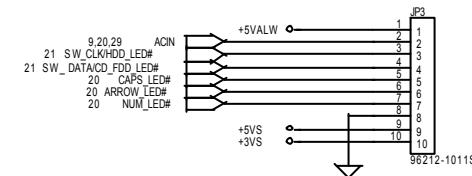
## Reset Button



CDON\_BTN# is Low, enable CD\_PLAY  
MAIL is High, enable MAIL



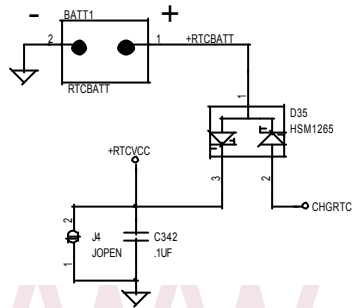
## System Window Connector



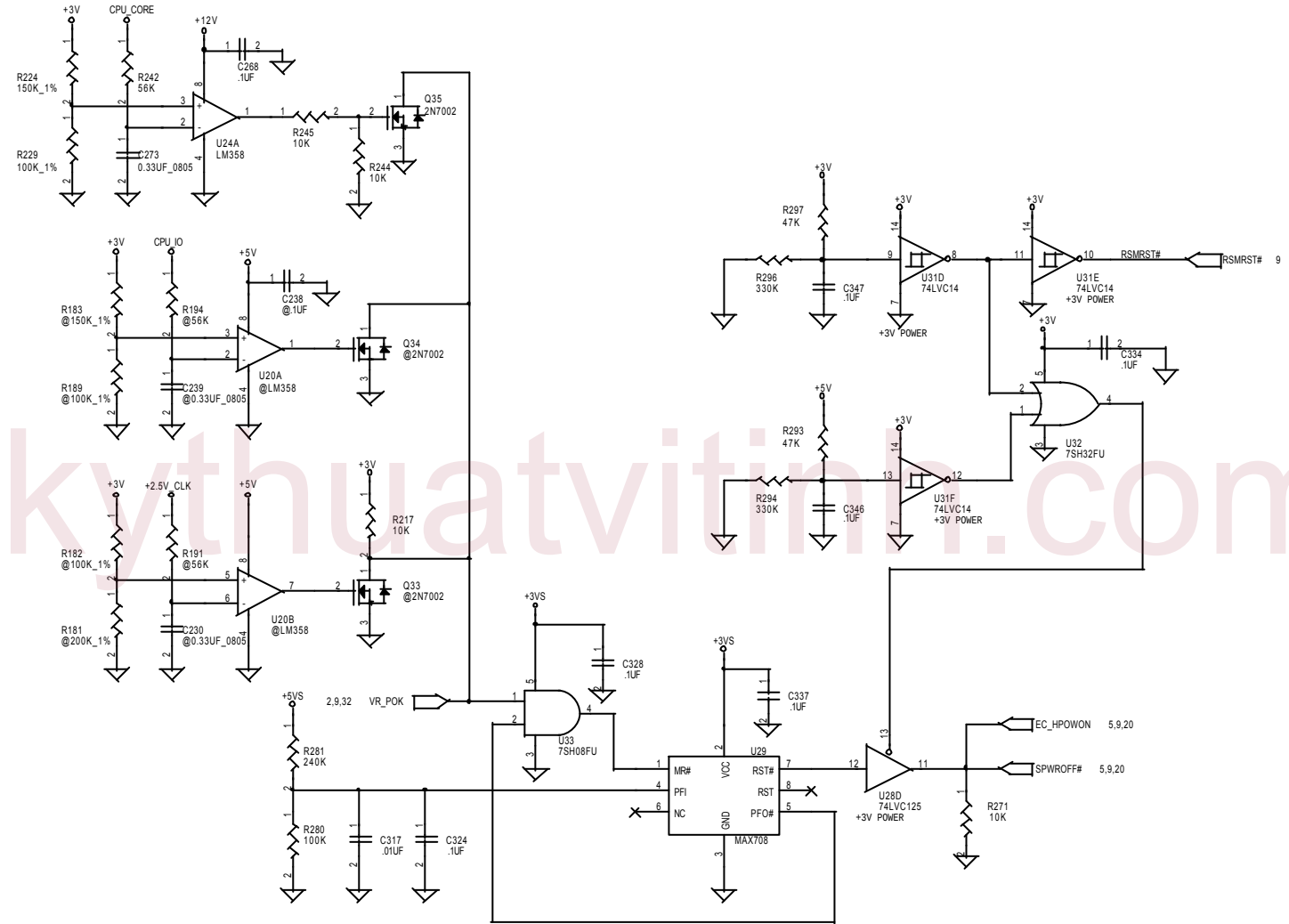
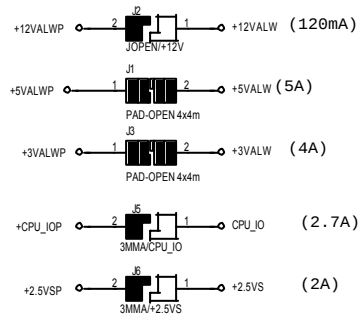
PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

|                          |          |        |
|--------------------------|----------|--------|
| Compal Electronics, Inc. |          |        |
| SCHEMATIC, M/B LA-1281   |          |        |
| Doc Number               | 401202   | Rev 2A |
| Date: 2002.04.04         | Sheet 26 | of 34  |

## RTC BATT



## DC/DC INTERFACE



Compal Electronics, Inc.

SCHEMATIC, M/B LA-1281

Document Number

401202

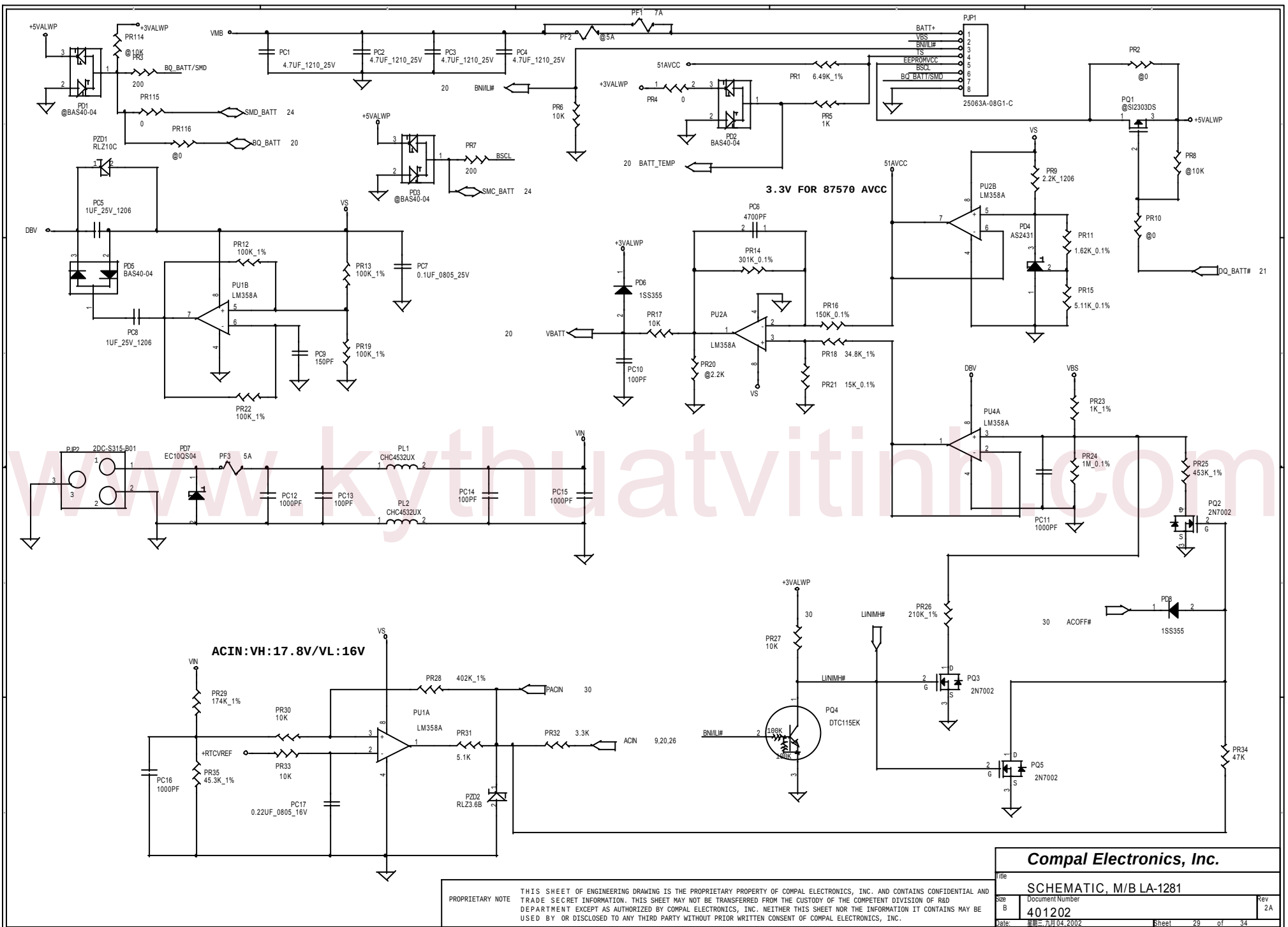
Date: 星期三, 九月04, 2002

Sheet 27 of 34

PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

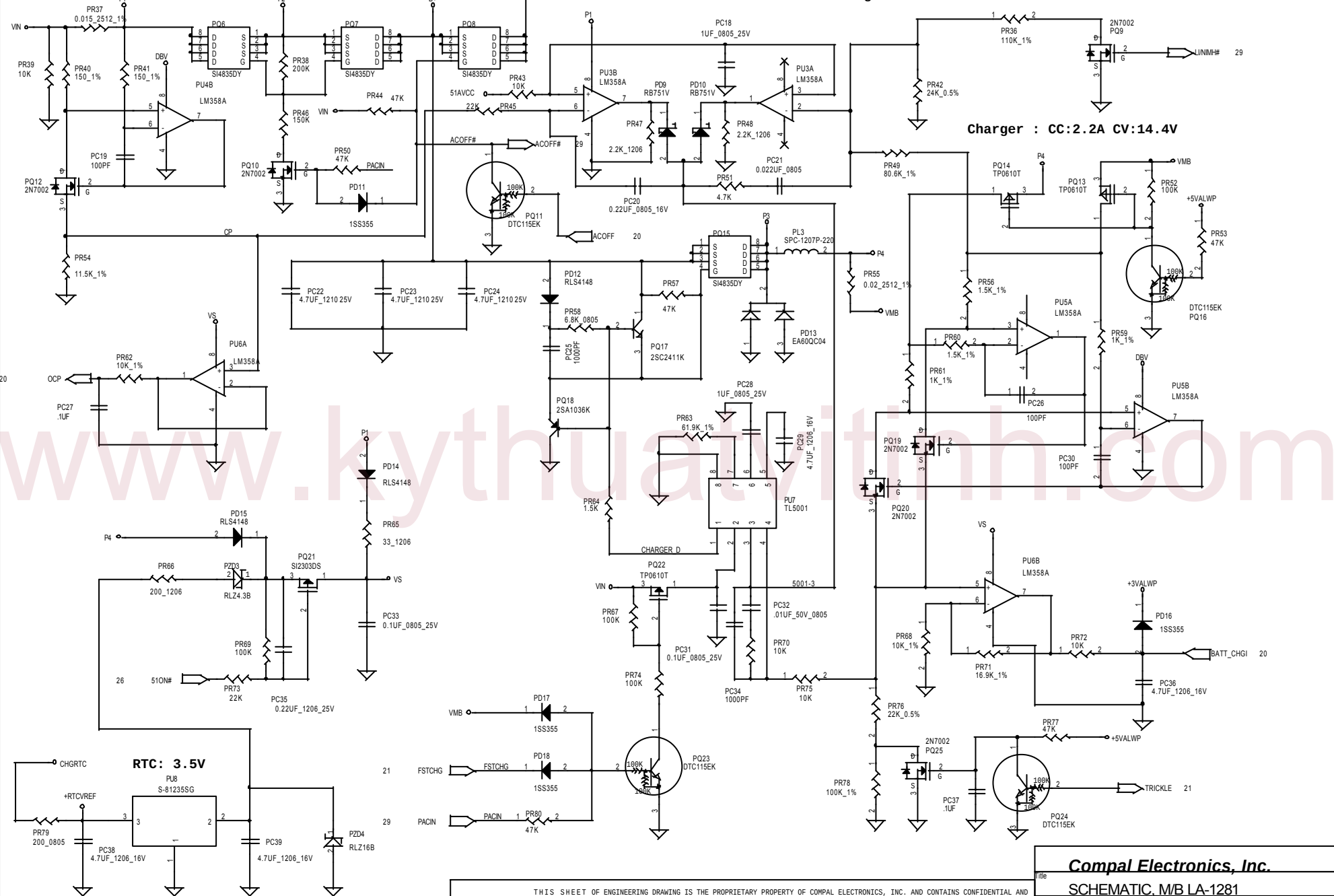
Rev 2A





ACIN: CC:2.87A/ OCP:3.48A

Charger : CC:2.2A CV:16.8V for LI-ION/CV:14.4V for NI-MH



PROPRIETARY NOTE THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

SCHEMATIC, M/B LA-1281

Document Number

401202

Date: 2002/04/04

Sheet 30 of 34

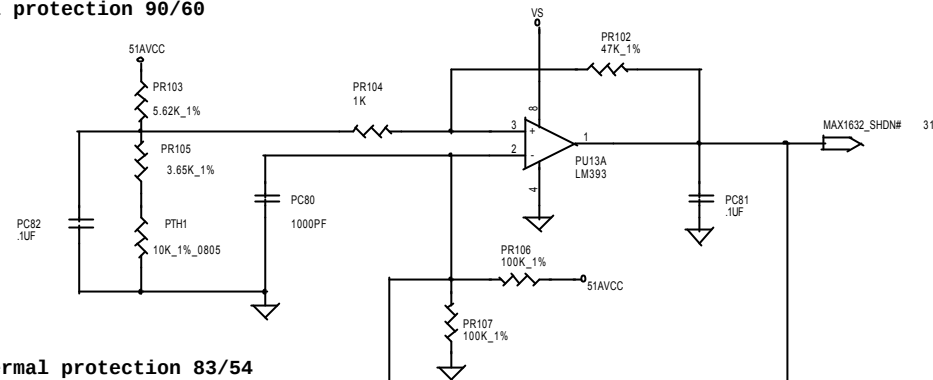
Rev

2A

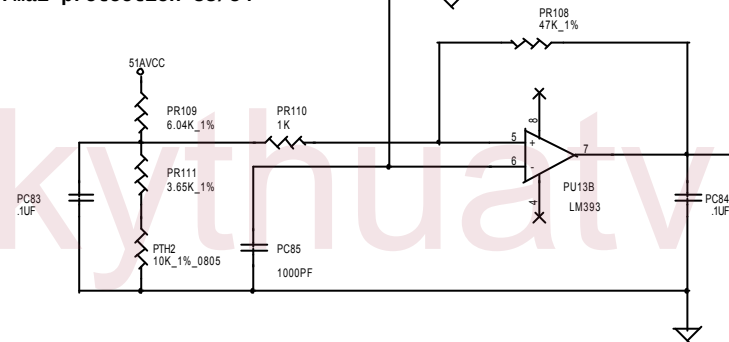




For CPU thermal protection 90/60  
度C



For Battery thermal protection 83/54  
度C



# CY23 PIR LIST

08/28/01 Written by Jerry

P11: Add Q61 and R407 for VTTWGRD

\*\*\*\*\* Rev1.0 PIR List \*\*\*\*\*

09/10/01 Written by Jerry

P26: Add @ at R309 and Q47

Add C507 and U46

Change value of R317 to 510K

P27: Remove @ at R224, R229, R242, R245, R244, C273, and Q35

P32: Add PC91, PC92, PC93, and PC94.

09/12/01 Written by Jerry

P06: Change value of RP34, RP50, RP54, RP62, RP38, RP33, RP57,

RP61,RP53, and RP56 to 16P8R-22

P26: Change value of R317 to 560K

09/19/01 Written by Jerry

P29: Change value of PR29 to 174K\_1%

P32: Change value of PR123 to 52.3K\_1%

\*\*\*\*\* Rev2.0 PIR List \*\*\*\*\*

10/03/01 Written by Jerry

P02: Change value of R27 to 100

Delete R395

R395's Pin 1 tie to RP90's pin5

P03: Change value of R353 to 56.2 1%

P28: Add R408 between +3V and pin 87 of U3

10/08/01 Written by Jerry

P11, P13, P28: Change footprint of Y1, Y2, and Y3

P23: Change footprint of JP11

P28: Change footprint of JP12

P31: Change footprint of PL5

P26: Change footprint of SW1

10/17/01 Written by Jerry

P02: Add @ at RP25, RP24, RP28, RP27, RP31, RP30, RP 42, RP41,

RP40, RP43, RP44, RP45, RP46, RP47, RP48, RP49, RP7, RP1, RP6,

RP14, RP20, RP19, RP22, RP9, RP10, RP11, RP8, RP2, and R7

P06: Remove @ at R164

P11: Add @ at R176

|                          |                 |       |          |
|--------------------------|-----------------|-------|----------|
| Compal Electronics, Inc. |                 |       |          |
| File                     |                 |       |          |
| SCHEMATIC, M/B LA-1281   |                 |       |          |
| Size                     | Document Number |       | Rev      |
| B                        | 401202          |       | 2A       |
| Date:                    | 日期: 九月04, 2002  | Sheet | 34 of 34 |

PROPRIETARY NOTE

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.