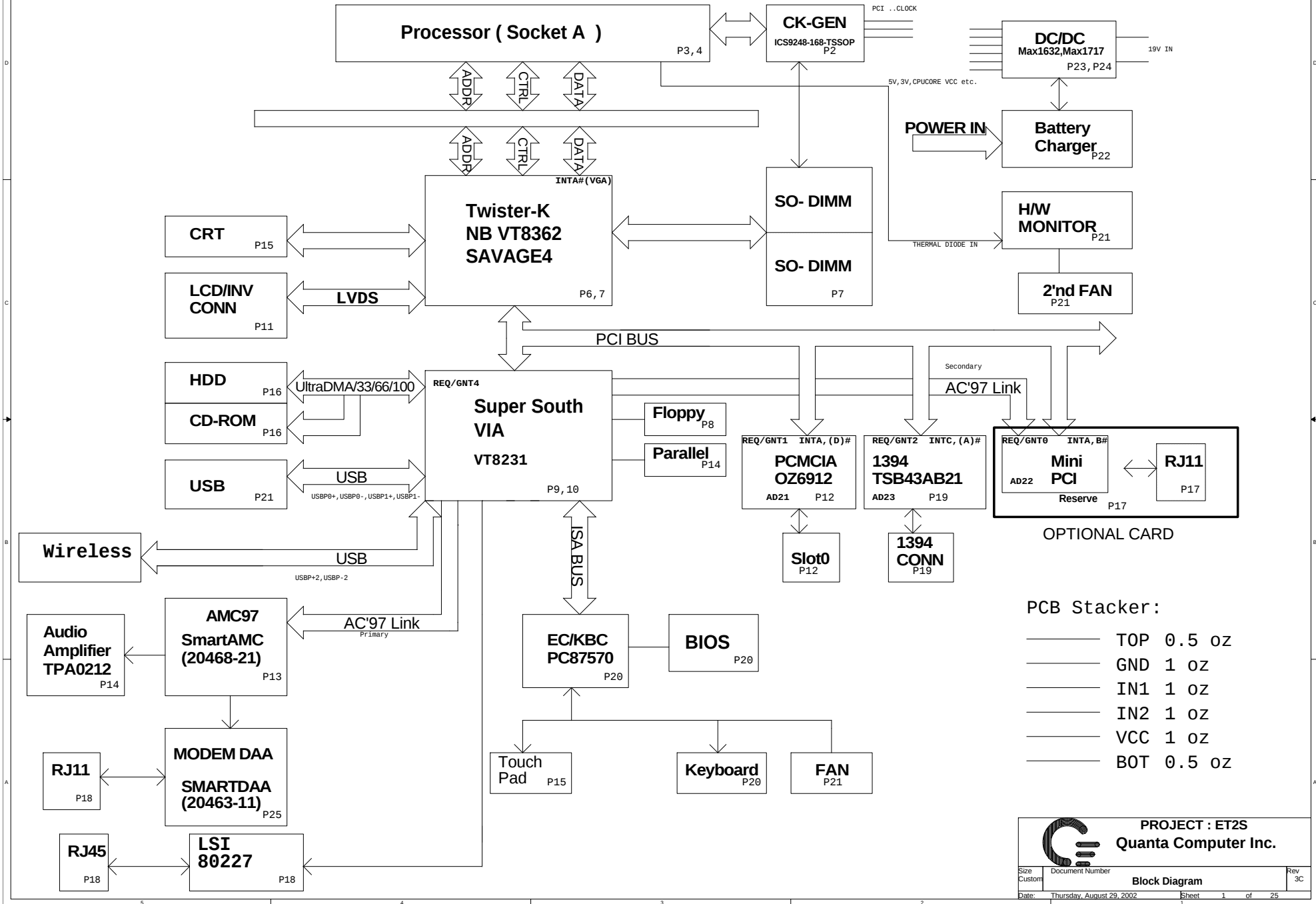
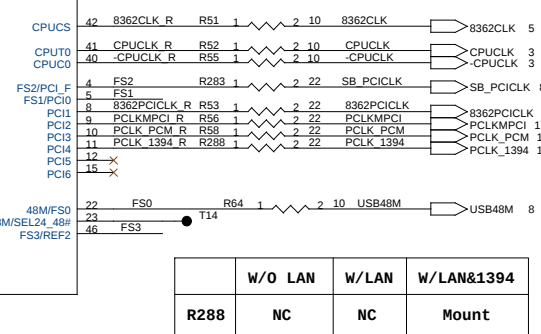
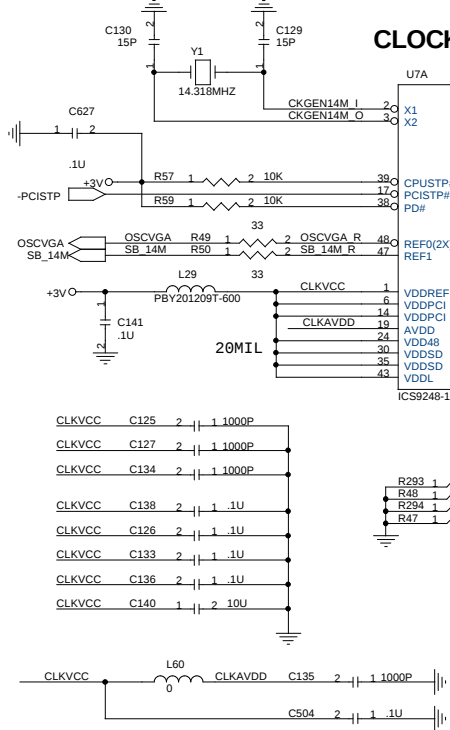


MODEL:	REV	CHANGE LIST	MODEL		ET2S MB (LAN)	
			PAGE	FROM		TO
ET2S MB (LAN)	3A	FIRST RELEASE	1	3C		
	3B	Reference ECN :E200206-0630	2	3C	3D	
	3C	Reference ECN :E200206-0675	3	3C		
	3D	Reference ECN :E200207-1262	4	3C		
			5	3C		
			6	3C		
			7	3C		
			8	3C		
			9	3C		
			10	3C		
			11	3C		
			12	3C		
			13	3C		
			14	3C		
			15	3C		
			16	3C		
			17	3C		
			18	3C		
			19	3C		
			20	3C		
			21	3C		
			22	3C		
			23	3C		
			24	3C		
			25	3C		
 Quanta Computer Inc.		Project : ET2S MB(LAN)	MB Assy' P/N: 21ET2MB00C1	Document No.: 204	REV 3D	
		Approved by : Jacky Lee	Drawing by : CHIN HAO KUO	DATE: 2002/5/28	Cover Sheet: 1 of 1	

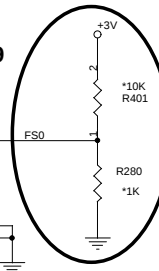
ET2S SYSTEM BLOCK DIAGRAM



CLOCK GENERATOR

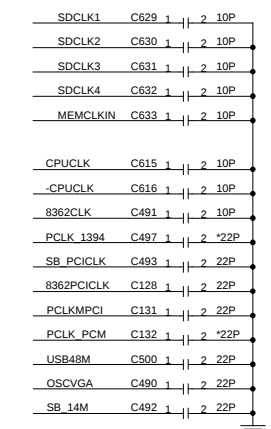


0429



CPU CLOCK SET				
FS2	FS1	FS0	CPU	SS
1	1	1	133	+/- 0.25%
1	1	0	100	+/- 0.25%
1	0	1	133	+/- 0.6%
1	0	0	100	+/- 0.6%
0	1	1	133	-0.5%
0	1	0	100	-0.5%
0	0	1	133	+/- 0.35%
0	0	0	100	+/- 0.35%

DEFAULT SETTING



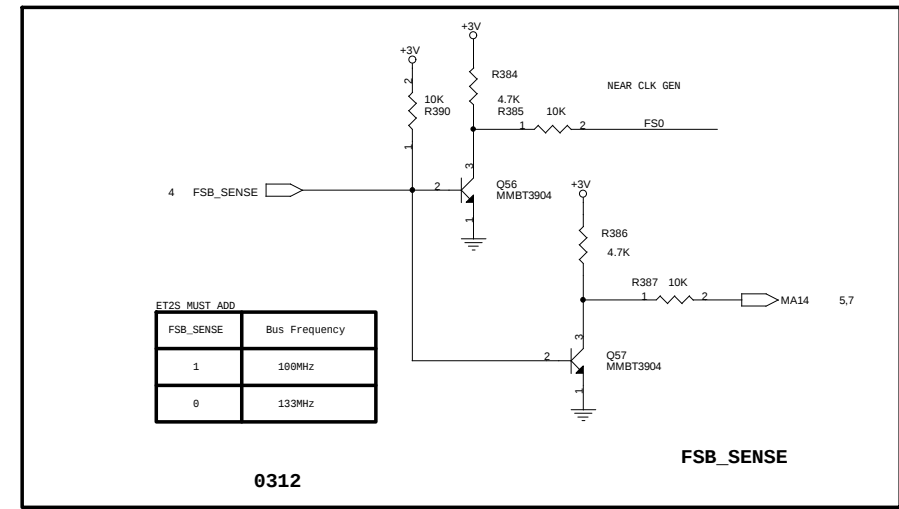
CLKVCC trace width: 30mil

- CPUCLK = Lnb-1" (0"-8")
- CPUCLK = Lnb-1" (0"-8")
- 8362CLK = Lnb (1"-9")
- MEMCLKOUT = As short as possible (1"-9")
- MEMCLKIN = Lsd+4.5" (5.5"-8.5")
- DIMCLK[1..4] = Lsd (1"-4")
- DIMCLK0 = Lsd+2.5" (3.5"-6.5")
- 8362PCICLK = Lpci +3" (4"-15")
- SB_PCICLK = Lpci +3" (4"-15")
- PCMCICLK = Lpci (1"-12")
- MINIPICLK = Lpci (1"-12")

ET2S MUST ADD	
FSB_SENSE	Bus Frequency
1	100MHz
0	133MHz

0312

FSB_SENSE



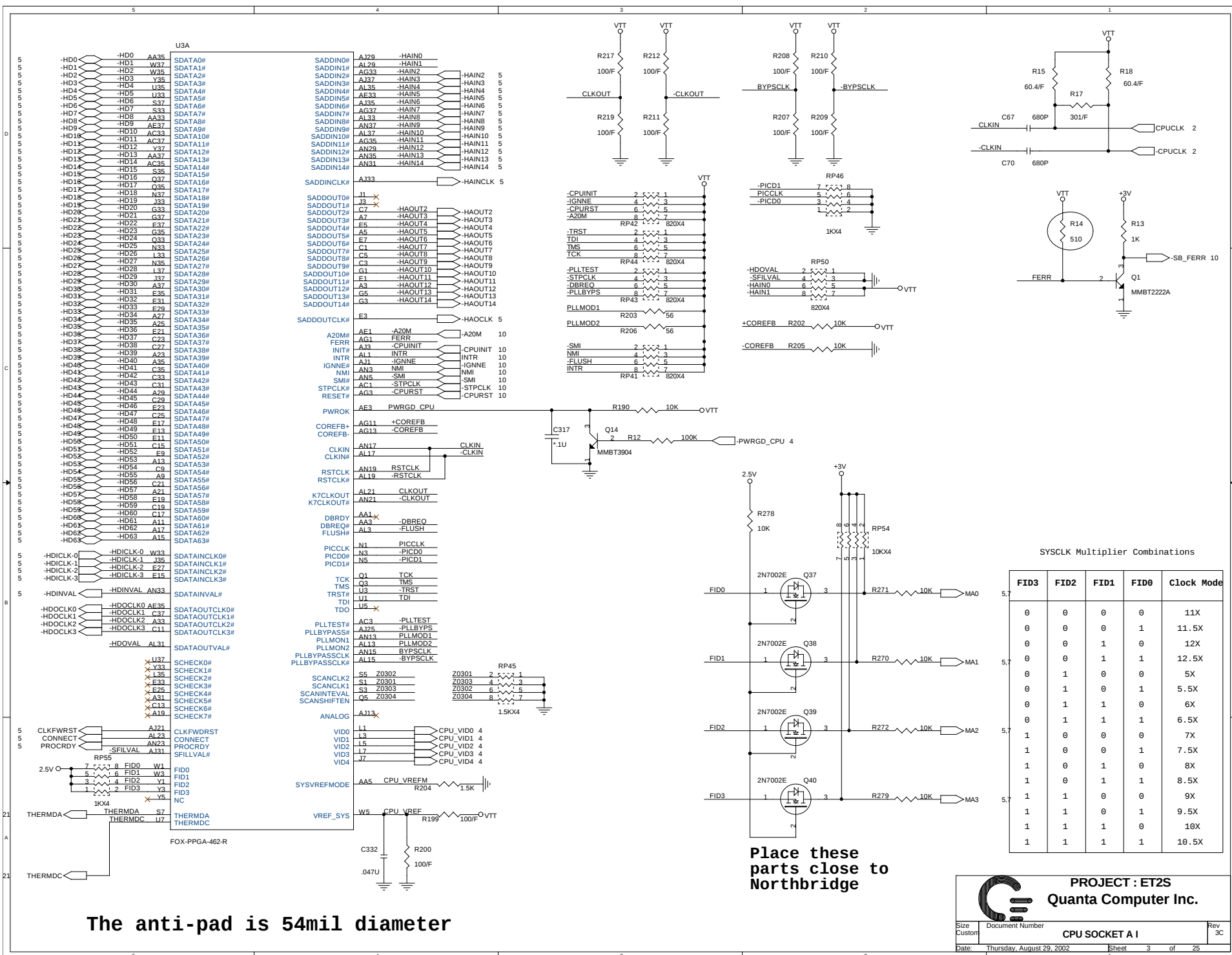
PROJECT : ET2S
Quanta Computer Inc.

Size Custom Document Number

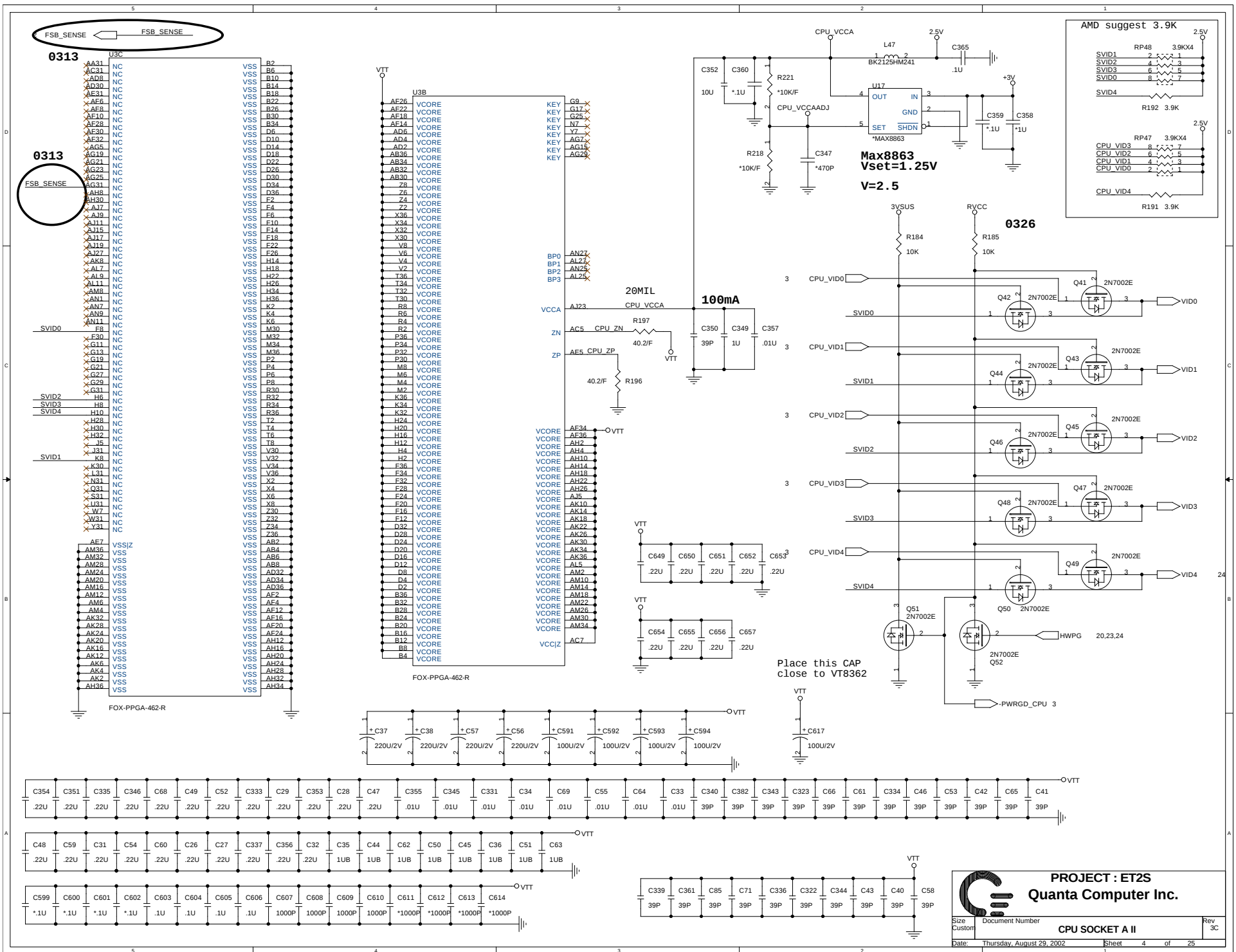
CLOCK GENERATOR

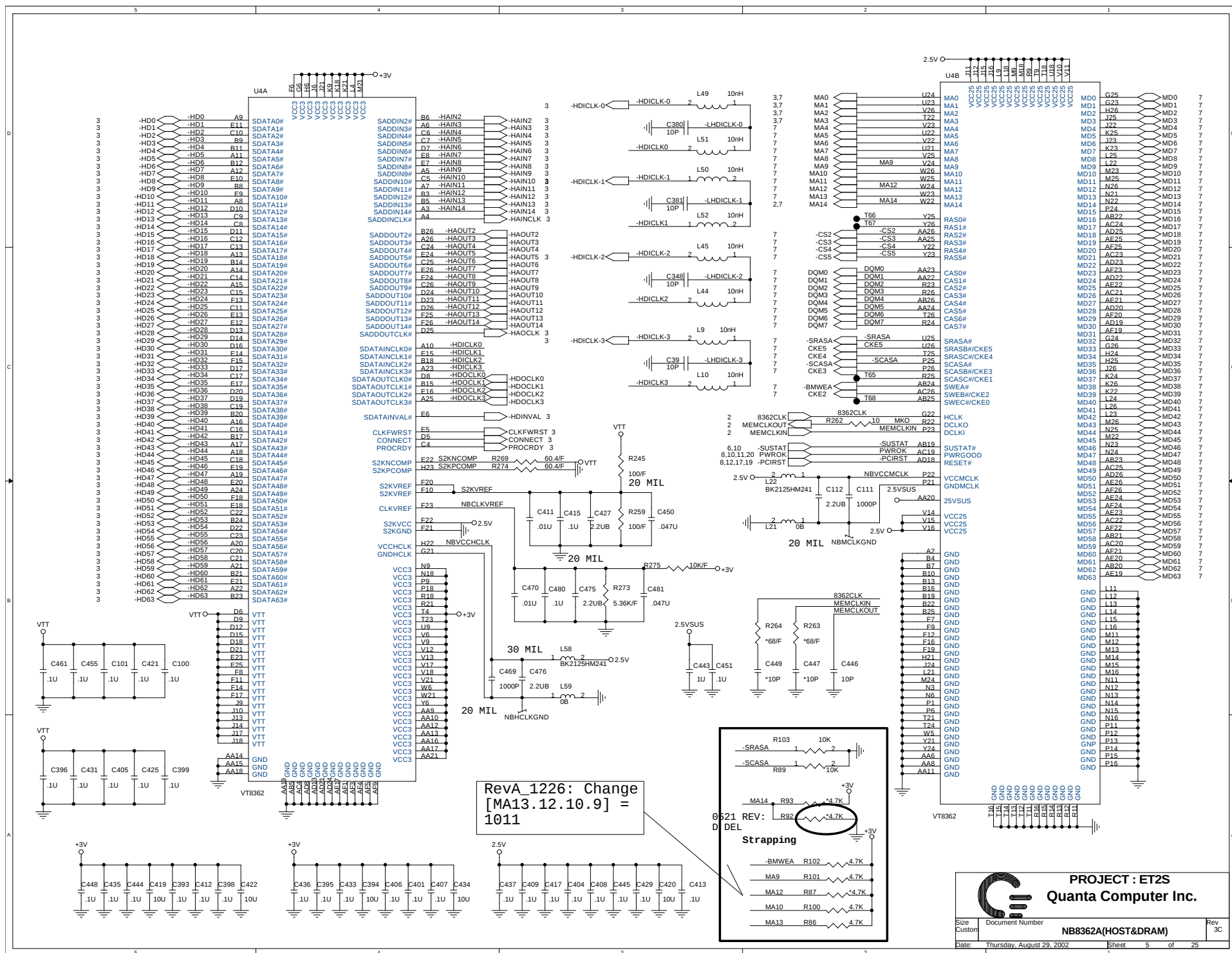
Date: Thursday, August 29, 2002 Sheet 2 of 25

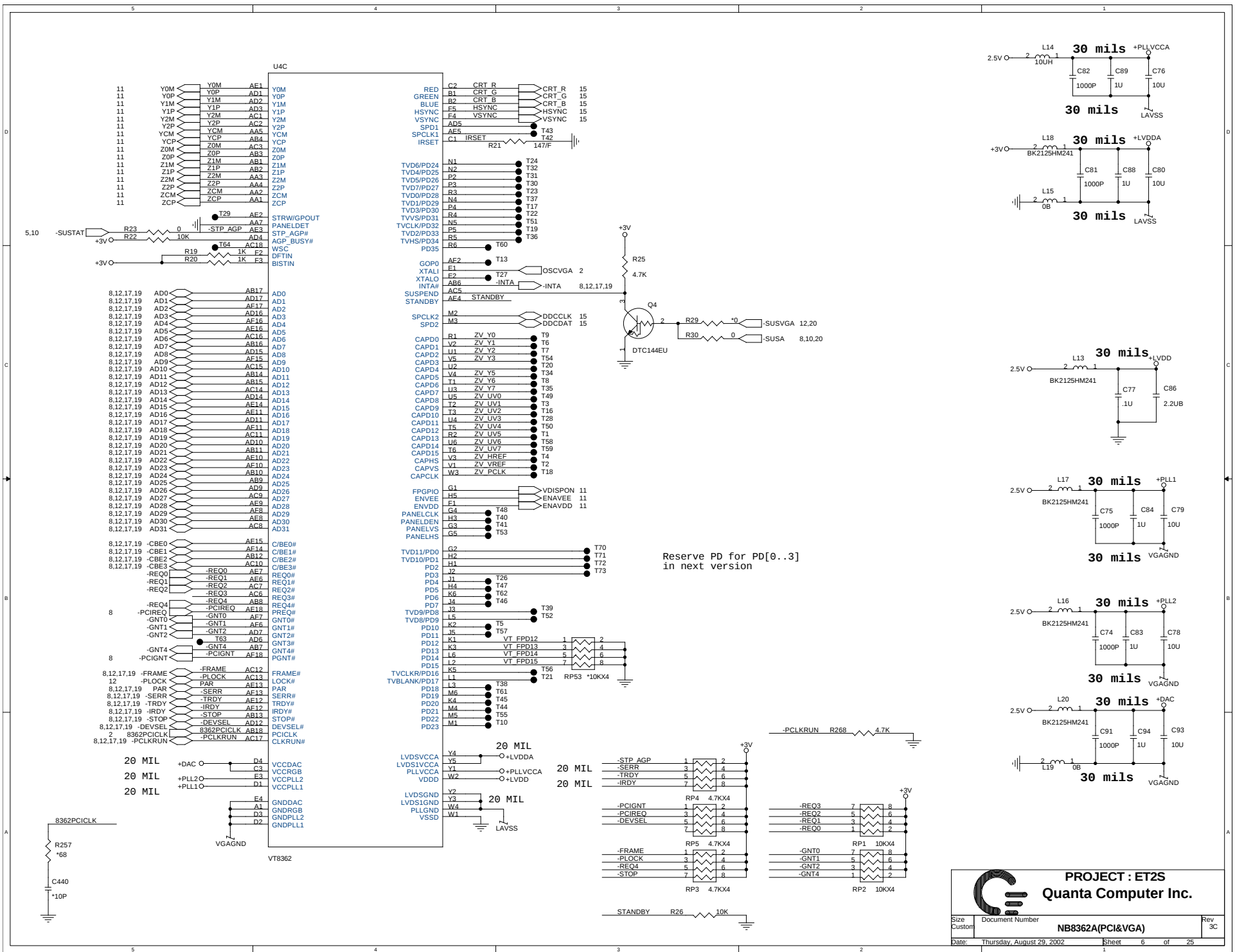
Rev 3D



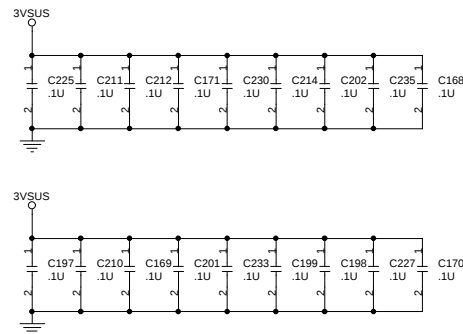
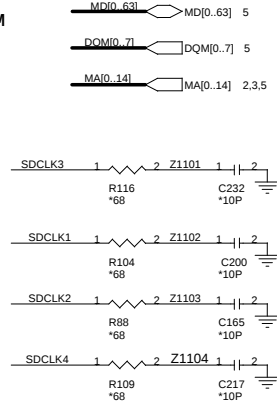
The anti-pad is 54mil diameter





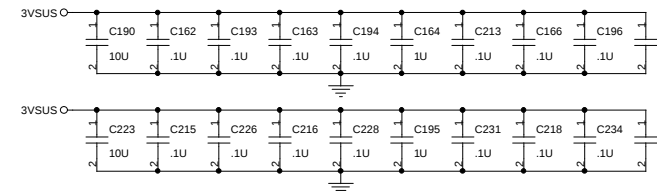


**EXPANSION DIMM
(BANK 2,3)**

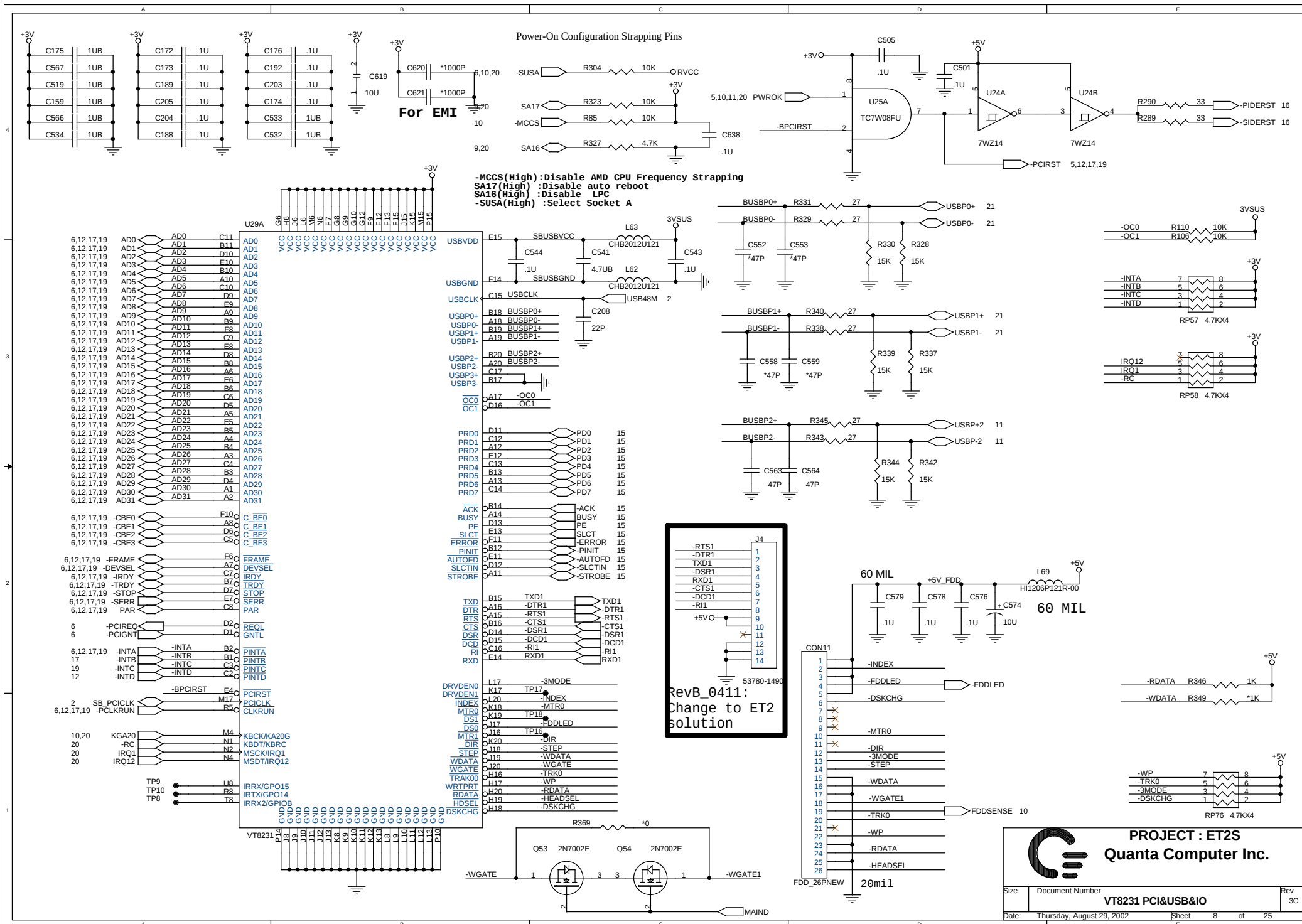


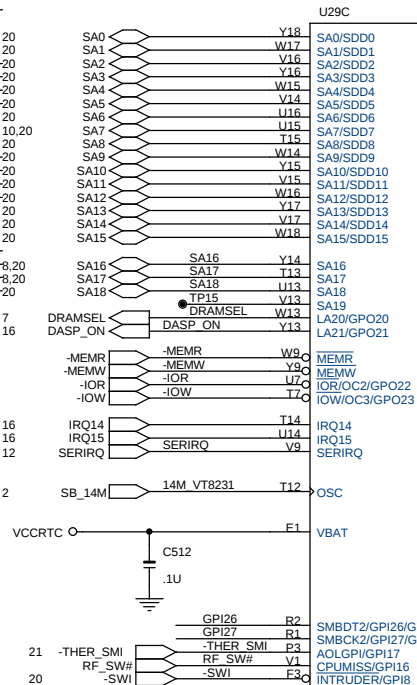
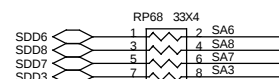
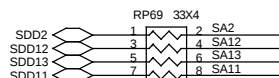
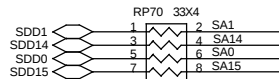
EXPANSION 11
(BANK4, 5)

Signal	Pin	Signal	Pin	Signal	Pin
MD0	1	VSS	2	MD32	3
MD1	3	DQ0	4	MD33	4
MD2	5	DQ1	6	MD34	6
MD3	7	DQ2	8	MD35	8
MD4	9	DQ3	10	MD36	10
MD5	11	VDD	12	MD37	12
MD6	13	DQ4	14	MD38	14
MD7	15	DQ5	16	MD39	16
MD8	17	DQ6	18	MD40	18
MD9	19	DQ7	20	MD41	20
MD10	21	VSS	22	MD42	22
MD11	23	DQ8	24	MD43	24
MD12	25	DQ9	26	MD44	26
MD13	27	DQ10	28	MD45	28
MD14	29	VDD	30	MD46	30
MD15	31	DQ11	32	MD47	32
MD16	33	DQ12	34	MD48	34
MD17	35	DQ13	36	MD49	36
MD18	37	DQ14	38	MD50	38
MD19	39	DQ15	40	MD51	40
MD20	41	VSS	42	MD52	42
MD21	43	DQ16	44	MD53	44
MD22	45	DQ17	46	MD54	46
MD23	47	DQ18	48	MD55	48
MD24	49	DQ19	50	MD56	50
MD25	51	DQ20	52	MD57	52
MD26	53	DQ21	54	MD58	54
MD27	55	DQ22	56	MD59	56
MD28	57	DQ23	58	MD60	58
MD29	59	VSS	60	MD61	60
MD30	61	DQ24	62	MD62	62
MD31	63	DQ25	64	MD63	64
MD32	65	DQ26	66	MD64	66
MD33	67	DQ27	68	MD65	68
MD34	69	DQ28	70	MD66	70
MD35	71	DQ29	72	MD67	72
MD36	73	DQ30	74	MD68	74
MD37	75	VSS	76	MD69	76
MD38	77	DQ31	78	MD70	78
MD39	79	DQ32	80	MD71	80
MD40	81	DQ33	82	MD72	82
MD41	83	VDD	84	MD73	84
MD42	85	DQ34	86	MD74	86
MD43	87	DQ35	88	MD75	88
MD44	89	DQ36	90	MD76	90
MD45	91	DQ37	92	MD77	92
MD46	93	VSS	94	MD78	94
MD47	95	DQ38	96	MD79	96
MD48	97	DQ39	98	MD80	98
MD49	99	DQ40	100	MD81	100
MD50	101	VDD	102	MD82	102
MD51	103	DQ41	104	MD83	104
MD52	105	DQ42	106	MD84	106
MD53	107	DQ43	108	MD85	108
MD54	109	VSS	110	MD86	110
MD55	111	DQ44	112	MD87	112
MD56	113	DQ45	114	MD88	114
MD57	115	DQ46	116	MD89	116
MD58	117	DQ47	118	MD90	118
MD59	119	VSS	120	MD91	120
MD60	121	DQ48	122	MD92	122
MD61	123	DQ49	124	MD93	124
MD62	125	DQ50	126	MD94	126
MD63	127	DQ51	128	MD95	128
MD64	129	VDD	130	MD96	130
MD65	131	DQ52	132	MD97	132
MD66	133	DQ53	134	MD98	134
MD67	135	DQ54	136	MD99	136
MD68	137	DQ55	138	MD100	138
MD69	139	DQ56	140	MD101	140
MD70	141	VSS	142	MD102	142
MD71	143	SDA	144	MD103	144
MD72	145	VDD	146	MD104	146

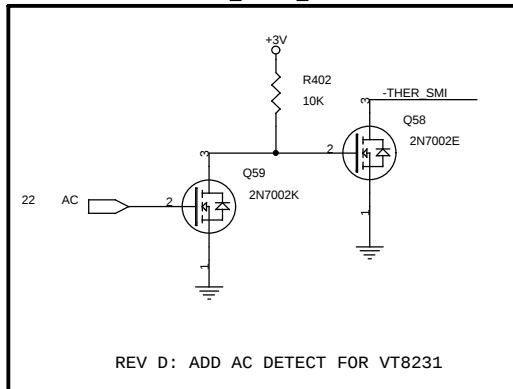
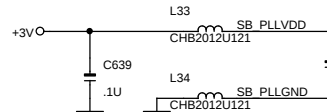


Size Custom	Document Number	Rev 3C
SO-DIMM		
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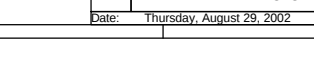
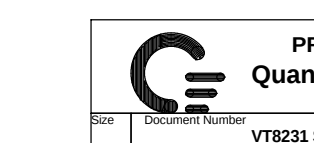
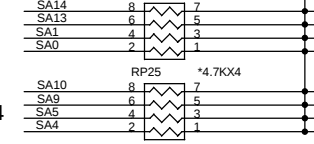
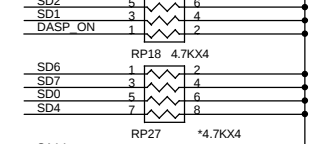
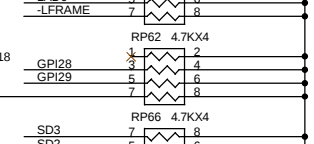
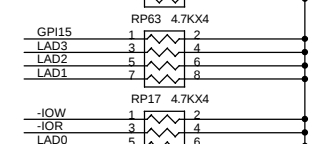
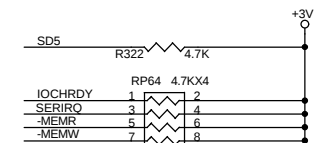
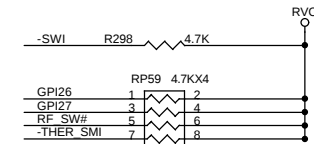
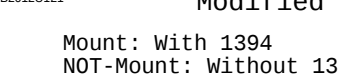
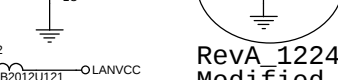
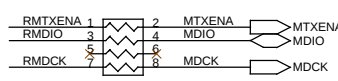
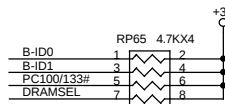
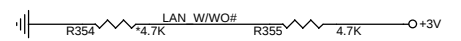
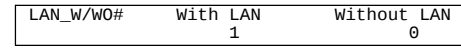
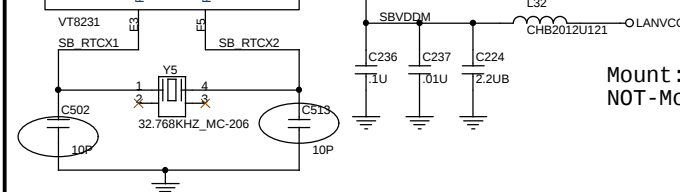




RevA_1224
Modified



REV D: ADD AC DETECT FOR VT8231

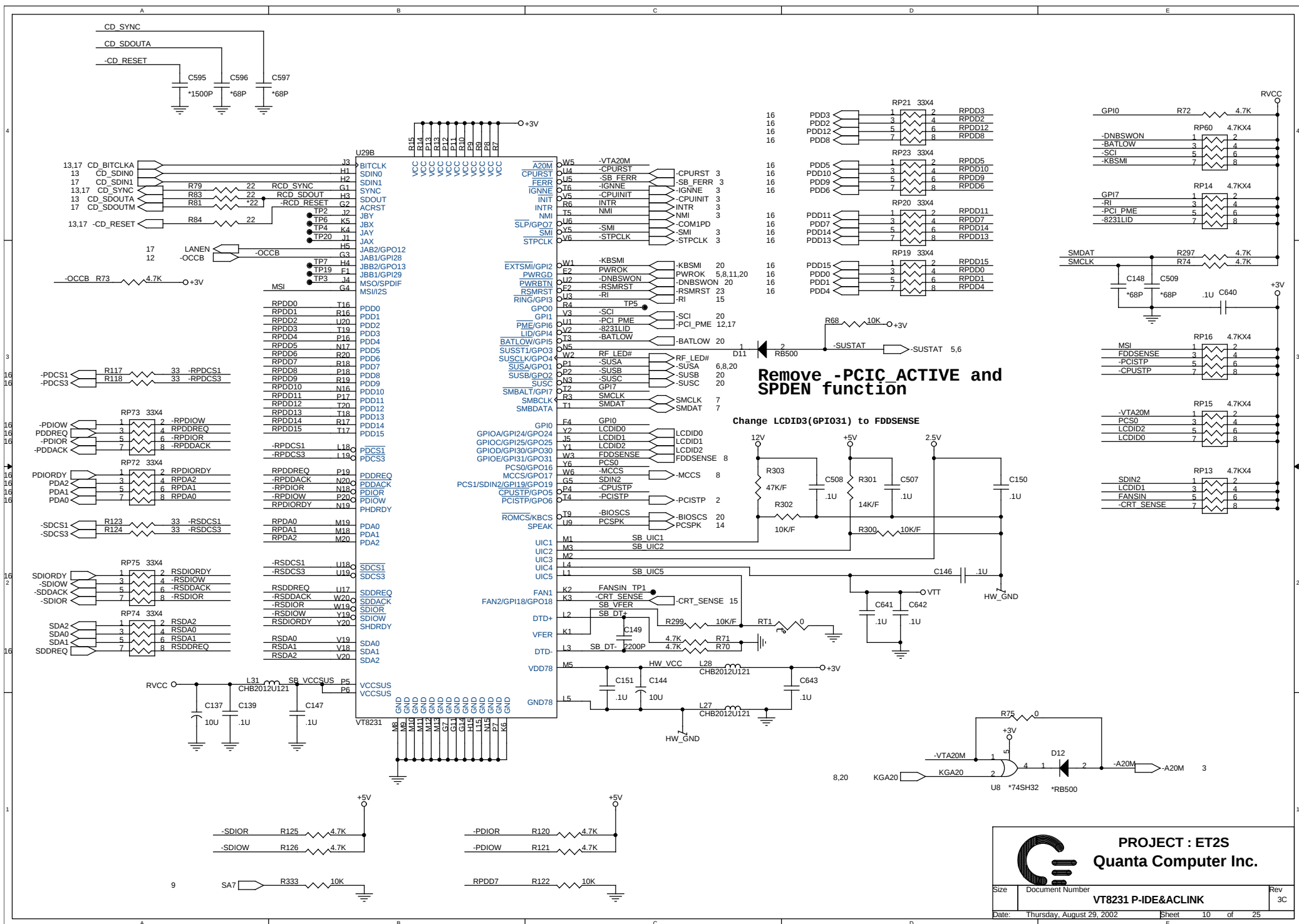


RevA_1224
Modified

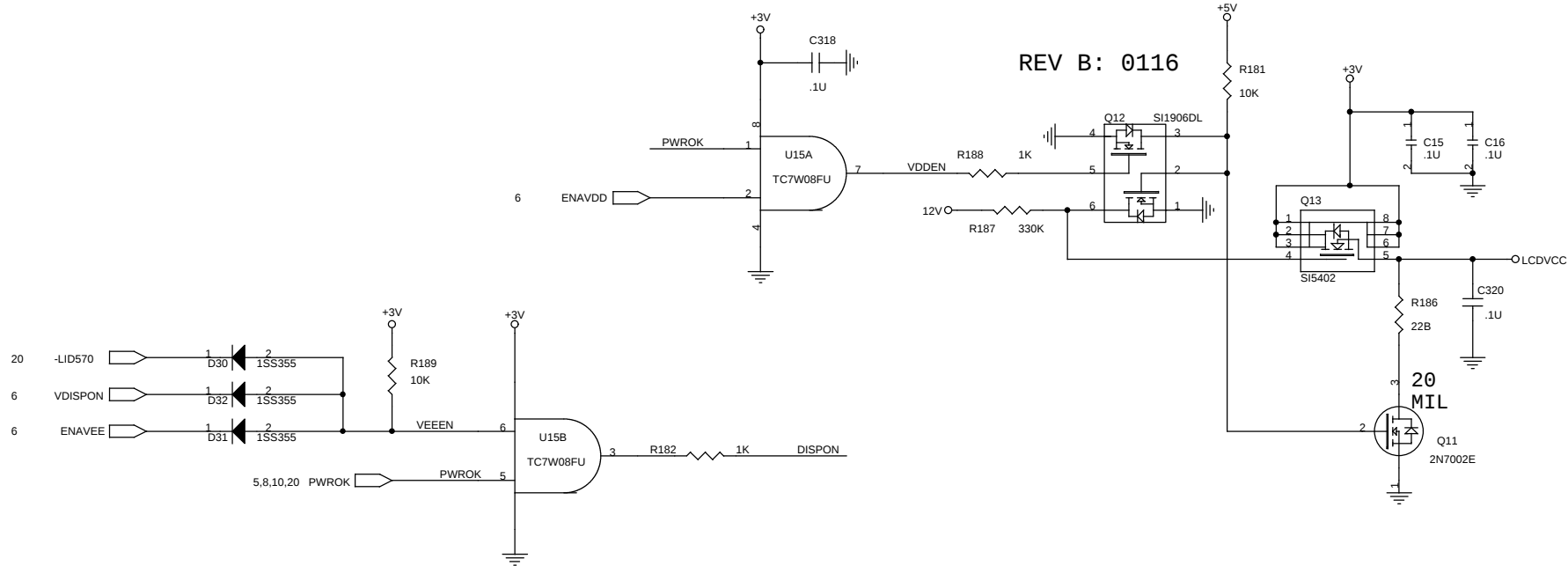
Mount: With 1394
NOT-Mount: Without 1394



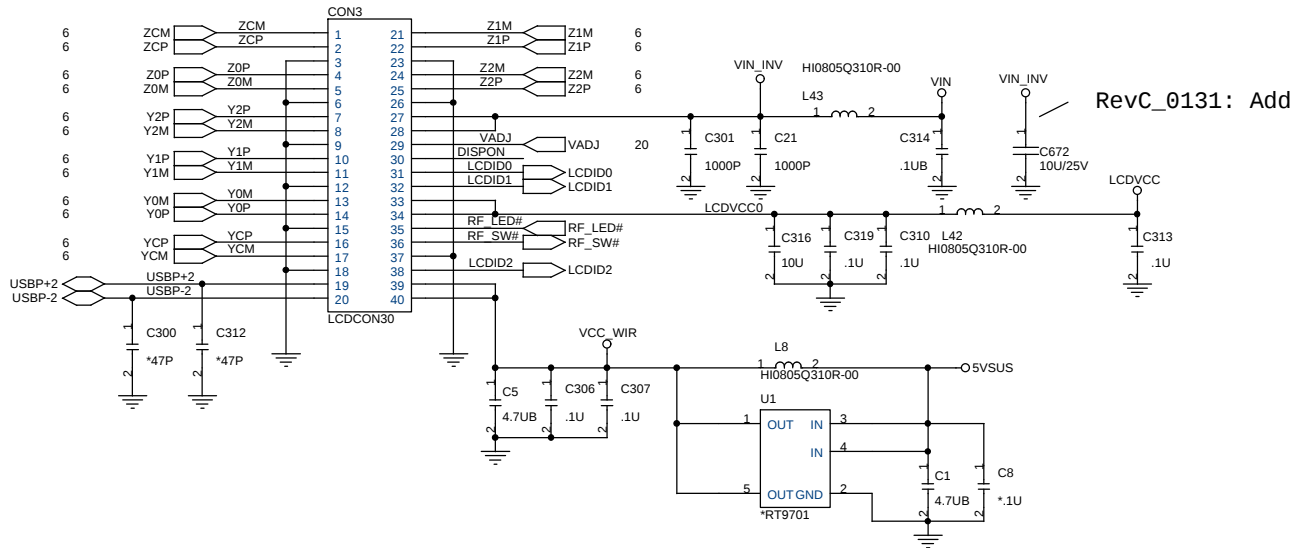
PROJECT : ET2S
Quanta Computer Inc.



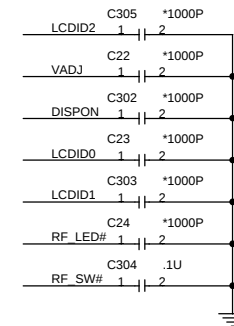
REV B: 0116



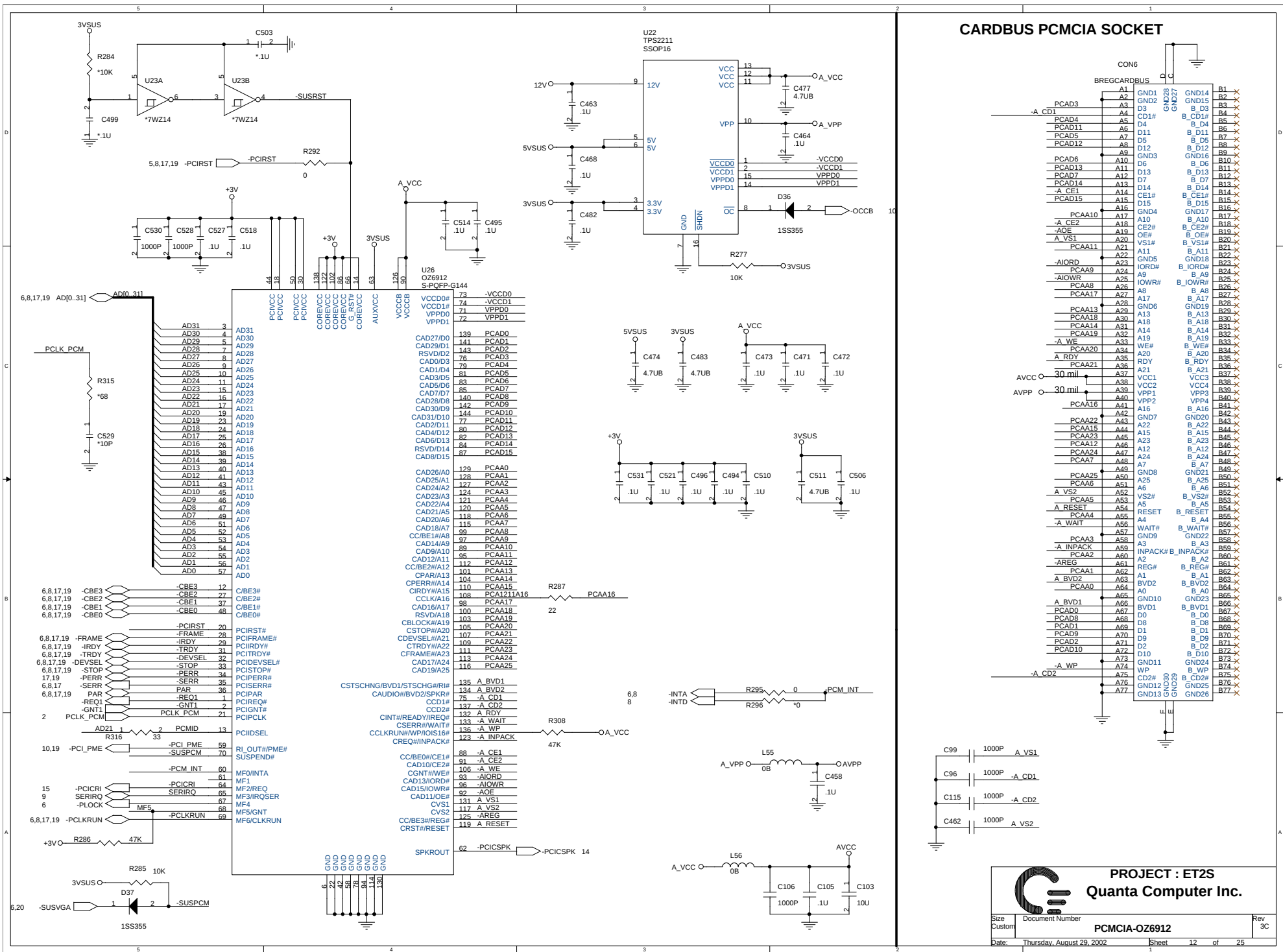
Reserve Dual Channels LVDS



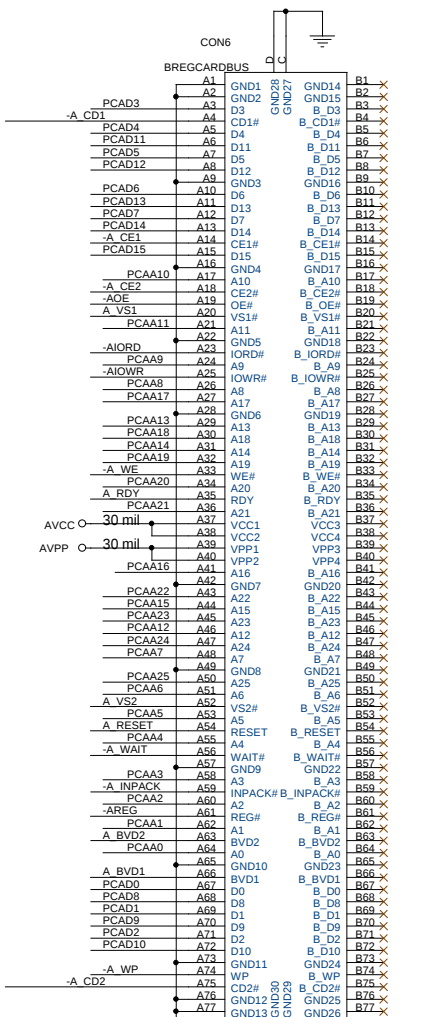
LCDID0	LCDID1	LCDID2	
1	1	1	QDI-141X1LH03
1	0	1	SAM-LTN141X8



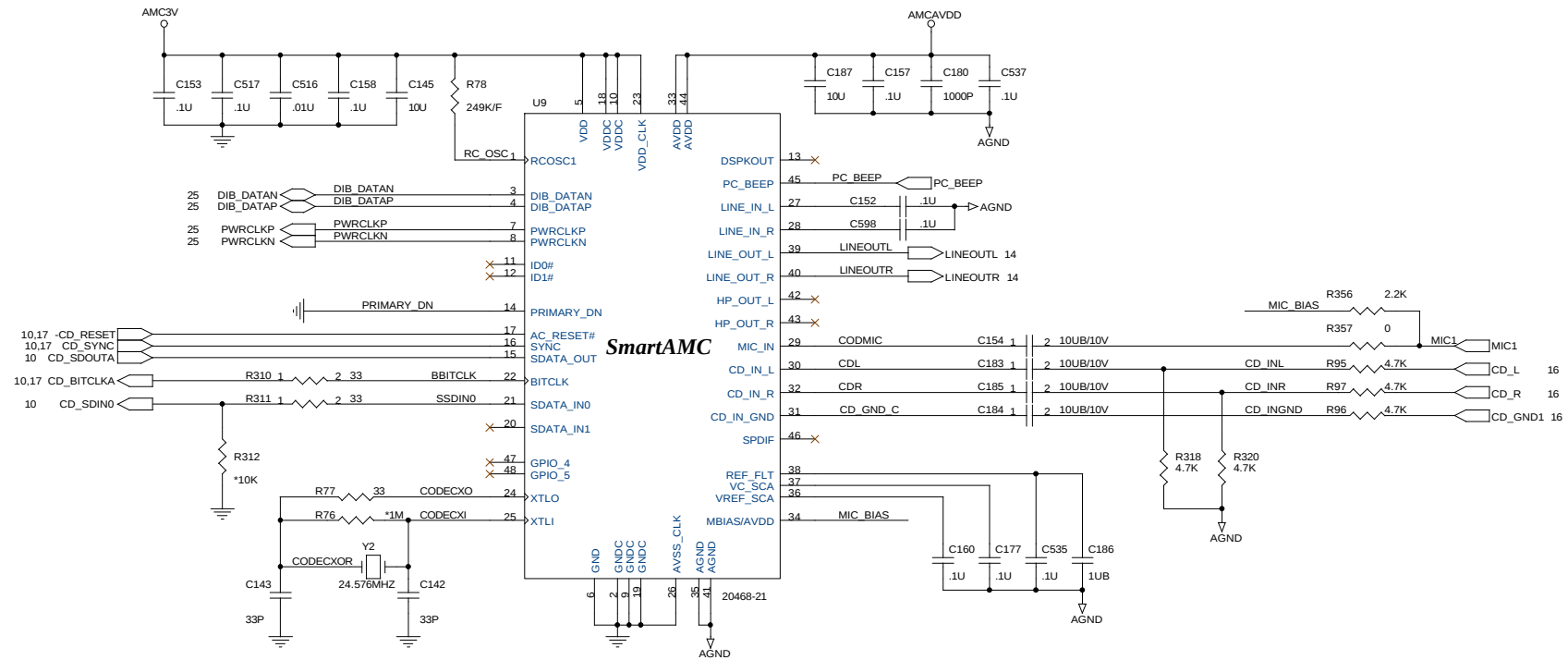
PROJECT : ET2S
Quanta Computer Inc.



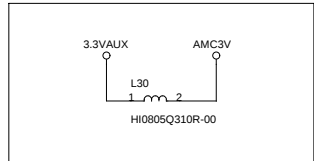
CARDBUS PCMCIA SOCKET



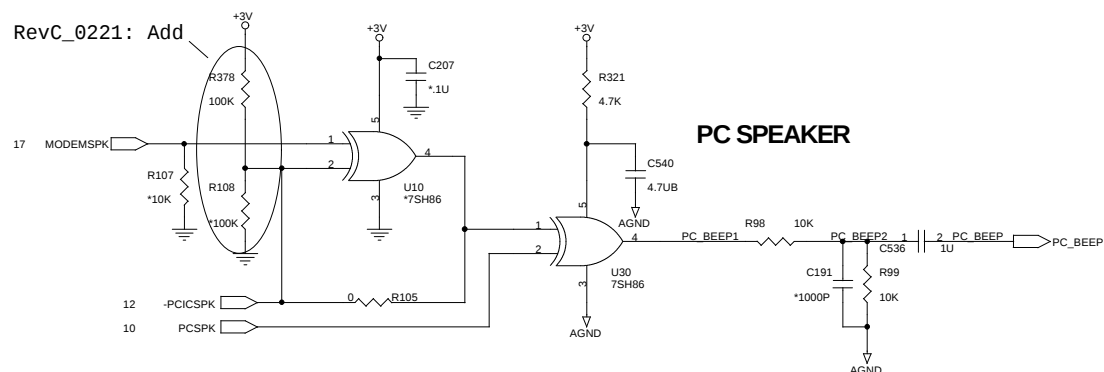
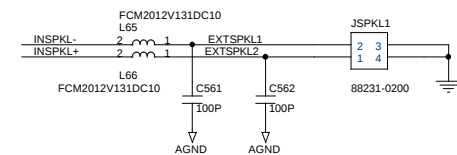
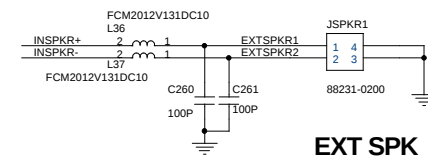
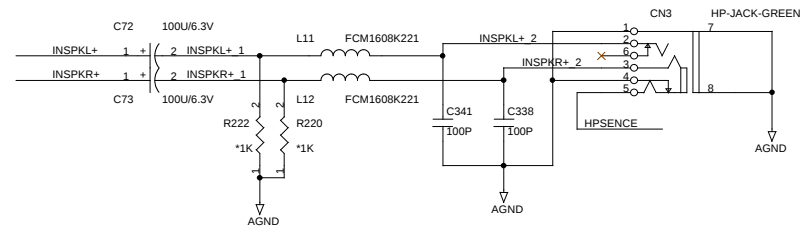
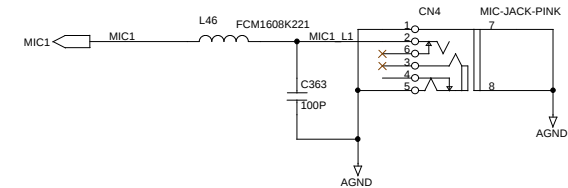
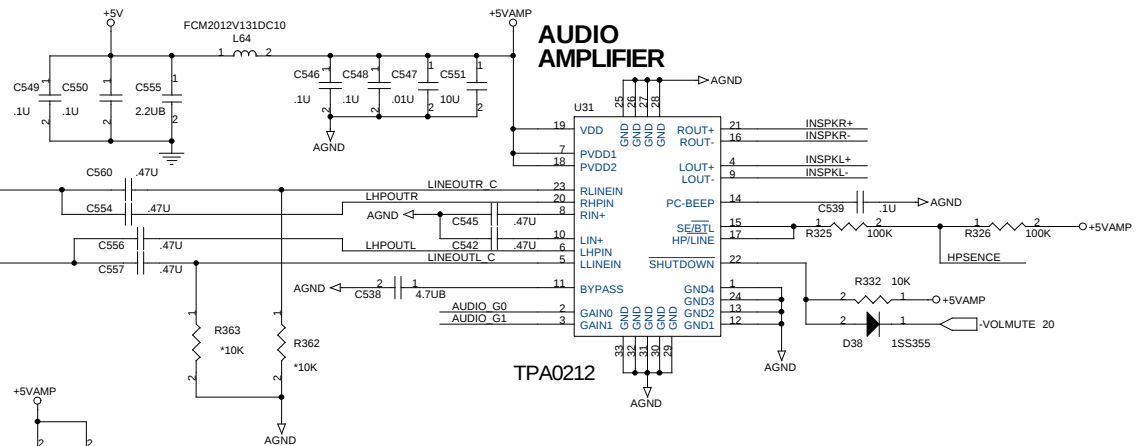
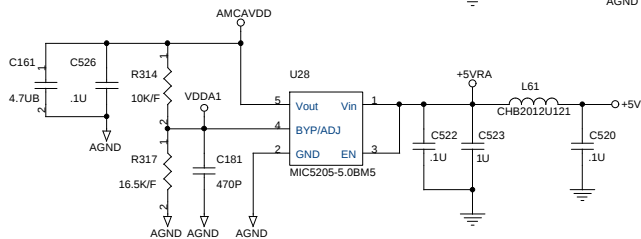
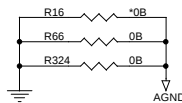
The AMC20463-002 modem is used for mother board family MBAMC20463-002.

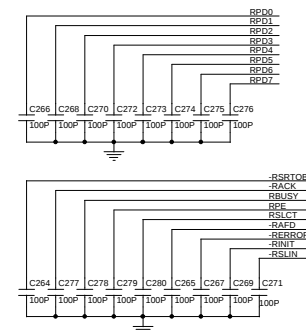
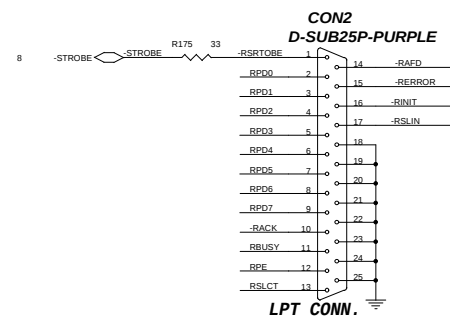
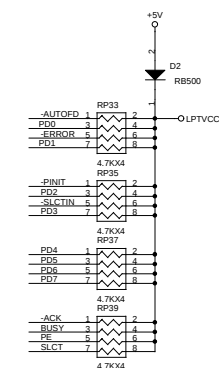
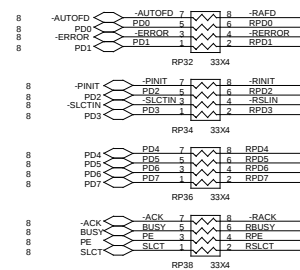
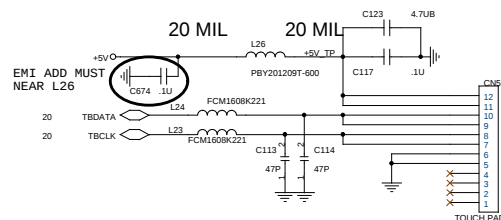


GAIN0	GAIN1	SE/BTL#	Av	Zi
0	0	0	-2 V/V	91K Ohm
0	1	0	-6 V/V	45.5K Ohm
1	0	0	-12 V/V	26K Ohm
1	1	0	-24 V/V	14K Ohm
X	X	1	-1 V/V	


$$V_{set}=1.242V ; AMCAVDD = 1.242 \times (10+16.5)/10 = 3.29V$$

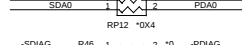
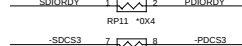
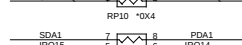
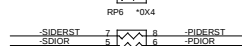
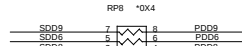
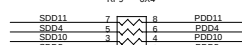
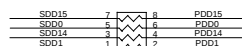
AMCVDD should be 3.3V



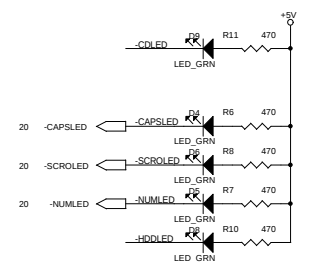
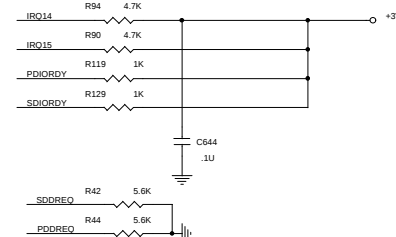
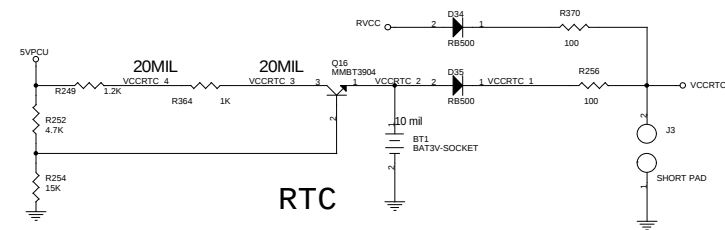


CD-ROM  CONNECTOR

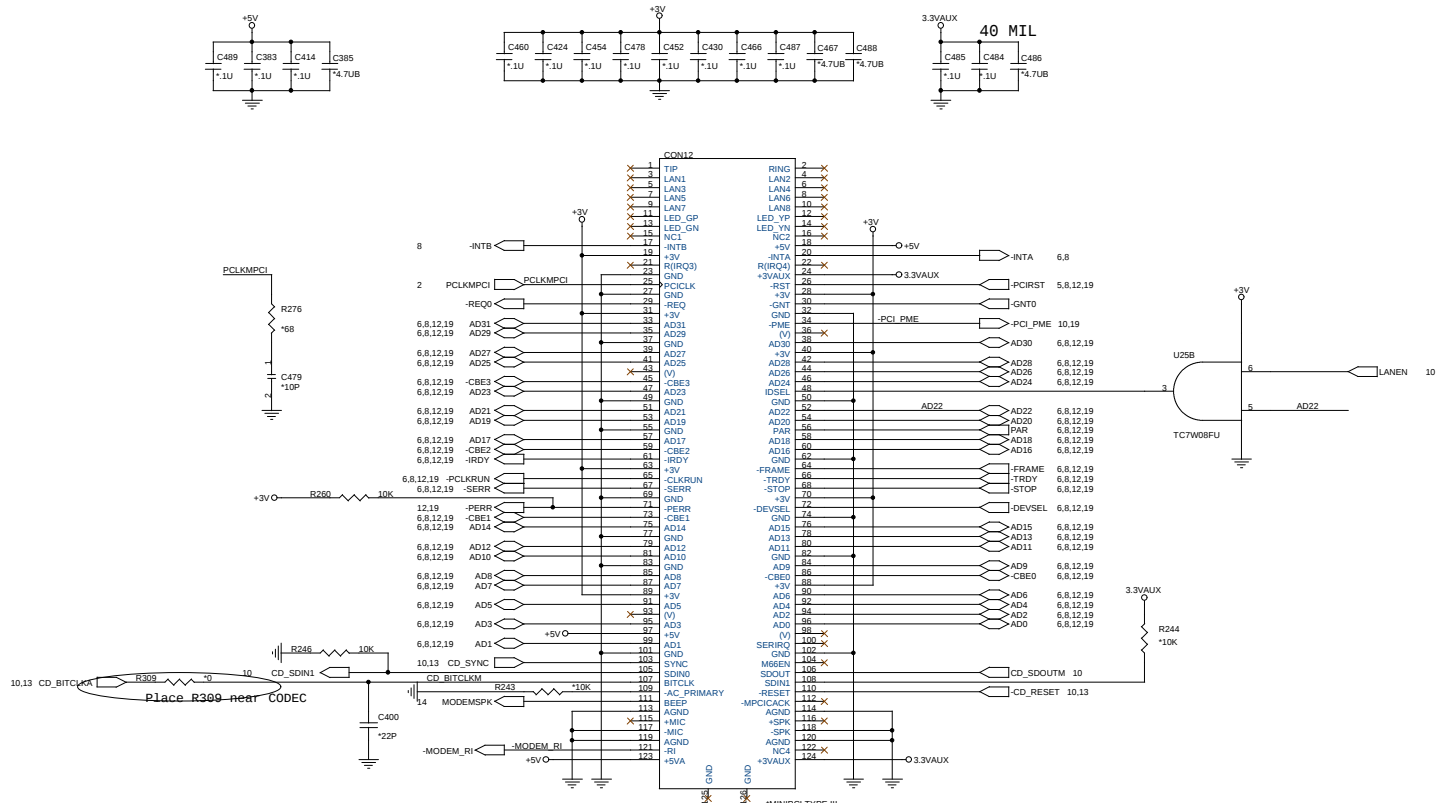
EMI SOLUTION

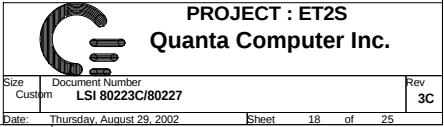


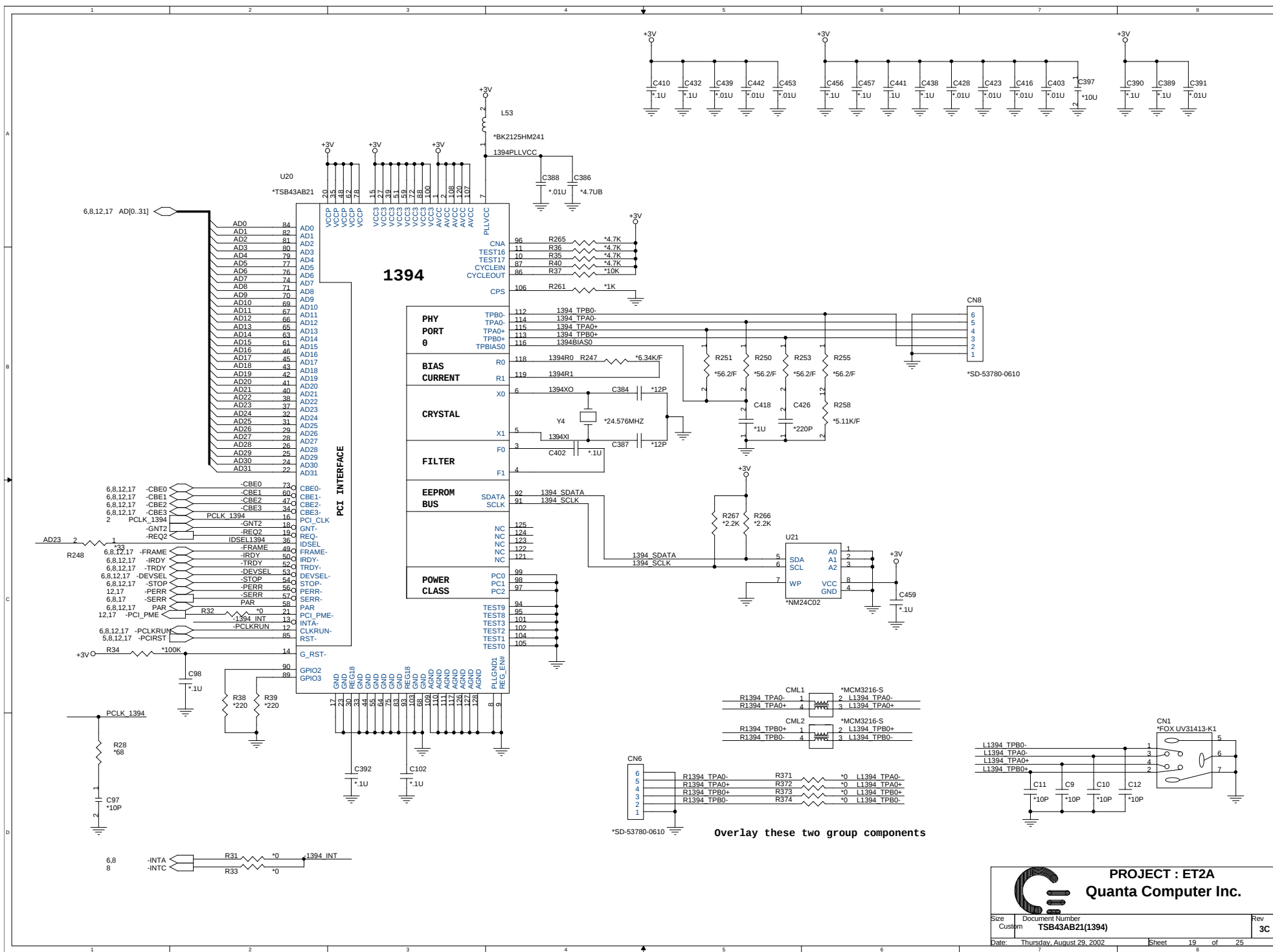
EMI ADD
MUST NEAR
L25

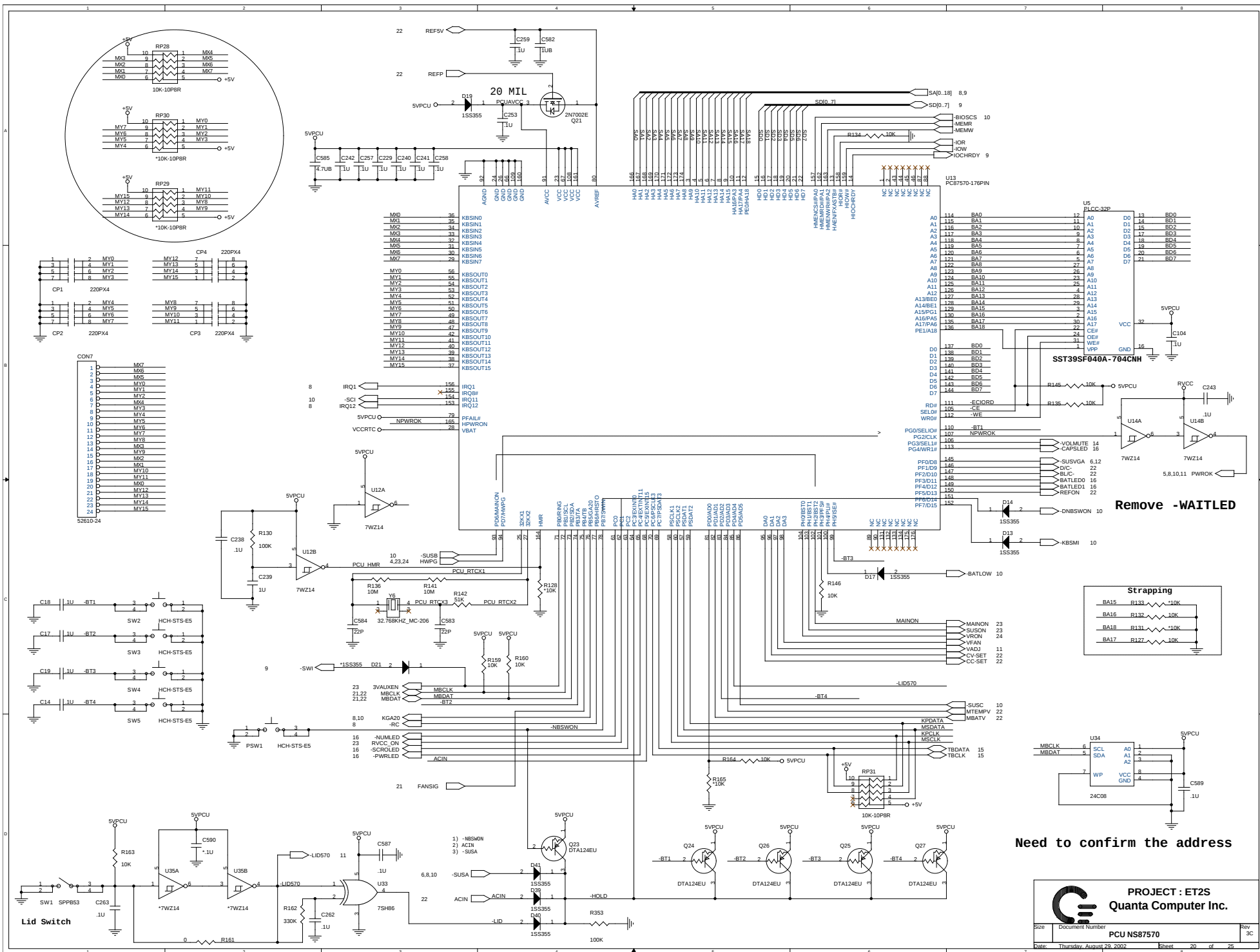


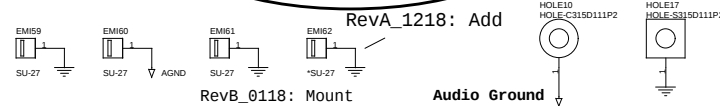
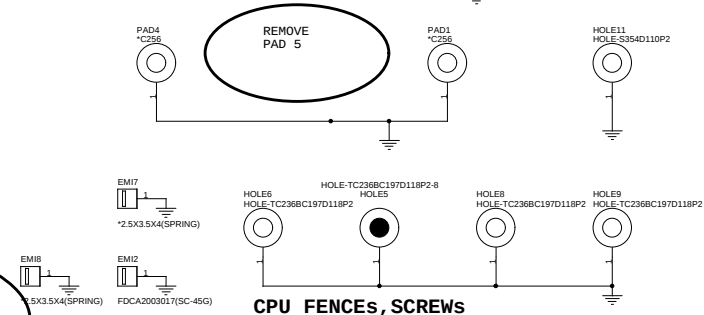
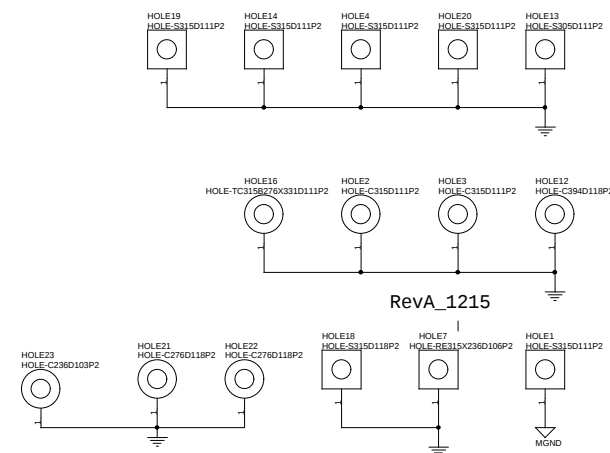
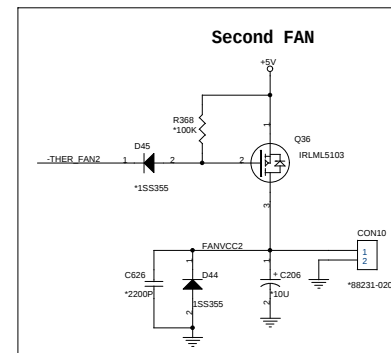
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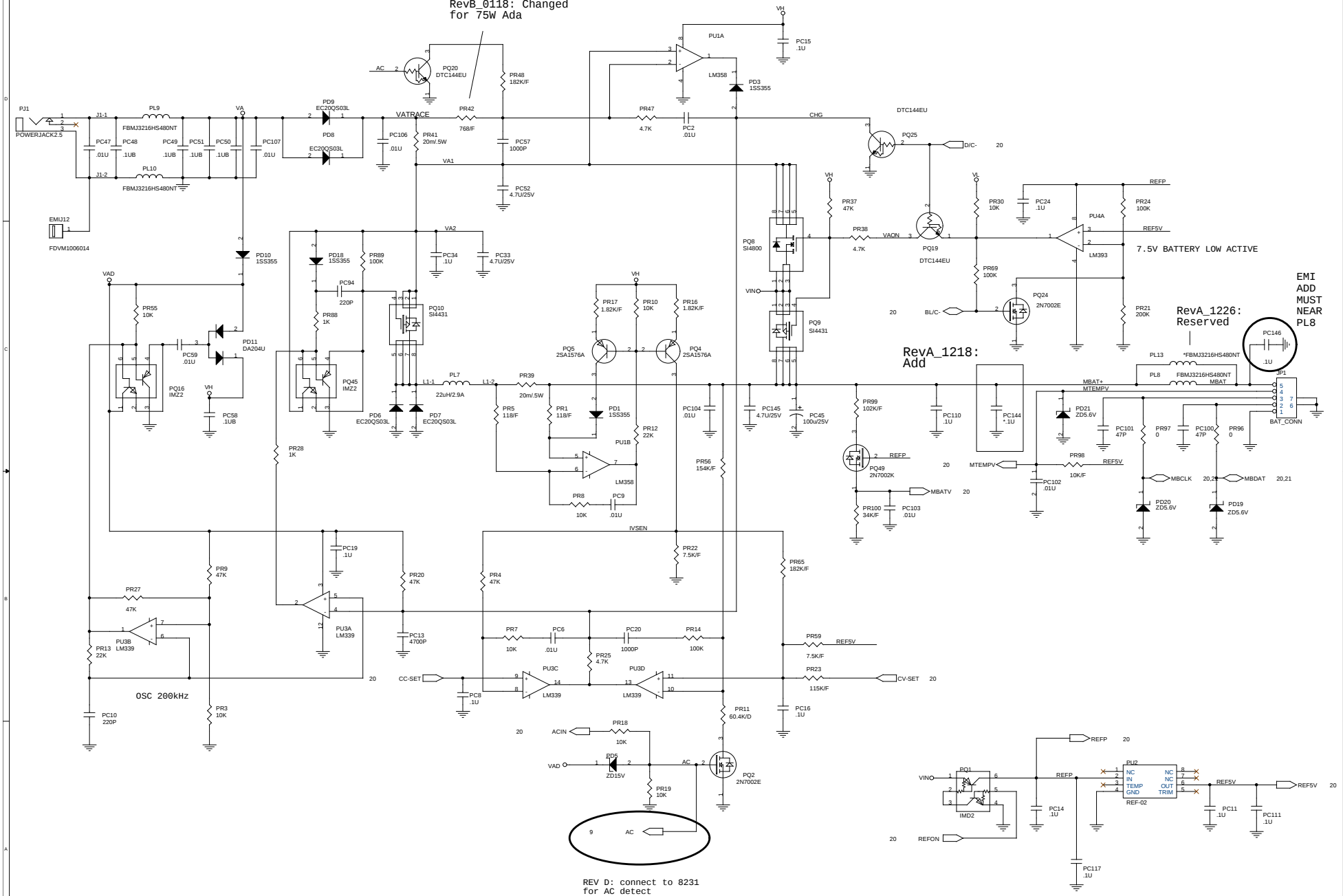


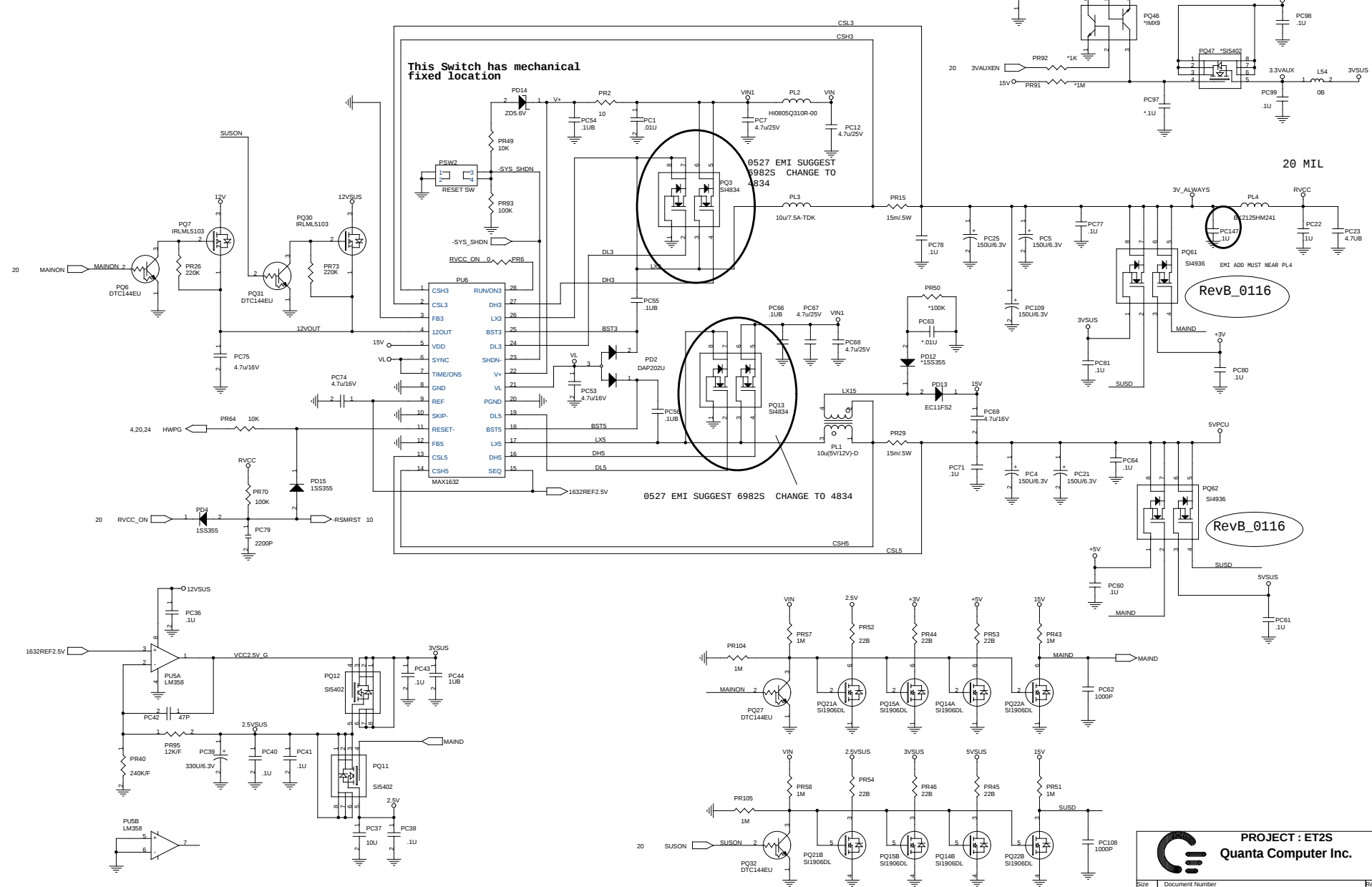


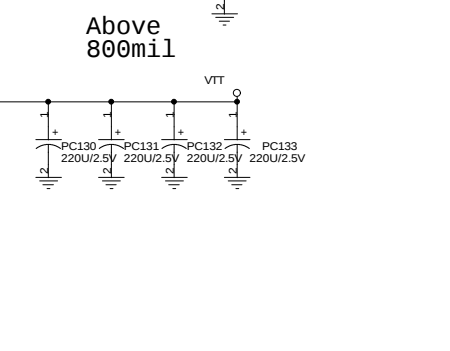
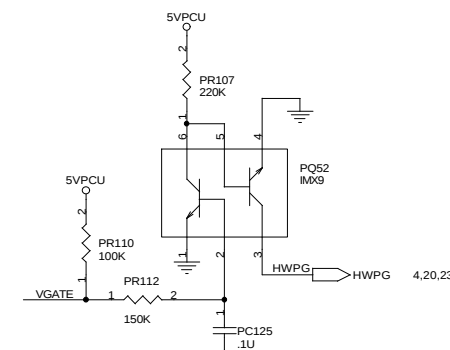
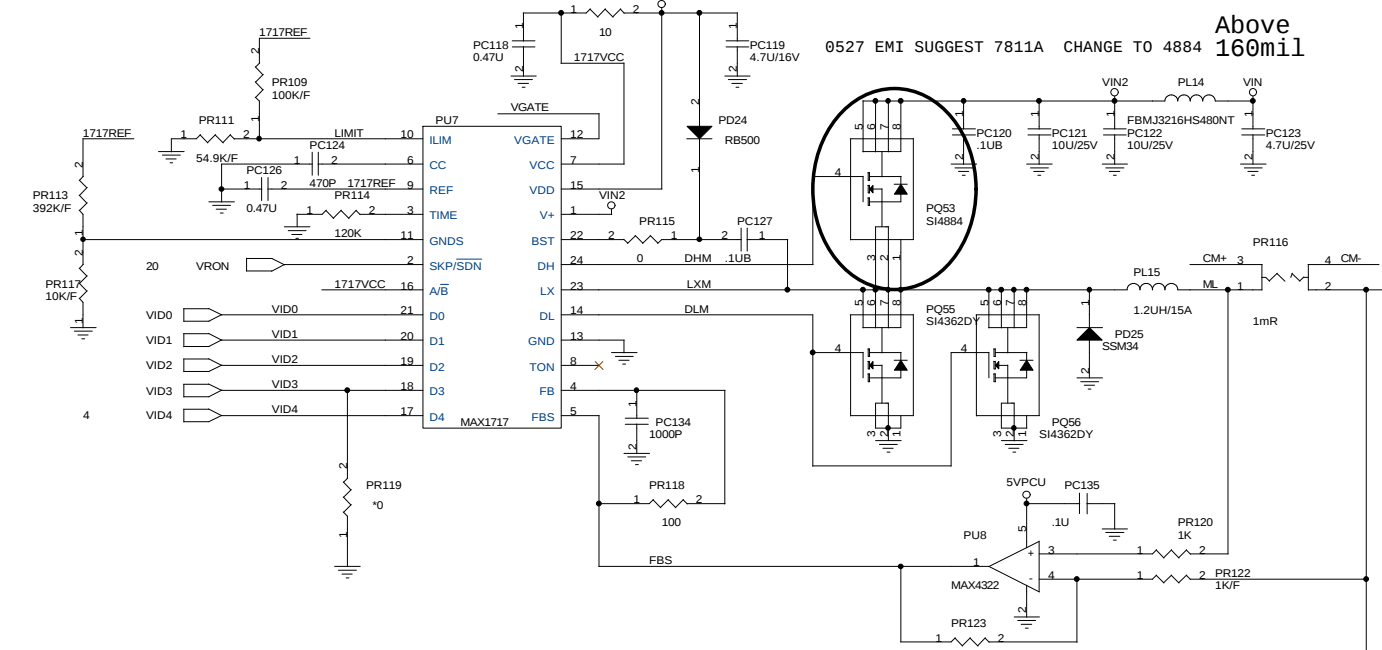
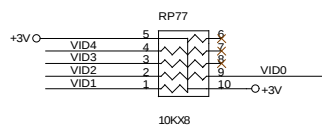




RevB_0118: Changed
for 75W Ada

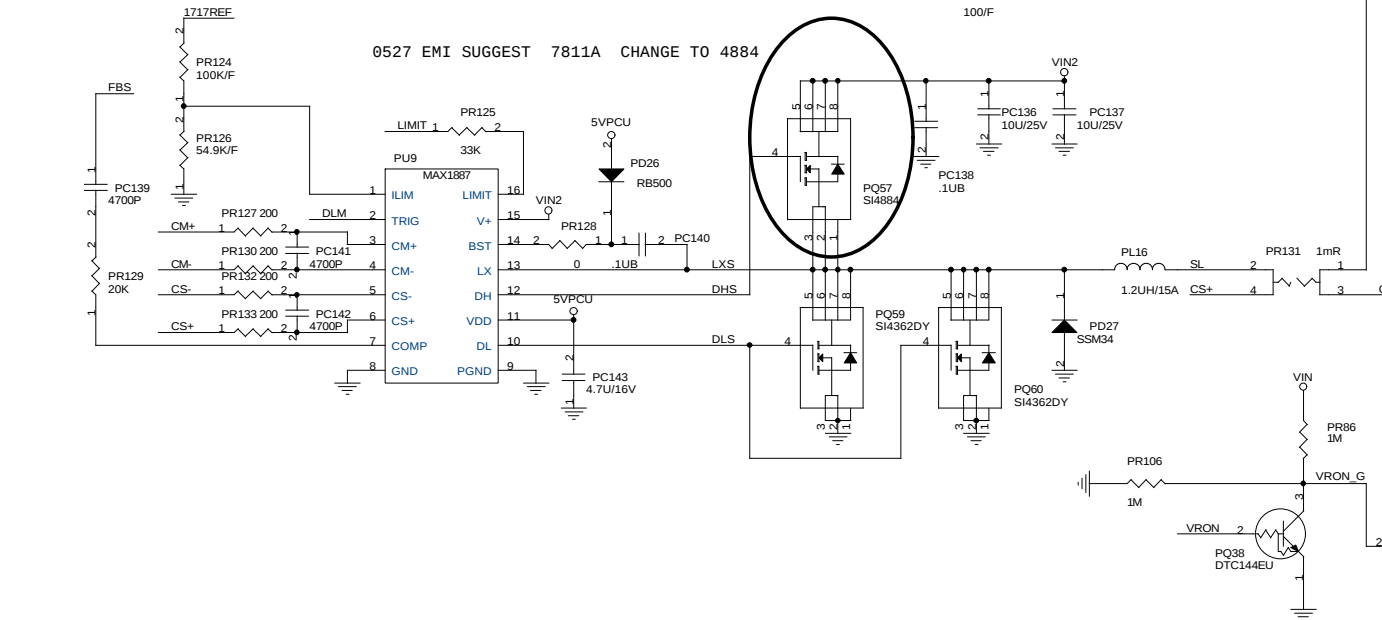






VTT VOLTAGE SETTING
0=GND 1=FLAOT

D4 = 1	D4 = 0	D3	D2	D1	D0
1.275	2.00	0	0	0	0
1.250	1.95	0	0	0	1
1.225	1.90	0	0	1	0
1.200	1.85	0	0	1	1
1.175	1.80	0	1	0	0
1.150	1.75	0	1	0	1
1.125	1.70	0	1	1	0
1.100	1.65	0	1	1	1
1.075	1.60	1	0	0	0
1.050	1.55	1	0	0	1
1.025	1.50	1	0	1	0
1.000	1.45	1	0	1	1
0.975	1.40	1	1	0	0
0.950	1.35	1	1	0	1
0.925	1.30	1	1	1	0
SHUTDOWN	SHUTDOWN	1	1	1	1



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The AMC20463-002 modem is used for mother board family MBAMC20463-002.



With 12 mil to connect both GND



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