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HIERARCHY						SHEET		PDF-SHEET	
A5B00900202727-33603						index		sheet 1	
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						poslist1		sheet 3	
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						sheet7		sheet 10	
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sheet10		sheet 13							
sheet11		sheet 14							

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A	1.5V /sheet7 14 B /sheet5 2 C	/sheet9 14 D /sheet5 14 D	/sheet1 7 A /sheet1 3 G	/sheet3 13 F	/sheet5 9 F /page1 3 B	/sheet4 4 B /sheet4 2 B /sheet1 6 B	/sheet5 1 E	/sheet7 7 C /sheet5 4 C /sheet5 9 B	/sheet7 10 D /sheet7 8 E		A
	A(0) /sheet6 6 C /sheet6 11 D	CCVCC /sheet9 14 E /sheet7 14 B	HALL_1 /sheet5 15 E /sheet11 7 G	KB6 /sheet5 8 E /sheet3 13 D	OEN /sheet5 10 F	RF_RAMP1 /sheet5 6 C /sheet4 2 C /sheet4 4 C /sheet1 2 G	/sheet5 9 F /page1 3 C	VDD_SIM /sheet5 15 D /sheet5 4 C	WA_DOG /sheet7 6 C /sheet5 10 C		
	A(0:23) /sheet6 15 E /sheet6 3 D /sheet6 8 D /sheet5 13 F	CHARGE_UC /sheet7 10 C /sheet6 15 F /sheet5 15 E	HF_OUT /sheet1 9 G	/sheet7 7 C /sheet5 7 E /sheet3 13 D	POWER /sheet8 3 G /sheet8 9 E /sheet7 2 C	RD /sheet6 15 E /sheet6 11 C /sheet6 11 D /sheet6 6 D /sheet5 10 F	RF_SIN26M /sheet5 11 C /sheet4 4 C /sheet4 2 C /sheet1 7 D	TDI /sheet5 9 F /page1 3 C	VDIG_2.65V /sheet8 11 F /sheet7 2 G /sheet7 11 C /sheet7 14 B /sheet7 2 F /sheet5 10 A /sheet5 4 C /sheet5 10 B /sheet5 13 B /sheet11 7 F /page1 2 C /page1 2 B	WR /sheet6 15 E /sheet8 11 C /sheet6 11 C /sheet6 6 C /sheet5 10 F	
	A(23) /sheet5 8 E	CS_FLASH_1 /sheet6 15 E /sheet6 11 C /sheet6 6 C /sheet5 10 F	HF_OUT1 /sheet4 10 E /sheet2 4 C /sheet1 11 G	KB8 /sheet5 8 E /sheet3 13 D	KB9 /sheet5 8 E /sheet3 13 D	RESET_UC /sheet7 11 C /sheet6 11 B /sheet6 6 C /sheet6 11 C /sheet5 12 C	RF_STR /sheet5 14 E /sheet4 2 A /sheet4 4 A /sheet1 9 D	TDO /sheet5 9 F /page1 3 C	TMS /sheet5 9 F /page1 3 C	TRIGIN /sheet6 15 F /sheet5 9 F	
	AC_D+_RTS /sheet8 15 F /sheet5 9 C	CS_SRAM /sheet6 15 E /sheet6 6 C	HW_CODING1 /sheet5 12 B /sheet5 8 D	LCD_CLK /sheet5 9 C	LCD_CS1 /sheet5 10 C /sheet11 8 C	RF_TXONPA /sheet5 8 E /sheet4 2 D /sheet4 4 D /sheet1 1 F	TRIGOUT /sheet6 15 F /sheet5 9 F	VEBU_1.8 /sheet7 5 E /sheet6 11 A	VEBU_1.8FLASH /sheet6 13 A /sheet6 11 E /sheet6 11 C /sheet6 4 C		B
B	AC_D-_CTS /sheet8 15 E /sheet5 9 C	D(0:15) /sheet6 15 B /sheet6 6 D /sheet6 11 D /sheet5 12 C	HW_CODING2 /sheet5 12 B /sheet5 14 E	LCD_CS2 /sheet5 15 E /sheet11 8 C	RF_AFC /sheet5 10 C /sheet4 2 C /sheet4 4 C	RINGIN /sheet7 7 E /sheet5 10 C	TRST /sheet5 9 F /page1 3 C	VEBU_2.9 /sheet7 5 F /sheet6 2 B /sheet6 13 B /sheet6 8 B /sheet5 4 B /sheet5 11 G /sheet5 13 B /sheet5 4 C /sheet11 8 C /sheet11 8 D /sheet10 3 D			
	AC_RX /sheet8 15 E /sheet5 9 C	D+_RTS /sheet8 9 E	HW_CODING3 /sheet5 12 B /sheet5 14 E	LCD_CS2 /sheet5 15 E /sheet11 8 C	RF_ANT_DET /sheet5 14 D /sheet4 2 D /sheet4 4 D /sheet2 7 D	RTC_INT /sheet7 7 C /sheet5 12 C	TVCO /sheet5 14 A /sheet5 1 D /sheet4 4 D /sheet4 2 D /sheet1 2 C	VEBU_2.9FLASH /sheet6 2 C /sheet6 6 C /sheet6 6 E /sheet6 11 D /sheet6 8 C /sheet6 2 B			
	AC_TX /sheet8 15 E /sheet5 9 C	D-_CTS /sheet8 9 E	HW_CODING4 /sheet5 8 D /sheet5 12 A	LCD_CS2 /sheet5 15 E /sheet11 8 C	RF_BAND_SW /sheet5 8 D /sheet4 2 F /sheet4 4 F /sheet1 1 F	RX /sheet8 3 G /sheet8 9 E	VBAT /sheet5 9 B /sheet5 1 E	VIBRA_CONTROL /sheet9 12 B /sheet11 8 B			
	AGND /sheet5 5 G /sheet5 3 G /sheet5 4 F /sheet5 5 F	DCS_TX /sheet1 7 A /sheet1 3 F	HW_CODING5 /sheet5 9 C /sheet5 10 C /sheet5 15 B	LCD_DAT /sheet5 10 C /sheet11 8 B	RF_CLK /sheet5 14 F /sheet4 2 A /sheet4 4 A /sheet1 9 D	RX1 /sheet8 3 G /sheet8 9 E	VBAT /sheet5 9 B /sheet5 1 E	VIBRA_UC /sheet9 10 B /sheet5 14 D			
C	AKKU_TYP /sheet8 4 A /sheet5 11 A	EPN1 /sheet7 13 D /sheet11 6 C	I2C_CLK /sheet7 10 C /sheet6 15 F /sheet5 9 C	LIGHT_KB /sheet5 15 E /sheet10 2 F	RF_FE_DTR_DCS /sheet5 8 D /sheet4 2 C /sheet4 4 C /sheet1 6 F	RX1X /sheet1 8 B /sheet1 7 F	VBOOST /sheet7 10 F /sheet11 8 B	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C	VDD_CHARGE /sheet7 5 B /sheet7 7 C	VMIC /sheet5 5 A /sheet5 4 A	C
	AUDIO_L /sheet8 9 F /sheet7 13 D	EPP1 /sheet7 13 D /sheet11 6 C	I2C_DAT /sheet7 10 C /sheet6 15 F /sheet5 9 C	LIGHT_K_LCD /sheet11 8 C /sheet10 8 B	RF_DAT /sheet5 14 F /sheet4 2 B /sheet4 4 B /sheet1 9 D	RX2 /sheet1 8 C /sheet1 8 F	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C	VDD_CHARGE /sheet7 5 B /sheet7 7 C	VMIC /sheet5 5 A /sheet5 4 A		
	AUDIO_R /sheet8 9 E /sheet7 13 D	EPP1_FIL /sheet7 4 D /sheet5 6 E /sheet5 8 A	I2C_IRQ /sheet7 10 C /sheet6 15 F /sheet5 11 D	LIGHT_K_LED /sheet11 8 C /sheet10 7 C	RF_FE_DTR_GSM /sheet5 8 E /sheet4 4 C /sheet4 2 C /sheet1 6 F	RX2X /sheet1 8 B /sheet1 8 F	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C	VDD_CHARGE /sheet7 5 B /sheet7 7 C	VMIC /sheet5 5 A /sheet5 4 A		
	BATT+ /sheet8 4 B /sheet7 5 B /sheet7 7 D /sheet7 7 A /sheet7 1 F /sheet7 6 F /sheet5 9 A /sheet11 8 B /sheet10 3 E /sheet1 2 F	FLASH_DCD /sheet8 9 E	I2SCA0 /sheet7 7 E /sheet5 15 E	LIGHT_LCD /sheet5 11 C /sheet10 4 A	RF_FE_DTR_GSM /sheet5 8 E /sheet4 4 C /sheet4 2 C /sheet1 6 F	RX3 /sheet1 8 C /sheet1 8 F	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C	VDD_CHARGE /sheet7 5 B /sheet7 7 C	VMIC /sheet5 5 A /sheet5 4 A		
	BHE /sheet6 15 E /sheet6 6 C /sheet6 11 C /sheet5 10 F	F_WAIT /sheet6 15 E /sheet6 6 C /sheet6 11 C	I2SDA0 /sheet7 7 E /sheet5 15 E	LIGHT_LED /sheet5 10 G /sheet10 4 B	RF_FE_DTR_GSM /sheet5 8 E /sheet4 4 C /sheet4 2 C /sheet1 6 F	RX3X /sheet1 8 C /sheet1 8 F	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C	VDD_CHARGE /sheet7 5 B /sheet7 7 C	VMIC /sheet5 5 A /sheet5 4 A		
D	BREF /sheet5 15 A /sheet5 1 E	GND_MIC /sheet9 4 C /sheet8 9 E /sheet5 6 C /sheet5 6 A /sheet5 4 B /sheet5 4 B /sheet5 1 B /sheet5 4 G	KB0 /sheet5 8 E /sheet3 13 D	MICN1 /sheet9 5 B /sheet5 3 B /sheet5 1 D	RF_I /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 6 B	SLEEPQ /sheet7 7 B /sheet5 8 D	VDD_RF1 /sheet7 14 B /sheet4 4 E /sheet4 2 E /sheet2 9 D /sheet1 5 C /sheet1 7 A	VPP /sheet8 3 G /sheet6 3 C	VREGA /sheet7 7 E		D
	CCCLK /sheet9 14 D /sheet5 14 D	GND_STEPUP /sheet7 8 G /sheet7 11 F /sheet7 7 F /sheet7 8 F /sheet7 10 G /sheet7 7 G /sheet7 9 G	KB2 /sheet5 8 E /sheet3 13 F	MICP1 /sheet9 5 B /sheet5 3 B /sheet5 1 D	RF_IX /sheet5 1 E /sheet4 4 B /sheet4 2 B /sheet1 6 B	STEPUP_SHUD /sheet7 7 G /sheet5 14 D	VDD_RF2 /sheet7 14 B /sheet4 2 E /sheet4 4 E /sheet1 7 A /sheet1 7 E /sheet1 9 B	VPP /sheet8 3 G /sheet6 3 C	VREGA /sheet7 7 E		
	CCIO /sheet9 14 E /sheet5 14 D	GSM_TX /sheet7 8 G /sheet7 11 F /sheet7 7 F /sheet7 8 F /sheet7 10 G /sheet7 7 G /sheet7 9 G	KB4 /sheet5 8 E /sheet3 13 E	MICP2 /sheet8 9 E /sheet5 4 A /sheet5 1 D	RF_Q /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 6 B	TBAT /sheet5 11 A	VDD_RTC /sheet7 14 B	VPP /sheet8 3 G /sheet6 3 C	VREGA /sheet7 7 E		
	CCRST		KB5 /sheet5 8 E	MON1 /sheet5 9 F	RF_QX /sheet5 1 E						
				MON2							
E											E

				DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	DONG WEI		
				APPR'D			
				COM MD PD HW4 BJ		241096	netlist1
				Siemens AG			
1	33604	12.06.2005	DW				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900202728-33604)			

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			USER	DONG WEI		
			APPR'D			
				COM MD PD HW4 BJ		
				Siemens AG	241096	poslist1
1	33604	12.06.2005	DW			
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900202728-33604)		

4001-4099

[illegible]

SR-RF-X75-FC

X300

1

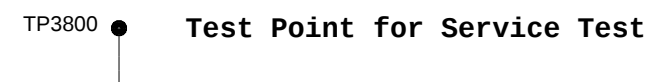
GND

NB = nicht bestueckt
NG = keine Sachnummer

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				DATE: 12.06.2005		Quail B2 Mainboard V1 Variant 1 TOP HIERARCHY LEVEL SCHEMATIC		1:1
				USER: DONG WEI				
				APPRD:				
				COM MD PD HW4 HB4 J3				
				Siemens AG		241096		sheet1
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VERS	CHANGE NUMBER	DATE	NAME					

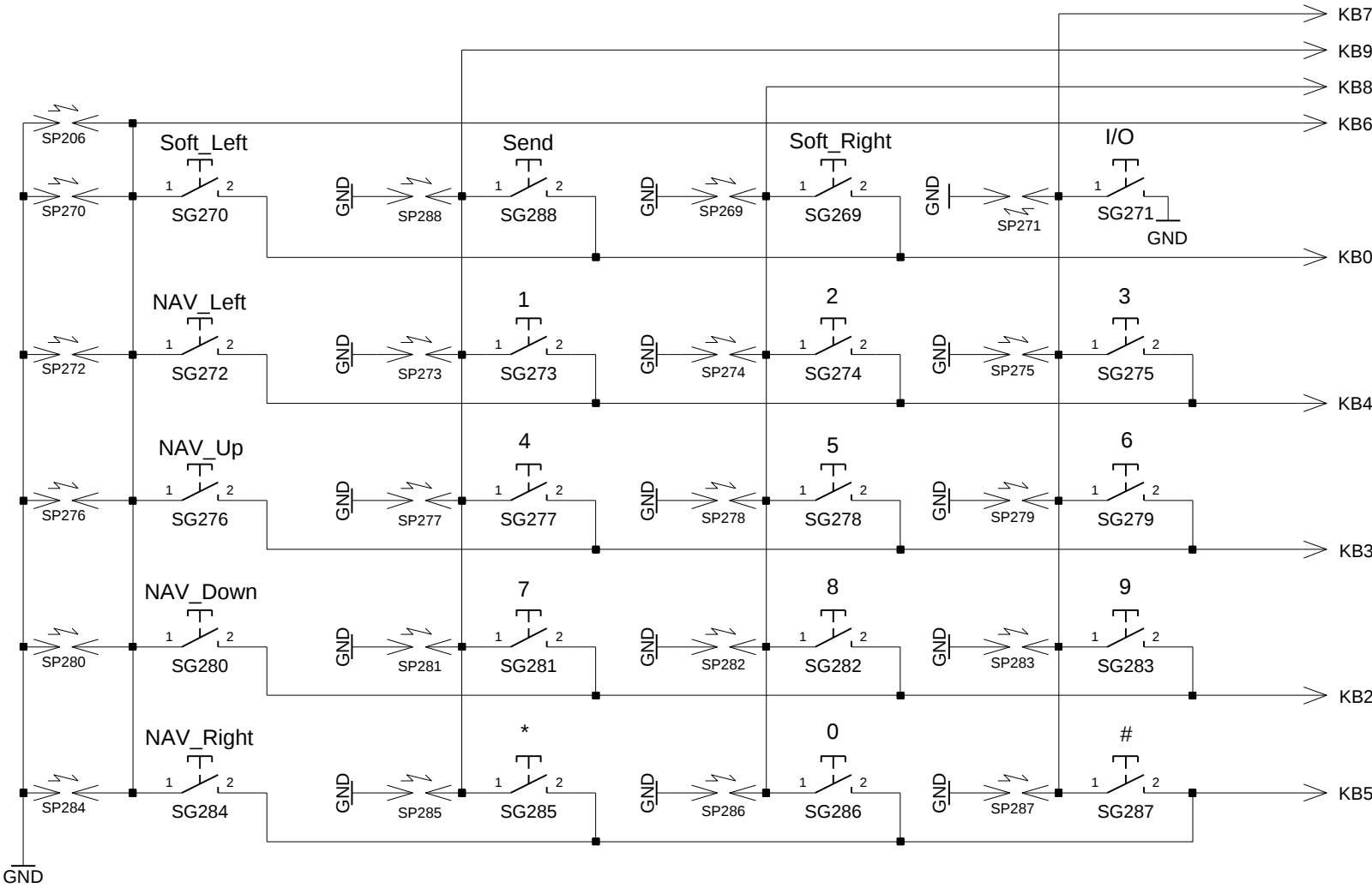
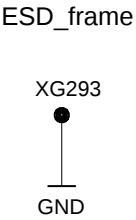


				DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 TOP HIERARCHY LEVEL SCHEMATIC	1:1
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1	33604	12.06.2005	DW				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900202728-33604)			

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MMI MAINBOARD

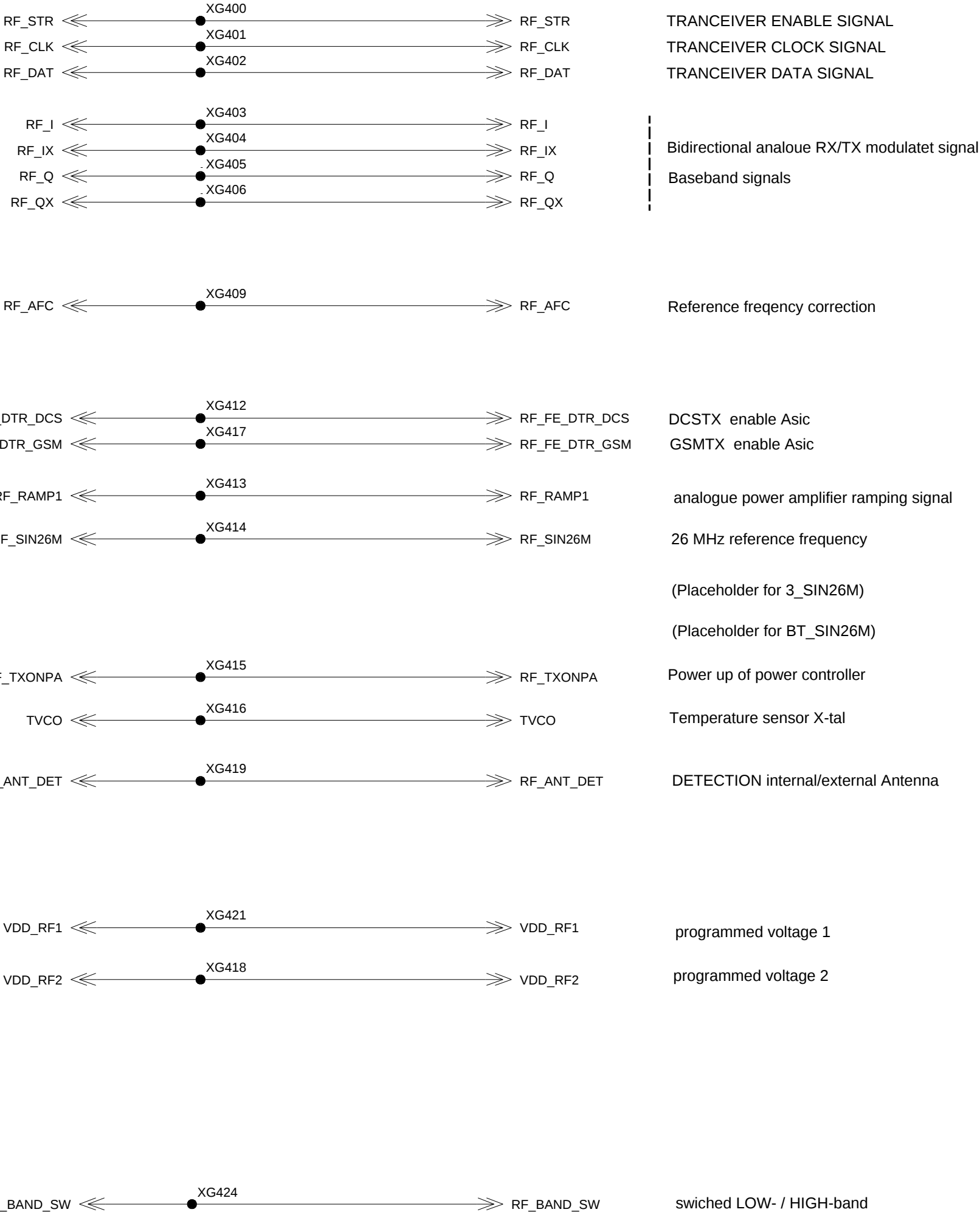


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1	33604	12.06.2005	DW	241096			sheet3
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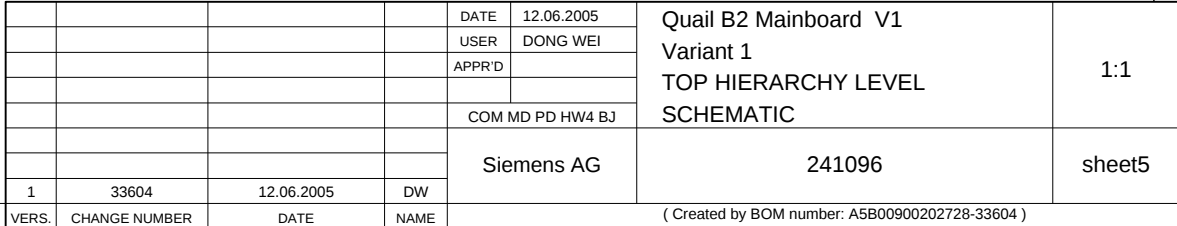
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HIT_POINTS LOGIK/HF_PART_1_SD2
400-449



HF_OUT1 << XG2826

				DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	DONG WEI		
				APPR'D			
				COM MD PD HW4 BJ			
				Siemens AG		241096	sheet4
1	33604	12.06.2005	DW	(Created by BOM number: A5B00900202728-33604)			
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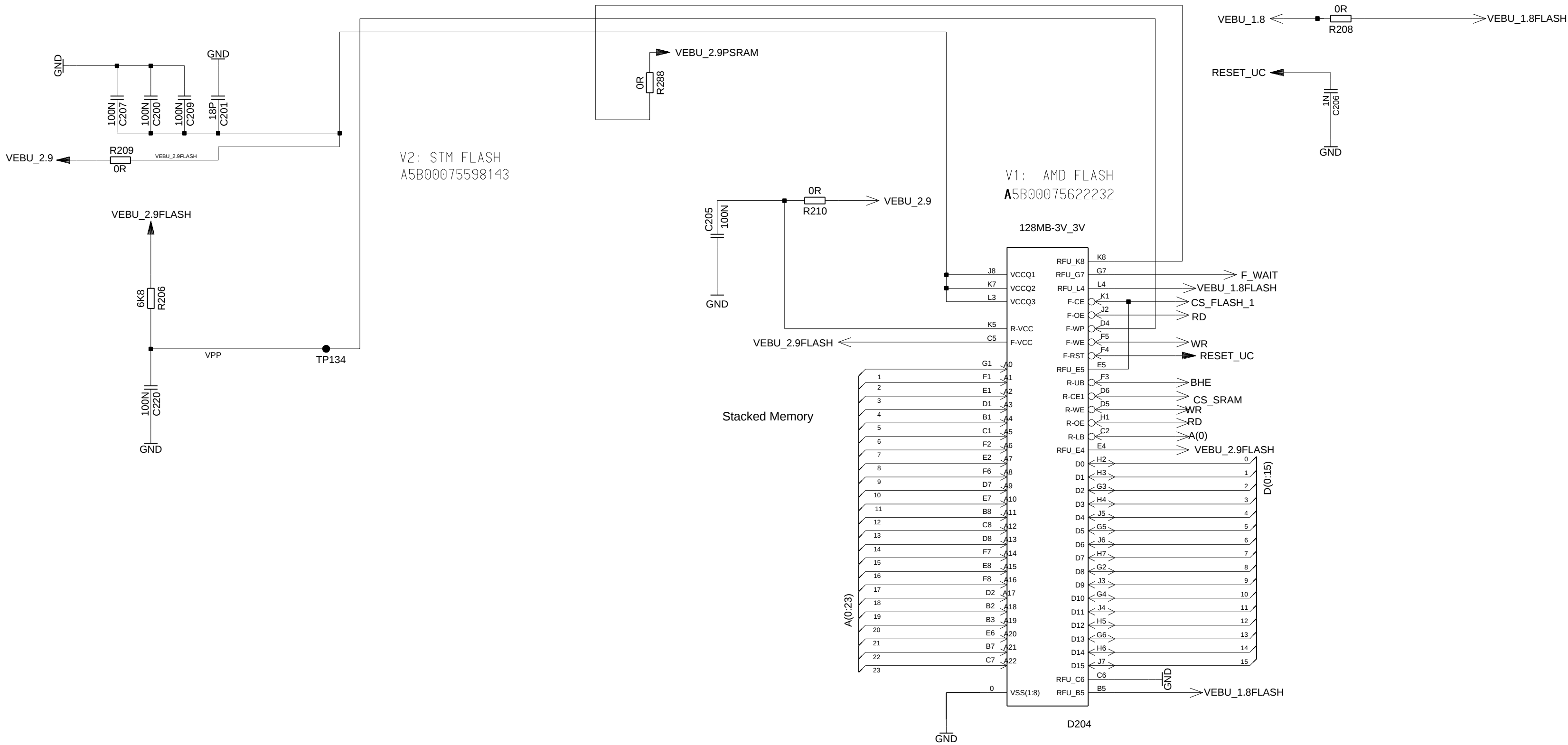
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C209 close to J8
C207 close to K7
C200 close to L3
TP134 placed on bottom side for Nano I/O Fast Booting

NB_C210 close to L4
NB_C211 close to B5
C205 close to K5

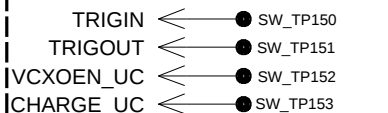
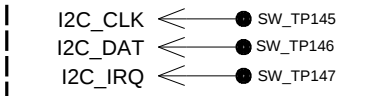
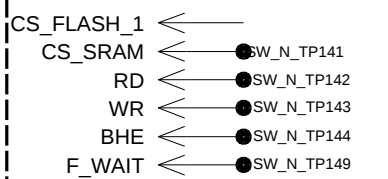
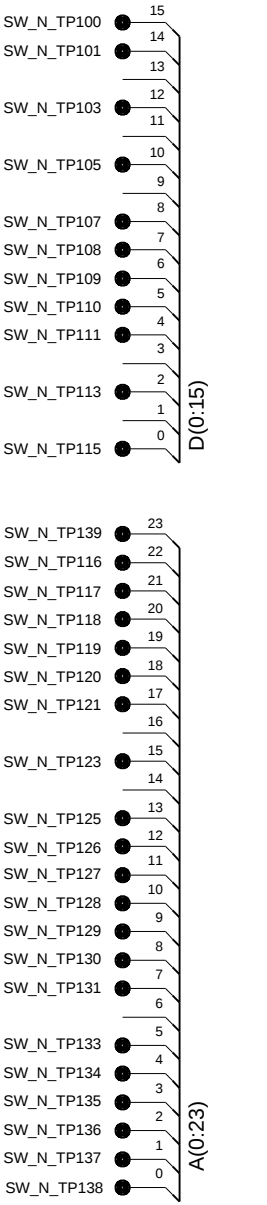
BASEBAND_FLASH_GSM

201 - 210



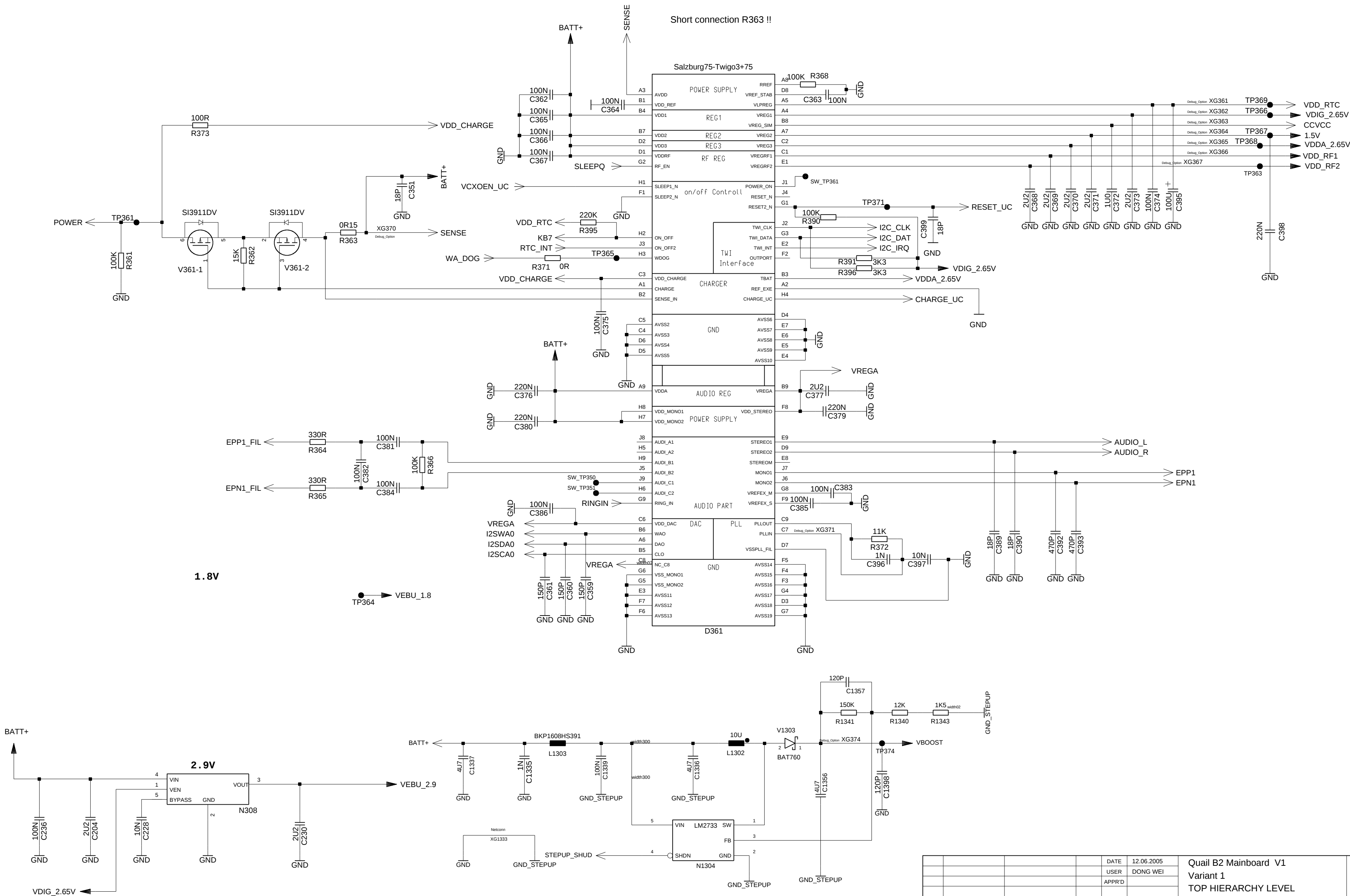
SW - TESTPUNKTE

100 - 153



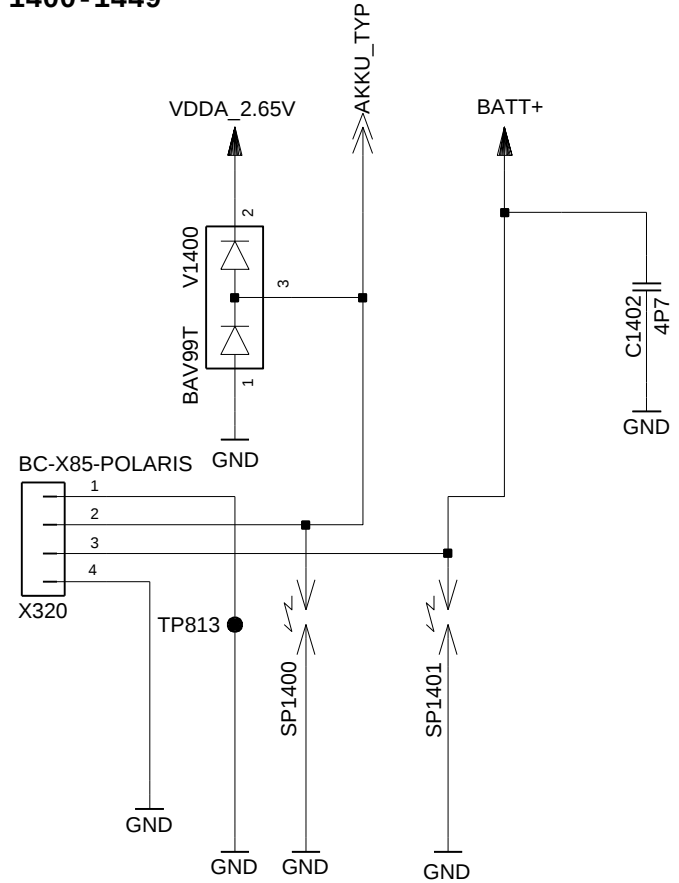
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1	33604	12.06.2005	DW	(Created by BOM number: A5B00900202728-33604)			
VERS.	CHANGE NUMBER	DATE	NAME				

BASEBAND_POWERSUPPLY_GSM



		DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 TOP HIERARCHY LEVEL SCHEMATIC	
		USER	DONG WEI		
		APPR'D			
		COM MD PD HW4 BJ			
		Siemens AG		241096	sheet7
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VERS.	CHANGE NUMBER	DATE	NAME		

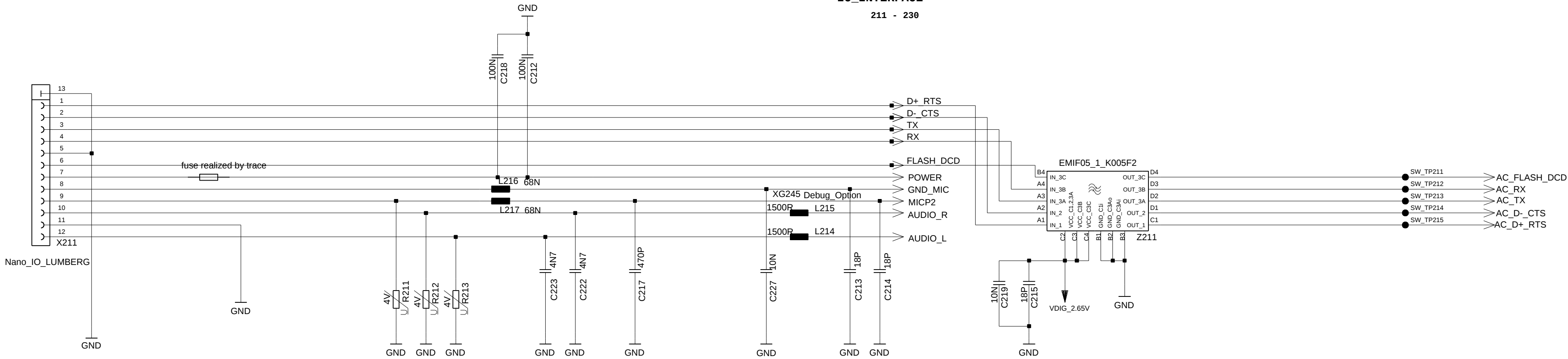
BATTERY CONNECTOR
1400-1449



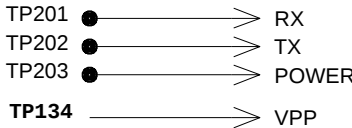
Please pay attention to the Battery connector pin1 and pin3
Because connector be rotated 180 degree in Quail layout
So pin1 and pin3 be exchanged in Quail schematic

fuse: shorted footprint with trace between pads at layout

IO_INTERFACE
211 - 230



Test Points for Fast Booting during ramp-up
Must be visible after assembled with mechanical parts (lift-part and baselower)

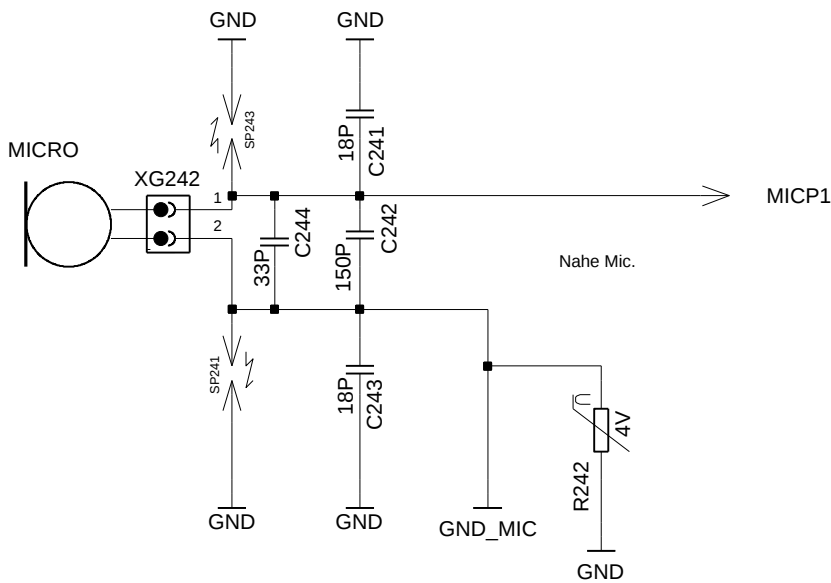


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				APPR'D			
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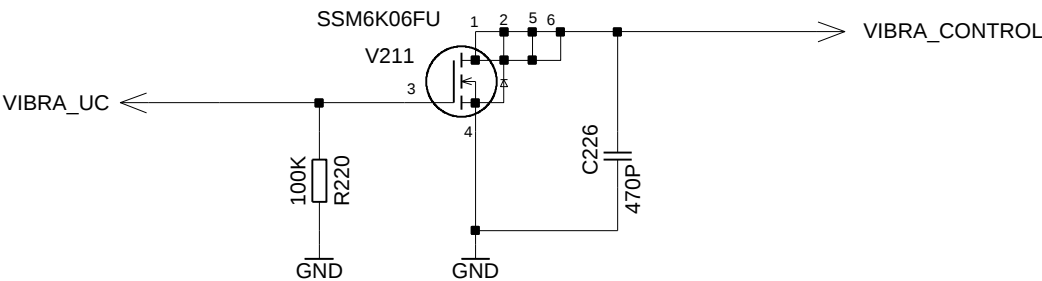
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MICROPHONE_CIRCUIT
241-250

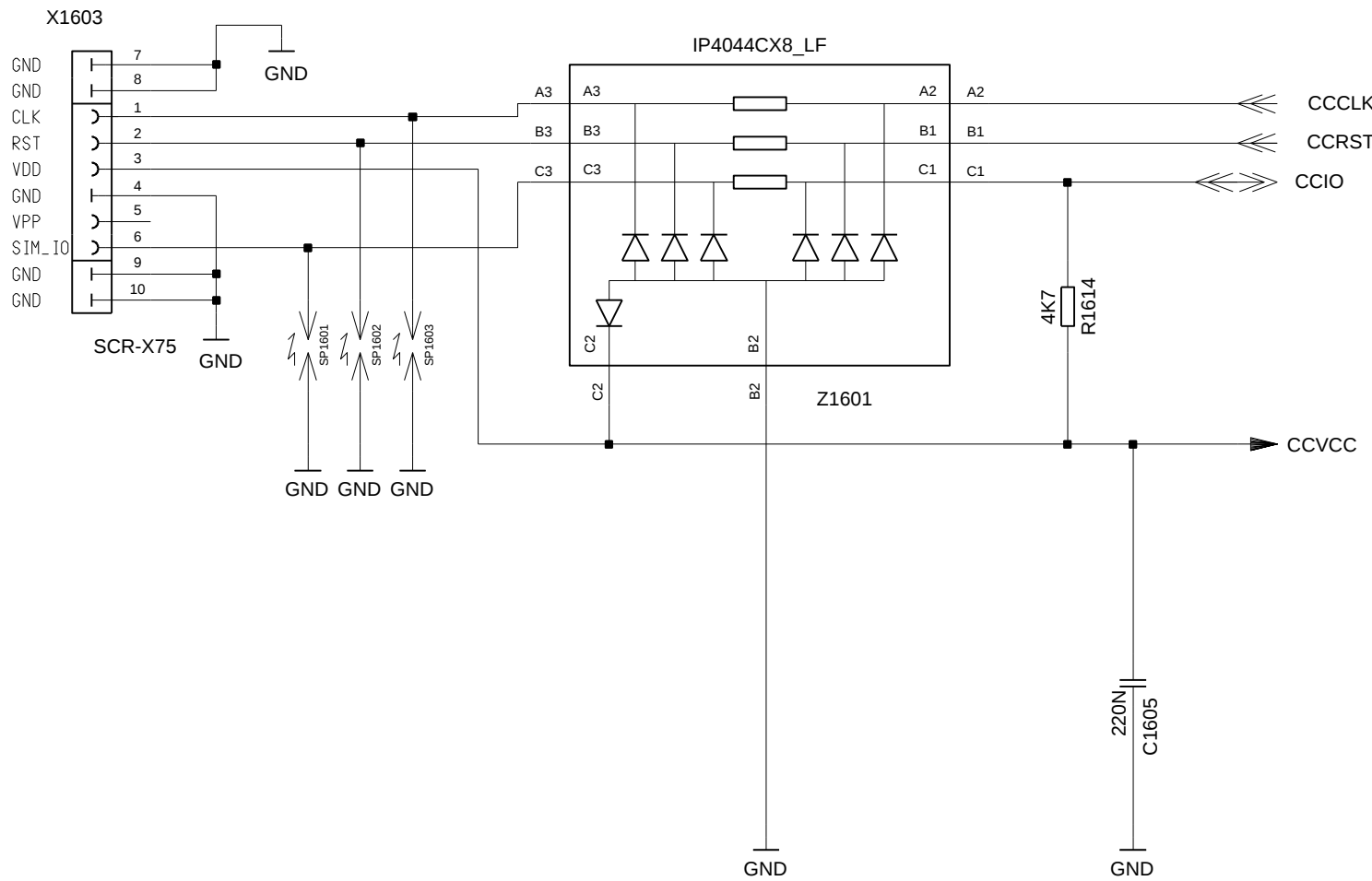


VIBRA_CIRCUIT
211 - 230

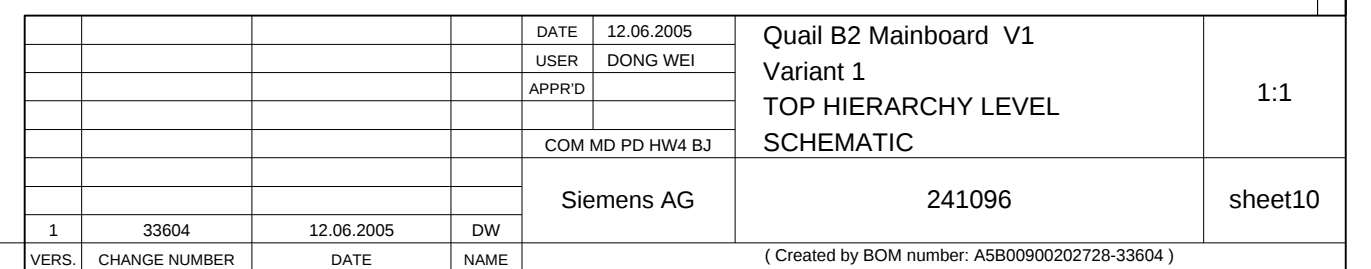
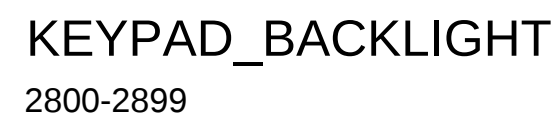


SIM_CONNECTOR

1600-1659



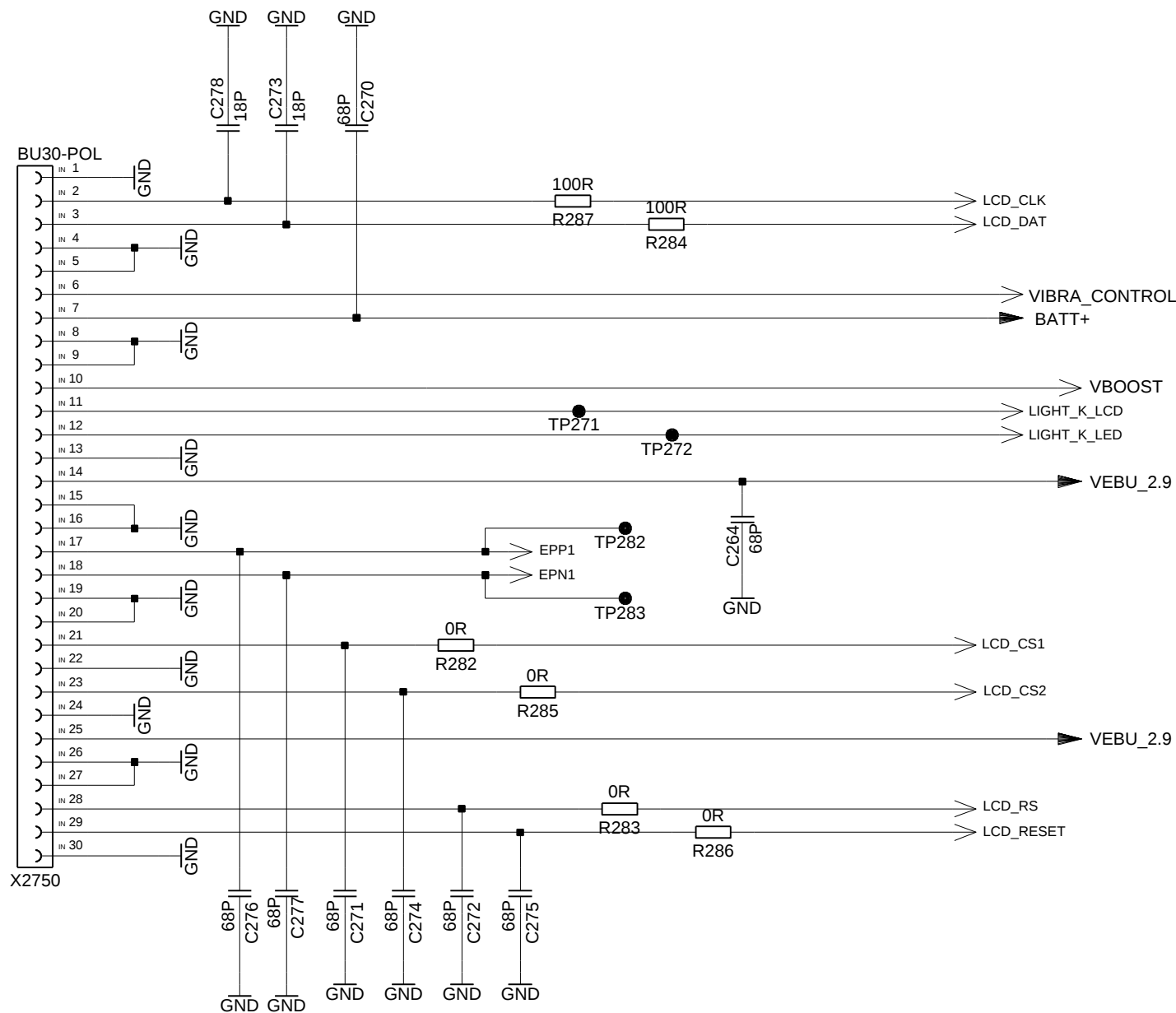
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				USER	DONG WEI		
				APPR'D			
				COM MD PD HW4 BJ			
				Siemens AG		241096	sheet9
1	33604	12.06.2005	DW	(Created by BOM number: A5B00900202728-33604)			
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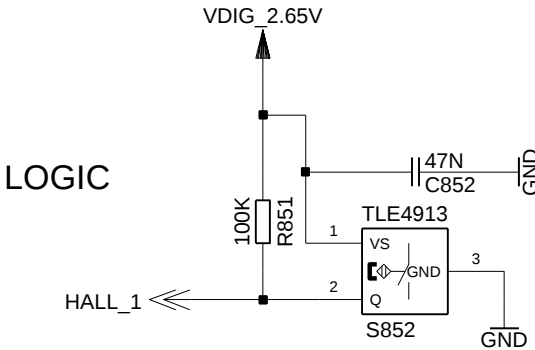
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MMI CONNECTOR
270-280



TP270 ● Fiducial for B2B connector automatic assembly

MAGNETIC SWITCH

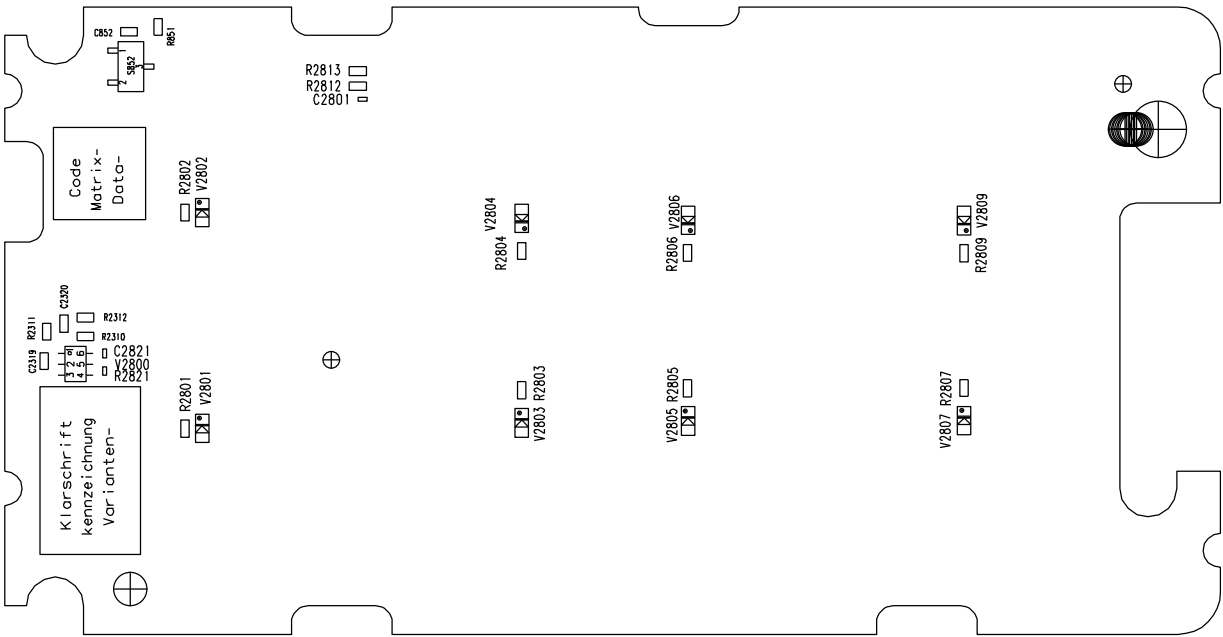


				DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	DONG WEI		
				APPR'D			
				COM MD PD HW4 BJ			
				Siemens AG		241096	sheet11
1	33604	12.06.2005	DW	(Created by BOM number: A5B00900202728-33604)			
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C852	4A
C2319	3B
C2320	4B
C2801	5A
C2821	4B
R851	4A
R2310	4B
R2311	3B
R2312	4B
R2801	4B
R2802	4B
R2803	6B
R2804	6B
R2805	6B
R2806	6B
R2807	8B
R2809	8B
R2812	5A
R2813	5A
R2821	4B
S852	4A
V2800	4B
V2801	4B
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V2805	6B
V2806	6B
V2807	8B
V2809	8B



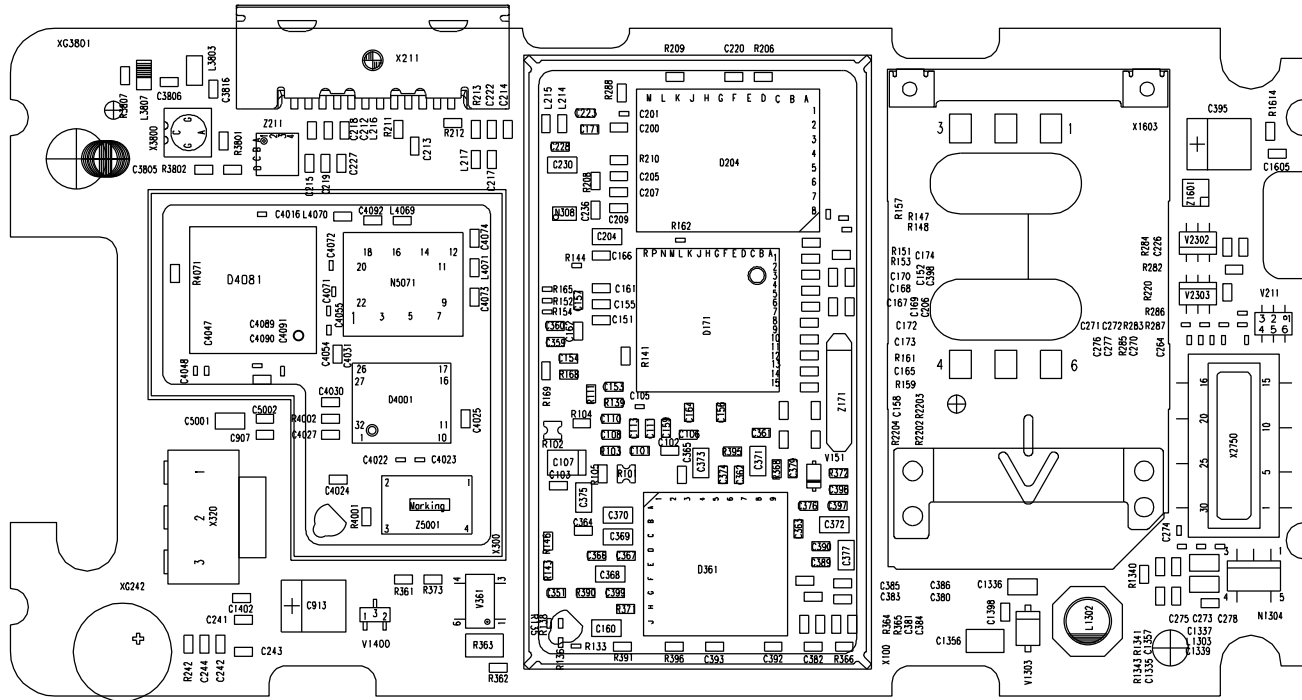
Achtung : Höhenprofil C24_---_---_42 (gilt nur fuer Entwicklung)
Liefervarianten siehe : V24851-Z2000-A1--35

				DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 WIRING DIAGRAM (SOLDER SIDE 1)	3:1
				NAME	DONG WEI		
				APPR'D			
				STD			
				COM MD PD HW4 BJ		241097	SHEET 1+
				SIEMENS AG			
1	33604	12.06.2005	DW				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900202728-33604)			

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ALLE RECHTE VORBEHALTEN

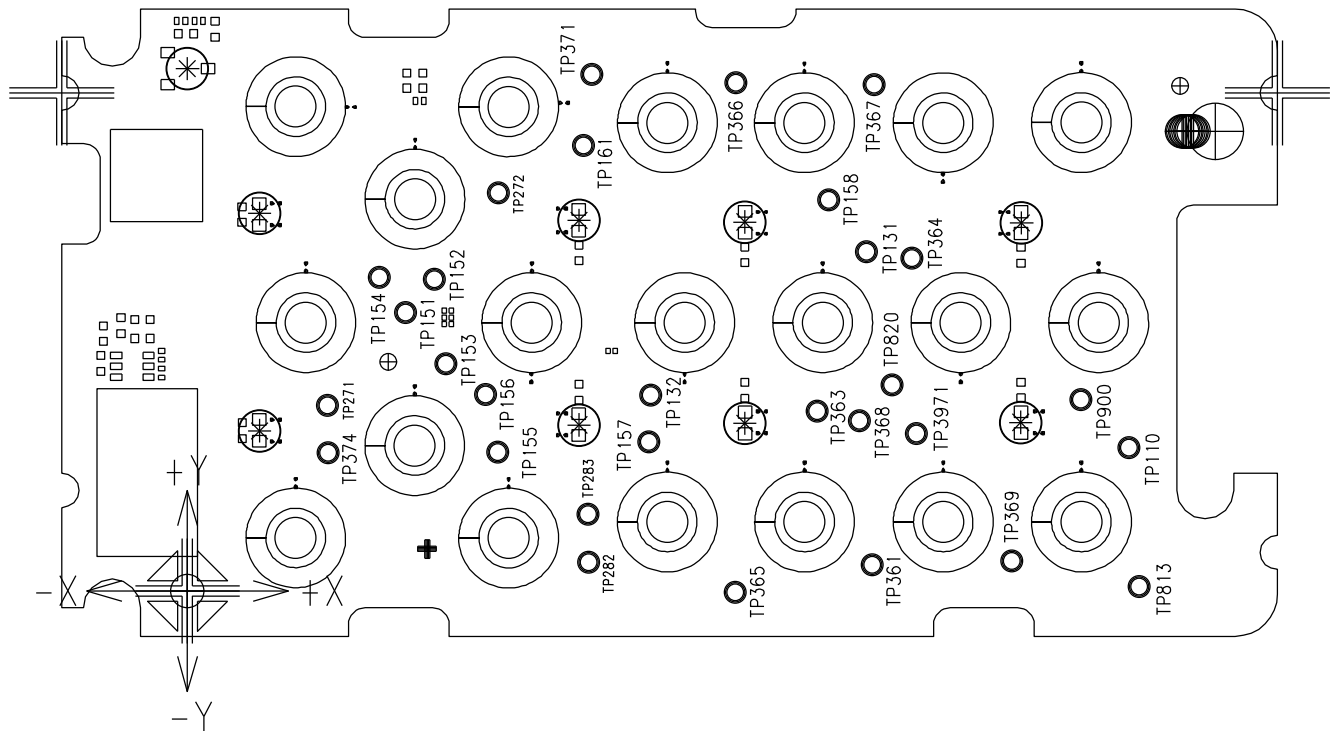
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C102	7B	C220	8A	C383	8B	C4092	6A	R162	7A	R4071	5B
C103	7B	C222	7A	C384	8C	C5001	5B	R165	7B	V151	8B
C105	7B	C223	7A	C385	8B	C5002	6B	R168	7B	V211	10B
C106	7B	C226	10A	C386	8B	D171	8A	R169	7B	V361	7C
C107	7B	C227	6A	C389	8B	D204	8A	R206	8A	V1303	9C
C108	7B	C228	7A	C390	8B	D361	7B	R208	7A	V1400	6C
C110	7B	C230	7A	C392	8C	D4001	6B	R209	7A	V2302	10A
C111	7B	C236	7A	C393	7C	D4081	6B	R210	7A	V2303	10B
C113	7B	C241	6C	C395	10A	L214	7A	R211	6A	X100	8C
C151	7B	C242	5C	C396	8B	L215	7A	R212	6A	X211	6A
C152	8A	C243	5C	C397	8B	L216	6A	R213	6A	X300	7B
C153	7B	C244	5C	C398	8B	L217	6A	R220	10B	X320	5B
C154	7B	C264	10B	C399	7B	L1302	9C	R242	5C	X1603	9A
C155	7B	C270	10B	C907	6B	L1303	10B	R282	10A	X2750	10B
C156	8B	C271	10B	C913	6B	L3803	5A	R283	10B	X3800	5A
C157	7B	C272	10B	C1335	10B	L3807	5A	R284	10A	Z171	8B
C158	8B	C273	10B	C1336	9B	L4069	6A	R285	10B	Z211	6A
C159	7B	C274	10B	C1337	10B	L4070	6A	R286	10B	Z1601	10A
C160	7C	C275	10B	C1339	10B	L4071	6A	R287	10B	Z5001	6B
C161	7B	C276	10B	C1356	9C	N308	7A	R288	7A		
C162	7B	C277	10B	C1357	10B	N1304	10B	R361	6B		
C164	7B	C278	10B	C1398	9C	N5071	6B	R362	7C		
C165	8B	C351	7B	C1402	5B	R101	7B	R363	6C		
C166	7A	C359	7B	C1605	10A	R102	7B	R364	8C		
C167	8B	C360	7B	C3805	5A	R103	7B	R365	8C		
C168	8B	C361	8B	C3806	5A	R104	7B	R366	8C		
C169	8B	C362	8B	C3816	5A	R105	7B	R368	8B		
C170	8A	C363	8B	C4016	6A	R111	7B	R371	7C		
C171	7A	C364	7B	C4022	6B	R133	7C	R372	8B		
C172	8B	C365	7B	C4023	6B	R135	7C	R373	6B		
C173	8B	C366	7B	C4024	6B	R136	7C	R390	7B		
C174	8A	C367	7B	C4025	6B	R138	7C	R391	7C		
C200	7A	C368	7B	C4027	6B	R139	7B	R395	8B		
C201	7A	C369	7B	C4030	6B	R141	7B	R396	7C		
C204	7A	C370	7B	C4031	6B	R143	7B	R1340	9B		
C205	7A	C371	8B	C4047	5B	R144	7A	R1341	9B		
C206	8B	C372	8B	C4048	5B	R146	7B	R1343	9B		
C207	7A	C373	7B	C4054	6B	R147	8A	R1614	10A		
C209	7A	C374	8B	C4055	6B	R148	8A	R2202	8B		
C212	6A	C375	7B	C4071	6B	R151	8A	R2203	8B		
C213	6A	C376	8B	C4072	6A	R152	7B	R2204	8B		
C214	7A	C377	8B	C4073	6B	R153	8A	R3801	5A		
C215	6A	C379	8B	C4074	6A	R154	7B	R3802	5A		
C217	7A	C380	8B	C4089	6B	R157	8A	R3807	5A		
C218	6A	C381	8C	C4090	6B	R159	8B	R4001	6B		



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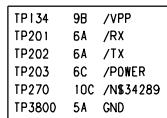
ALS BETRIEBSGEIMNIS ANVERTRAUT
ALLE RECHTE VORBEHALTEN

TP110	8B	/TVCO
TP131	7B	/BREF
TP132	6B	/TBAT
TP151	5B	/MON2
TP152	5B	/TMS
TP153	5B	/TCK
TP154	5B	/TD1
TP155	5B	/TRST
TP156	5B	/TD0
TP157	6B	/KB7
TP158	7A	VEBU_2.9
TP161	6A	/MON1
TP271	4B	/LIGHT_K_LCD
TP272	5A	/LIGHT_K_LED
TP282	6C	/EPP1
TP283	6B	/EPN1
TP361	7C	/POWER
TP363	7B	VDD_RF2
TP364	7B	VEBU_1.8
TP365	6C	/NS5679
TP366	6A	VD1G_2.65V
TP367	7A	1.5V
TP368	7B	VDDA_2.65V
TP369	8C	/VDD_RTC
TP371	6A	RESET_UC
TP374	4B	VB00ST
TP813	8C	GND
TP820	7B	/RF_S1N26M
TP900	8B	/RF_TXONPA
TP3971	7B	VDD_RF1



				DATE	12.06.2005	Quail B2 Mainboard V1 Variant 1 TESTPOINT DIAGRAMM (SIDE 1)	3:1
				NAME	DONG WEI		
				APPR'D			
				STD			
				COM MD PD HW4 BJ		241098	SHEET 1+
1	33604	12.06.2005	DW	SIEMENS AG		(Created by BOM number: A5B00900202728-33604)	
VERS.	CHANGE NUMBER	DATE	NAME				

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ALLE RECHTE VORBEHALTEN



VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900202728-33604
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