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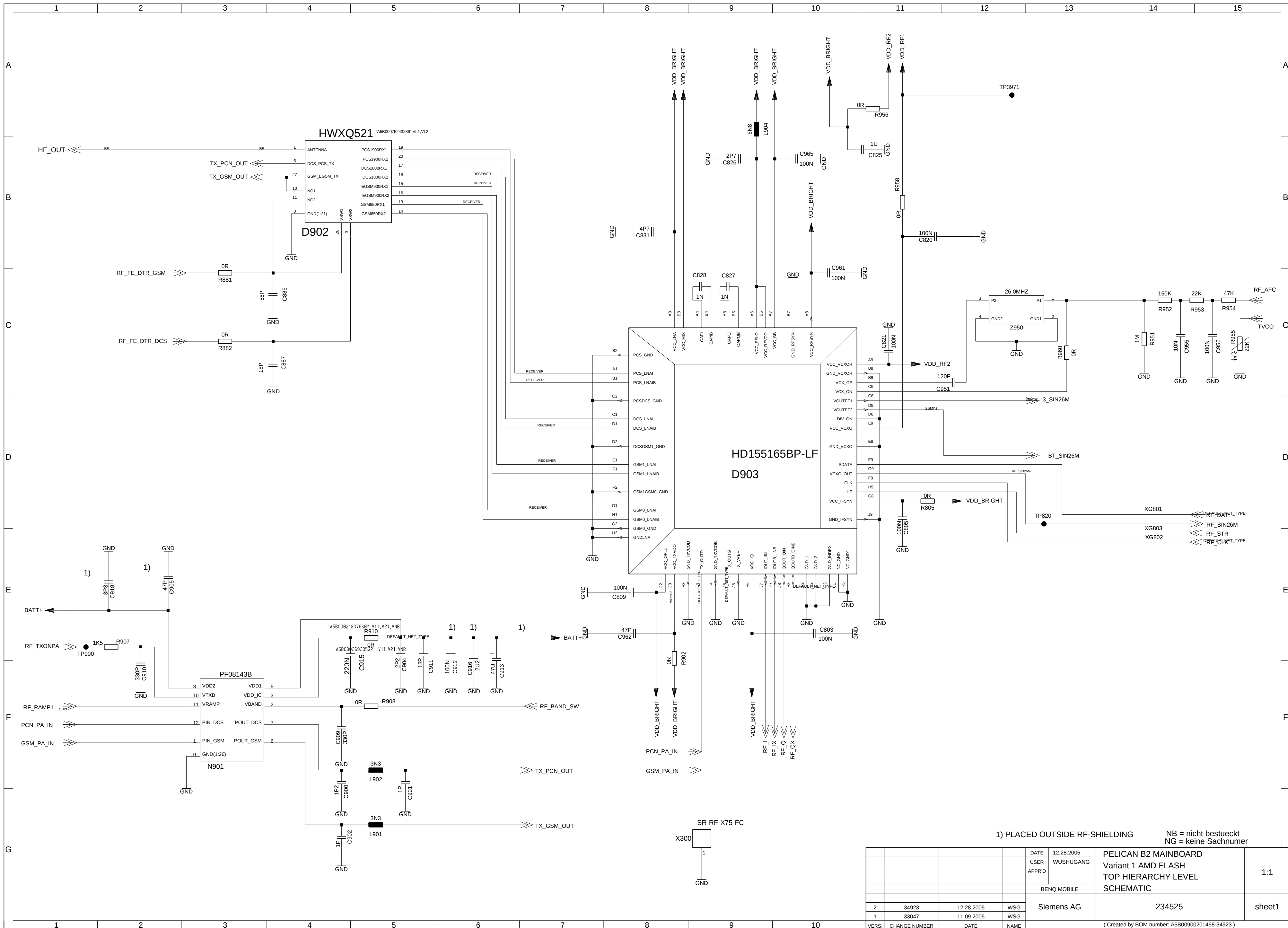
1		2		3		4		5	
HIERARCHY						SHEET		PDF-SHEET	
A5B00900202751-33061						index		sheet 1	
						netlist1		sheet 2	
						poslist1		sheet 3	
						page1		----	
						sheet1		sheet 4	
						sheet2		sheet 5	
						sheet3		sheet 6	
						sheet4		sheet 7	
						sheet5		sheet 8	
						sheet6		sheet 9	
						sheet7		sheet 10	
						sheet8		sheet 11	
						sheet9		sheet 12	
						sheet10		sheet 13	
						sheet11		sheet 14	
								</	

1	2	3	4	5	6	7	8	9	10
1.5V /sheet7 14 B /sheet5 2 C	CCVCC /sheet7 14 B /sheet10 7 F	HF_OUT /sheet2 4 C /sheet1 1 B	KB8 /sheet5 8 E /sheet3 11 D /sheet11 8 B	/sheet1 9 F POWER /sheet8 9 E /sheet7 2 C	RF_RAMP1 /sheet5 6 C /sheet4 4 C /sheet4 2 C /sheet1 1 F	TTRACE /sheet5 13 D TVCO /sheet5 14 A /sheet5 1 D /sheet4 2 D /sheet4 4 D /sheet1 15 C	/sheet8 11 F /sheet7 14 B /sheet7 2 G /sheet7 2 F /sheet7 11 C /sheet5 10 A /sheet5 4 C /sheet5 10 B /sheet5 13 B /sheet11 7 F /sheet11 10 F /page1 2 C /page1 2 B		
3_SIN26M /sheet1 13 D	CHARGE_UC /sheet7 10 C /sheet6 15 F /sheet5 14 D	HF_OUT1 /sheet4 10 E /sheet2 6 C	KB9 /sheet5 8 E /sheet3 11 D /sheet11 8 B	RD /sheet6 5 E /sheet6 15 E /sheet6 5 B /sheet6 11 D /sheet5 10 F	RF_SIN26M /sheet5 11 C /sheet4 2 C /sheet4 4 C /sheet1 14 D	TX /sheet8 13 F /sheet5 9 C	VEBU_1.8 /sheet7 5 E /sheet6 6 E /sheet11 8 C		
A(0) /sheet6 5 E	CS_FLASH_1 /sheet6 15 E /sheet6 5 B /sheet6 5 C /sheet6 11 D /sheet6 11 D /sheet5 10 F	HW_CODING1 /sheet5 12 B /sheet5 8 D	LCD_CLK /sheet5 9 C /sheet11 7 C	RESET_UC /sheet7 11 C /sheet6 11 D /sheet6 7 B /sheet6 5 B /sheet5 12 C	RF_STR /sheet5 14 E /sheet4 4 A /sheet4 2 A /sheet1 15 E	TX_1 /sheet8 9 E	VEBU_1.8FLASH /sheet6 5 D /sheet6 5 D /sheet6 2 D /sheet6 7 D /sheet6 8 D /sheet6 11 C /sheet6 10 C		
A(0:23) /sheet6 2 F /sheet6 15 E /sheet6 9 E /sheet6 2 C /sheet5 13 F	CS_SRAM /sheet6 5 E /sheet6 15 E /sheet5 10 F	HW_CODING2 /sheet5 12 B /sheet5 14 E	LCD_CS /sheet5 10 C /sheet11 7 C	LCD_DAT /sheet5 9 C /sheet11 7 C	RF_TXONPA /sheet5 8 E /sheet4 4 D /sheet1 1 E	TX_GSM_OUT /sheet1 4 B /sheet1 7 G			
A(23) /sheet5 8 E	CS_SRAM /sheet6 5 E /sheet6 15 E /sheet5 10 F	HW_CODING3 /sheet5 12 B /sheet5 14 E	LCD_DAT /sheet5 9 C /sheet11 7 C	RF_AFC /sheet5 10 C /sheet4 4 C /sheet4 2 C /sheet1 15 C	RINGIN /sheet7 7 E /sheet5 10 C	VBAT /sheet5 9 B /sheet5 1 E			
AC_CTS /sheet8 13 F /sheet5 9 C	CTS_1 /sheet8 9 E	HW_CODING4 /sheet5 8 D /sheet5 12 A	LCD_RESET /sheet5 10 C /sheet11 7 C	RF_ANT_DET /sheet5 14 D /sheet4 4 D /sheet4 2 D /sheet2 7 D	RTX_INT /sheet7 7 C /sheet5 12 C	VBOOST /sheet7 10 F /sheet11 8 B /sheet10 9 E	VEBU_1.8PSRAM /sheet6 8 E /sheet6 1 E		
AC_DCD /sheet8 13 E /sheet5 14 E	D(0:15) /sheet6 15 B /sheet6 5 B /sheet6 12 E /sheet6 4 F /sheet5 12 C	HW_CODING5 /sheet5 10 C /sheet5 9 C /sheet5 15 B	LCD_RS /sheet5 9 C /sheet11 7 C	RF_BAND_SW /sheet5 8 D /sheet4 4 F /sheet4 2 F /sheet1 7 F	RTS_1 /sheet8 9 E	VCXOEN_UC /sheet7 6 B /sheet6 15 F /sheet5 10 C	VEBU_2.9 /sheet7 5 F /sheet6 7 A /sheet6 6 A /sheet6 1 E /sheet6 2 B /sheet6 5 E /sheet5 4 C /sheet5 4 B /sheet5 11 G /sheet5 13 B /sheet11 8 C		
AC_RTS /sheet8 13 F /sheet5 9 C	DCD_1 /sheet8 9 E	HW_CODING6 /sheet5 10 F /sheet5 10 G /sheet5 15 B	LIGHT_1 /sheet10 11 B /sheet10 14 E	LIGHT_2 /sheet11 8 B /sheet10 7 C	RX /sheet8 13 F /sheet5 9 C	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C	VEBU_2.9FLASH /sheet6 5 C /sheet6 11 D /sheet6 2 B /sheet6 9 D /sheet6 11 D		
AGND /sheet5 3 G /sheet5 5 G /sheet5 4 F /sheet5 5 F	EPN1 /sheet9 5 F /sheet7 13 D	I2C_CLK /sheet7 10 C /sheet6 15 F /sheet5 9 C	LIGHT_KB /sheet5 15 E /sheet10 9 B	RF_CLK /sheet5 14 F /sheet4 4 A /sheet4 2 A /sheet1 15 E	RX_1 /sheet8 9 E	SLEEPQ /sheet7 7 B /sheet5 8 D	VIBRA_UC /sheet9 10 B /sheet5 14 D		
AKKU_TYP /sheet8 4 A /sheet5 11 A	EPP1 /sheet9 5 E /sheet7 13 D	I2C_DAT /sheet7 10 C /sheet6 15 F /sheet5 9 C	LIGHT_LCD /sheet5 11 C /sheet10 4 A	RF_DAT /sheet5 14 F /sheet4 4 B /sheet4 2 B /sheet1 15 D	STEPUP_SHUD /sheet7 7 G /sheet5 15 E	TBAT /sheet5 11 A /sheet5 1 E	VMIC /sheet5 5 A /sheet5 4 A		
AUDIO_L /sheet8 9 F /sheet7 13 D	EPPA1 /sheet5 7 E /sheet11 6 C	I2C_IRQ /sheet7 10 C /sheet6 15 F /sheet5 11 D	MICN1 /sheet9 5 B /sheet5 3 B /sheet5 1 D	RF_FE_DTR_DCS /sheet5 8 D /sheet4 4 C /sheet4 2 C /sheet1 2 C	TCK /sheet5 9 F /page1 3 C	VDD_BRIGHT /sheet1 8 A /sheet1 8 F /sheet1 9 F /sheet1 12 D /sheet1 10 A /sheet1 8 F /sheet1 8 A /sheet1 10 B /sheet1 10 A /sheet1 9 A	VREGA /sheet7 7 E /sheet7 10 D /sheet7 8 E		
AUDIO_R /sheet8 9 F /sheet7 13 D	EPPA2 /sheet5 7 E /sheet11 6 D	I2C_SDA0 /sheet7 7 E /sheet5 15 E	MICN2 /sheet5 4 A /sheet5 1 D	RF_FE_DTR_GSM /sheet5 8 E /sheet4 2 C /sheet4 4 C /sheet1 2 C	TDO /sheet5 9 F /page1 3 C	VDD_CHARGE /sheet7 5 B /sheet7 7 C	WA_DOG /sheet7 6 C /sheet5 10 C		
BATT+ /sheet9 10 B /sheet8 4 B /sheet7 5 B /sheet7 7 A /sheet7 7 D /sheet7 1 F /sheet7 6 F /sheet5 9 A /sheet1 1 E /sheet1 7 E	F_WAIT /sheet6 15 E /sheet6 5 D /sheet6 11 D	I2C_SDA0 /sheet7 7 E /sheet5 15 E	MICP1 /sheet9 5 B /sheet5 3 B /sheet5 1 D	RF_I /sheet5 1 E /sheet4 4 B /sheet4 2 B /sheet1 9 F	TRIGOUT /sheet6 15 F /sheet5 9 F	VDD_RF1 /sheet7 14 B /sheet4 2 E /sheet4 4 E /sheet2 9 D /sheet1 11 A	WR /sheet6 5 E /sheet6 15 E /sheet6 5 B /sheet6 11 D /sheet5 10 F		
BHE /sheet6 5 E /sheet6 15 E /sheet5 10 F	GND_MIC /sheet9 4 C /sheet8 9 F /sheet5 4 B /sheet5 6 A /sheet5 4 G /sheet5 6 C /sheet5 1 B /sheet5 4 B	I2C_SWA0 /sheet7 7 E /sheet5 15 E	MICP2 /sheet8 9 F /sheet5 4 A /sheet5 1 D	RF_IX /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 10 F	TRIGIN /sheet6 15 F /sheet5 9 F	VDD_RF2 /sheet7 14 B /sheet4 4 E /sheet4 2 E /sheet1 11 A /sheet1 11 C			
BREF /sheet5 15 A /sheet5 1 E	GND_STEPUP /sheet7 8 F /sheet7 7 G /sheet7 11 F /sheet7 10 G /sheet7 8 G /sheet7 7 F /sheet7 9 G	KB0 /sheet5 8 E /sheet3 11 E	MON1 /sheet5 9 F	RF_Q /sheet5 1 E /sheet4 4 B /sheet4 2 B /sheet1 10 F	TRIST /sheet5 9 F /page1 3 C	VDD_RTC /sheet7 14 B /sheet7 7 C /sheet5 9 C /sheet5 4 C			
BT_SIN26M /sheet1 13 D	GSM_PA_IN /sheet1 1 F /sheet1 9 F	KB2 /sheet5 8 E /sheet3 11 D	MON2 /sheet5 9 F /page1 3 B	RF_QX /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 10 F	VDIG_2.65V				
CCCLK /sheet5 14 D /sheet10 7 E	HALL_1 /sheet5 15 E /sheet11 7 G	KB3 /sheet5 8 E /sheet3 11 D	ON_OFF /sheet7 7 C /sheet5 7 E /sheet11 8 B						
CCIO /sheet5 14 D /sheet10 7 E	HALL_2 /sheet5 8 E /sheet11 9 G	KB4 /sheet5 8 E /sheet3 11 D /sheet11 8 B	PCN_PA_IN /sheet1 1 F						
CCRST /sheet5 14 D /sheet10 7 E		KB6 /sheet5 8 E /sheet3 11 D /sheet11 8 B							

				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
2	34923	12.28.2005	WSG	Siemens AG		234525	netlist1
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

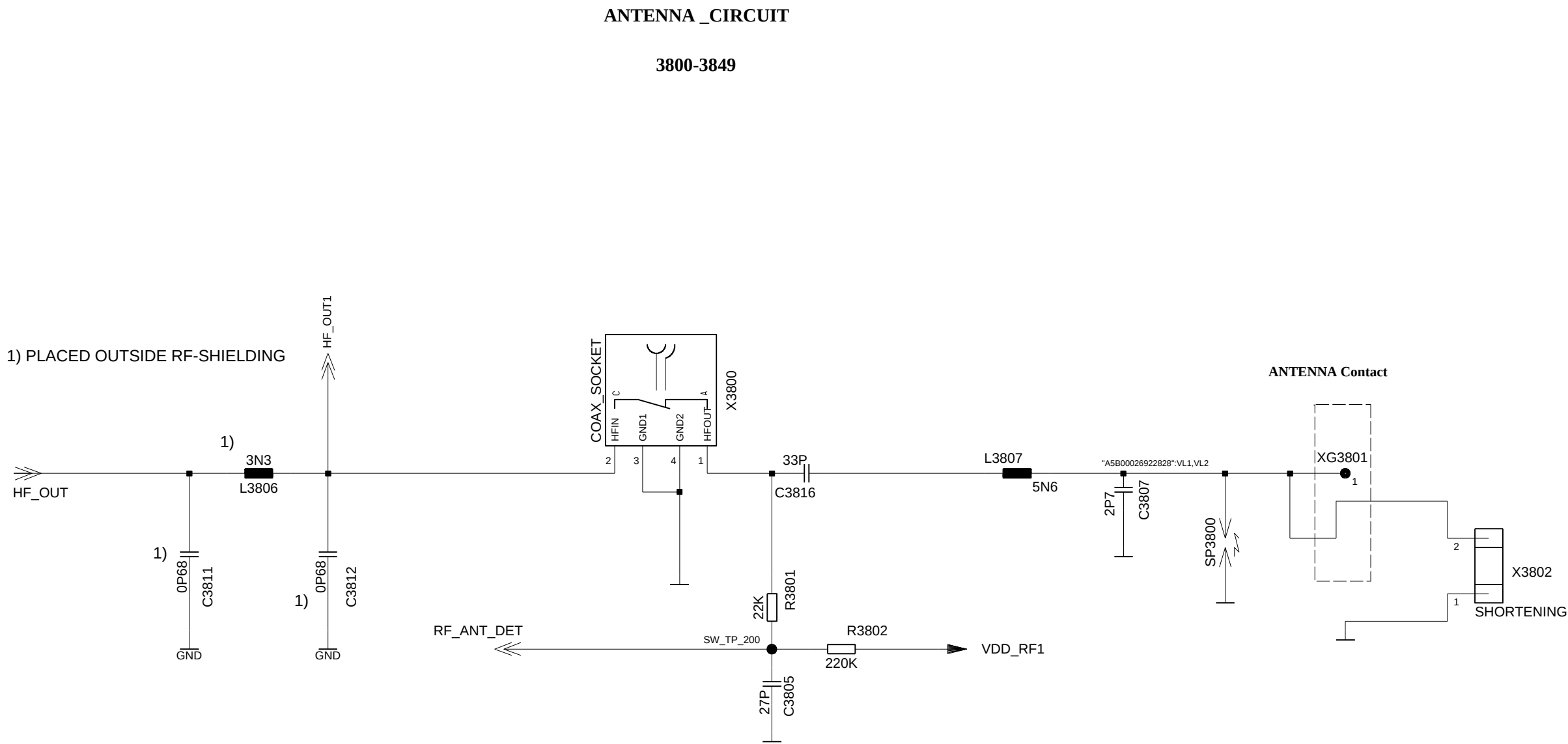
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				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
				Siemens AG		234525	poslist1
2	34923	12.28.2005	WSG				
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			



1) PLACED OUTSIDE RF-SHIELDING NB = nicht bestueckt NG = keine Sachnummer

				NG – keine Sachnummer		
			DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
			USER	WUSHUGANG		
			APPR'D			
			BENQ MOBILE			
2	34923	12.28.2005	WSG	Siemens AG	234525	sheet1
1	33047	11.09.2005	WSG			
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)		

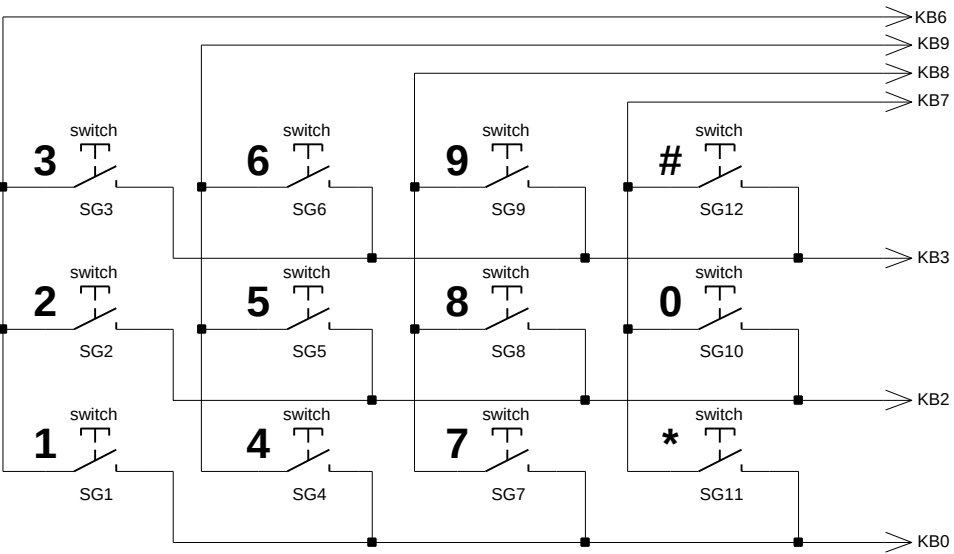


				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
2	34923	12.28.2005	WSG	Siemens AG		234525	sheet2
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

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MMI MAINBOARD

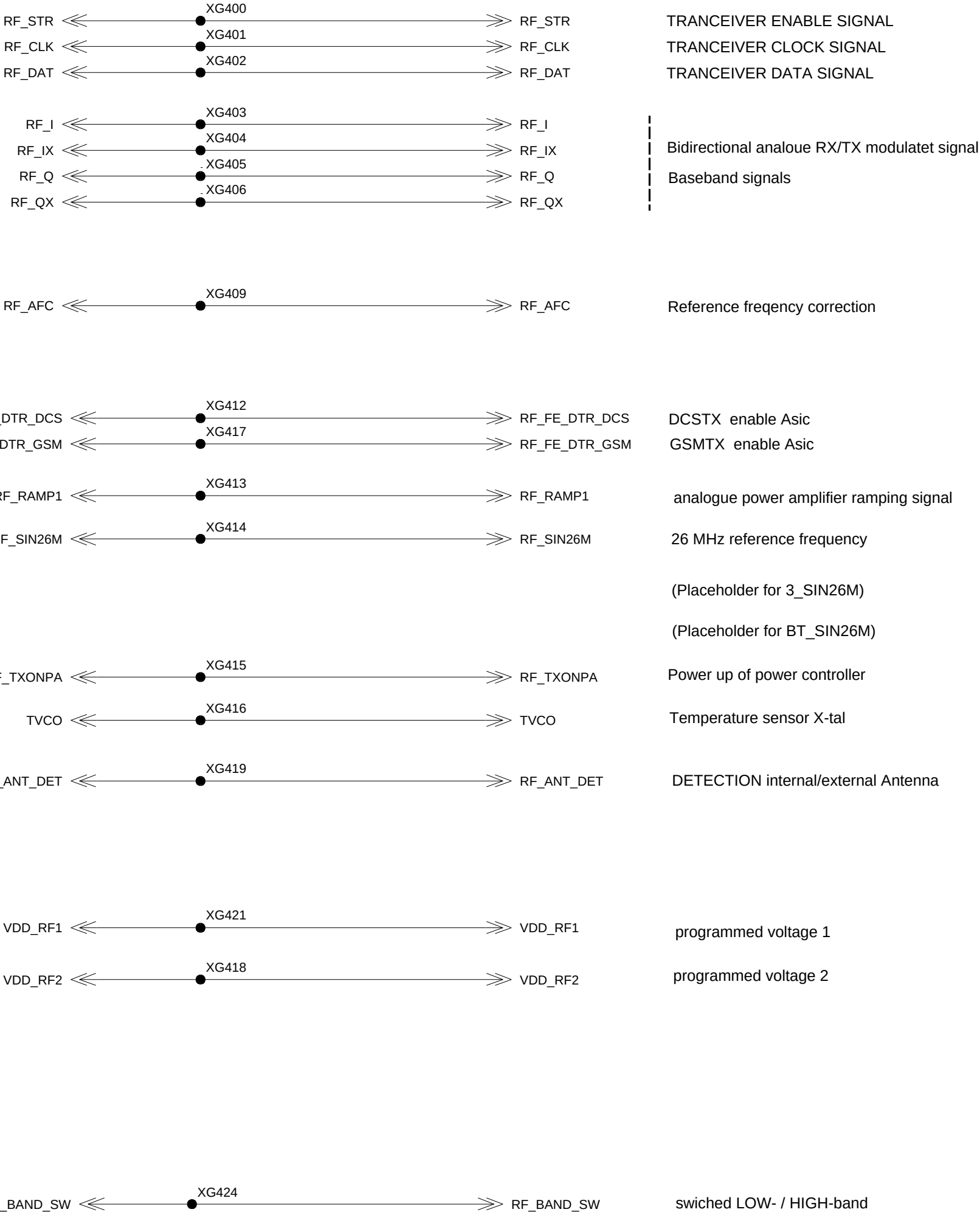


				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
				Siemens AG		234525	sheet3
2	34923	12.28.2005	WSG				
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

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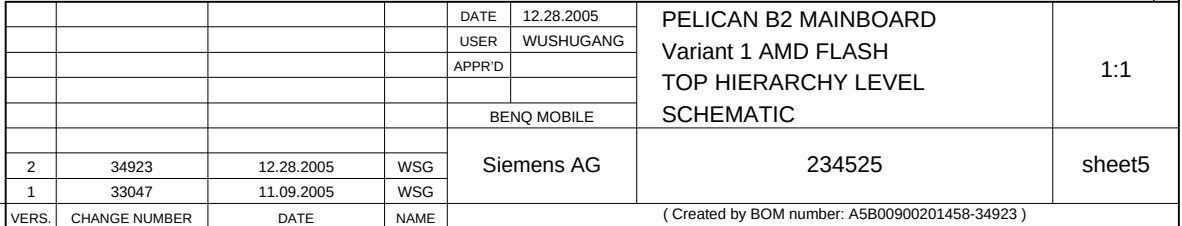
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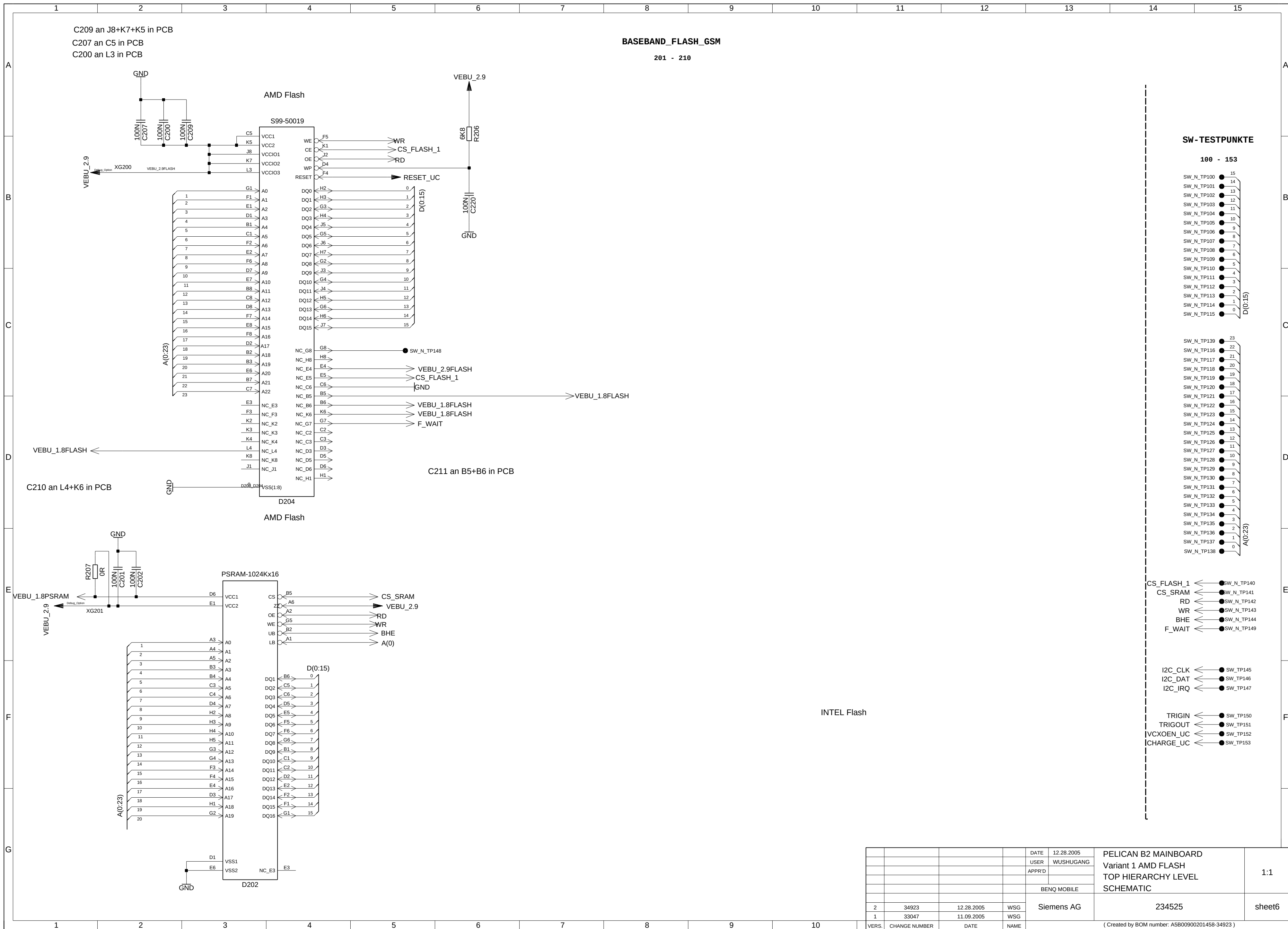
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400-449

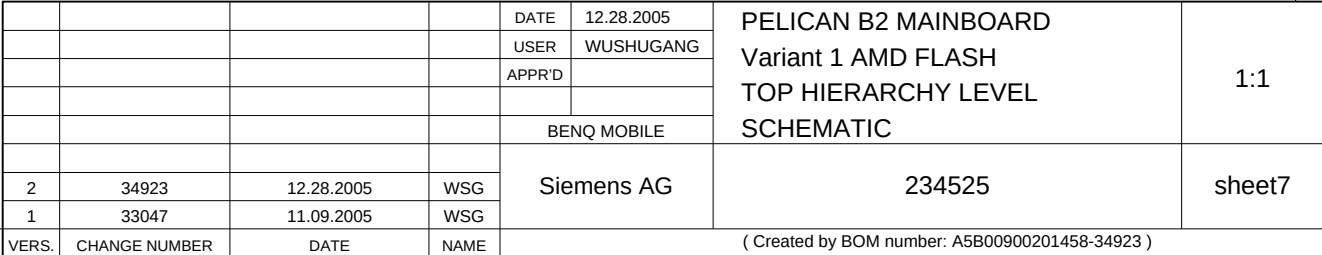


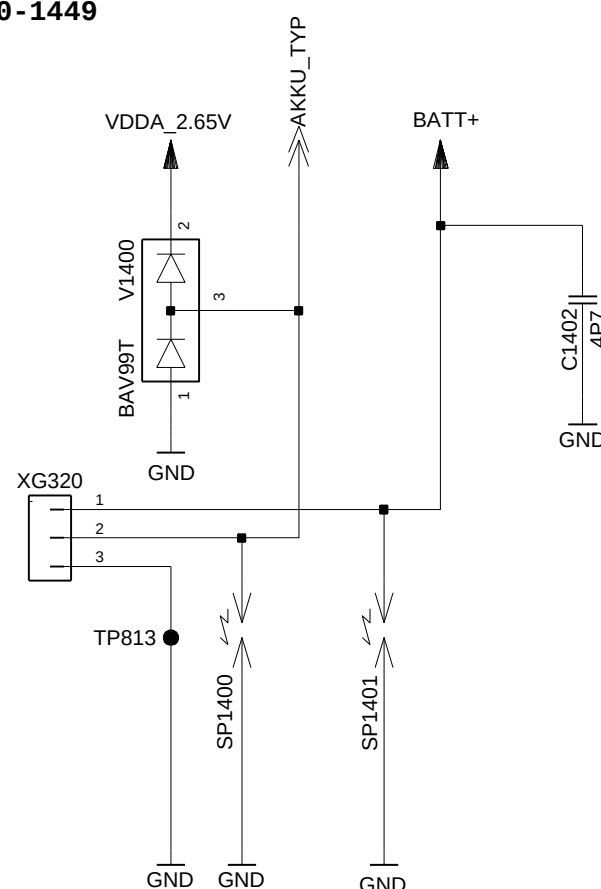
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				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
2	34923	12.28.2005	WSG	Siemens AG		234525	sheet4
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

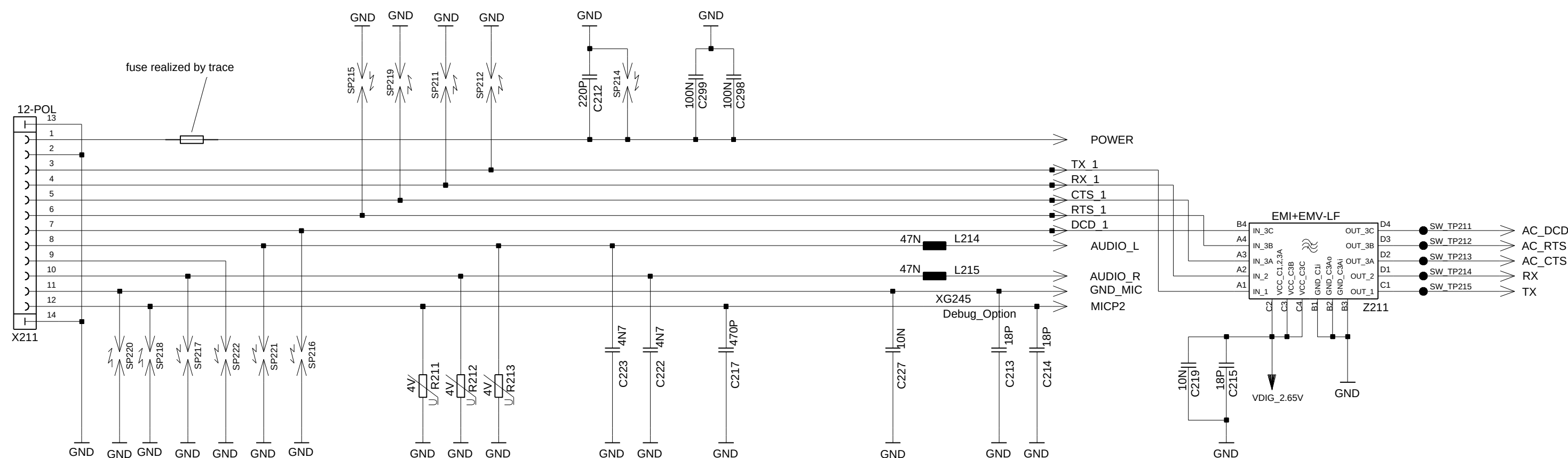








211 - 230

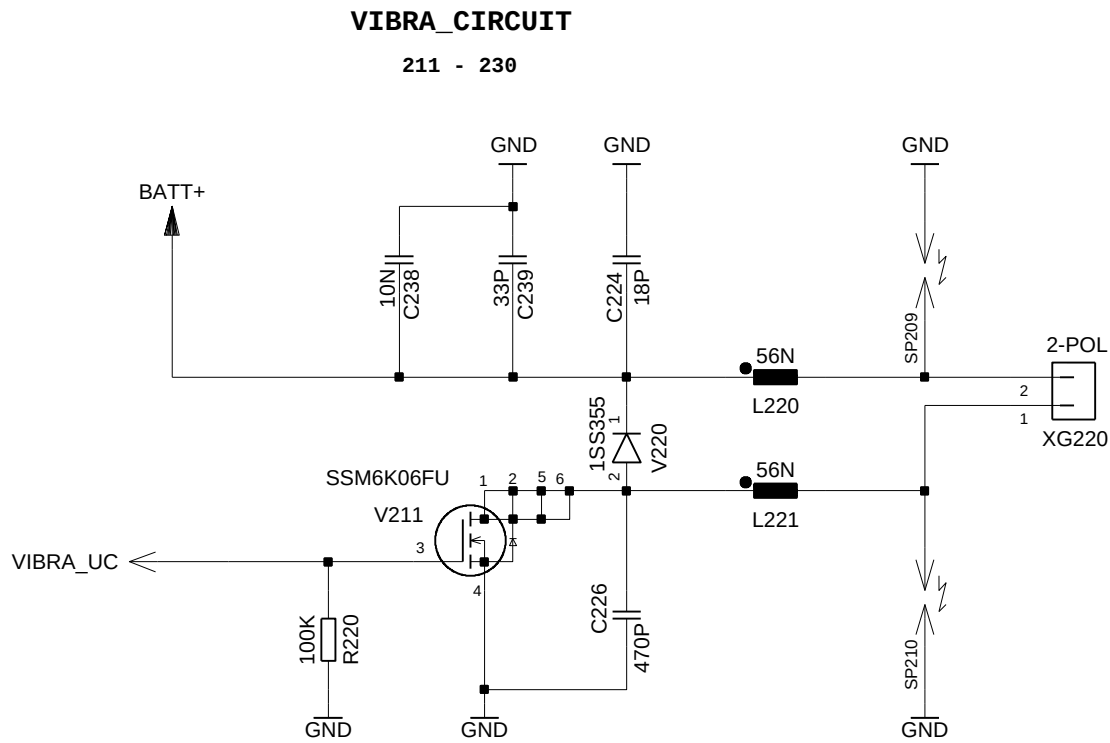
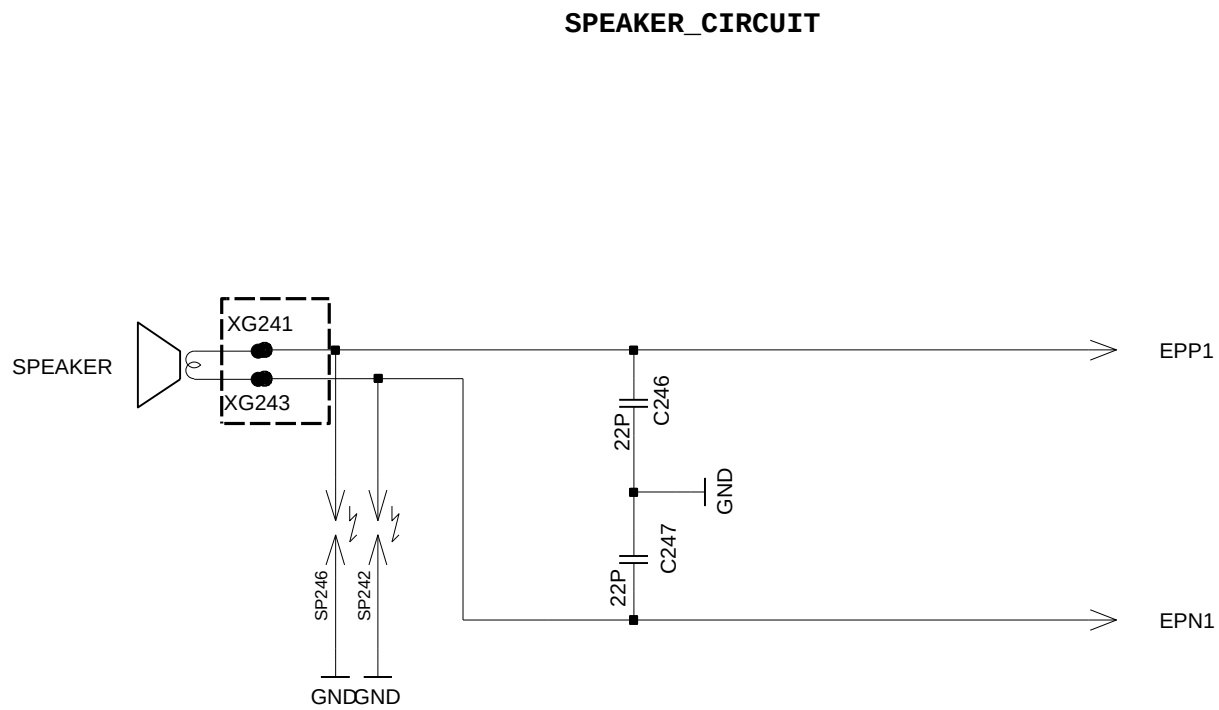
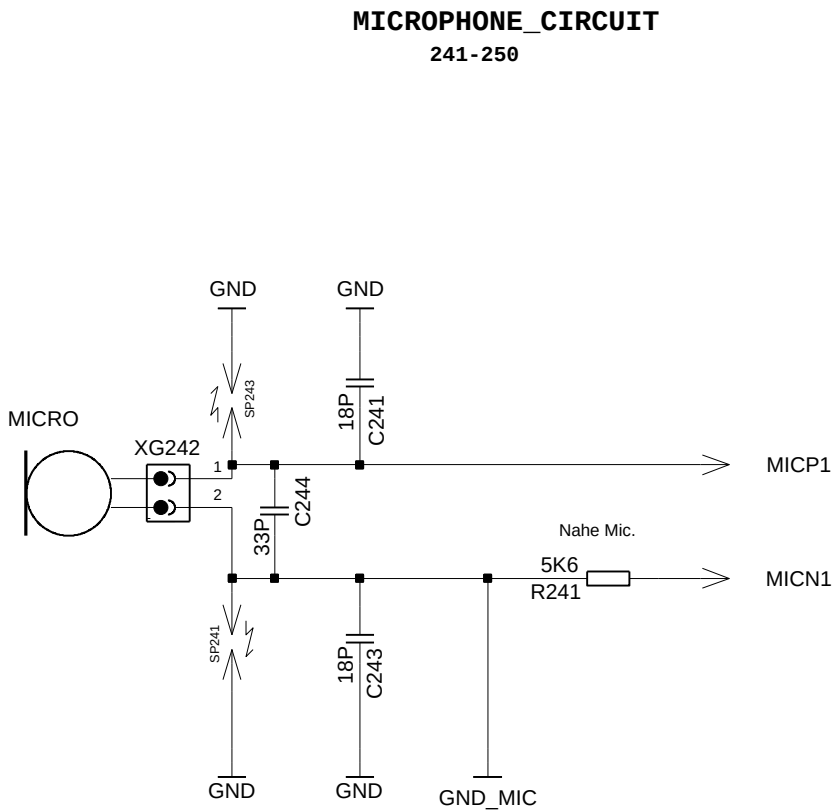


shorted footprint with trace between pads at layout

				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
					BENQ MOBILE		
2	34923	12.28.2005	WSG	Siemens AG	234525	sheet8	
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

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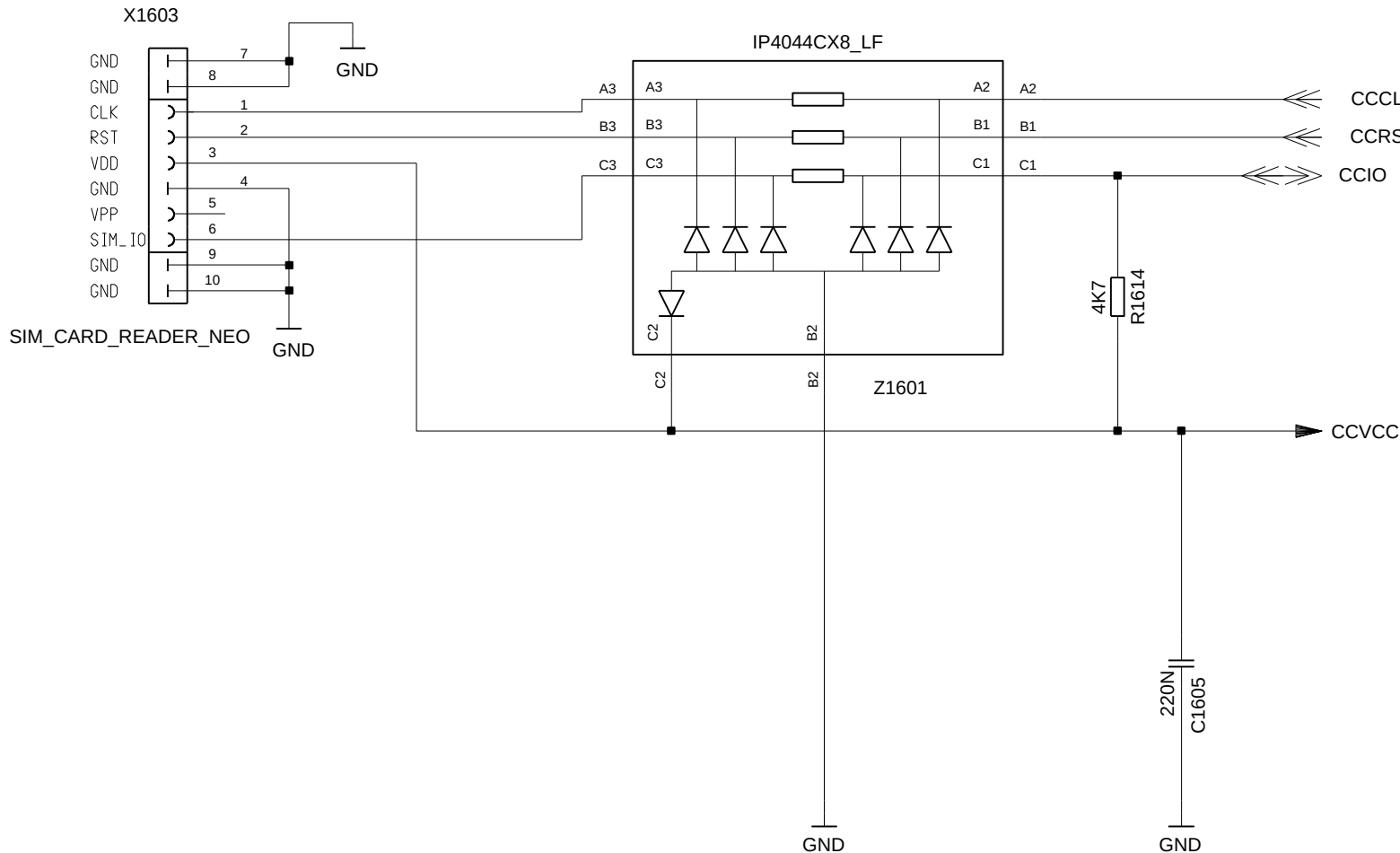
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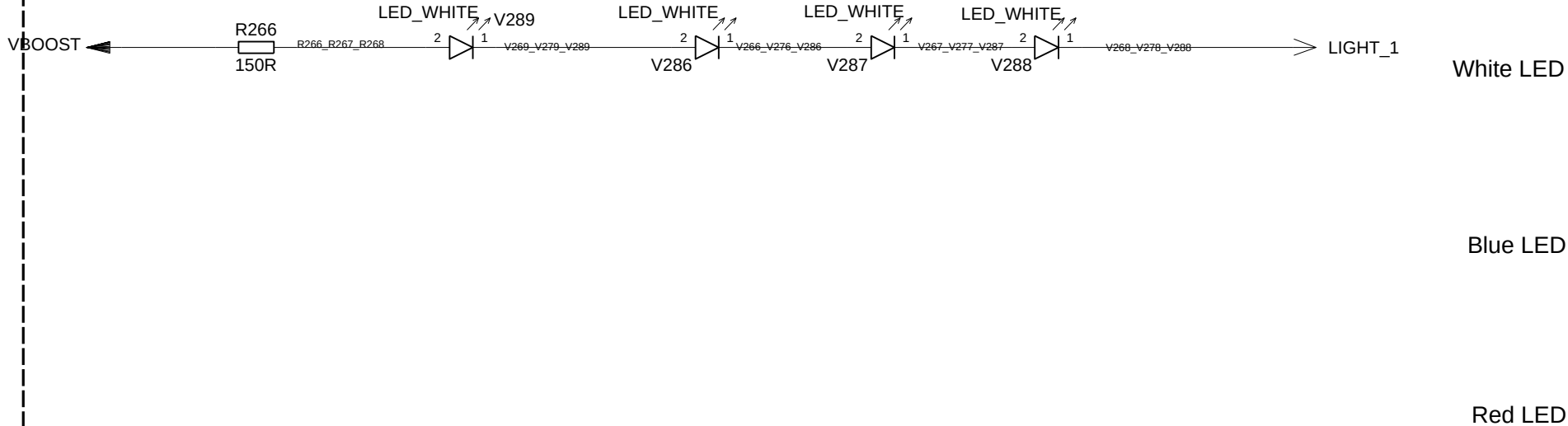
				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
				Siemens AG		234525	sheet9
2	34923	12.28.2005	WSG				
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

SIM_CONNECTOR

1600-1659



KEYPAD_BACKLIGHT



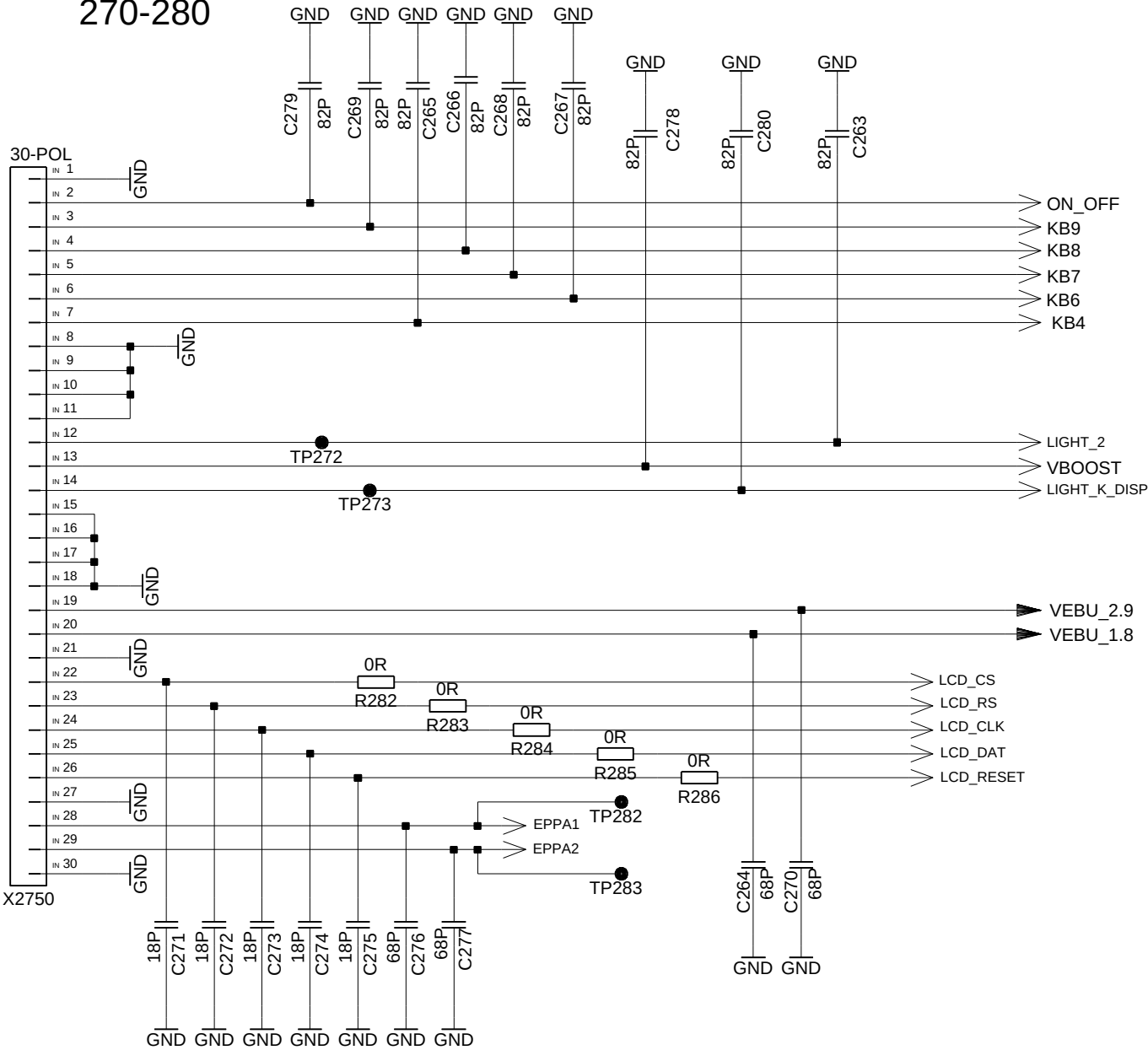
				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
2	34923	12.28.2005	WSG	Siemens AG		234525	sheet10
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

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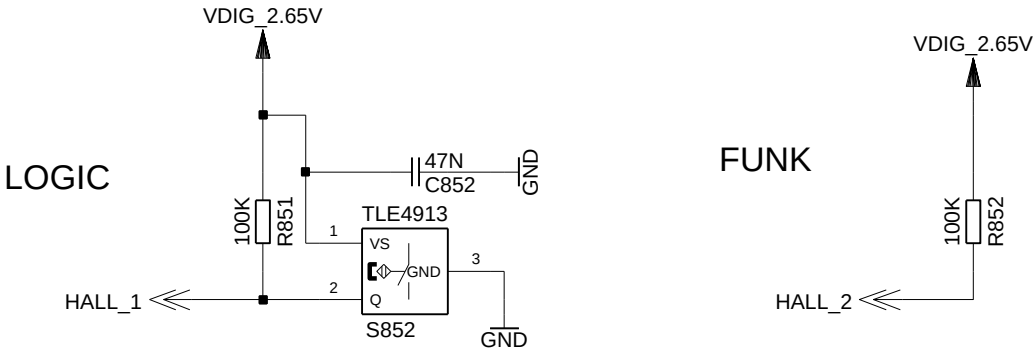
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MMI CONNECTOR

270-280

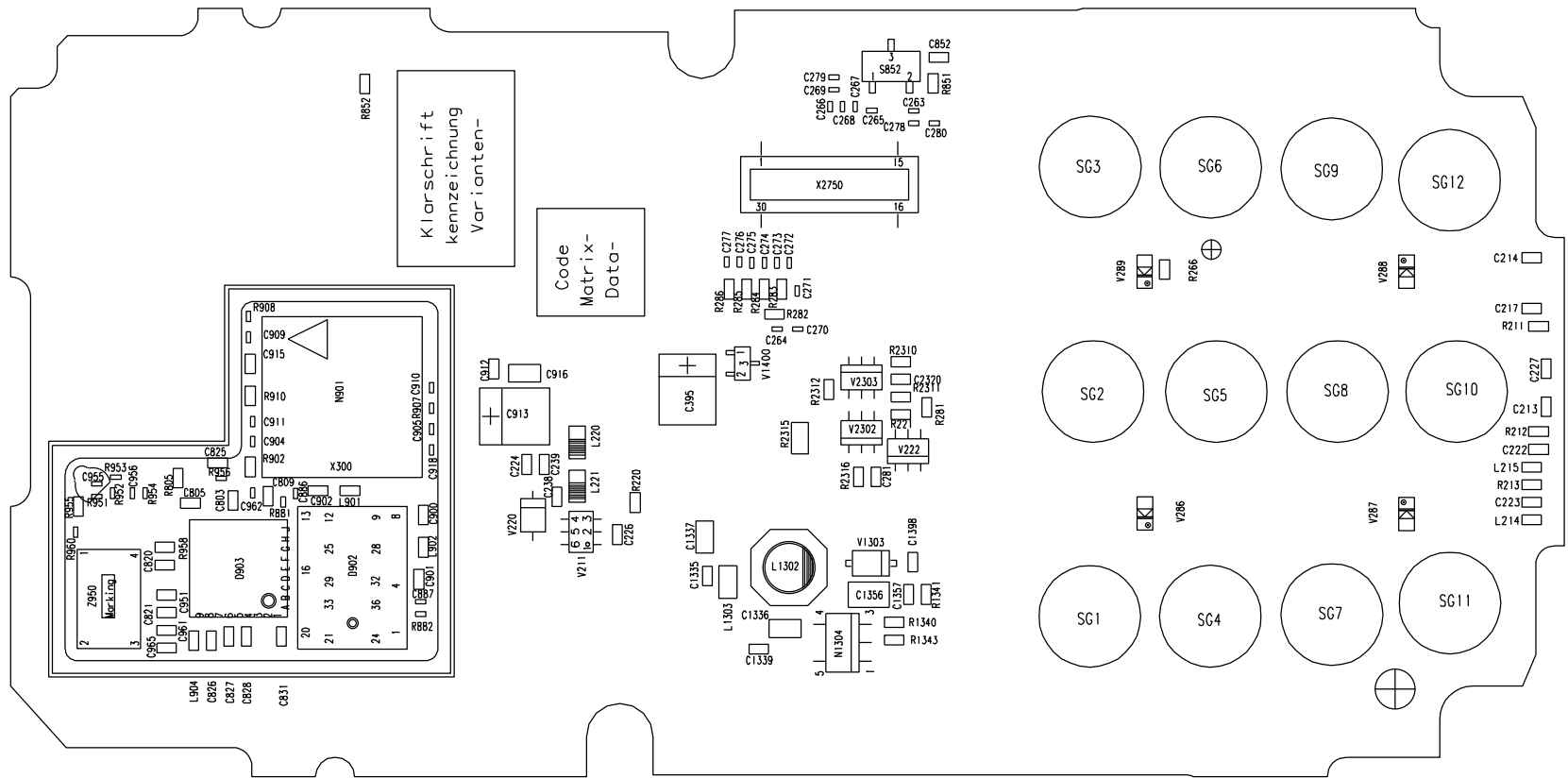


MAGNETIC SWITCH



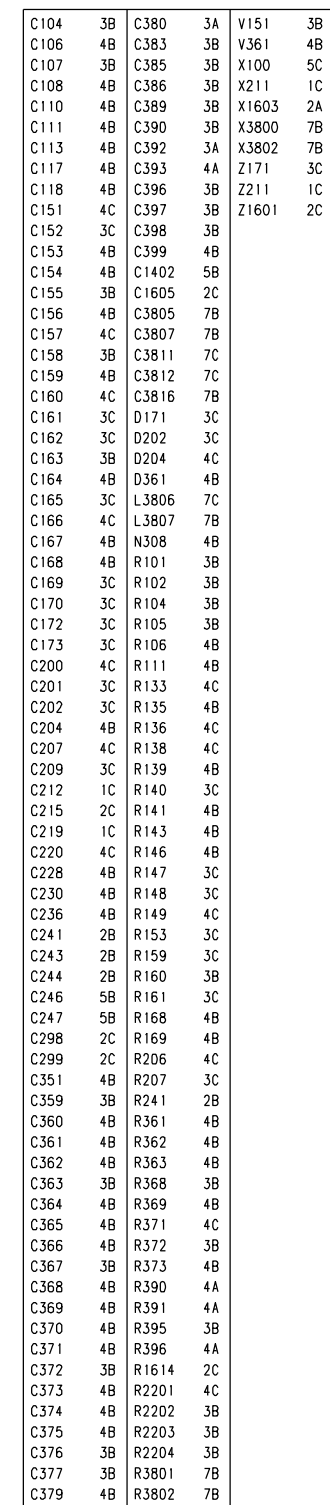
				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	WUSHUGANG		
				APPR'D			
				BENQ MOBILE			
				Siemens AG			
2	34923	12.28.2005	WSG	234525		sheet11	
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

C213	10B	R281	8B
C214	10B	R282	7B
C217	10B	R283	7B
C222	10C	R284	7B
C223	10C	R285	7B
C224	6C	R286	7B
C226	6C	R805	5C
C227	10B	R851	8B
C238	6C	R852	5B
C239	6C	R881	5C
C263	7B	R882	6C
C264	7B	R902	5C
C265	7B	R907	6B
C266	7B	R908	5B
C267	7B	R910	5B
C268	7B	R951	4C
C269	7B	R952	4C
C270	7B	R953	4C
C271	7B	R954	4C
C272	7B	R955	4C
C273	7B	R956	5C
C274	7B	R958	5C
C275	7B	R960	4C
C276	7B	R1340	7C
C277	7B	R1341	8C
C278	7B	R1343	7C
C279	7B	R2310	7B
C280	8B	R2311	7B
C281	7C	R2312	7B
C395	7B	R2315	7B
C803	5C	R2316	7C
C805	5C	S852	7B
C809	5C	V211	6C
C820	5C	V220	6C
C821	4C	V222	7C
C825	5C	V286	8C
C826	5C	V287	9C
C827	5C	V288	9B
C828	5C	V289	8B
C831	5C	V1303	7C
C852	8A	V1400	7B
C886	5C	V2302	7C
C887	5C	V2303	7B
C900	6C	X300	4C
C901	5C	X2750	7B
C902	5C	Z950	4C
C904	5C		
C905	6C		
C909	5B		
C910	6B		
C911	5B		
C912	6B		
C913	6B		
C915	5B		
C916	6B		
C918	6C		
C951	5C		
C955	4C		
C956	4C		
C961	4C		
C962	5C		
C965	4C		
C1335	7C		
C1336	7C		
C1337	7C		
C1339	7C		
C1356	7C		
C1357	7C		
C1398	7C		
C2320	7B		
D902	5C		
D903	5C		
L214	10C		
L215	10C		
L220	6C		
L221	6C		
L901	5C		
L902	6C		
L904	5C		
L1302	7C		
L1303	7C		
N901	5B		
N1304	7C		
R211	10B		
R212	10C		
R213	10C		
R220	6C		
R221	7B		
R266	8B		



Achtung : Höhenprofil C24_____-42 (gilt nur fuer Entwicklung)
Liefervarianten siehe : V24851-Z2000-A1-*-35

				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH WIRING DIAGRAMM (SOLDER SIDE 1)	3:1
				NAME	WUSHUGANG		
				APPR'D			
				STD			
				BENQ MOBILE		SIEMENS AG	SHEET 1+
2	34923	12.28.2005	WSG				
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			



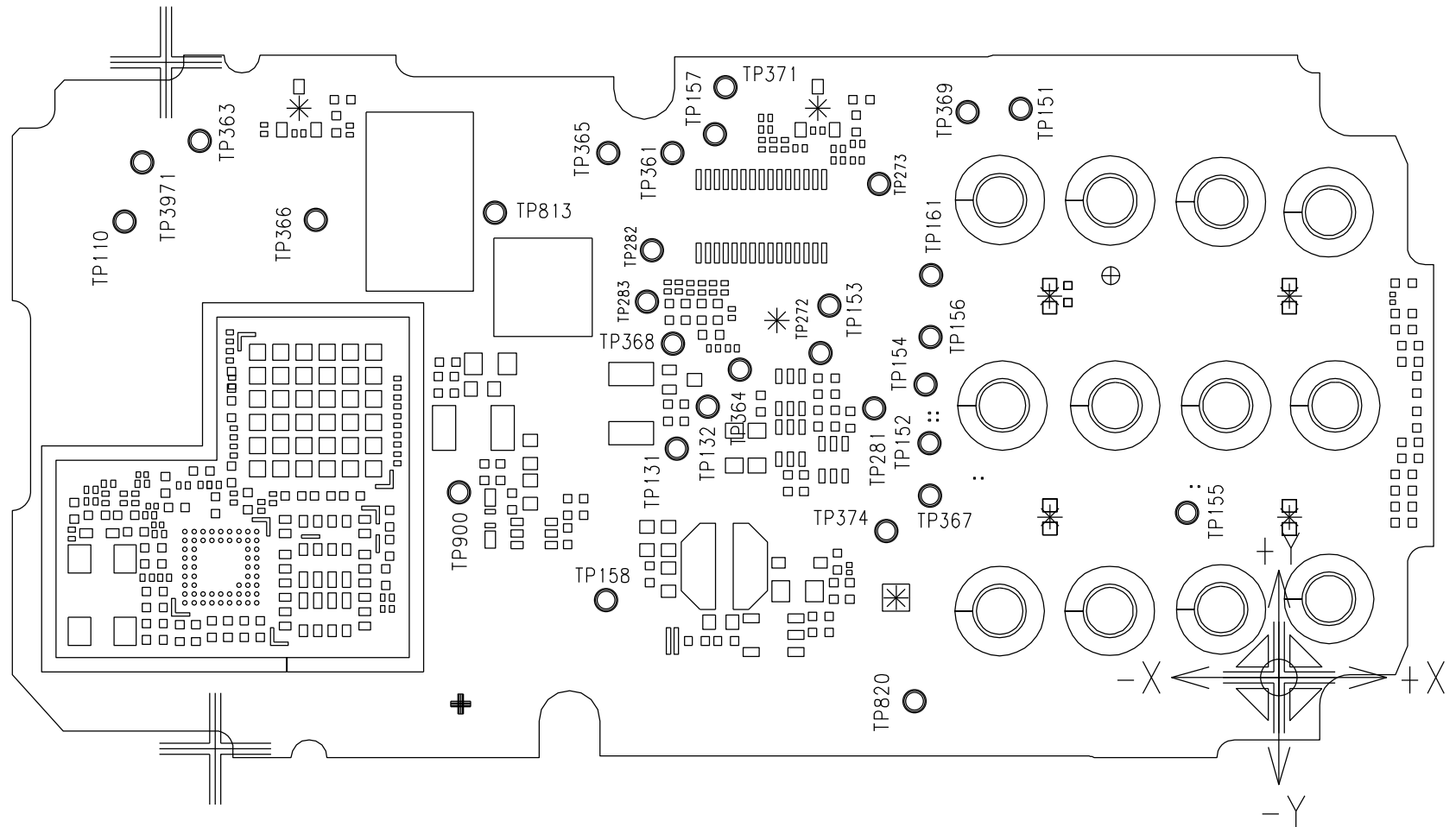
Achtung : Höhenprofil C24___-___-___-42 (gilt nur fuer Entwicklung)
Liefervarianten siehe : V24851-Z2000-A1-*-35

				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH WIRING DIAGRAMM (SOLDER SIDE 2)	3:1
				NAME	WUSHUGANG		
				APPR'D			
				STD			
				BENQ MOBILE			
2	34923	12.28.2005	WSG	SIEMENS AG		234526	SHEET 2+
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

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ALS BETRIEBSGEHEIMNIS ANVERTRAUT
ALLE RECHTE VORBEHALTEN

TP110	4B	/TVCO
TP131	7C	/BREF
TP132	7B	/TBAT
TP151	8B	/MON2
TP152	8C	/TMS
TP153	7B	/TCK
TP154	8B	/TD1
TP155	9C	/TRST
TP156	8B	/TDO
TP157	7B	/ON_OFF
TP158	6C	VEBU_2.9
TP161	8B	/MON1
TP272	7B	/LIGHT_2
TP273	8B	/LIGHT_K_DISP
TP281	8B	/LIGHT_1
TP282	7B	/EPPA1
TP283	7B	/EPPA2
TP361	7B	/POWER
TP363	5B	VDD_RF2
TP364	7B	VEBU_1.8
TP365	6B	/NS475
TP366	5B	VDD_2.65V
TP367	8C	1.5V
TP368	7B	VDDA_2.65V
TP369	8B	/VDD_RTC
TP371	7A	RESET_UC
TP374	8C	VB00ST
TP813	6B	GND
TP820	8C	/RF_SIN26M
TP900	6C	/RF_TXONPA
TP3971	4B	VDD_RF1



				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TESTPOINT DIAGRAMM (SIDE 1)	3:1
				NAME	WUSHUGANG		
				APPR'D			
				STD			
				BENQ MOBILE		234527	SHEET 1+
2	34923	12.28.2005	WSG	SIEMENS AG			
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

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ALS BETRIEBSGEHEIMNIS ANVERTRAUT
ALLE RECHTE VORBEHALTEN

1 2 3 4 5 6 7 8 9 10

A

A

B

B

C

C

D

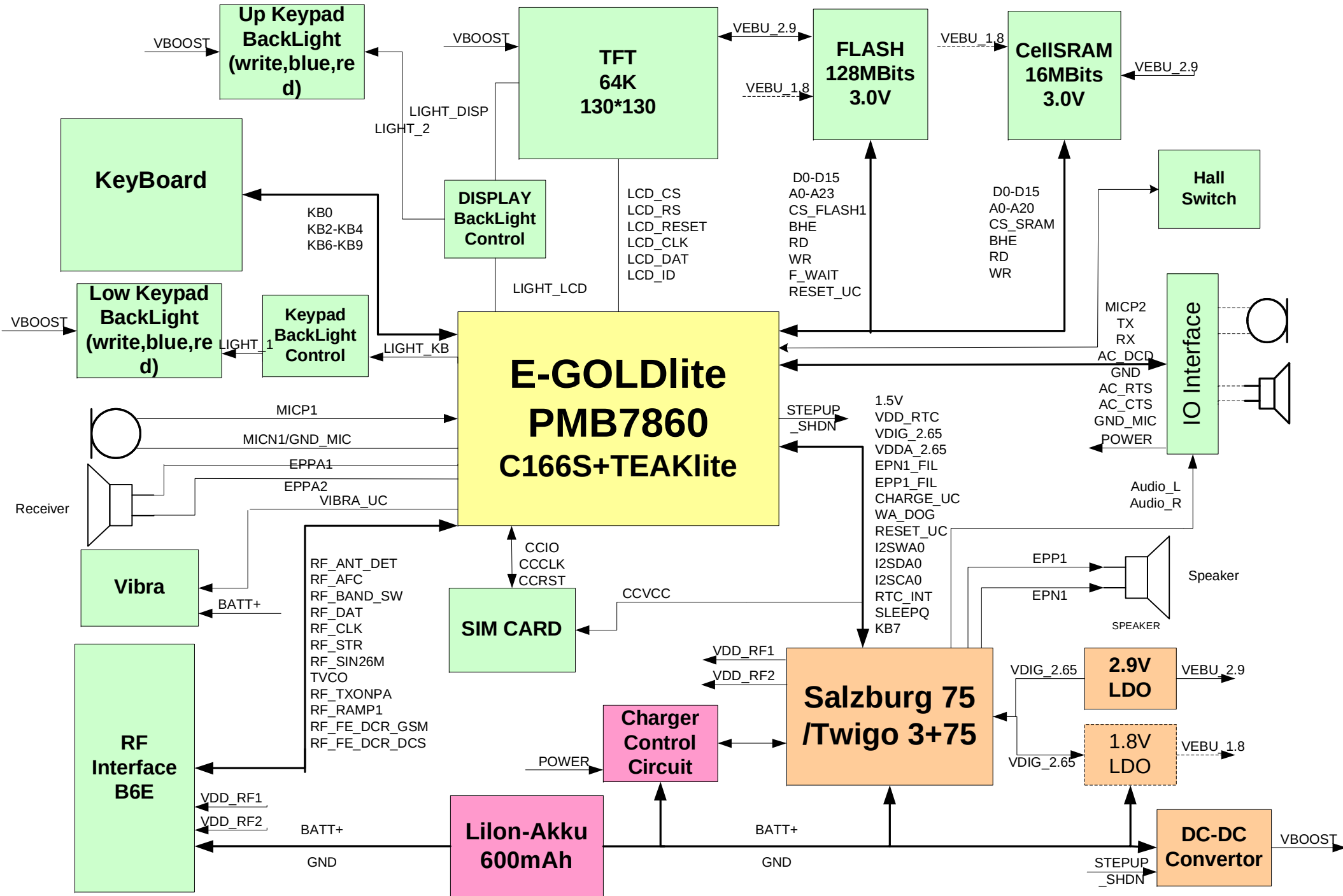
D

E

1 2 3 4 5

				DATE	12.28.2005	PELICAN B2 MAINBOARD Variant 1 AMD FLASH TESTPOINT DIAGRAMM (SIDE 2)	3:1
				NAME	WUSHUGANG		
				APPR'D			
				STD			
				BENQ MOBILE		234527	SHEET 2-
2	34923	12.28.2005	WSG	SIEMENS AG			
1	33047	11.09.2005	WSG				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900201458-34923)			

AL21 Logic Diagram



AL21 - RF Diagram

