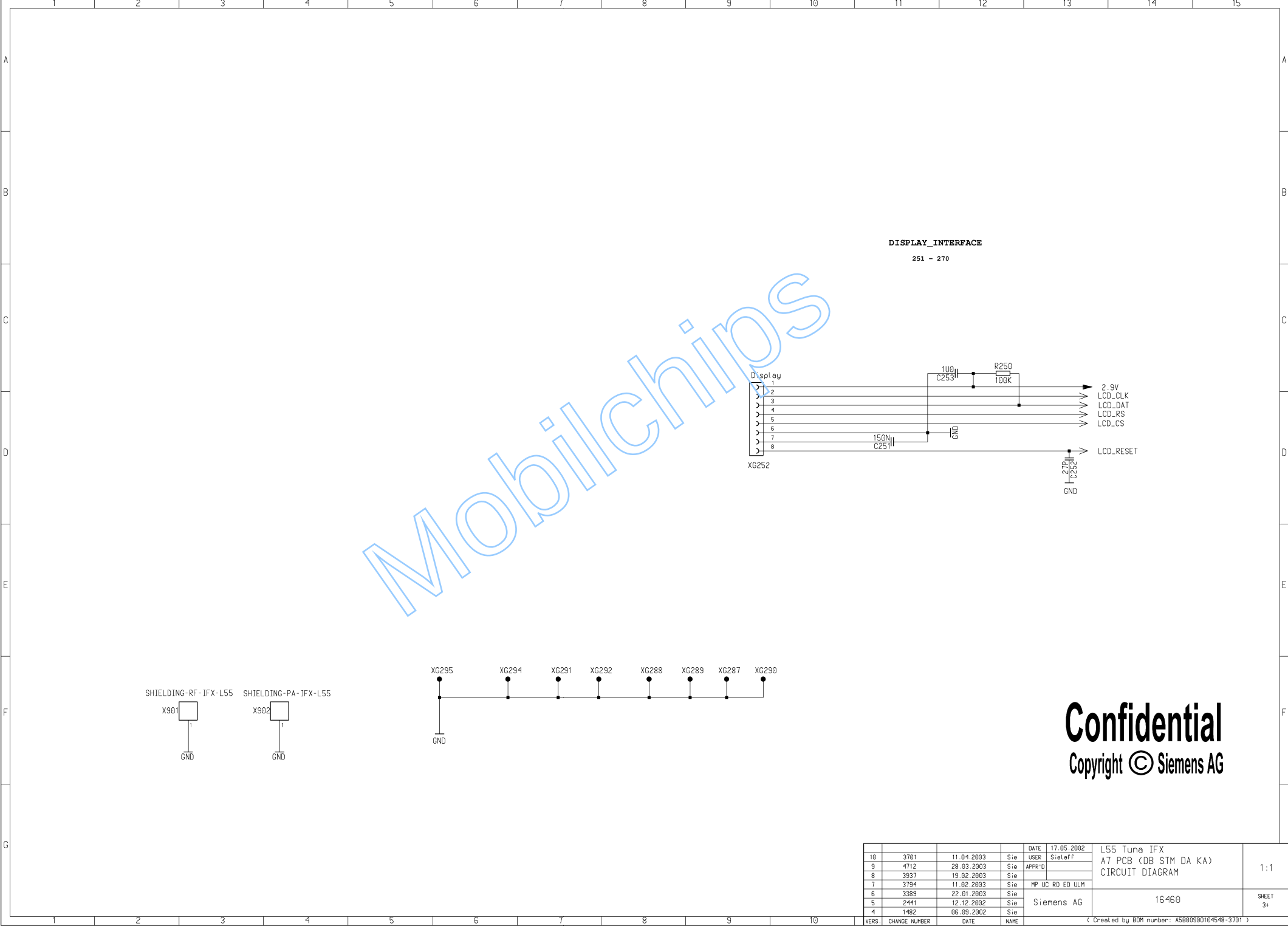




				DATE 17.05.2002		L55 Tune IFX A7 PCB (DB STM DA KA) CIRCUIT DIAGRAM	1:1
10	3701	11.04.2003	Sie	USER	Sietoff		
9	4712	28.03.2003	Sie	APP'D			
8	3937	18.02.2003	Sie				
7	3794	11.02.2003	Sie	MP UC RD ED ULM			
6	3389	22.01.2003	Sie				
5	2441	12.12.2002	Siemens AG			16460	SHEET 2+
4	1482	06.09.2002	Sie				
VER.S		CHANGE NUMBER	DATE	NAME		(Created by BOM number: A5809500104548-3701)	



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			DATE	17.05.2002	L55 Tune IFX	
10	3701	11.04.2003	Sie USER	SieLeff	A7 PCB (DB STM DA KA)	
9	4712	28.03.2003	Sie APPR'D		CIRCUIT DIAGRAM	1:1
8	3937	19.02.2003	Sie			
7	3794	11.02.2003	Sie	MP UC RD ED ULM		
6	3389	22.01.2003	Sie			
5	2441	12.12.2002	Sie	Siemens AG	16460	SHEET 3+
4	1482	06.09.2002	Sie			
VERS	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5800900104548-3701)		

INTERFACE LOGIK/HF 2450-2499

RFSTR <- XG2450 -> RFSTR
RFCLK <- XG2451 -> RFCLK
RFDATA <- XG2452 -> RFDATA

TRANCEIVER ENABLE SIGNAL
TRANCEIVER CLOCK SIGNAL
TRANCEIVER DATA SIGNAL

RF_I <- XG2453 -> RF_I
RF_IX <- XG2454 -> RF_IX
RF_Q <- XG2455 -> RF_Q
RF_QX <- XG2456 -> RF_QX

Bidirectional analoue RX/TX modulattet signal
Baseband signals

TXPWM_FEM1 <- XG2457 -> TXPWM_FEM1
PLLON <- XG2458 -> PLLON
AFC <- XG2459 -> AFC
VCXOEN_UC <- XG2460 -> VCXOEN_UC

PA PWM control (not used for IFX solution)
PLL enable
reference frequency correction
VCC_SYN enable for backup regulators

TXONPA <- XG2461 -> TXONPA
RF_SW <- XG2462 -> RF_SW

Power up Power amplifier
DCSTX enable Asic for Hitachisolution

PA_RAMP <- XG2463 -> PA_RAMP

analogue power amplifier ramping signal

BB_SIN26M <- XG2465 -> BB_SIN26M

26 MHz reference frequency

TXON1 <- XG2468 -> TXON1
TVCX0 <- XG2469 -> TVCX0

HIT: Power up of power controller
IFX: enable of Limiter buffer
temperature sensor X-tal

TXON_GSM <- XG2471 -> TXON_GSM

only Hiitachi: enable for GSMTX of FEM

VCC_SYN <- XG2473 -> VCC_SYN

DETECTION internal/external Antenna

ANT_DET <- XG2474 -> ANT_DET

PA_SW <- XG2475 -> PA_SW

not Used anymore

VCC2_8 <- XG2480 -> VCC2_8

VCC2_8 <- XG2472 -> VCC2_8

SLEEPQ <- XG2467 -> SLEEPQ

enable for backup regulator VCC2_8

HF_OUT <- XG2824 -> HF_OUT

Antenna - RF chamber interface

VCO_BS <- XG2481 -> VCO_BS

not used anymore

2.9V LCD_CLK <- XG2810 -> 2.9V LCD_CLK
LCD_DAT <- XG2811 -> LCD_DAT
LCD_RS <- XG2812 -> LCD_RS
LCD_CS <- XG2813 -> LCD_CS
LCD_RESET <- XG2814 -> LCD_RESET

LIGHT_2 <- XG2816 -> LIGHT_2
LIGHT <- XG2817 -> LIGHT

BATT+ <- XG2823 -> BATT+

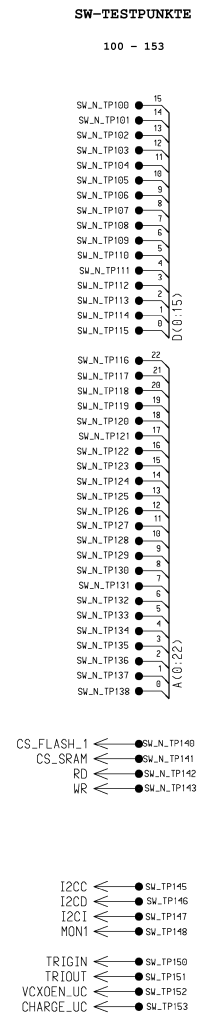
HF_OUT1 <- XG2826 -> HF_OUT1 not used anymore

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			DATE	17.05.2002	L55 Tune IFX	
10	3701	11.04.2003	Sie USER	SieLaEff	A7 PCB (DB STM DA KA)	
9	4712	28.03.2003	Sie APPR'D		CIRCUIT DIAGRAM	1:1
8	3937	19.02.2003	Sie			
7	3794	11.02.2003	Sie	MP UC RD ED ULM		
6	3389	22.01.2003	Sie			
5	2441	12.12.2002	Sie	Siemens AG	16460	SHEET 4+
4	1482	06.09.2002	Sie			
VERS	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5800900104548-3701)		

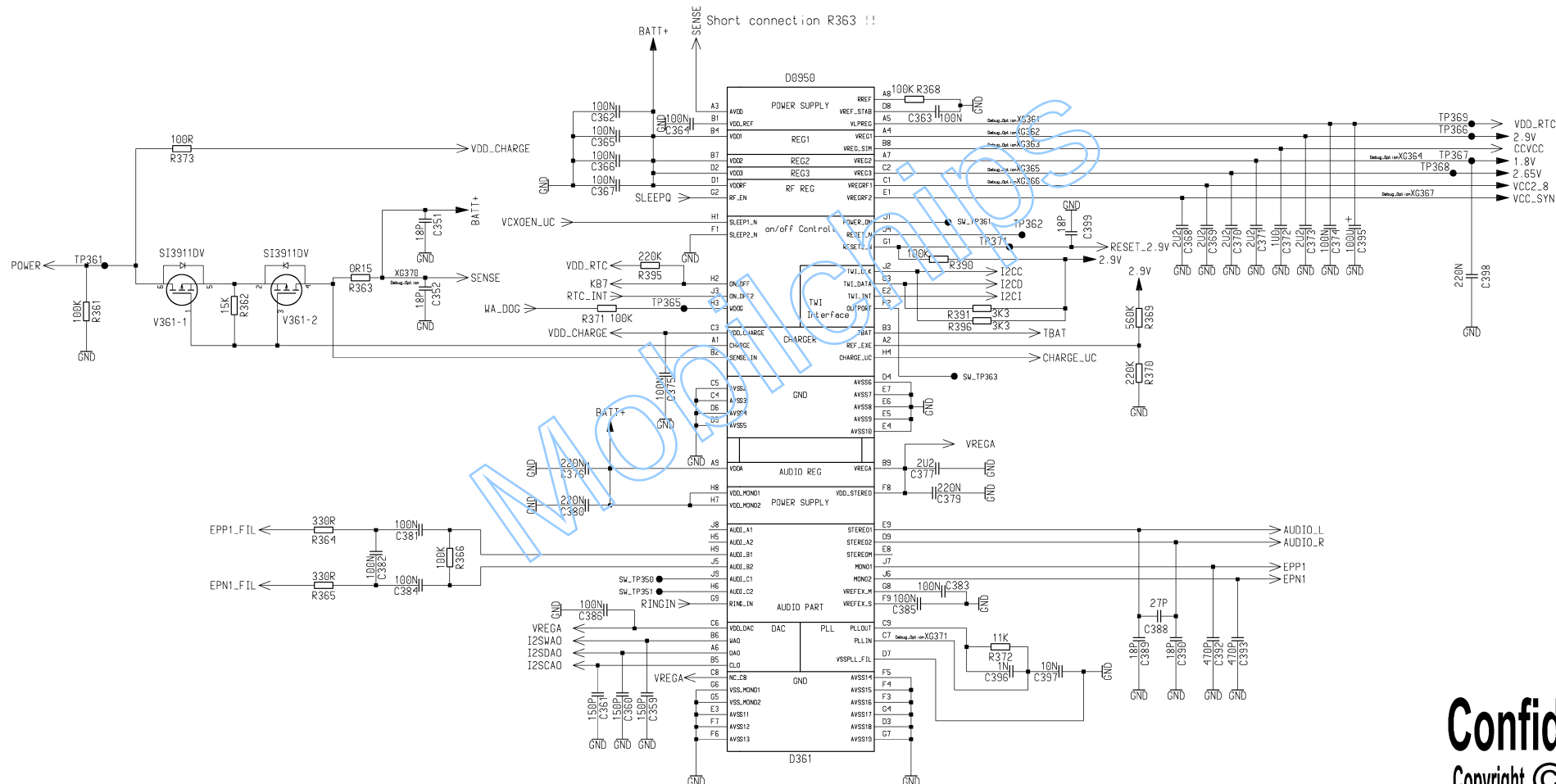


10	3701	11.04.2003	Sie	DATE	17.05.2002	L55 Tuna JFX A7 PCB (DB STM DA KA) CIRCUIT DIAGRAM	1:1	
9	4712	28.03.2003	Sie	USER	Sieteff			
8	3937	19.02.2003	Sie	APPR'D				
7	3734	11.02.2003	Sie	MP	UC RD ED ULM			
6	3989	22.01.2003	Sie					
5	2441	12.12.2002	Sie					
4	1482	06.09.2002	Sie	Siemens AG		16460	SHEET 5+	
VERS.		CHANGE NUMBER	DATE	NAME				(Created by BOM number: A580900010548-3701)



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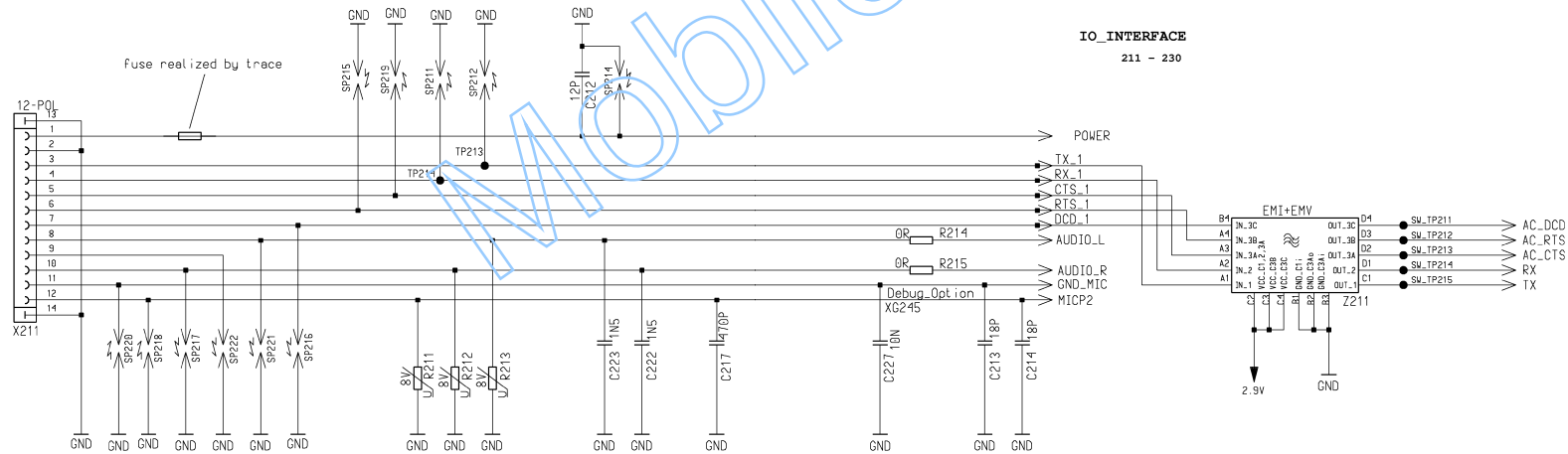
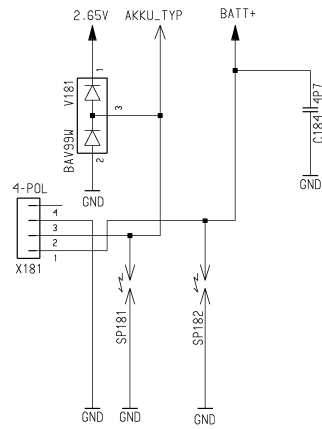
				DATE	17.05.2002	L55 Tune IFX A7 PCB <DB STM DA KA> CIRCUIT DIAGRAM	1:1
10	3701	11.04.2003	Sie	USER	Sietoff		
9	4712	28.03.2003	Sie	APPR'D			
8	3937	19.02.2003	Sie				
7	3794	11.02.2003	Sie	MP	UC RD ED ULM		
6	3389	22.01.2003	Sie			16460	SHEET 6+
5	2411	12.12.2002	Sie	Siemens	AG		
4	1482	06.09.2002	Sie				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B09900104548-3701)			



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				DATE 17.05.2002		L55 Tune IFX			
10	3701	11.04.2003	Sie	USER	Sietaff	A7 PCB (DB STM DA KA)			
9	4712	28.03.2003	Sie	APPR'D		CIRCUIT DIAGRAM		1:1	
8	3937	19.02.2003	Sie						
7	3794	11.02.2003	Sie	MP UC RD ED ULM					
6	3389	22.01.2003	Sie						
5	2411	12.12.2002	Sie			16460			
4	1482	06.09.2002	Sie			Siemens AG		SHEET 7+	
VERS. CHANGE NUMBER		DATE	NAME		(Created by BOM number: A5809080194548-3701)				

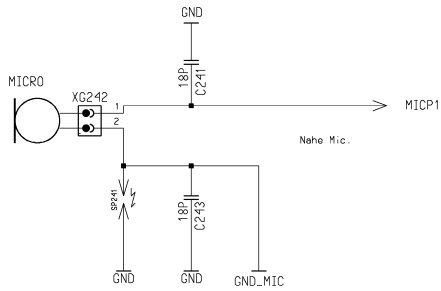
BASEBAND_POWERSUPPLY_GSM
181-185



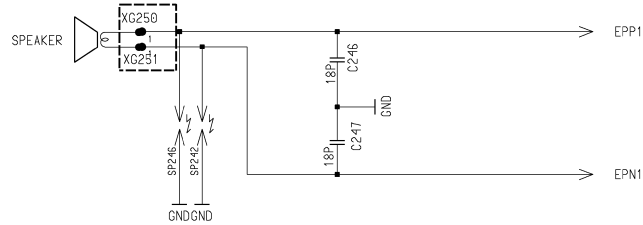
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				DATE	17.05.2002	L55 Tune IFX A7 PCB (DB SIM DA KA) CIRCUIT DIAGRAM	1:1
10	3701	11.04.2003	Sie	Sietoff			
9	4712	28.03.2003	Sie	APPR'D			
8	3937	19.02.2003	Sie				
7	3794	11.02.2003	Sie	MP UC RD EO ULM			
6	3389	22.01.2003	Sie				
5	2411	12.12.2002	Siemens AG			16460	SHEET 8*
4	1482	06.09.2002					
(Created by BOM number: A580990010548-3701)							
VERS	CHANGE	NUMBER	DATE	NAME			

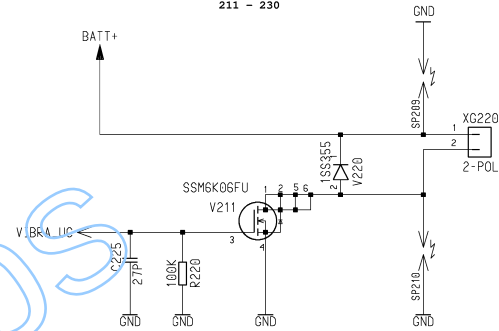
MICROPHONE_CIRCUIT
241-250



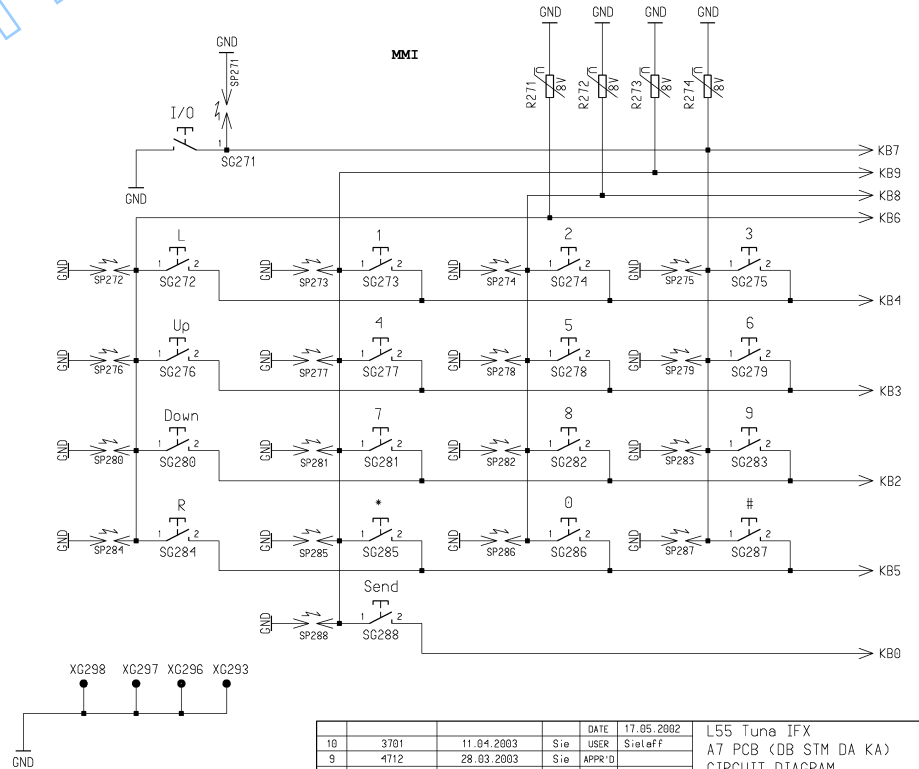
SPEAKER_CIRCUIT



VIBRA_CIRCUIT
211 - 230



MMI



VERS	CHANGE NUMBER	DATE	NAME	DATE	USER	DATE	USER	DATE	USER	DATE	USER
10	3701	11.04.2003	Sie	17.05.2002	Sie	17.05.2002	Sie	17.05.2002	Sie	17.05.2002	Sie
9	4712	28.03.2003	Sie	APPR'D							
8	3937	19.02.2003	Sie								
7	3794	11.02.2003	Sie	MP	UC	RD	ED	ULM			
6	3389	22.01.2003	Sie								
5	2441	12.12.2002	Sie								
4	1482	06.09.2002	Sie								
				Siemens AG				16460			
				(Created by BOM number: A5800900104548-3701)				1:1			

KEYPAD_BACKLIGHT

2.9V → 0R R294 → VREGA_2.9V

LIGHT_1 →

0V R293 → GND

150R R287, 150R R288, 150R R289, 150R R290, 150R R291, 150R R292

V286, V287, V288, V289, V291 (CHIPLED_0603_AMBER)

8V R293

DISPLAY_BACKLIGHT

VREGA_2.9V → 0R R275 → LIGHT

BLUELED_1, BLUELED_2, BLUELED_3, BLUELED_4

TP282, TP283, TP284, TP285

V282, V283, V284, V285 (CL-270TLY-C)

82R R283, 82R R284, 82R R285, 82R R286

LIGHT_2

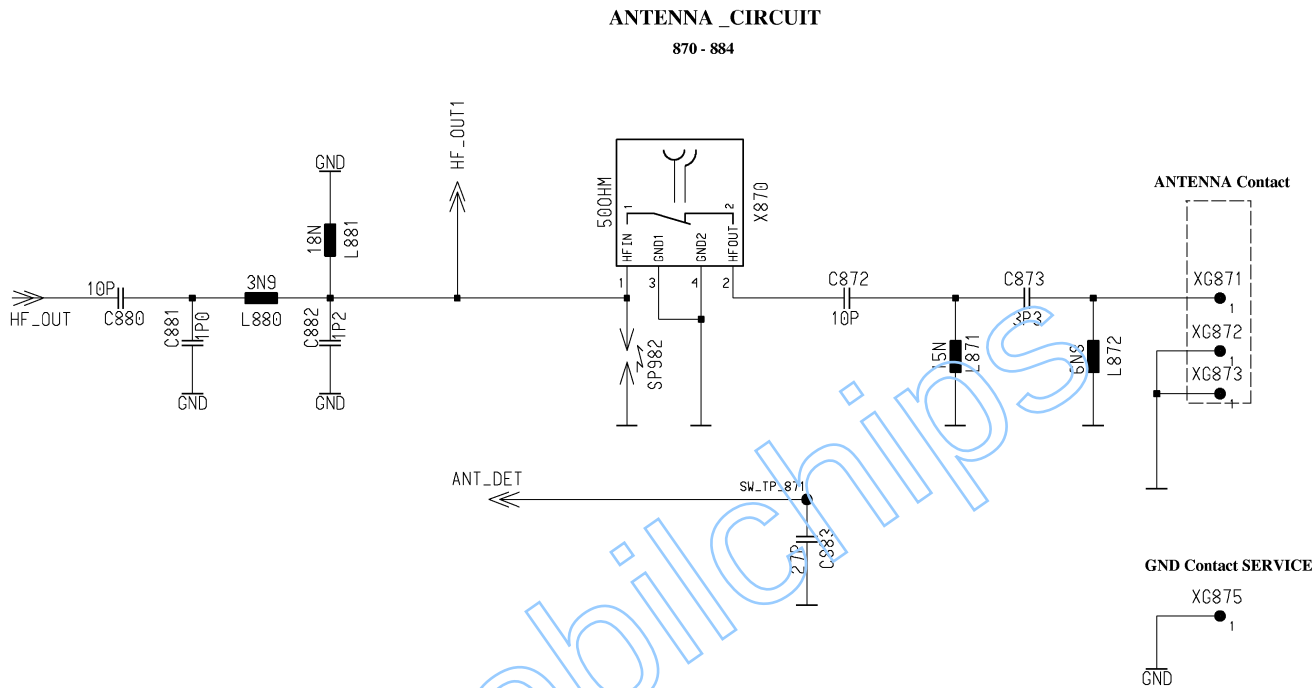
Schirmrahmen

VERS.	CHANGE NUMBER	DATE	NAME	DATE	USER	Siemens AG
10	3701	11.04.2003	Sie	17.05.2002	Siemens AG	L55 Tune IFX
9	4712	28.03.2003	Sie	APPR'D	Siemens AG	A7 PCB (DB STM DA KA)
8	3837	19.02.2003	Sie		Siemens AG	CIRCUIT DIAGRAM
7	3794	11.02.2003	Sie		Siemens AG	
6	3389	22.01.2003	Sie		Siemens AG	
5	2441	12.12.2002	Sie		Siemens AG	
4	1482	06.09.2002	Sie		Siemens AG	

1:1

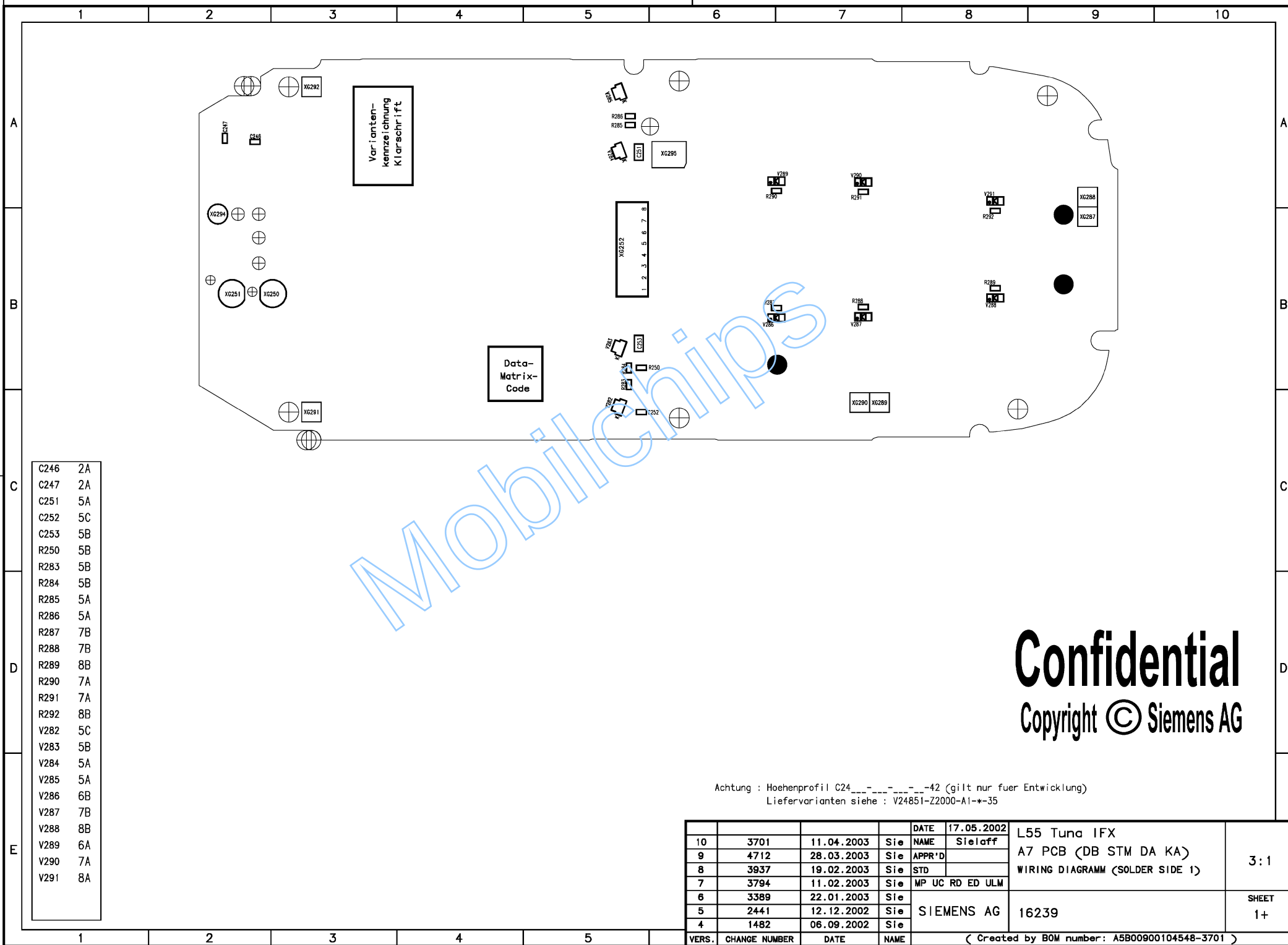
SHEET 10+

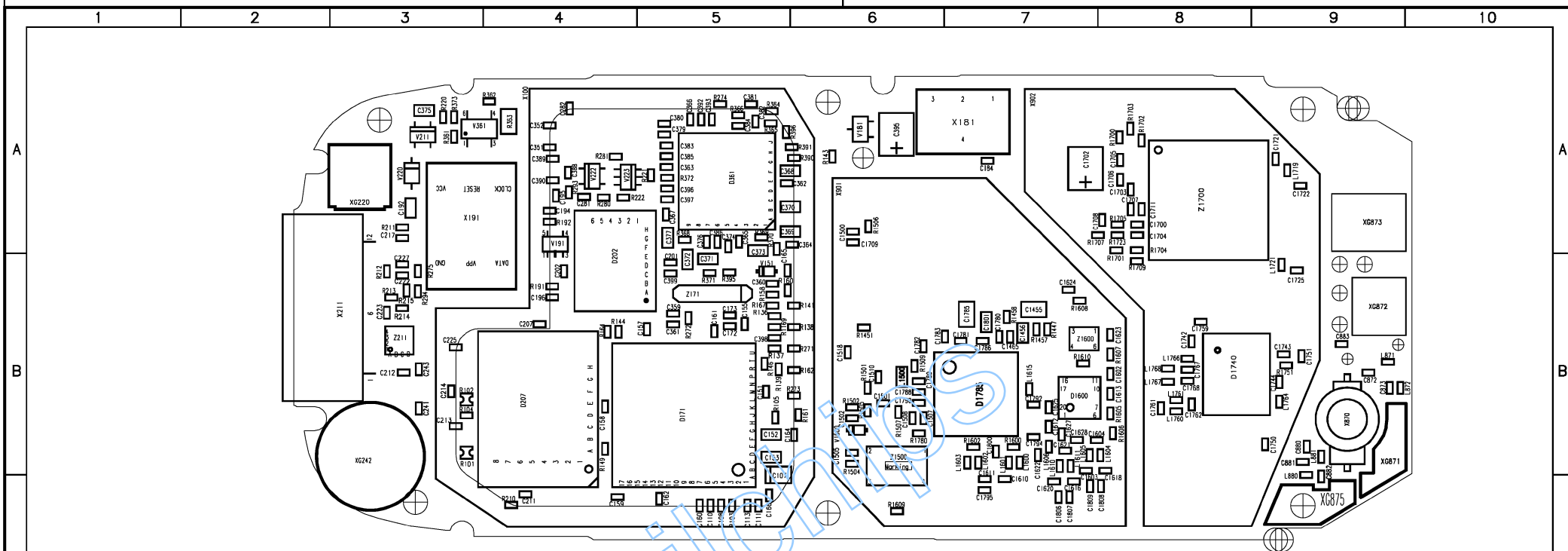
(Created by BOM number: A5B00900104548-3701)



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			DATE	17.05.2002	L55 Tuna IFX	
10	3701	11.04.2003	Sie USER	Sieleff	A7 PCB (DB STM DA KA)	
9	4712	28.03.2003	Sie APPR'D		CIRCUIT DIAGRAM	1:1
8	3937	19.02.2003	Sie			
7	3794	11.02.2003	Sie	MP UC RD ED ULM		
6	3389	22.01.2003	Sie			
5	2441	12.12.2002	Sie	Siemens AG	16460	SHEET 11-
4	1482	06.09.2002	Sie			
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900104548-3701)		





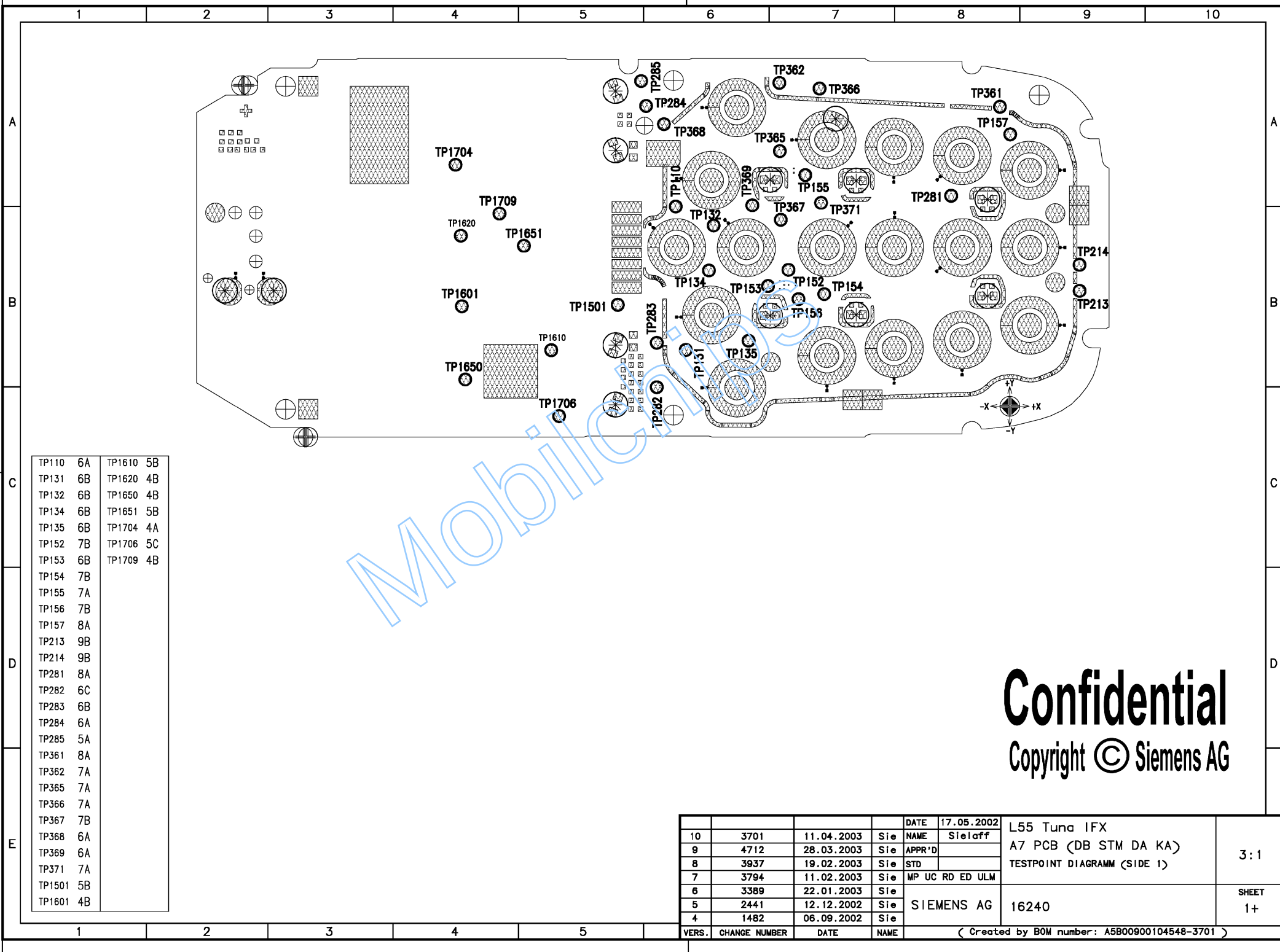
C107	5B	C211	4C	C372	5B	C882	9B	C1627	7B	C1783	7B	L1601	7B	R144	4B	R294	3B	R1509 6B R1600 7B R1602 7B R1605 8B R1606 8B R1607 8B R1608 7B R1609 6C R1610 7B R1700 8A R1701 8A R1702 8A R1703 8A R1704 8A R1705 8A R1707 7A R1709 8B R1723 8A	R1751 9B R1780 6B V151 5B V181 6A V191 4B V211 3A V220 3A V222 4A V223 5A V361 3A V1500 6B X100 3B X181 7A X191 3A X211 3B X870 9B X901 6A X902 7A	Z171 5B Z211 3B Z1500 6B Z1600 7B Z1700 8A
C108	5C	C212	3B	C373	5A	C883	9B	C1628	7B	C1785	7B	L1602	7B	R145	5B	R351	3A			
C110	5C	C213	3B	C374	5A	C1455	7B	C1700	8A	C1786	7B	L1603	7B	R149	4B	R362	4A			
C111	5C	C214	3B	C375	3A	C1456	7B	C1702	7A	C1788	6B	L1604	7B	R158	5B	R363	4A			
C113	5C	C217	3A	C376	5A	C1465	7B	C1703	8A	C1789	6B	L1605	7B	R160	5B	R364	5A			
C151	5B	C222	3A	C377	5A	C1500	6A	C1704	8A	C1790	6B	L1606	7B	R161	6B	R365	5A			
C152	5B	C223	3B	C379	5A	C1501	6B	C1705	8A	C1792	7B	L1610	7B	R162	6B	R366	5A			
C153	5B	C225	3B	C380	5A	C1502	6B	C1706	8A	C1794	7B	L1611	7B	R164	4B	R368	5A			
C155	5B	C227	3B	C381	5A	C1505	6B	C1707	8A	C1795	7C	L1615	7B	R167	5B	R369	5A			
C157	5B	C241	3B	C382	5A	C1507	6B	C1708	8A	C1800	7B	L1719	9A	R191	4B	R370	5B			
C158	4B	C243	3B	C383	5A	C1508	6B	C1709	6A	C1801	7B	L1721	9B	R192	4A	R371	5B			
C159	4C	C281	4A	C384	5A	C1510	6B	C1711	8A	C1806	7C	L1760	8B	R210	4C	R372	5A			
C160	5C	C282	4A	C385	5A	C1518	6B	C1721	9A	C1807	7C	L1761	8B	R211	3A	R373	3A			
C161	5B	C351	4A	C386	5A	C1602	8B	C1722	9A	C1808	8C	L1764	9B	R212	3B	R390	5A			
C162	5C	C352	4A	C388	4A	C1603	7B	C1725	9B	C1809	7C	L1766	8B	R213	3B	R391	5A			
C164	6B	C359	5B	C389	4A	C1604	7B	C1742	8B	D171	5C	L1767	8B	R214	3B	R395	5B			
C165	5B	C360	5B	C390	4A	C1610	7C	C1743	9B	D202	5B	L1768	8B	R215	3B	R396	5A			
C166	5C	C361	5B	C392	5A	C1611	7C	C1744	9B	D207	4B	R101	3B	R220	3A	R1169	5B			
C172	5B	C362	6A	C393	5A	C1612	7B	C1750	9B	D361	5A	R102	3B	R221	5A	R1447	7B			
C173	5B	C363	5A	C395	6A	C1613	8B	C1751	9B	D1600	7B	R103	5C	R222	4A	R1451	6B			
C184	7A	C364	6A	C396	5A	C1616	7C	C1759	8B	D1740	8B	R104	3B	R271	6B	R1457	7B			
C192	3A	C365	5A	C397	5A	C1618	8B	C1761	8B	D1785	6B	R105	5B	R272	5B	R1458	7B			
C194	4A	C366	5A	C398	5B	C1620	7C	C1762	8B	L871	9B	R136	5B	R273	6B	R1501	6B			
C195	4A	C367	5A	C399	5B	C1621	7B	C1767	8B	L872	9B	R137	5B	R274	5A	R1502	6B			
C196	4B	C368	6A	C872	9B	C1622	7B	C1768	8B	L880	9B	R138	6B	R275	3B	R1504	6B			
C201	5B	C369	6A	C873	9B	C1623	8B	C1780	7B	L881	9B	R139	5B	R280	4A	R1505	6B			
C202	4B	C370	6A	C880	9B	C1624	7B	C1781	7B	L1500	6B	R141	6B	R281	4A	R1506	6A			
C207	4B	C371	5B	C881	9B	C1625	7B	C1782	6B	L1600	7B	R143	6A	R293	4A	R1507	6B			

Achtung : Höhenprofil C2
Liefervarianten

10	3701	11.04.
9	4712	28.03.
8	3937	19.02.
7	3794	11.02.
6	3389	22.01.
5	2441	12.10.

Achtung : Höhenprofil C24___-___-___-42 (gilt nur fuer Entwicklung)
Liefervarianten siehe : V24851-Z2000-A1-*-35

				DATE	17.05.2002	L55 Tuna IFX A7 PCB (DB STM DA KA) WIRING DIAGRAMM (SOLDER SIDE 2)	3:1
10	3701	11.04.2003	Sie	NAME	Sieloff		
9	4712	28.03.2003	Sie	APPR'D			
8	3937	19.02.2003	Sie	STD			
7	3794	11.02.2003	Sie	MP UC RD ED ULM			
6	3389	22.01.2003	Sie			16239	SHEET 2+
5	2441	12.12.2002	Sie	SIEMENS AG			
4	1482	06.09.2002	Sie				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900104548-3701)			



TP110	6A	TP1610	5B
TP131	6B	TP1620	4B
TP132	6B	TP1650	4B
TP134	6B	TP1651	5B
TP135	6B	TP1704	4A
TP152	7B	TP1706	5C
TP153	6B	TP1709	4B
TP154	7B		
TP155	7A		
TP156	7B		
TP157	8A		
TP213	9B		
TP214	9B		
TP281	8A		
TP282	6C		
TP283	6B		
TP284	6A		
TP285	5A		
TP361	8A		
TP362	7A		
TP365	7A		
TP366	7A		
TP367	7B		
TP368	6A		
TP369	6A		
TP371	7A		
TP1501	5B		
TP1601	4B		

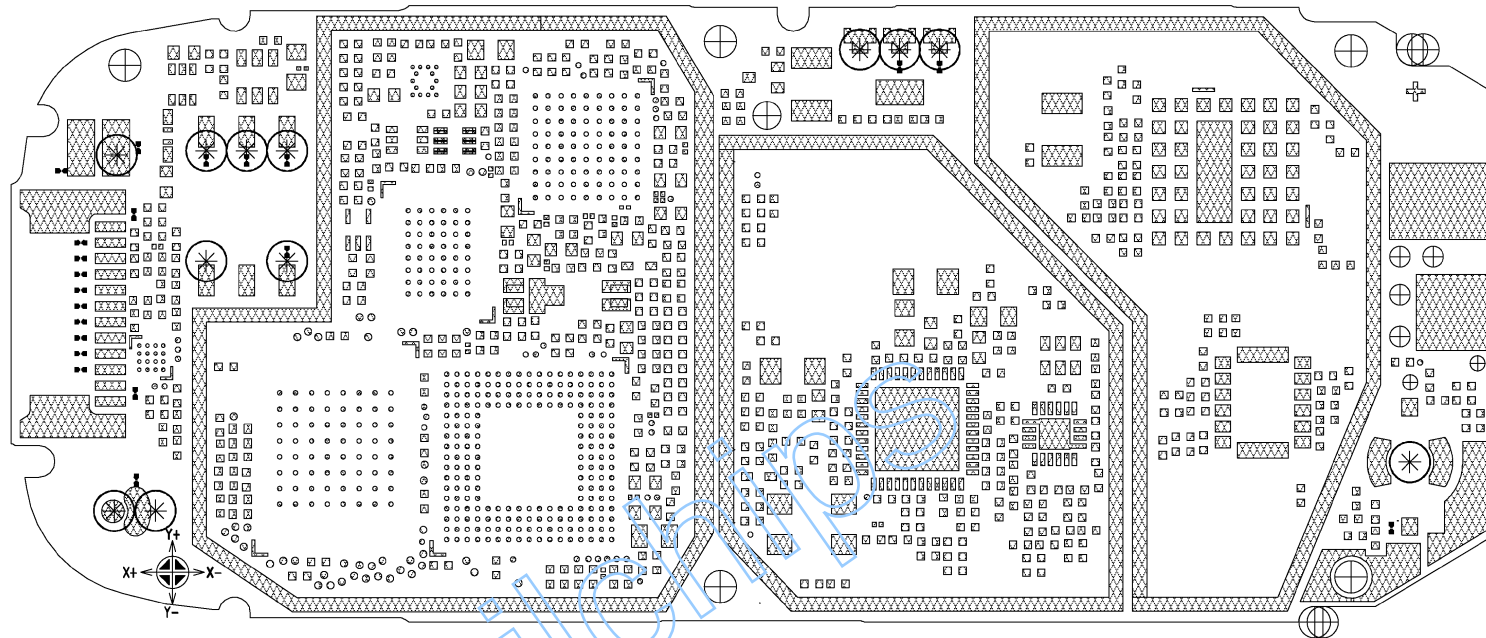
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				DATE	17.05.2002	L55 Tuna IFX	
10	3701	11.04.2003	Sie	NAME	Sielaff	A7 PCB (DB STM DA KA)	3:1
9	4712	28.03.2003	Sie	APPR'D		TESTPOINT DIAGRAM (SIDE 1)	
8	3937	19.02.2003	Sie	STD			
7	3794	11.02.2003	Sie	MP UC RD ED ULM			
6	3389	22.01.2003	Sie				
5	2441	12.12.2002	Sie	SIEMENS AG		16240	SHEET
4	1482	06.09.2002	Sie				1+
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900104548-3701)			

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ALLE RECHTE VORBEHALTEN

SAP-Ident: 16240 / BOM / 000 / 10 Created/Changed: SIELAFF, 2003-04-11



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				DATE	17.05.2002	L55 Tuna IFX A7 PCB (DB STM DA KA) TESTPOINT DIAGRAMM (SIDE 2)	3:1
10	3701	11.04.2003	Sie	NAME	Sielaff		
9	4712	28.03.2003	Sie	APPR'D			
8	3937	19.02.2003	Sie	STD			
7	3794	11.02.2003	Sie	MP UC RD ED ULM			
6	3389	22.01.2003	Sie			SIEMENS AG 16240	SHEET 2-
5	2441	12.12.2002	Sie				
4	1482	06.09.2002	Sie				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900104548-3701)			