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HIERARCHY						SHEET		PDF-SHEET					
A5B00900197562-31328						index		sheet 1					
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				DATE		06.09.2005		Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC		1:1			
				USER		Zhang Yanhong							
				APPR'D									
						COM MD D HW4							
2		32049		26.09.2005		zyh		Siemens AG		217094		index	
1		31329		06.09.2005		ZYH							
VERS.		CHANGE NUMBER		DATE		NAME		(Created by BOM number: A5B00900197564-32049)					

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3_SIN26M /sheet1 13 D	CHARGE_UC /sheet7 10 C /sheet6 15 F /sheet5 14 D	HF_OUT1 /sheet4 10 E /sheet2 6 C	KB9 /sheet5 8 E /sheet3 11 D /sheet11 8 B	RD /sheet6 5 E /sheet6 15 E /sheet6 5 B /sheet6 11 D /sheet5 10 F	RF_SIN26M /sheet5 11 C /sheet4 2 C /sheet4 4 C /sheet1 14 D	TX /sheet8 13 F /sheet5 9 C	VEBU_1.8 /sheet7 5 E /sheet6 6 E /sheet11 8 C		
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AC_DCD /sheet8 13 E /sheet5 14 E	CTS_1 /sheet8 9 E	HW_CODING4 /sheet5 8 D /sheet5 12 A	LCD_RS /sheet5 9 C /sheet11 7 C	RF_ANT_DET /sheet5 14 D /sheet4 4 D /sheet4 2 D /sheet2 7 D	RTS_1 /sheet8 9 E	VBAT /sheet5 9 B /sheet5 1 E			
AC_RTS /sheet8 13 F /sheet5 9 C	D(0:15) /sheet6 15 B /sheet6 5 B /sheet6 12 E /sheet6 4 F /sheet5 12 C	HW_CODING5 /sheet5 10 C /sheet5 9 C /sheet5 15 B	LIGHT_1 /sheet10 11 B /sheet10 14 E	RF_BAND_SW /sheet5 8 D /sheet4 4 F /sheet4 2 F /sheet1 7 F	RTC_INT /sheet7 7 C /sheet5 12 C	VBOOST /sheet7 10 F /sheet11 8 B /sheet10 9 E	VEBU_1.8PSRAM /sheet6 8 E /sheet6 1 E		
AGND /sheet5 5 G /sheet5 4 F /sheet5 5 F /sheet5 3 G	DCD_1 /sheet8 9 E	HW_CODING6 /sheet5 10 F /sheet5 10 G /sheet5 15 B	LIGHT_2 /sheet11 8 B /sheet10 7 C	RF_CLK /sheet5 14 F /sheet4 4 A /sheet4 2 A /sheet1 15 E	RX /sheet8 13 F /sheet5 9 C	VCXOEN_UC /sheet7 6 B /sheet6 15 F /sheet5 10 C	VEBU_2.9 /sheet7 5 F /sheet6 6 A /sheet6 7 A /sheet6 1 E /sheet6 2 B /sheet6 5 E /sheet5 4 C /sheet5 4 B /sheet5 11 G /sheet5 13 B /sheet11 8 C		
AKKU_TYP /sheet8 4 A /sheet5 11 A	EPP1 /sheet9 5 E /sheet7 13 D	I2C_CLK /sheet7 10 C /sheet6 15 F /sheet5 9 C	LIGHT_KB /sheet5 15 E /sheet10 9 B	RF_DAT /sheet5 14 F /sheet4 4 B /sheet4 2 B /sheet1 15 D	RX_1 /sheet8 9 E	VDDA_2.65V /sheet8 3 B /sheet7 14 B /sheet7 10 C /sheet5 5 C			
AUDIO_L /sheet8 9 F /sheet7 13 D	EPPA1 /sheet5 7 E /sheet11 6 C	I2C_DAT /sheet7 10 C /sheet6 15 F /sheet5 9 C	LIGHT_LCD /sheet5 11 C /sheet10 4 A	RF_FE_DTR_DCS /sheet5 8 D /sheet4 4 C /sheet4 2 C /sheet1 2 C	SENSE /sheet7 5 C /sheet7 8 A	VDD_BRIGTH /sheet1 10 A /sheet1 8 F /sheet1 8 F /sheet1 10 A /sheet1 8 A /sheet1 9 F /sheet1 12 D /sheet1 12 D /sheet1 8 A /sheet1 9 A /sheet1 10 B	VEBU_2.9FLASH /sheet6 5 C /sheet6 11 D /sheet6 2 B /sheet6 9 D /sheet6 11 D		
AUDIO_R /sheet8 9 F /sheet7 13 D	EPPA2 /sheet5 7 E /sheet11 6 D	I2C_IRQ /sheet7 10 C /sheet6 15 F /sheet5 11 D	MICN1 /sheet9 5 B /sheet5 3 B /sheet5 1 D	RF_FE_DTR_GSM /sheet5 8 E /sheet4 2 C /sheet4 4 C /sheet1 2 C	STEPUP_SHUD /sheet7 7 G /sheet5 15 E	VIBRA_UC /sheet9 10 B /sheet5 14 D			
BATT+ /sheet9 10 B /sheet8 4 B /sheet7 5 B /sheet7 7 A /sheet7 7 D /sheet7 1 F /sheet7 6 F /sheet5 9 A /sheet1 7 E /sheet1 1 E	F_WAIT /sheet6 15 E /sheet6 5 D /sheet6 11 D	I2SCA0 /sheet7 7 E /sheet5 15 E	MICN2 /sheet5 4 A /sheet5 1 D	RF_I /sheet5 1 E /sheet4 4 B /sheet4 2 B /sheet1 9 F	TBAT /sheet5 11 A /sheet5 1 E	VMIC /sheet5 5 A /sheet5 4 A			
BHE /sheet6 5 E /sheet6 15 E /sheet5 10 F	GND_MIC /sheet9 4 C /sheet8 9 F /sheet5 6 A /sheet5 4 G /sheet5 4 B /sheet5 6 C /sheet5 1 B /sheet5 4 B	I2SDA0 /sheet7 7 E /sheet5 15 E	MICP1 /sheet9 5 B /sheet5 3 B /sheet5 1 D	RF_IX /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 10 F	TCK /sheet5 9 F /page1 3 C	VREGA /sheet7 7 E /sheet7 10 D /sheet7 8 E			
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BT_SIN26M /sheet1 13 D	GSM_PA_IN /sheet1 1 F /sheet1 9 F	KB0 /sheet5 8 E /sheet3 11 E	MON1 /sheet5 9 F	RF_QX /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 10 F	TMS /sheet5 9 F /page1 3 C	WR /sheet6 5 E /sheet6 15 E /sheet6 5 B /sheet6 11 D /sheet5 10 F			
CCCLK /sheet5 14 D /sheet10 7 E	HALL_1 /sheet5 15 E /sheet11 7 G	KB2 /sheet5 8 E /sheet3 11 D	MON2 /sheet5 9 F /page1 3 B	RF_QX /sheet5 1 E /sheet4 2 B /sheet4 4 B /sheet1 10 F	TRIGIN /sheet6 15 F /sheet5 9 F				
CCIO /sheet5 14 D /sheet10 7 E	HALL_2 /sheet5 8 E /sheet11 9 G	KB3 /sheet5 8 E /sheet3 11 D	OEN /sheet5 10 F	ON_OFF /sheet7 7 C /sheet5 7 E /sheet11 8 B	TRIGOUT /sheet6 15 F /sheet5 9 F				
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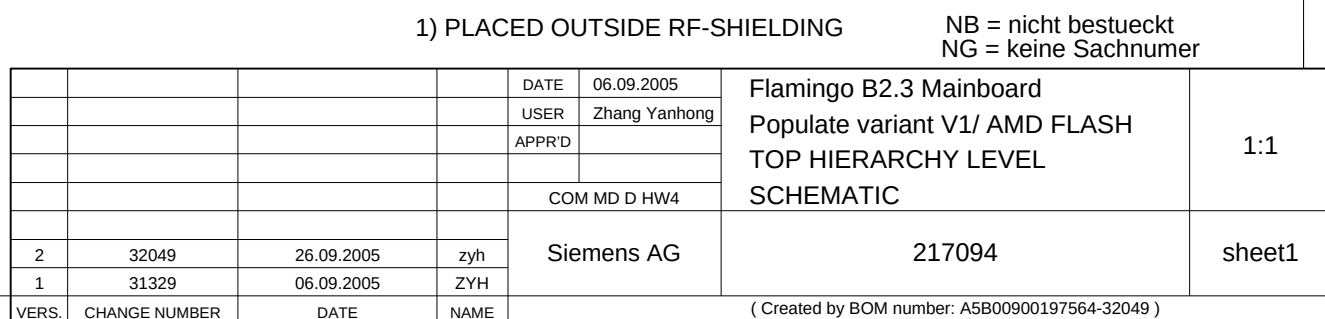
				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
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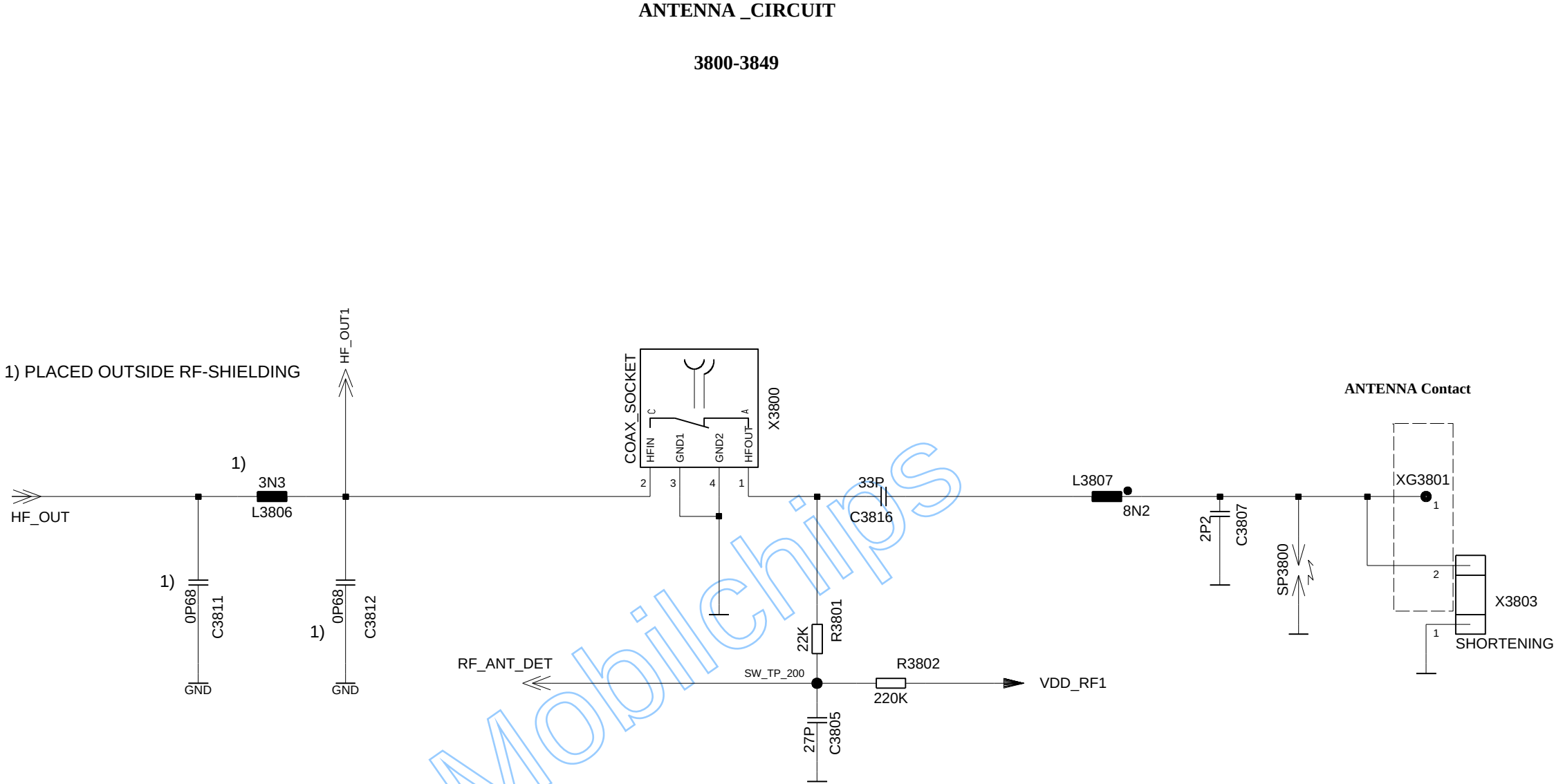
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A	C101 /sheet5 2 B	C268 /sheet11 6 A	C909 /sheet1 4 F	R147 /sheet5 10 G	R3801 /sheet2 8 D	B
	C102 /sheet5 3 B	C269 /sheet11 5 A	C910 /sheet1 2 F	R148 /sheet5 14 B	R3802 /sheet2 9 D	
	C103 /sheet5 3 B	C270 /sheet11 7 C	C911 /sheet1 5 F	R149 /sheet5 14 B	S851 /sheet11 10 G	
	C104 /sheet5 4 B	C271 /sheet11 4 D	C912 /sheet1 6 F	R153 /sheet5 4 C	S852 /sheet11 7 G	
	C105 /sheet5 2 D	C272 /sheet11 4 D	C913 /sheet1 6 E	R156 /sheet5 11 G	TP813 /sheet8 3 C	
	C106 /sheet5 6 A	C273 /sheet11 5 D	C914 /sheet1 7 F	R158 /sheet5 8 C	TP367 /sheet7 14 B	
	C107 /sheet5 6 A	C274 /sheet11 5 D	C915 /sheet1 5 F	R159 /sheet5 10 C	TP361 /sheet7 2 C	
	C108 /sheet5 1 D	C275 /sheet11 5 D	C916 /sheet1 6 E	R160 /sheet5 6 D	TP364 /sheet7 5 E	
	C109 /sheet5 2 D	C276 /sheet11 5 D	C918 /sheet1 2 E	R161 /sheet5 10 C	TP374 /sheet7 10 F	
	C110 /sheet5 1 D	C277 /sheet11 5 D	C951 /sheet1 12 C	R162 /sheet5 15 D	TP369 /sheet7 14 B	
B	C111 /sheet5 1 D	C278 /sheet11 6 A	C952 /sheet1 14 C	R168 /sheet5 13 A	TP363 /sheet7 14 B	C
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	C118 /sheet5 6 E	C298 /sheet8 7 E	C958 /sheet1 1 E	R207 /sheet6 2 E	TP365 /sheet7 7 C	
	C131 /sheet5 13 A	C299 /sheet8 7 E	C961 /sheet1 10 C	R208 /sheet6 7 D	TP154 /sheet5 9 F	
	C132 /sheet5 9 A	C302 /sheet7 12 D	C962 /sheet1 8 E	R209 /sheet6 7 E	TP161 /sheet5 9 F	
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	C152 /sheet5 3 C	C350 /sheet7 5 B	C1335 /sheet7 6 F	R211 /sheet8 5 F	TP110 /sheet5 13 A	
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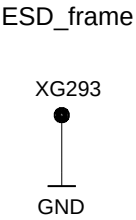
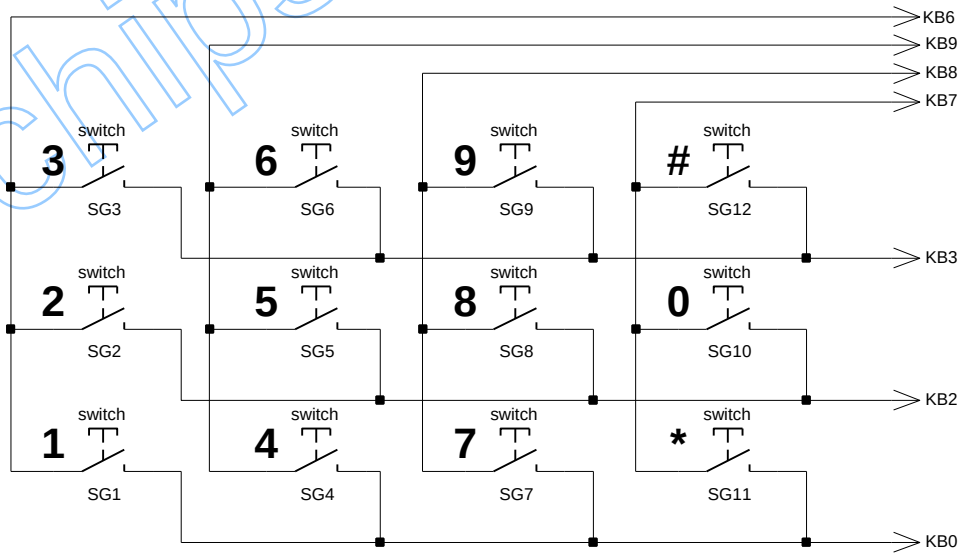


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MMI MAINBOARD



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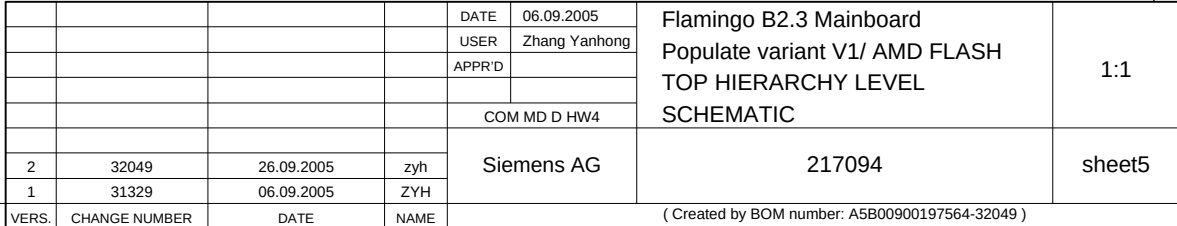
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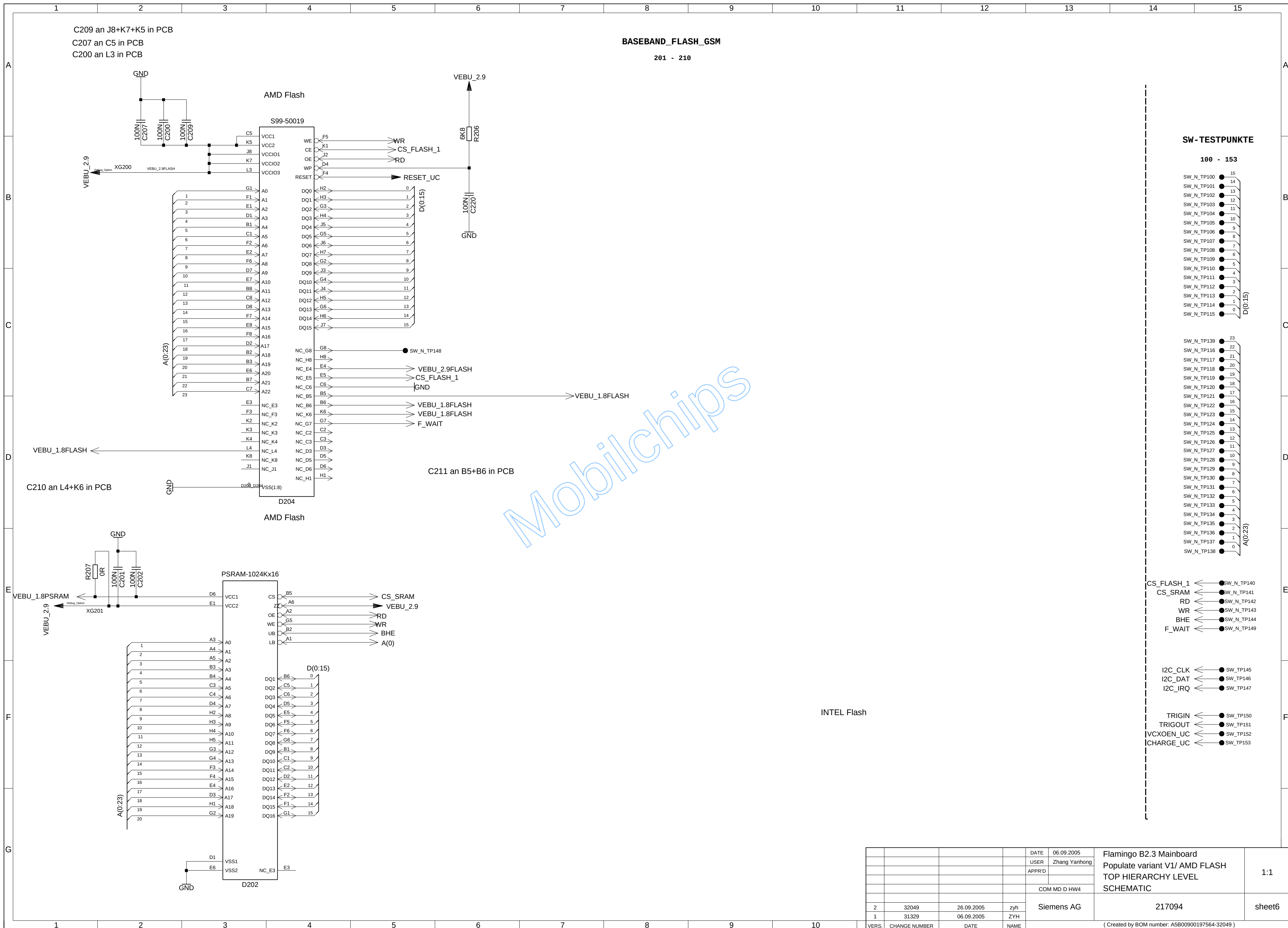
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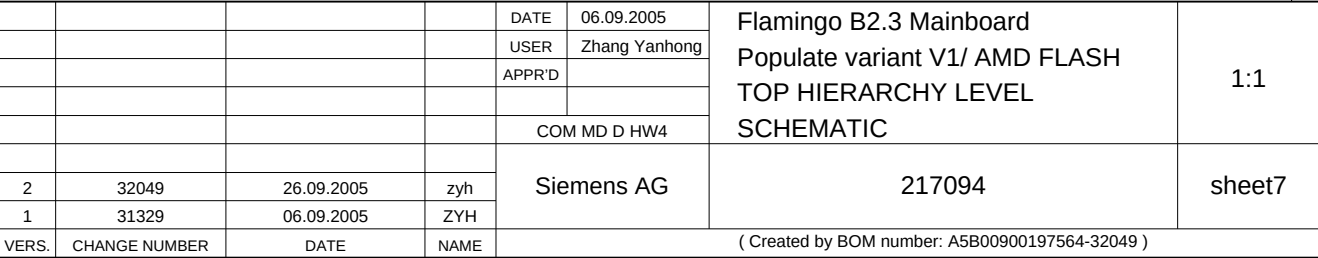
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400-449



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1	31329	06.09.2005	ZYH				
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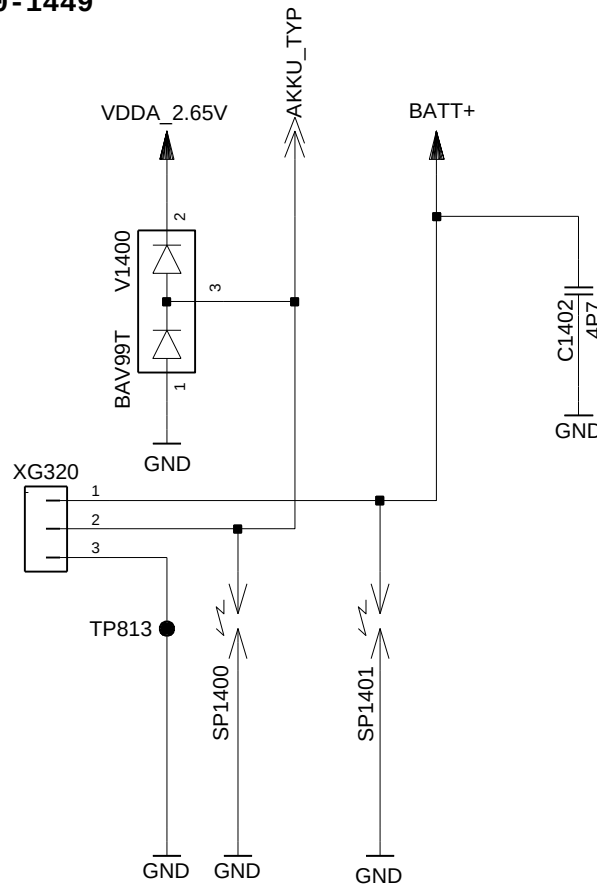






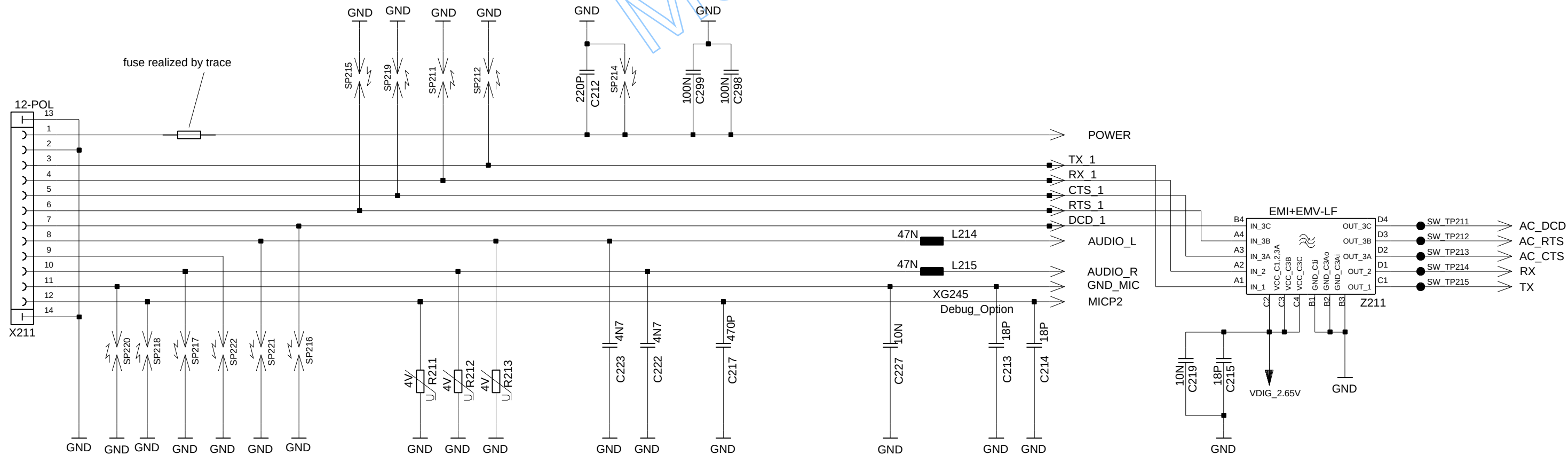
BATTERY CONNECTOR

1400-1449



IO_INTERFACE

211 - 230

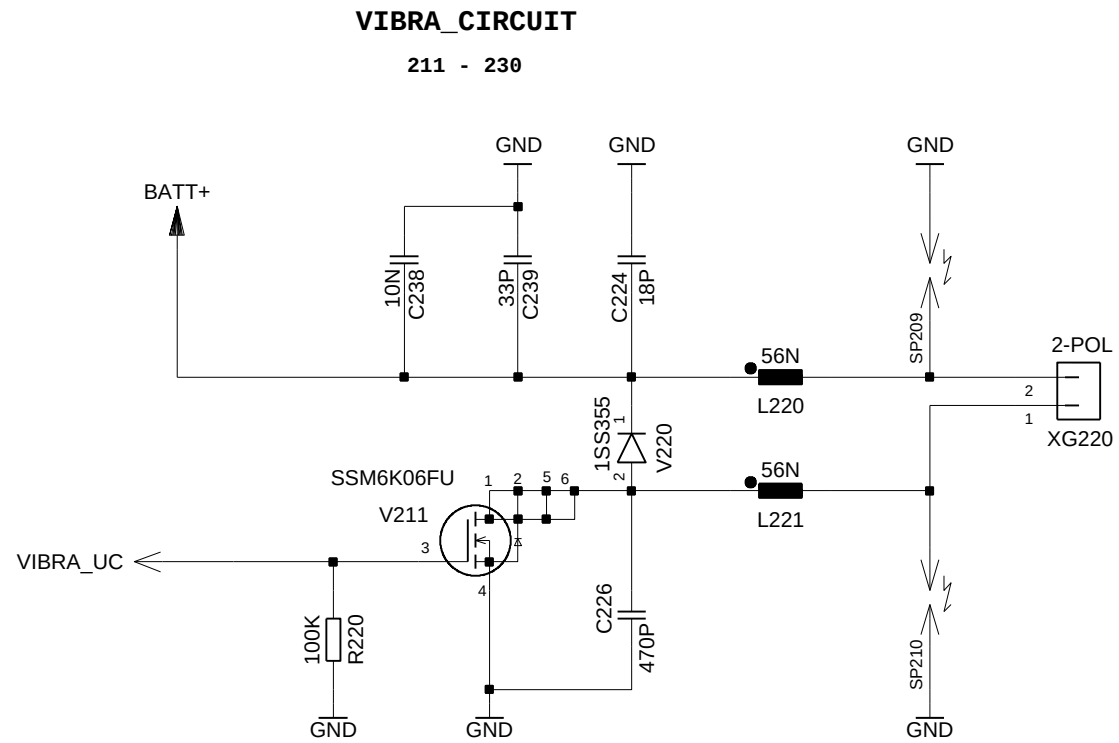
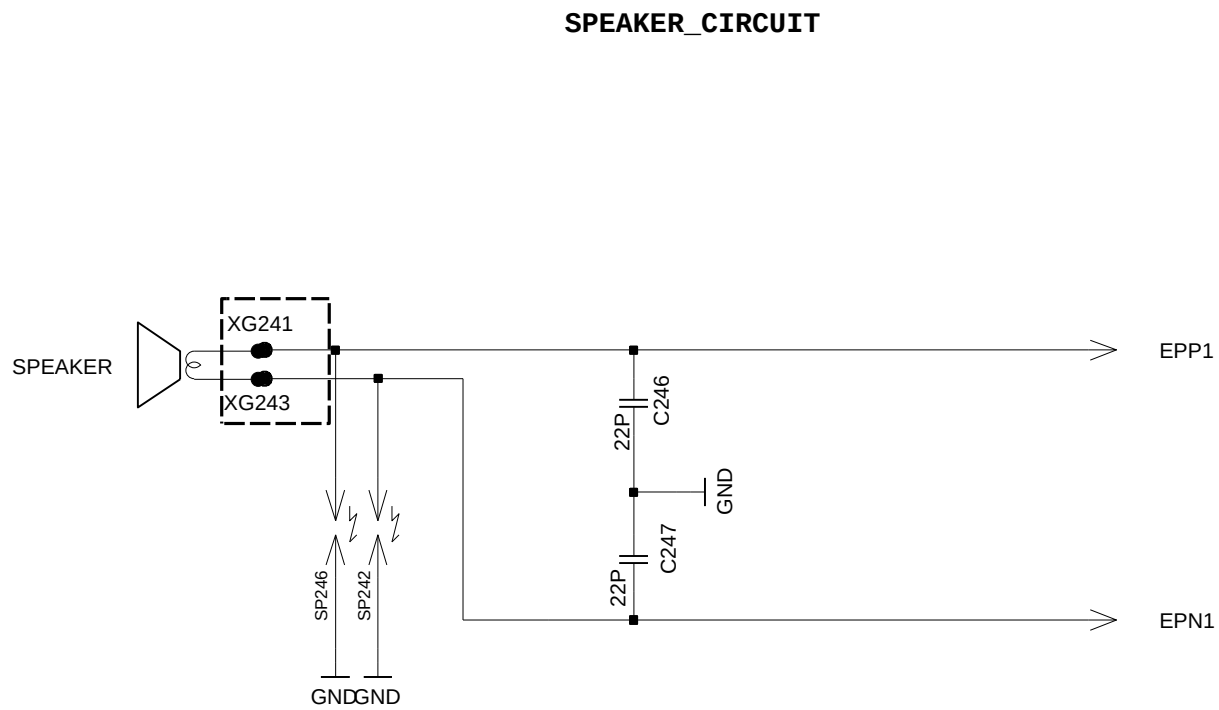
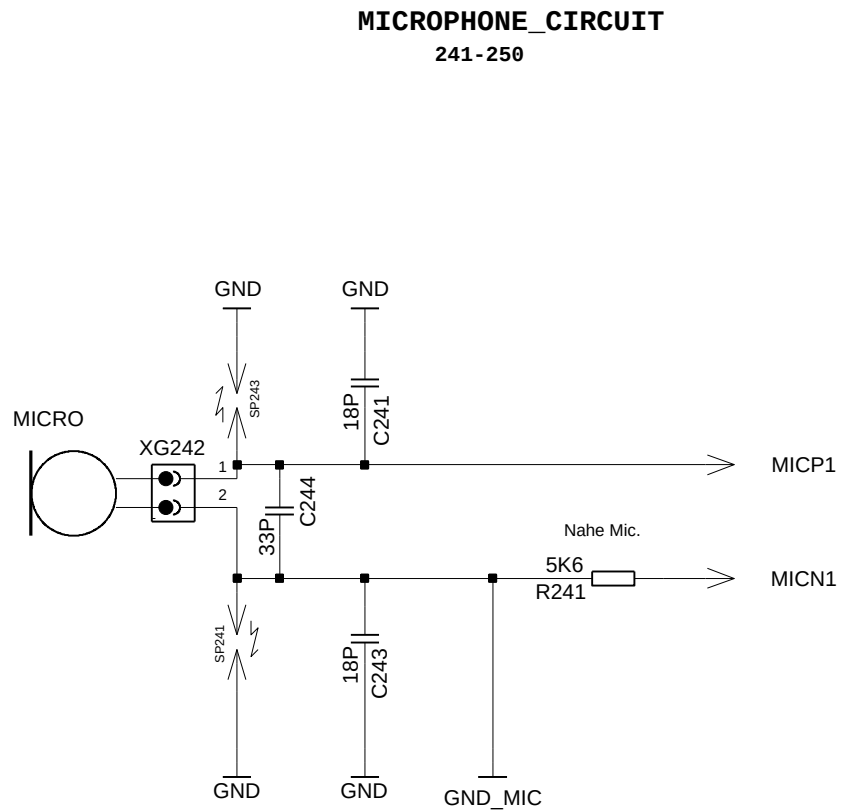


shorted footprint with trace between pads at layout

			DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1	
			USER	Zhang Yanhong			
			APPR'D				
			COM MD D HW4				
					Siemens AG	217094	sheet8
2	32049	26.09.2005	zyh				
1	31329	06.09.2005	ZYH				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			

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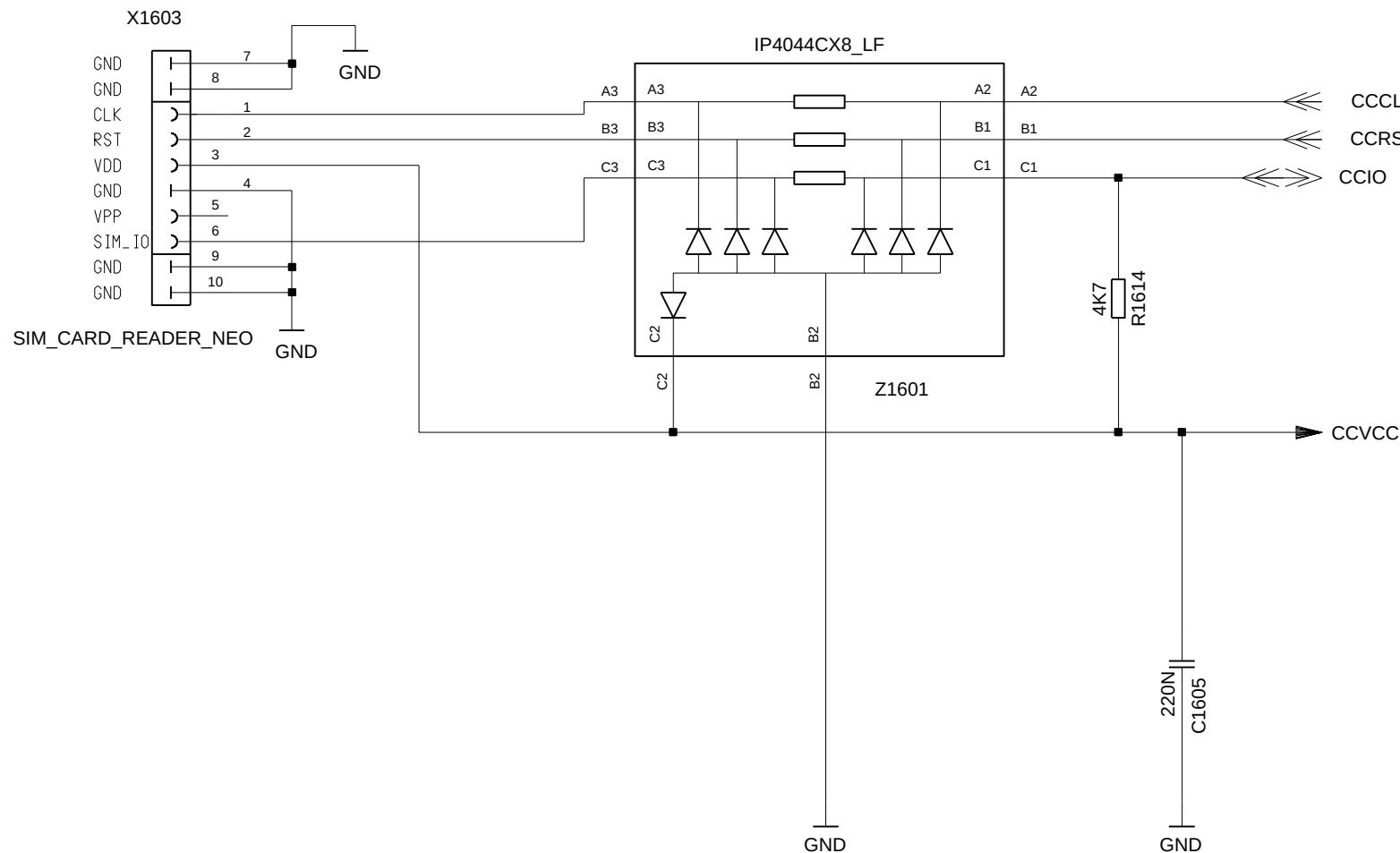
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alle Rechte vorbehalten



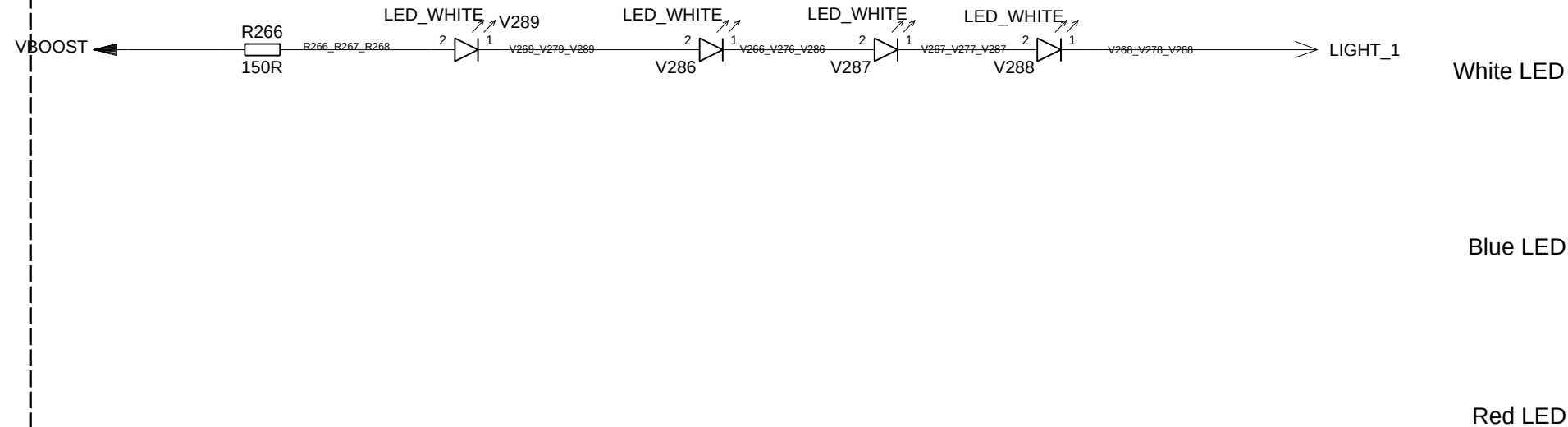
				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	Zhang Yanhong		
				APPR'D			
				COM MD D HW4			
				Siemens AG		217094	sheet9
2	32049	26.09.2005	zyh				
1	31329	06.09.2005	ZYH				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			

SIM_CONNECTOR

1600-1659



KEYPAD_BACKLIGHT



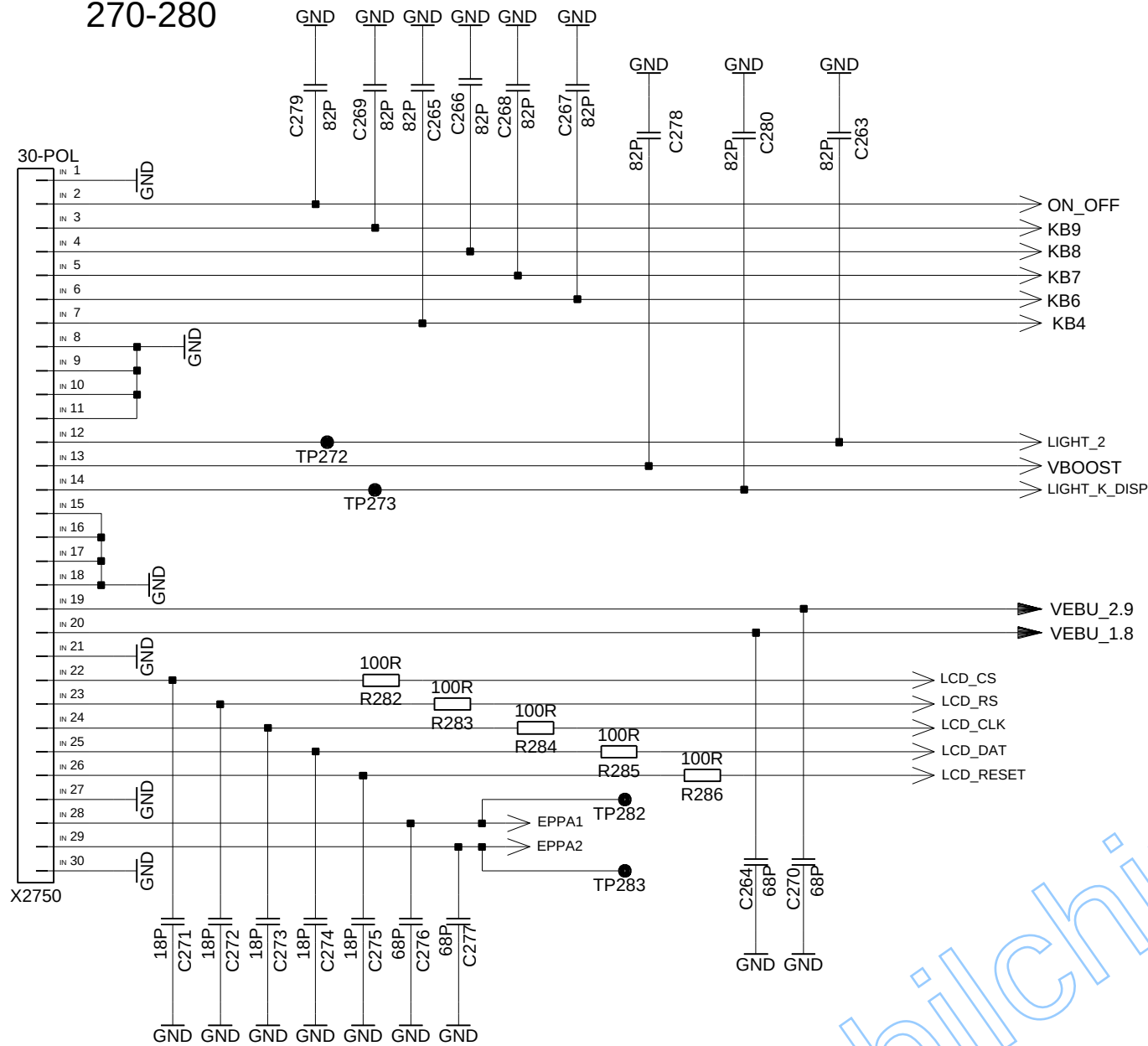
				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	Zhang Yanhong		
				APPR'D			
				COM MD D HW4			
				Siemens AG		217094	sheet10
2	32049	26.09.2005	zyh				
1	31329	06.09.2005	ZYH				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			

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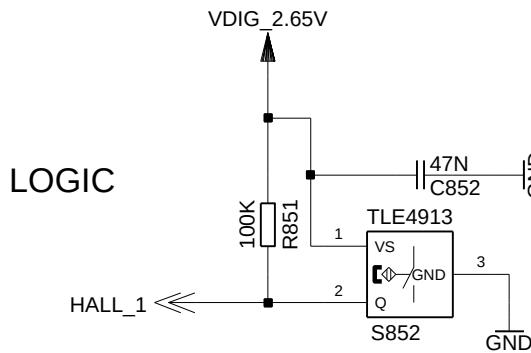
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MMI CONNECTOR

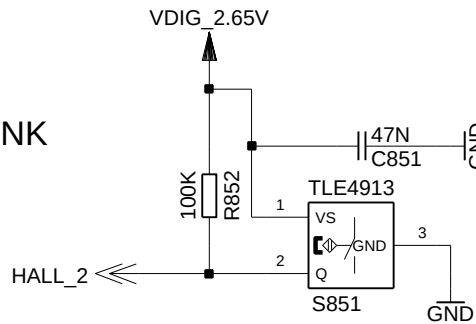
270-280



MAGNETIC SWITCH



FUNK



				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	Zhang Yanhong		
				APPR'D			
					COM MD D HW4		
2	32049	26.09.2005	zyh	Siemens AG		217094	sheet11
1	31329	06.09.2005	ZYH				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			

1 2 3 4 5 6 7 8 9 10

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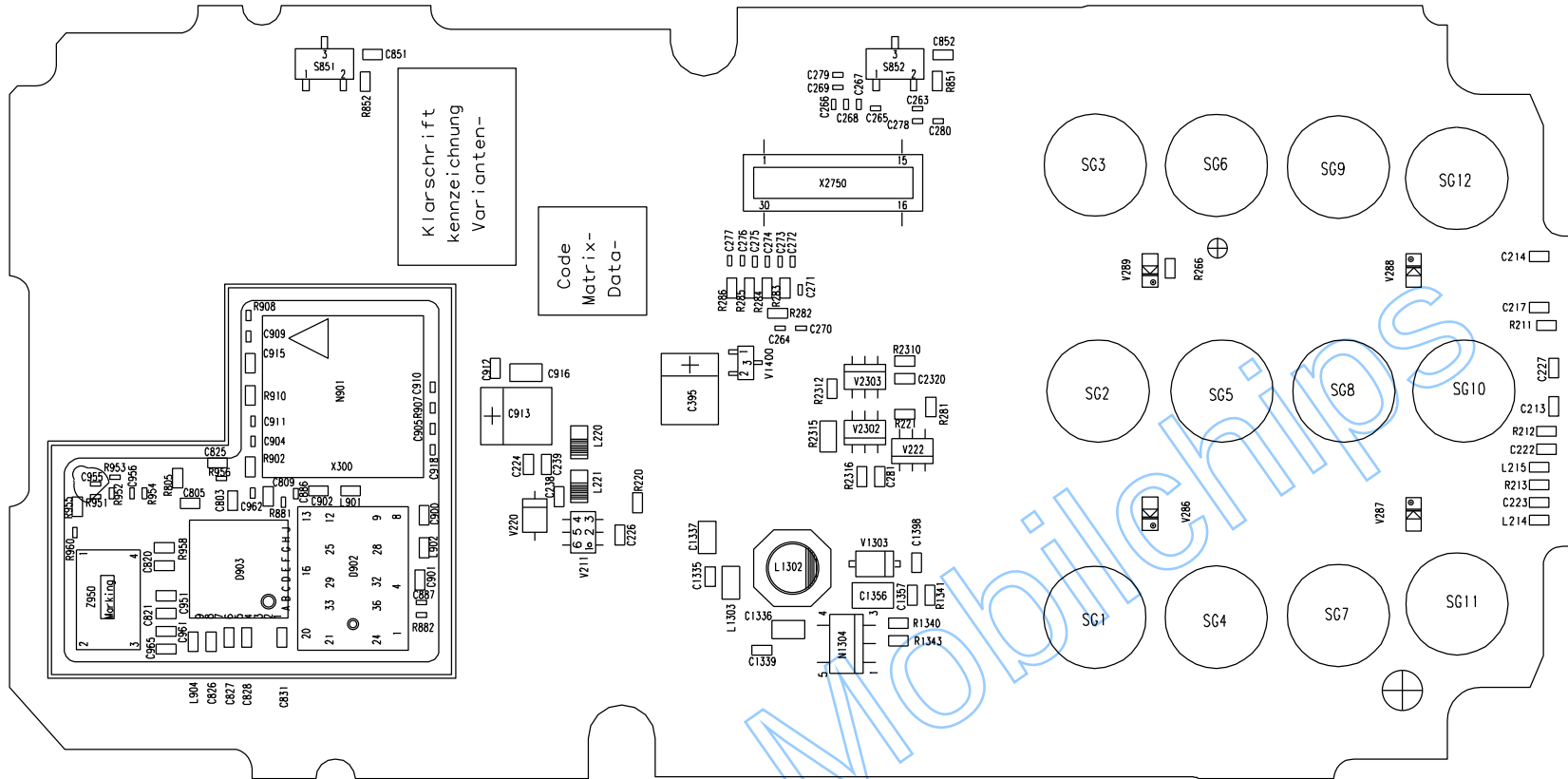
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1 2 3 4 5



C213	7B	C956	1C	R2315	4C
C214	7B	C961	1C	R2316	4C
C217	7B	C962	2C	S851	2B
C222	7C	C965	1C	S852	4B
C223	7C	C1335	4C	V211	3C
C224	3C	C1336	4C	V220	3C
C226	3C	C1337	4C	V222	4C
C227	7B	C1339	4C	V286	5C
C238	3C	C1356	4C	V287	6C
C239	3C	C1357	4C	V288	6B
C263	5B	C1398	5C	V289	5B
C264	4B	C2320	4B	V1303	4C
C265	4B	D902	2C	V1400	4B
C266	4B	D903	2C	V2302	4C
C267	4B	L214	7C	V2303	4B
C268	4B	L215	7C	X300	1C
C269	4B	L220	3C	X2750	4B
C270	4B	L221	3C	Z950	1C
C271	4B	L901	2C		
C272	4B	L902	3C		
C273	4B	L904	2C		
C274	4B	L1302	4C		
C275	4B	L1303	4C		
C276	4B	N901	2B		
C277	4B	N1304	4C		
C278	5B	R211	7B		
C279	4B	R212	7C		
C280	5B	R213	7C		
C281	4C	R220	3C		
C395	4B	R221	4B		
C803	2C	R266	6B		
C805	2C	R281	5B		
C809	2C	R282	4B		
C820	2C	R283	4B		
C821	1C	R284	4B		
C825	2C	R285	4B		
C826	2C	R286	4B		
C827	2C	R805	2C		
C828	2C	R851	5B		
C831	2C	R852	2B		
C851	2B	R881	2C		
C852	5B	R882	3C		
C886	2C	R902	2C		
C887	3C	R907	3B		
C900	3C	R908	2B		
C901	3C	R910	2B		
C902	2C	R951	1C		
C904	2C	R952	1C		
C905	3C	R953	1C		
C909	2B	R954	1C		
C910	3B	R955	1C		
C911	2C	R956	2C		
C912	3B	R958	2C		
C913	3B	R960	1C		
C915	2B	R1340	4C		
C916	3B	R1341	5C		
C918	3C	R1343	4C		
C951	2C	R2310	4B		
C955	1C	R2312	4B		

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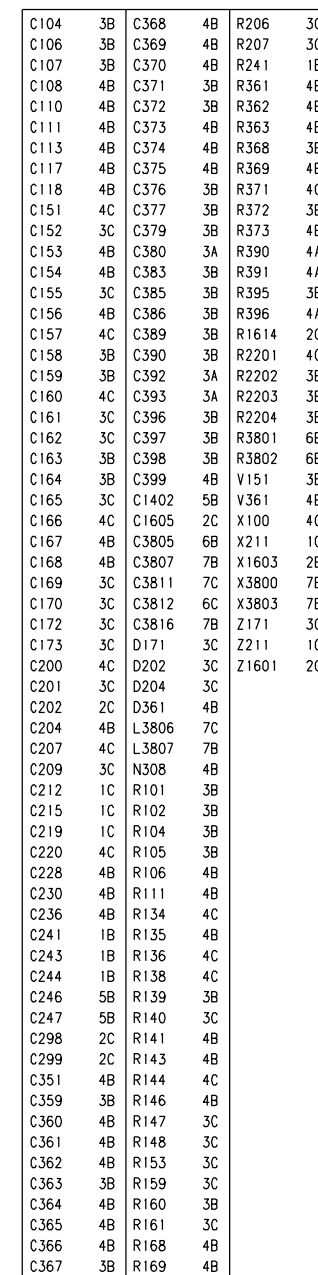
B

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Achtung : Höhenprofil C24____-____-42 (gilt nur fuer Entwicklung)
Liefervarianten siehe : V24851-Z2000-A1-*-35

				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH WIRING DIAGRAMM (SOLDER SIDE 1)	3:1
				NAME	Zhang Yanhong		
				APPR'D			
				STD			
				COM MD D HW4		217095	SHEET 1+
2	32049	26.09.2005	zyh	SIEMENS AG			
1	31329	06.09.2005	ZYH				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			



Achtung : Höhenprofil C24___-___-___-42 (gilt nur fuer Entwicklung)
Liefervarianten siehe : V24851-Z2000-A1-*-35

				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH 3:1 WIRING DIAGRAMM (SOLDER SIDE 2)	
				NAME	Zhang Yanhong		
				APPR'D			
				STD			
				COM MD D HW4			
2	32049	26.09.2005	zyh	SIEMENS AG		217095	SHEET
1	31329	06.09.2005	ZYH				2+
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			

1 2 3 4 5 6 7 8 9 10

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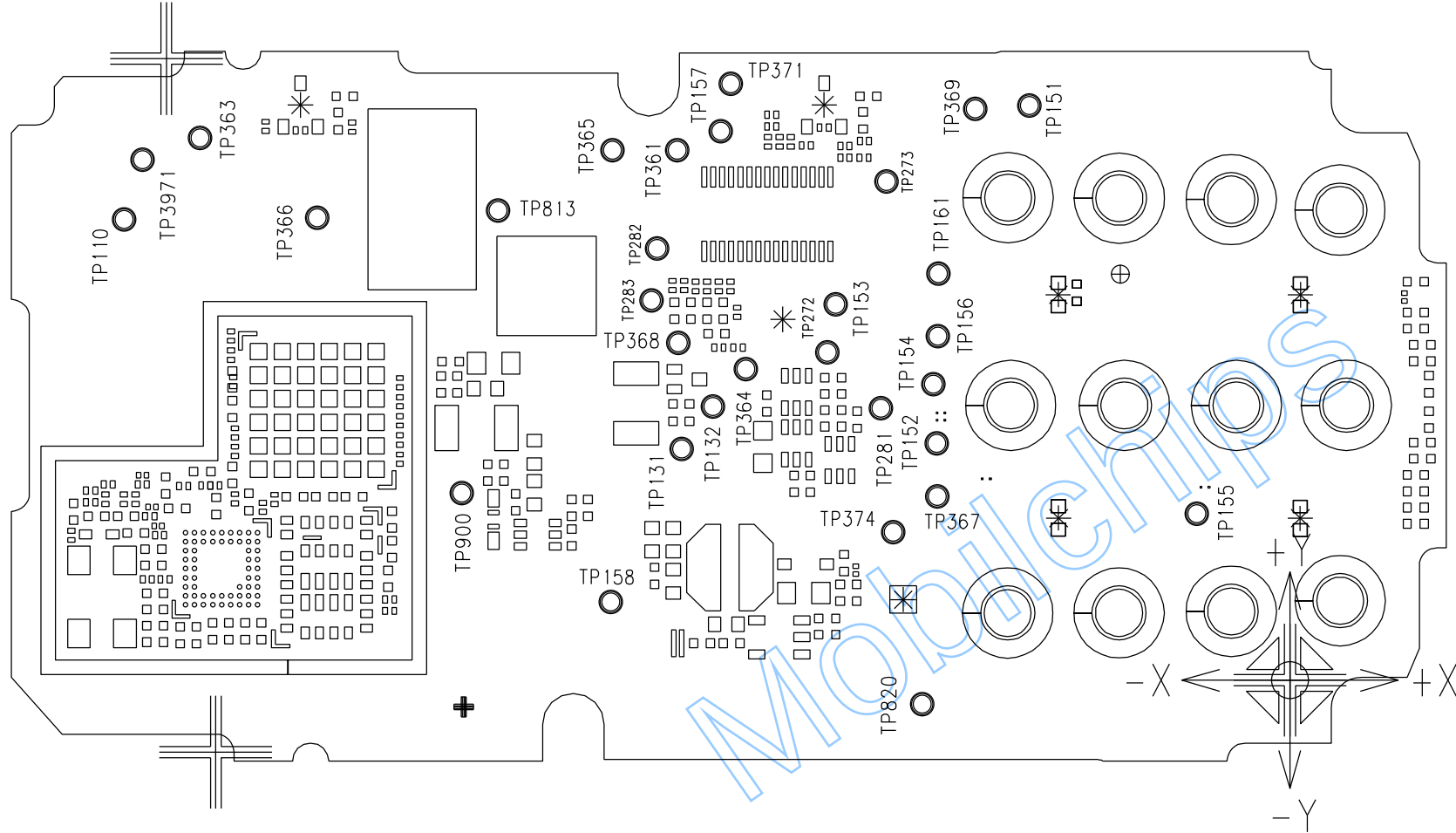
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1 2 3 4 5



TP110	1B	/TVCO
TP131	4C	/BREF
TP132	4B	/TBAT
TP151	5B	/MON2
TP152	5C	/TMS
TP153	4B	/TCK
TP154	5B	/TD1
TP155	6C	/TRST
TP156	5B	/TD0
TP157	4B	/ON_OFF
TP158	4C	VEBU_2.9
TP161	5B	/MON1
TP272	4B	/LIGHT_2
TP273	5B	/LIGHT_K_DISP
TP281	5B	/LIGHT_1
TP282	4B	/EPPA1
TP283	4B	/EPPA2
TP361	4B	/POWER
TP363	2B	VDD_RF2
TP364	4B	VEBU_1.8
TP365	4B	/NB475
TP366	2B	VDIG_2.65V
TP367	5C	1.5V
TP368	4B	VDDA_2.65V
TP369	5B	/VDD_RTC
TP371	4A	RESET_UC
TP374	5C	VB00ST
TP813	3B	GND
TP820	5C	/RF_SIN26M
TP900	3C	/RF_TXONPA
TP3971	1B	VDD_RF1

				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TESTPOINT DIAGRAMM (SIDE 1)	3:1	
				NAME	Zhang Yanhong			
				APPR'D				
				STD				
				COM MD D HW4				
2	32049	26.09.2005	zyh	SIEMENS AG		217096	SHEET	
1	31329	06.09.2005	ZYH				1+	
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)				

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ALLE RECHTE VORBEHALTEN

1 2 3 4 5 6 7 8 9 10

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1 2 3 4 5

				DATE	06.09.2005	Flamingo B2.3 Mainboard Populate variant V1/ AMD FLASH TESTPOINT DIAGRAMM (SIDE 2)	3:1
				NAME	Zhang Yanhong		
				APPR'D			
				STD			
				COM MD D HW4			
2	32049	26.09.2005	zyh	SIEMENS AG		217096	SHEET 2-
1	31329	06.09.2005	ZYH				
VERS.	CHANGE NUMBER	DATE	NAME	(Created by BOM number: A5B00900197564-32049)			