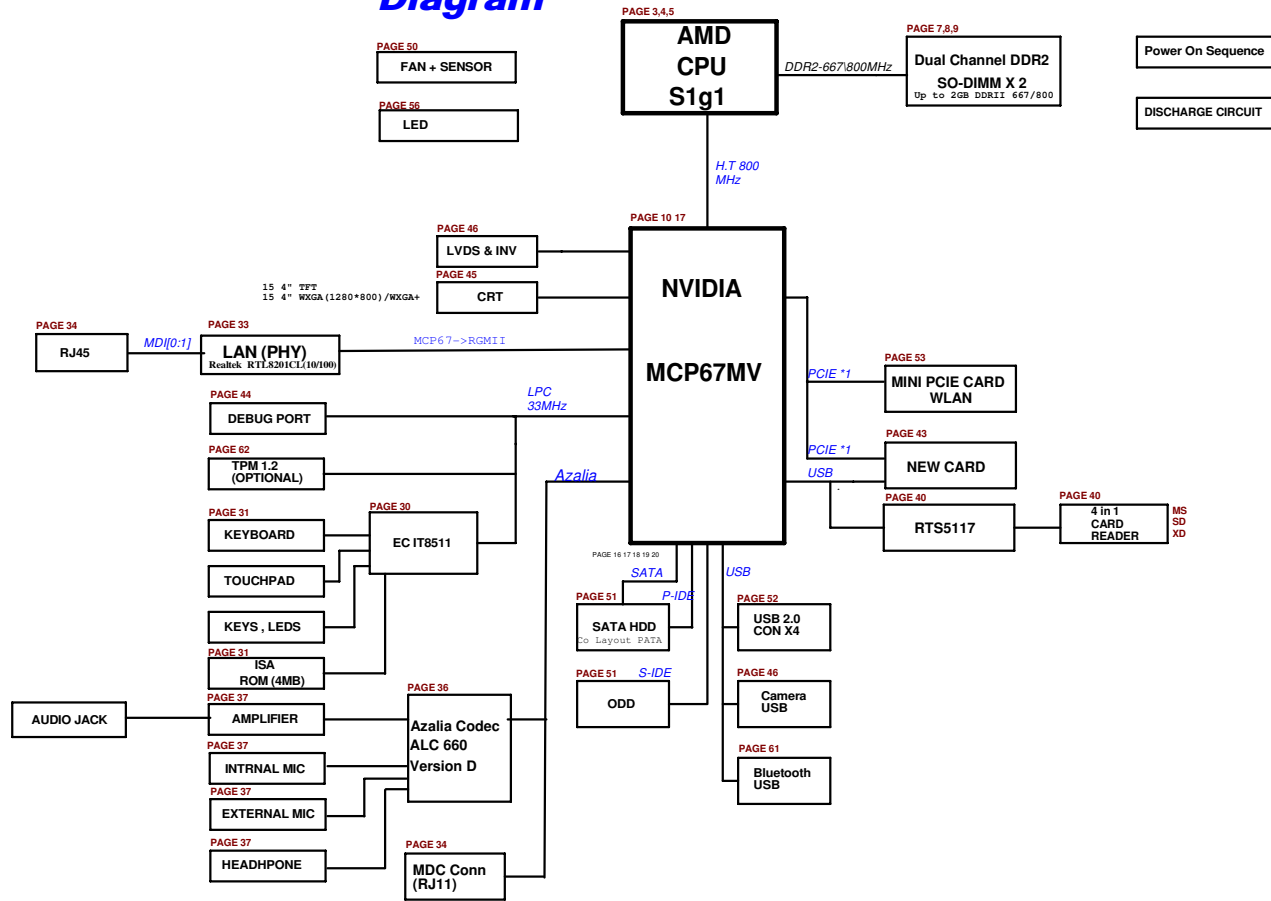


F5N Block Diagram



<http://konweer.kiev.ua>

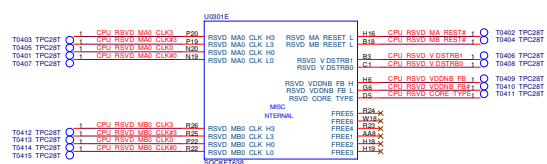
« Kennedy_Zhang »

ASUS		Title : FSU	
ASUS Test Computer INC		Engineer: He Wang	
Size	1	1	1
Rev	1	1	1
Date: 2007-10-26 Time: 10:00:00			

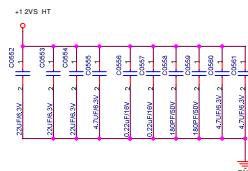
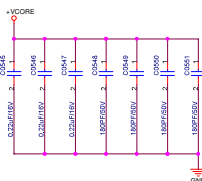
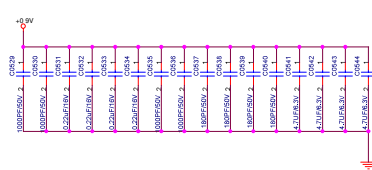
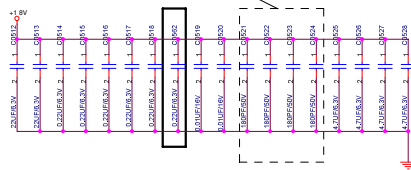
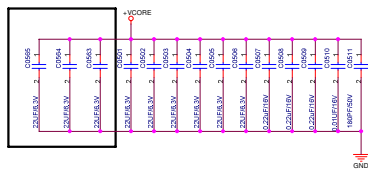
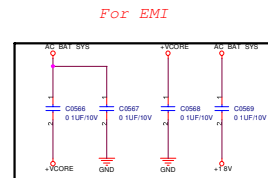
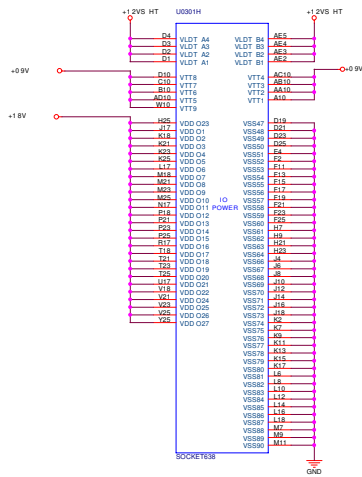
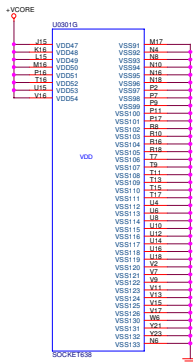
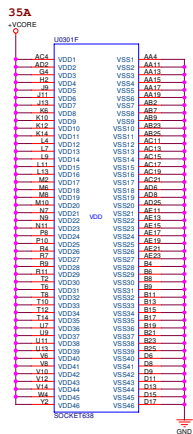
<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Effect: 2007-10-26	



<< Kennedy_Zhang >>



For DDR2 add/cmd refer
to split plane

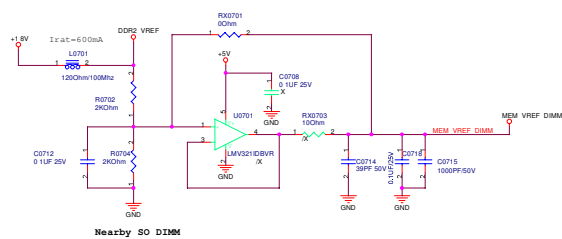
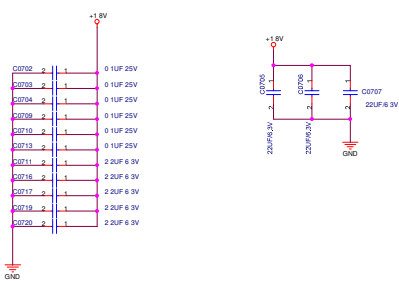
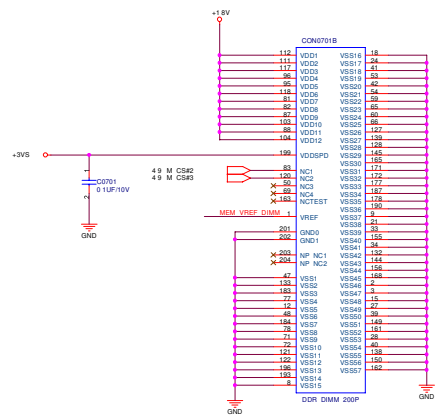
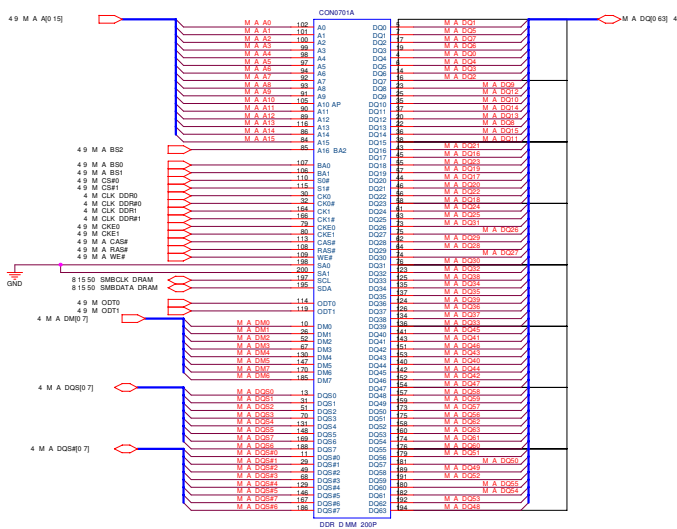
« Kennedy_Zhang »

<http://konweer.kiev.ua>

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

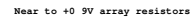
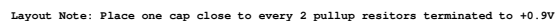
		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01		Sheet: 6	of 26



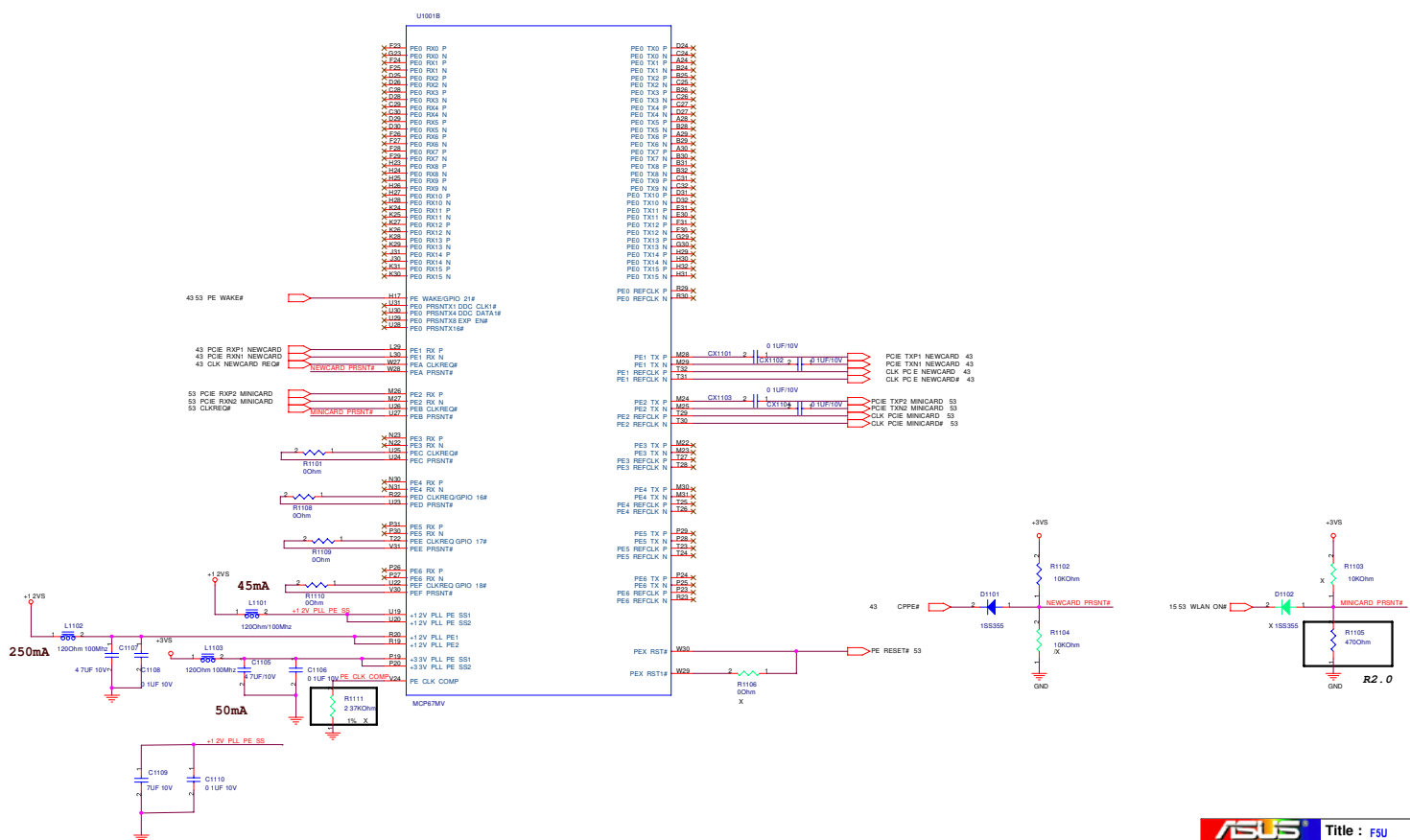
<http://konweeer.kiev.ua>

<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
BS2	P	Q	Rev
1	1	1	1.0



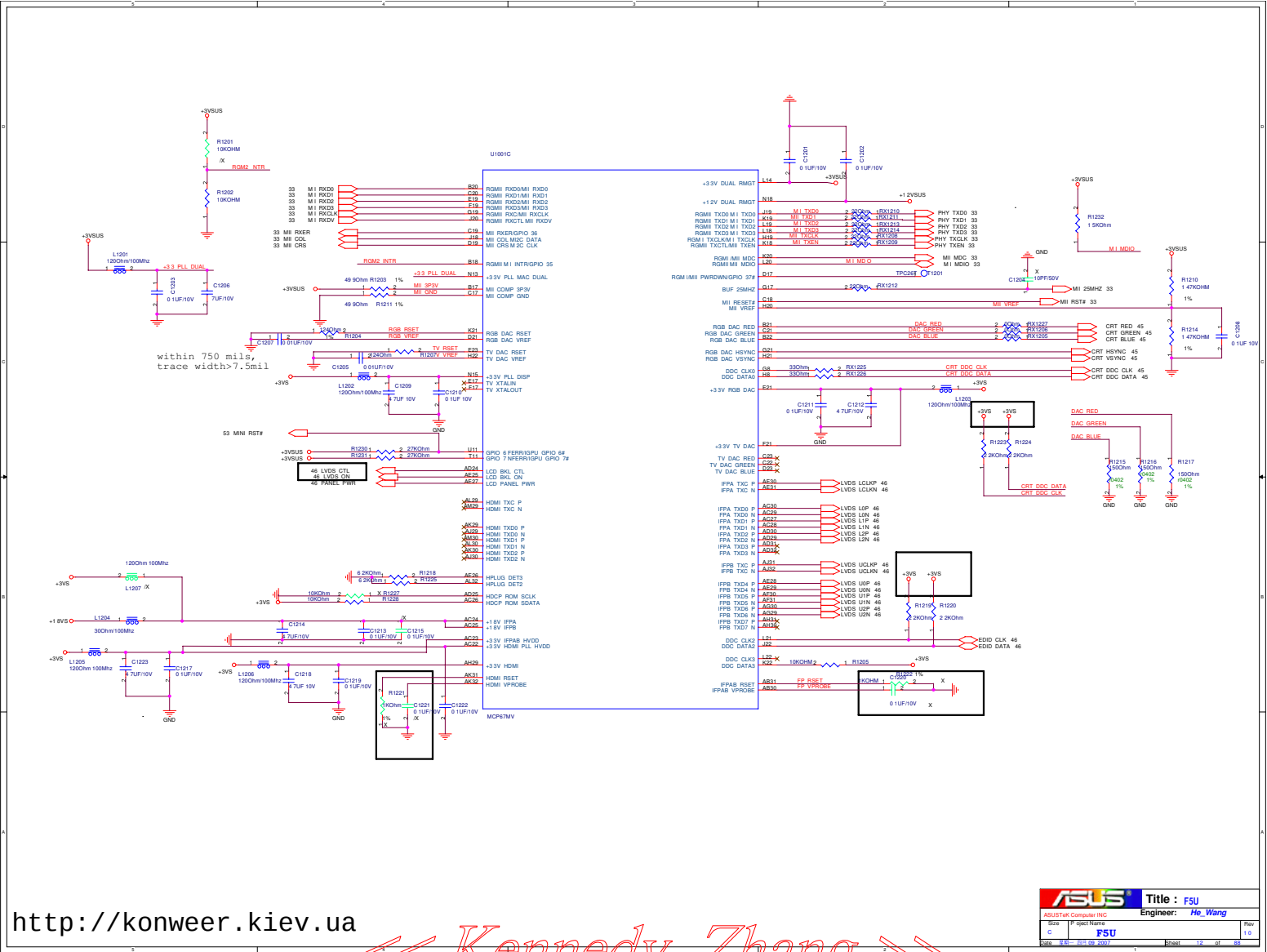
<< Kennedy_Zhang >>



<http://konweer.kiev.ua>

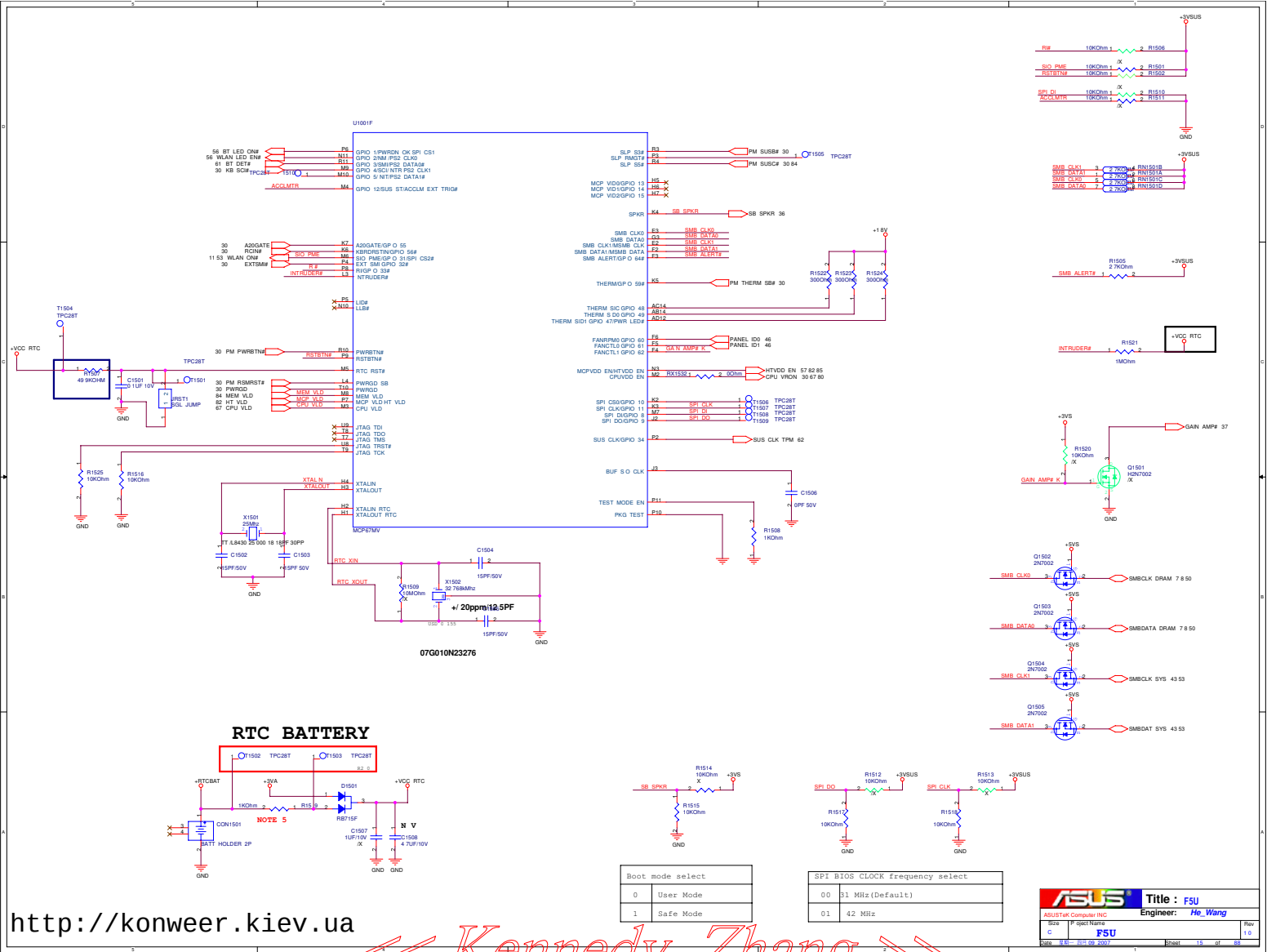
<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUS Taiwan Computer INC.		Engineer: He Wang	
BS24	P: 0801 Name	Rev	1.0
C	FSU	Rev	1.0
Rev	Rev	Rev	Rev



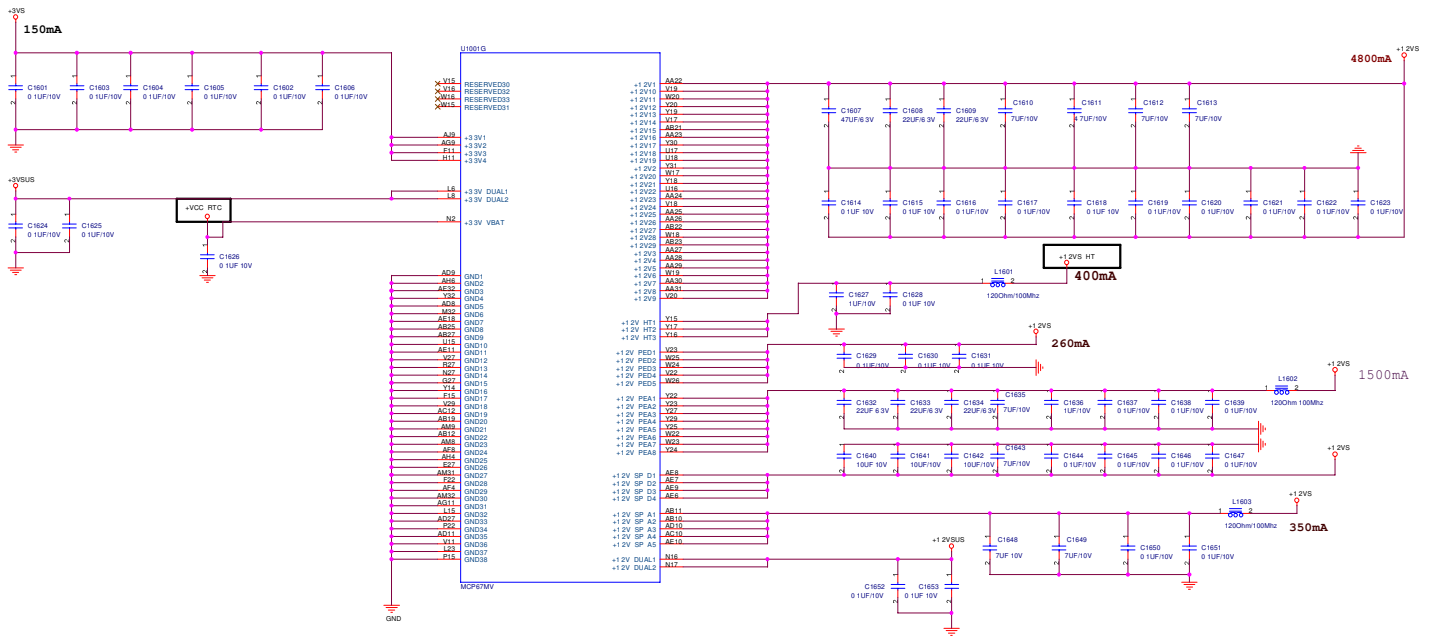
<< Kennedy_Zhang >>





<http://konweer.kiev.ua>

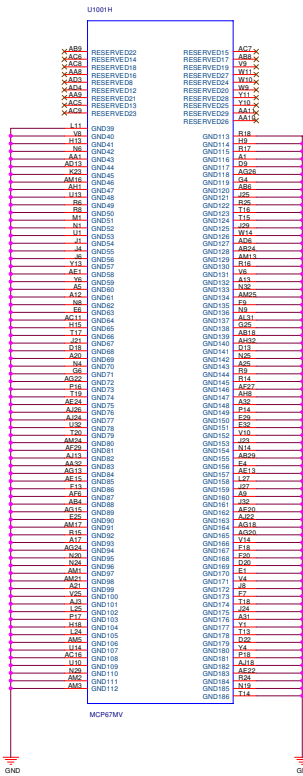
<< Kennedy_Zhang >>



<http://konweer.kiev.ua>

« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Rev	1.0	Rev	1.0
Date: 2012-10-26 10:00	Sheet: 16	of	20



<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-05-25 10:01	Sheet: 17	of	26

<http://konweer.kiev.ua>

«Kennedy_Zhang»

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 18 of 20		

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 15:01		Sheet: 18 of 20	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 01 of 01	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Drawn: 21	28

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 25 of 25	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 25 of 25	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 24 of 25	

<http://konweeer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
KOLCHAK Computer INC		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 25 of 25	

<http://konweeer.kiev.ua>

«Kennedy_Zhang»

		Title : FSU	
KOLCHAK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 25 of 25	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 21 of 25	

<http://konweer.kiev.ua>

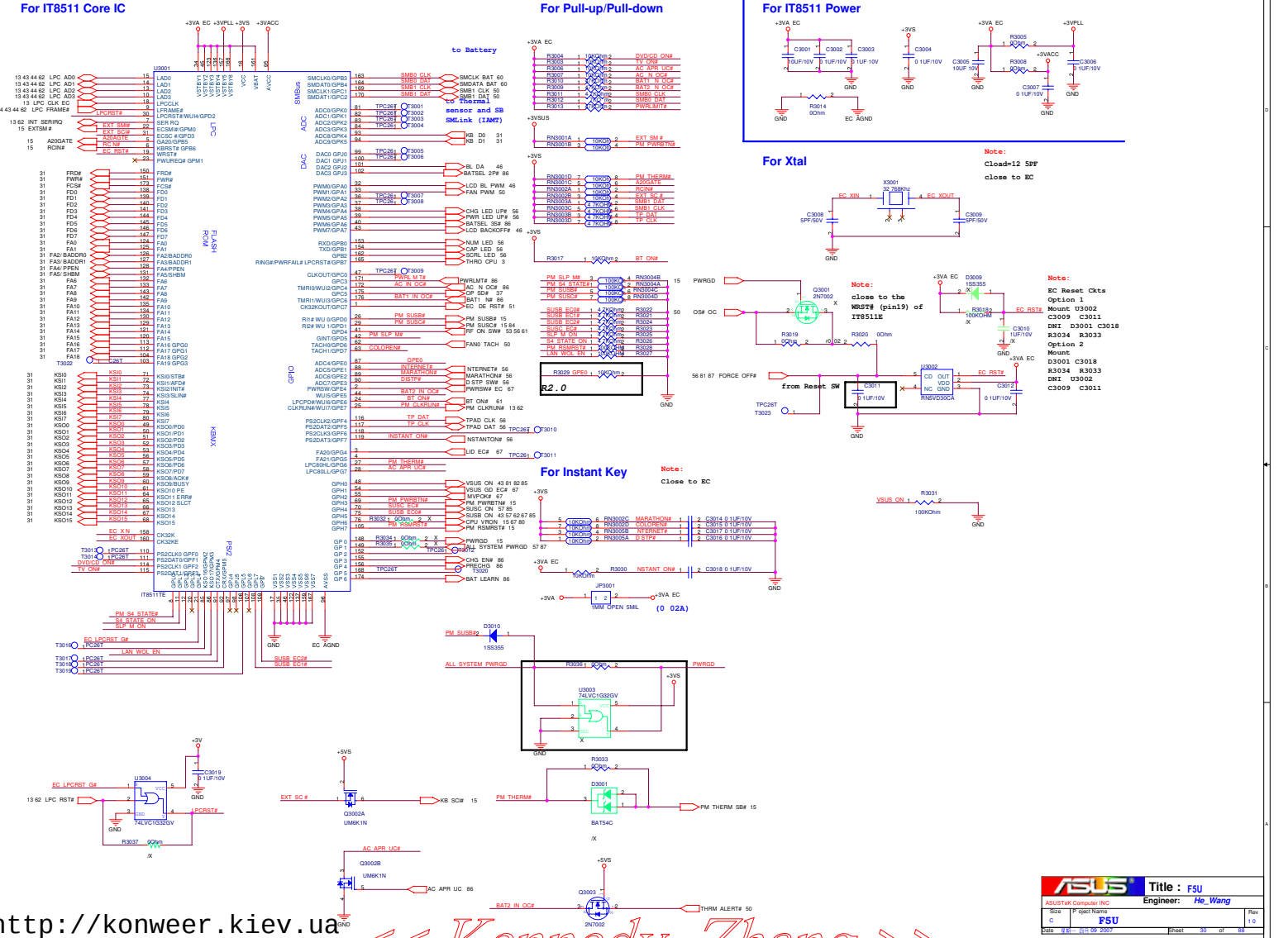
<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 25 of 25	

<http://konweer.kiev.ua>

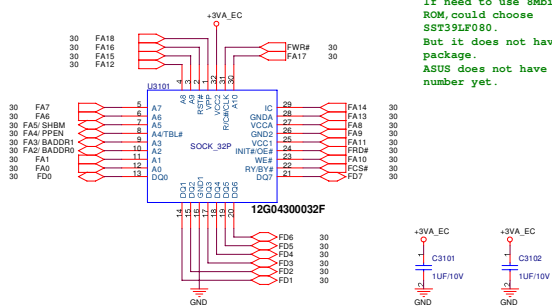
<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 25 of 25	



<http://konweer.kiev.ua>

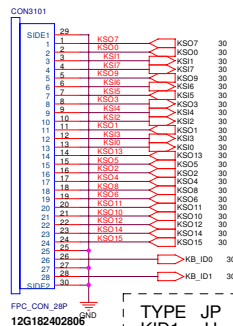
ISA ROM (4Mbits)



SST-PLCC32 4Mbits Flash ROM
PN:05G001014110
(FLASH SST S39VF040-70-4C-NHE
4M-70 PLCC-32)

If need to use 8Mbits ISA
ROM, could choose
SST39LF080.
But it does not have PLCC32
package.
ASUS does not have part
number yet.

KBC connector

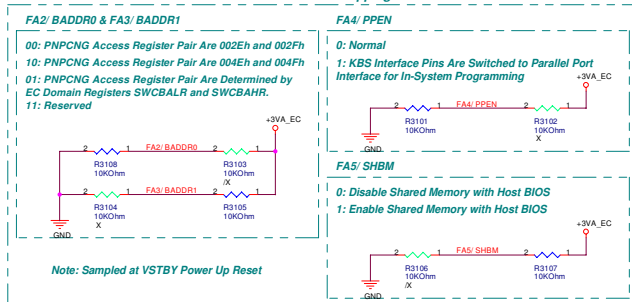


CON3101
SIDE1
SIDE2
FPC_CON 28P
12G182402806

TYPE	JP	UK	US
KID1	H	H	L
KID2	L	H	L

ISA ROM

EC Hardware Strapping



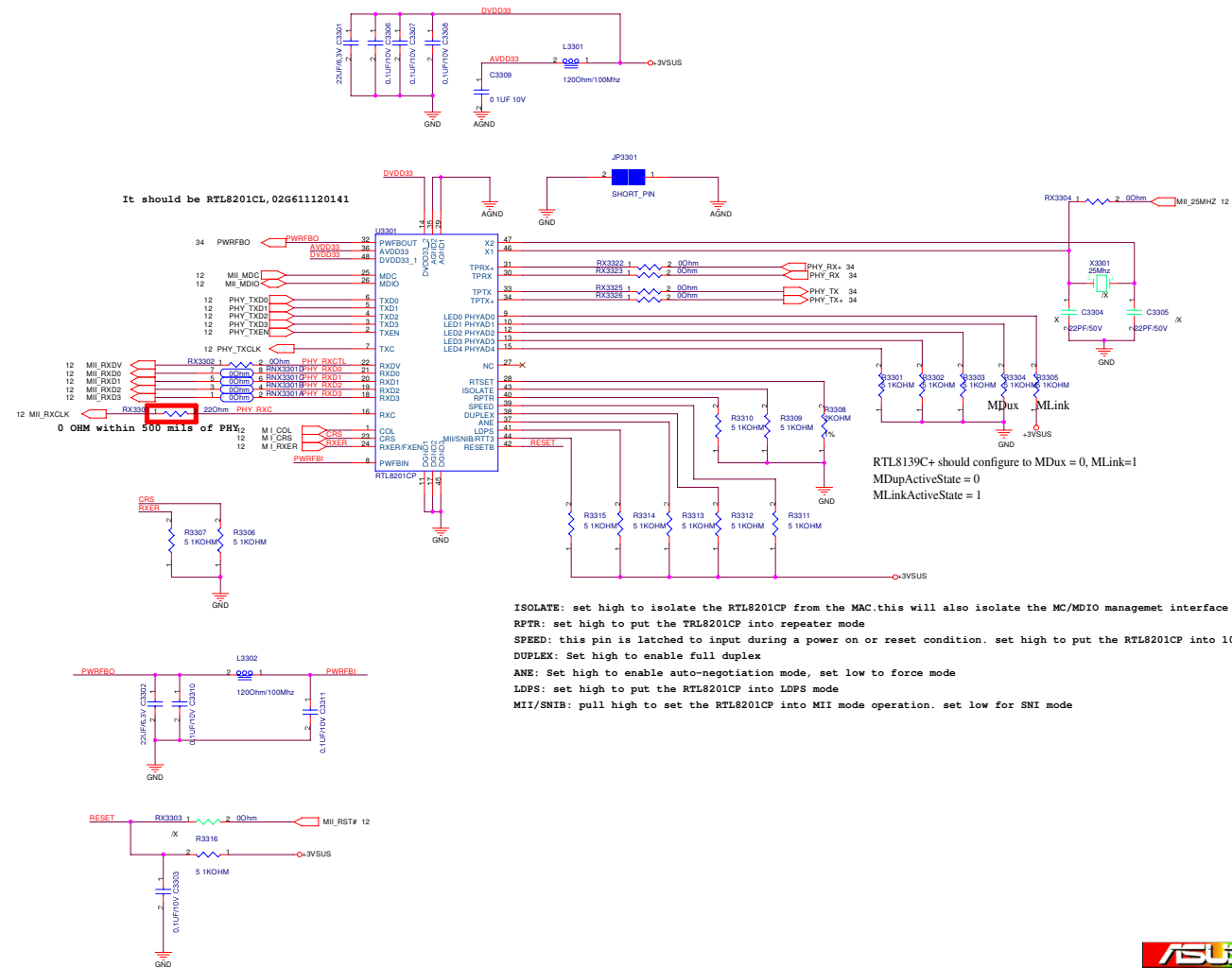
ASUS		Title : FSU	
ASUS Tek Computer NC		Engineer: He_Wang	
Size	Project Name	Rev	
Custom	FSU	1.0	
Date: 2019-09-20	Sheet	31	of 38

<< Kennedy_Zhang >>

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

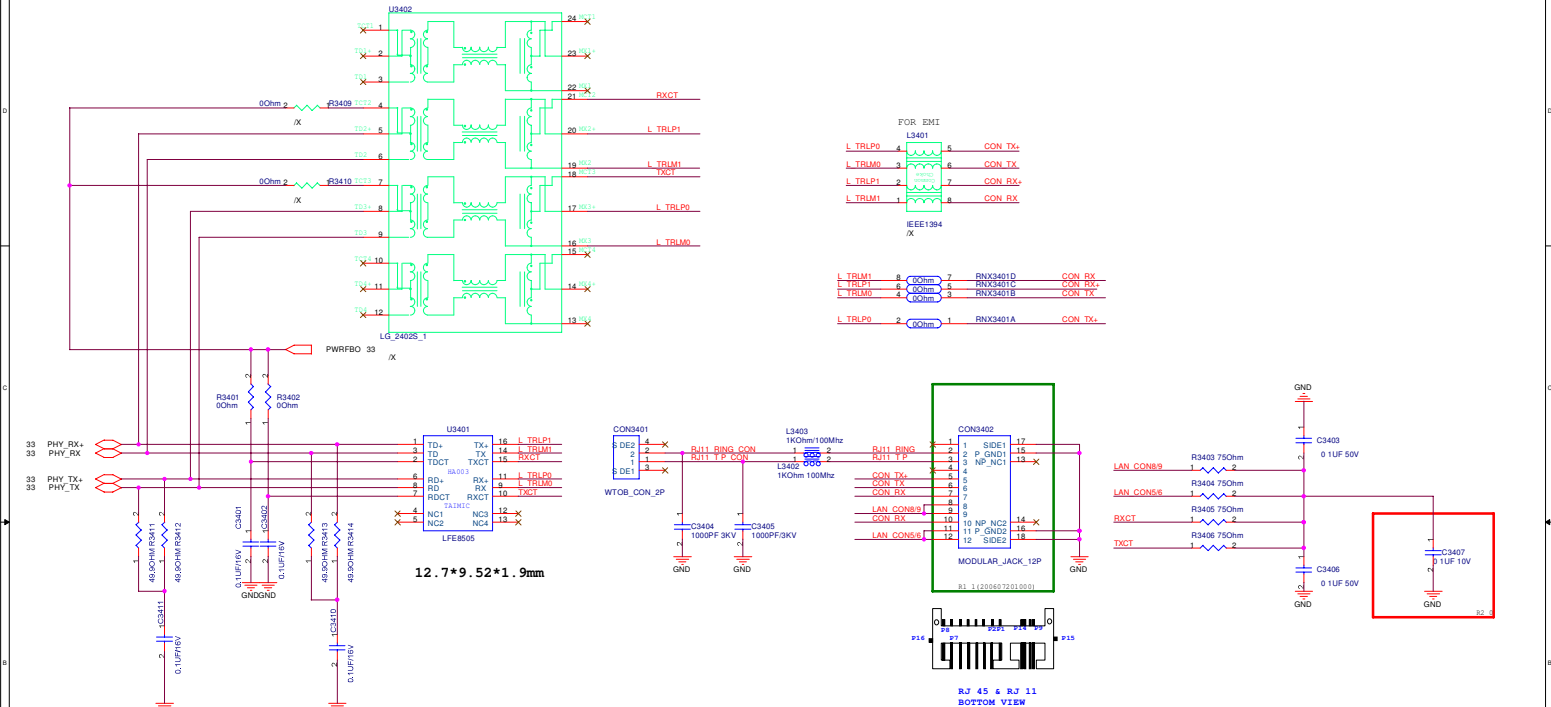
		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-03-26 12:01	Sheet: 01 of 01		



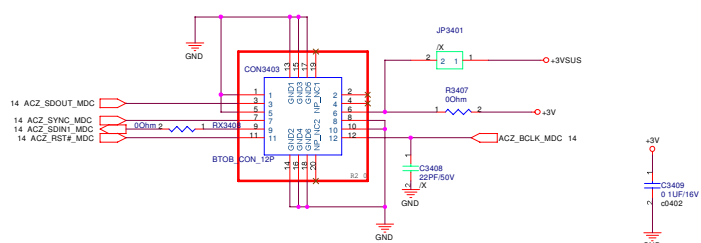
ASUS		Title : FSU	
ASUSTek Computer NC		Engineer: He_Wang	
Size	Project Name	Rev	
Custom	FSU	1.0	
Date: 2014.09.25	Sheet: 33	of	38

<< Kennedy_Zhang >>

LAN PORT



MDC CONN.



<http://konweer.kiev.ua>

		Title : F5U	
ASUSTeK Computer NC		Engineer: He_Wang	
Size Custom	Project Name F5U	Rev 1.0	
Date: 8/21/05 17:21:59, 30007	Echant: 3.4	of: 89	

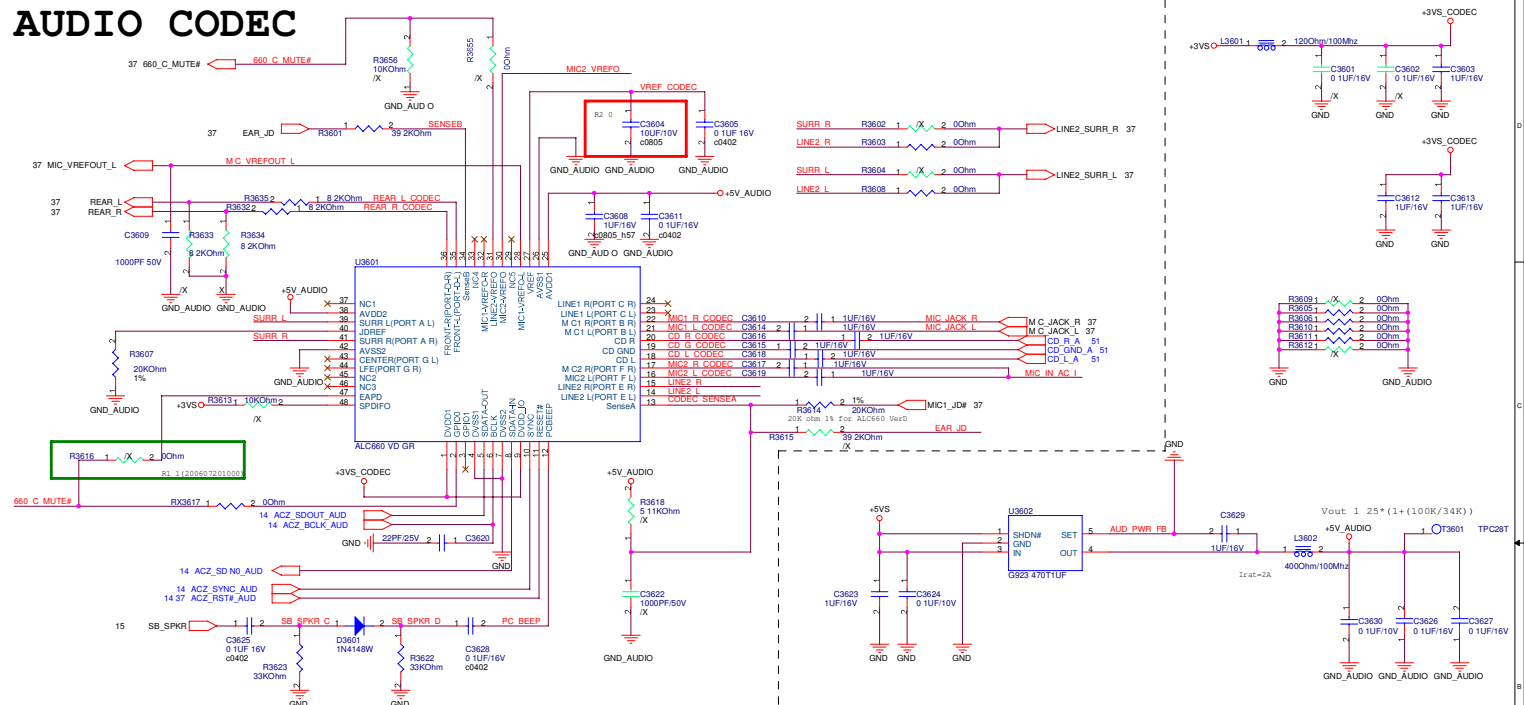
<< Kennedy_Zhang >>

<http://konweer.kiev.ua>

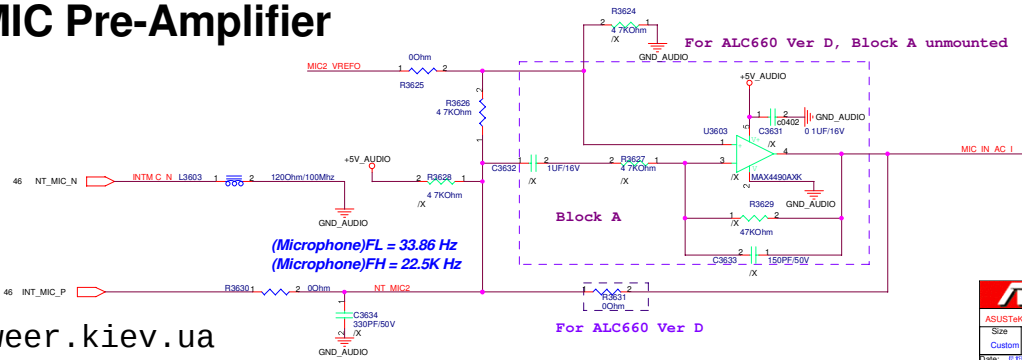
		Title : FSU	
ASUS Tek Computer NC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 11.11.2007		Sheet 05 of 08	

<< Kennedy_Zhang >>

AUDIO CODEC



Internal MIC Pre-Amplifier



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<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUSTek Computer INC	Project Name	Engineer: He_Wang	Rev
Custom	FSU		1.0
Date: 08/08/2007	Sheet	36	of 38

[illegible][illegible][illegible]

AUDIO AMPLIFIER

SPEAKER

HeadPhone

DEPOP CIRCUIT

External Microphone

ASUS Title : FSU

Size	Project Name	Engineer:	Rev
Custom	FSU	He_Wang	1.0
Date: 11/11/2007	Sheet	37	of 38

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

[illegible][illegible]

AUDIO AMPLIFIER

1 -> -5 V/V
0 -> NORMAL

36 REAR_L

15 GAIN_AMP#

36 REAR_R

36 LINE2_SURR_R

36 LINE2_SURR_L

36 MIC_JACK_R

36 MIC_JACK_L

36 MIC_VREFOUT_L

36 OP_SD#

36 660_C_MUTE#

DEPOP CIRCUIT

36 LINE2_SURR_R

36 LINE2_SURR_L

36 MIC_JACK_R

36 MIC_JACK_L

36 MIC_VREFOUT_L

36 OP_SD#

36 660_C_MUTE#

EXTERNAL MICROPHONE

36 MIC1_IDM

36 MIC_JACK_R

36 MIC_JACK_L

36 MIC_VREFOUT_L

36 OP_SD#

36 660_C_MUTE#

HeadPhone

36 EAR_JD

36 EAR_SW#

ASUS

ASUSTek Computer INC

Project Name

Size

Custom

Date: 08/08/2007

Sheet 37 of 38

Rev 1.0

Title: F5U

Engineer: He_Wang

12G14030106E

PIN2 and PIN6 normal close

PIN4 and PIN5 normal open

http://konweer.kiev.ua

<< Kennedy_Zhang >>

AUDIO AMPLIFIER

1 -> -5 V/V
0 -> NORMAL

36 REAR_L

15 GAIN_AMP#

36 REAR_R

36 LINE2_SURR_R

36 LINE2_SURR_L

36 MIC_JACK_R

36 MIC_JACK_L

36 MIC_VREFOUT_L

36 OP_SD#

36 660_C_MUTE#

DEPOP CIRCUIT

36 LINE2_SURR_R

36 LINE2_SURR_L

36 MIC_JACK_R

36 MIC_JACK_L

36 MIC_VREFOUT_L

36 OP_SD#

36 660_C_MUTE#

EXTERNAL MICROPHONE

36 MIC1_IDM

36 MIC_JACK_R

36 MIC_JACK_L

36 MIC_VREFOUT_L

36 OP_SD#

36 660_C_MUTE#

HeadPhone

36 EAR_JD

36 EAR_SW#

ASUS

ASUSTek Computer INC

Project Name

Size

Custom

Date: 08/08/2007

Sheet 37 of 38

Rev 1.0

Title: F5U

Engineer: He_Wang

12G14030106E

PIN2 and PIN6 normal close

PIN4 and PIN5 normal open

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<< Kennedy_Zhang >>

<http://konweer.kiev.ua>

		Title : FSU	
ASUSTek Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 11/11/2007	Sheet 38 of 38		

<< Kennedy_Zhang >>

<http://konweeer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 01 of 01		

<http://konweer.kiev.ua>

		Title : FSU	
ASUSTeK Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 11.11.2007		Sheet	41 of 88

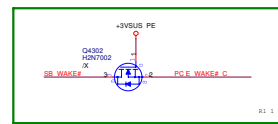
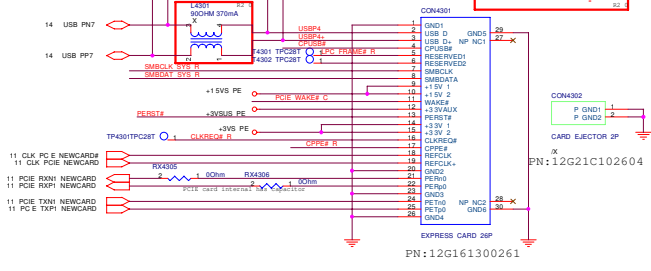
<< Kennedy_Zhang >>

<http://konweer.kiev.ua>

		Title : FSU	
ASUSTeK Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 11/28/2007		Sheet 42 of 68	

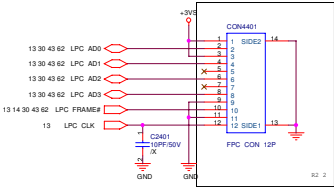
<< Kennedy_Zhang >>

!! ExpressCard Standard 1.0:
Change Pin7 from RESERVED to SMBCLK
Change Pin8 from SMBCLK to SMBDATA
Change Pin9 from SMBDATA to +1.5V



<< Kennedy_Zhang >>

LPC DEBUG CONNCTOR

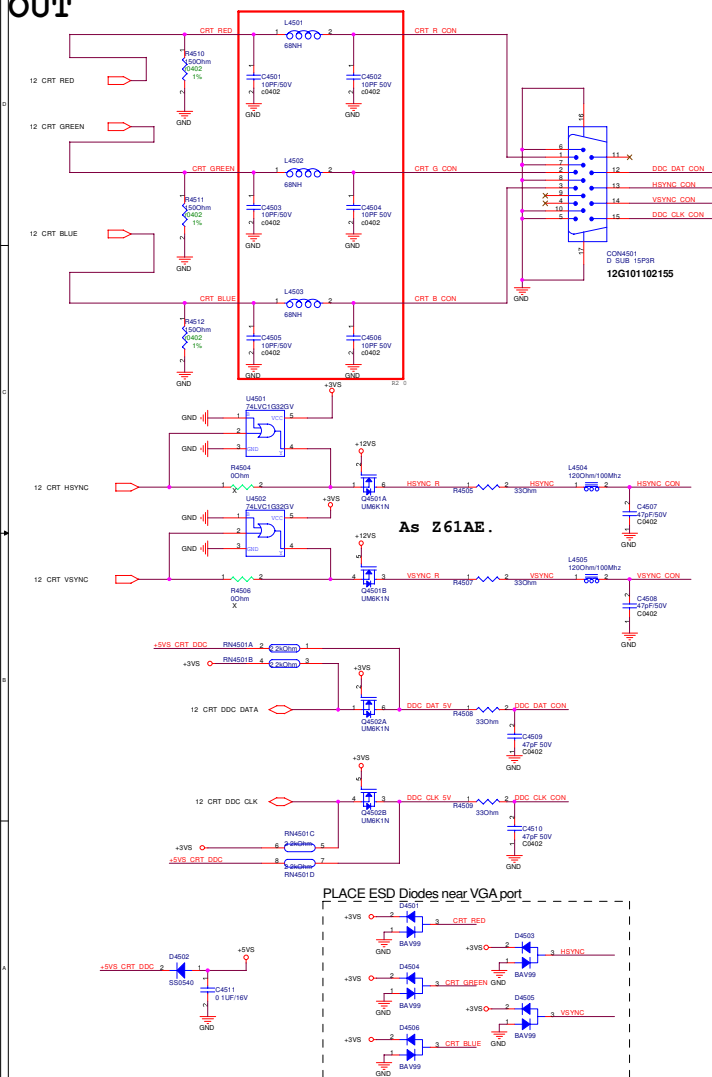


<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUS Test Connector PCB		Engineer: He Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2012-05-29 10:01	Sheet: 44	of	45

VGA OUT

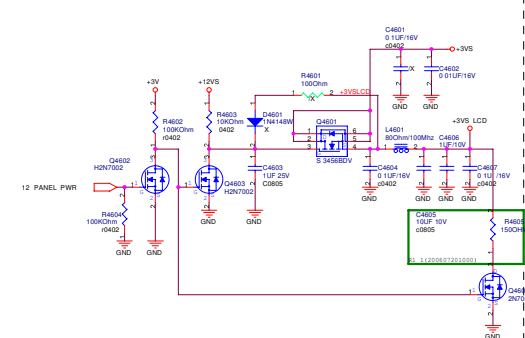


<http://konweer.kiev.ua>

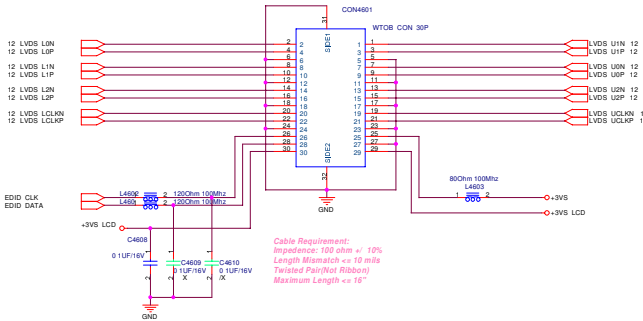
ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Size	1	Rev	1.0
File	FSU	Sheet	45 of 45

« Kennedy_Zhang »

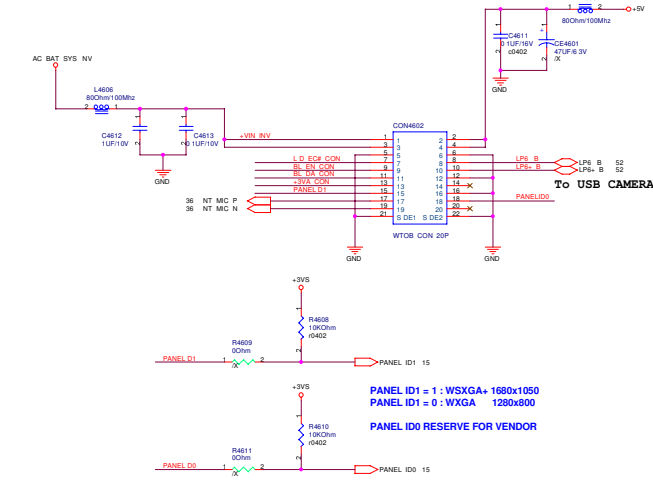
LCD Power



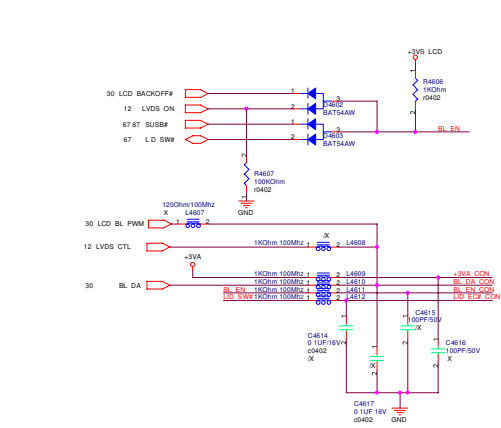
LCD LVDS Interface



INVERTER Interface



Backlight Enable



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<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUSTek Computer INC		Engineer: He Wang	
Rev	1.0	Rev	1.0
Date	2012-01-20	Sheet	45 of 45

5		4		3		2		1	
D									
C									
B									
A									
http://konweer.kiev.ua									
5		4		3		2		1	

		Title : FSU	
ASUSTek Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 11/11/2007	Sheet 47 of 88		

<< Kennedy_Zhang >>

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

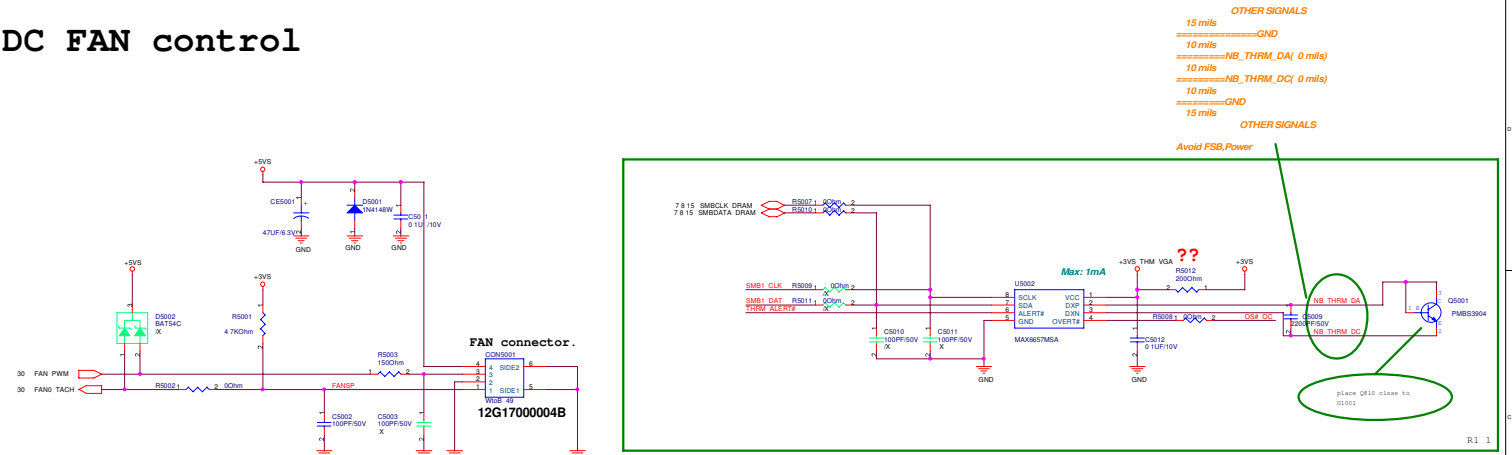
		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 48 of 48		

<http://konweer.kiev.ua>

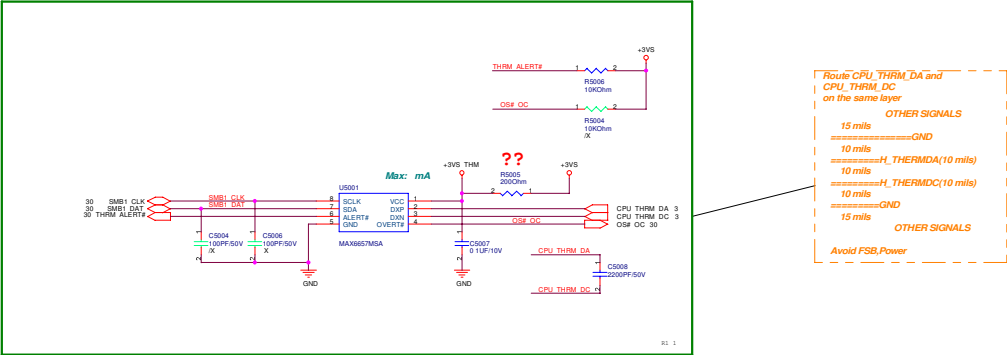
<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 48 of 48		

DC FAN control



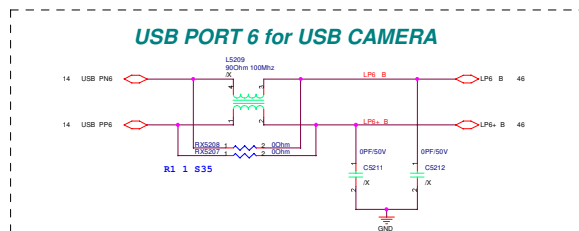
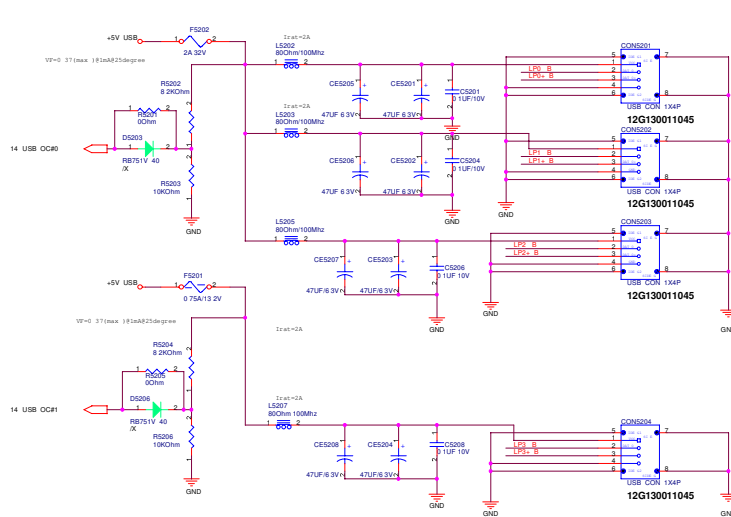
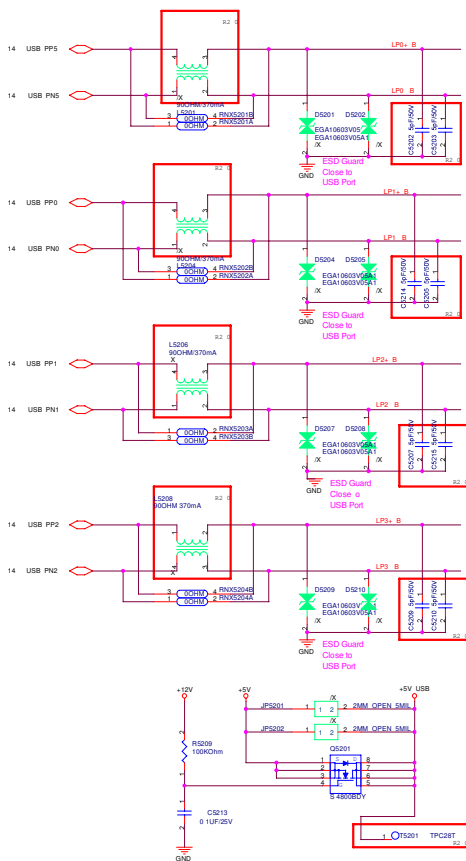
Thermal Sensor



<http://konweer.kiev.ua>

« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Doc	P	Object	Rev
C		FSU	1.0
Doc	Rev	1/1/2007	Sheet 30 of 30



<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
BS24	P	0001 Name	Rev
C		FSU	1.0
Date: 2007-05-24 10:00	Drawn: 2007-05-24	Sheet: 20	20

<http://konweeer.kiev.ua>

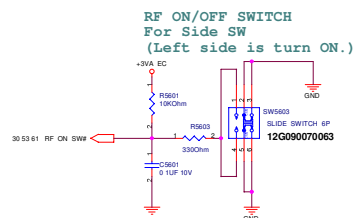
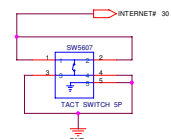
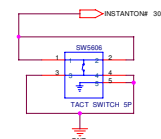
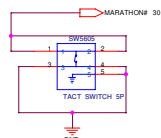
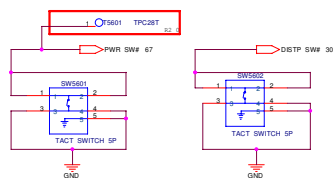
<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 54 of 55		

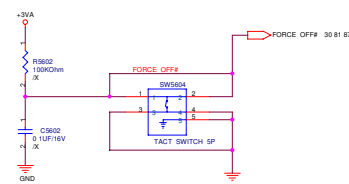
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<< Kennedy_Zhang >>

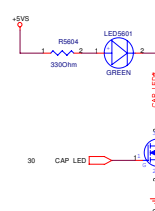
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ASUSTek Computer INC.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-03-26 12:01	Sheet: 05 of 05		



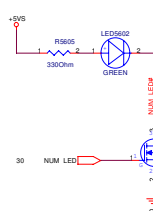
SHUT_DOWN#



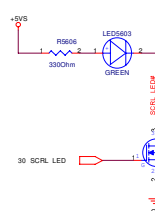
for Cap. Lock



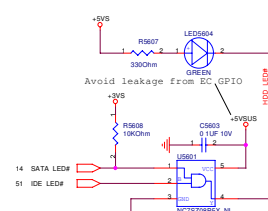
for Num Lock



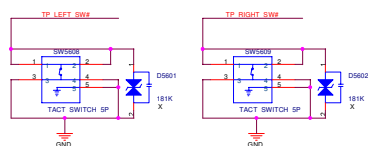
for Scroll Lock



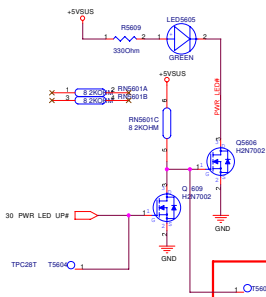
For SATA/IDE LED



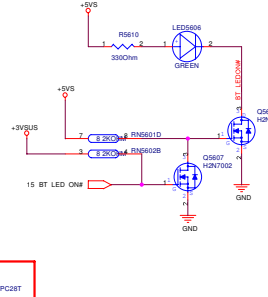
TOUCH PAD SWITCH



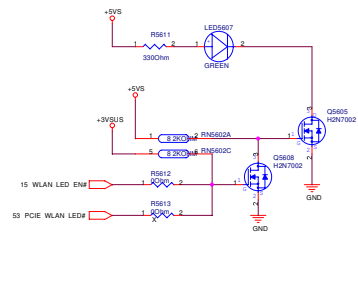
For POWER LED



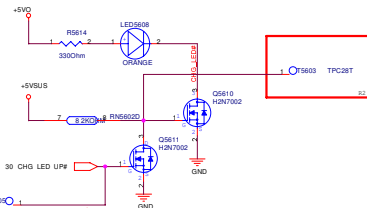
For BT LED



For WireLess LED



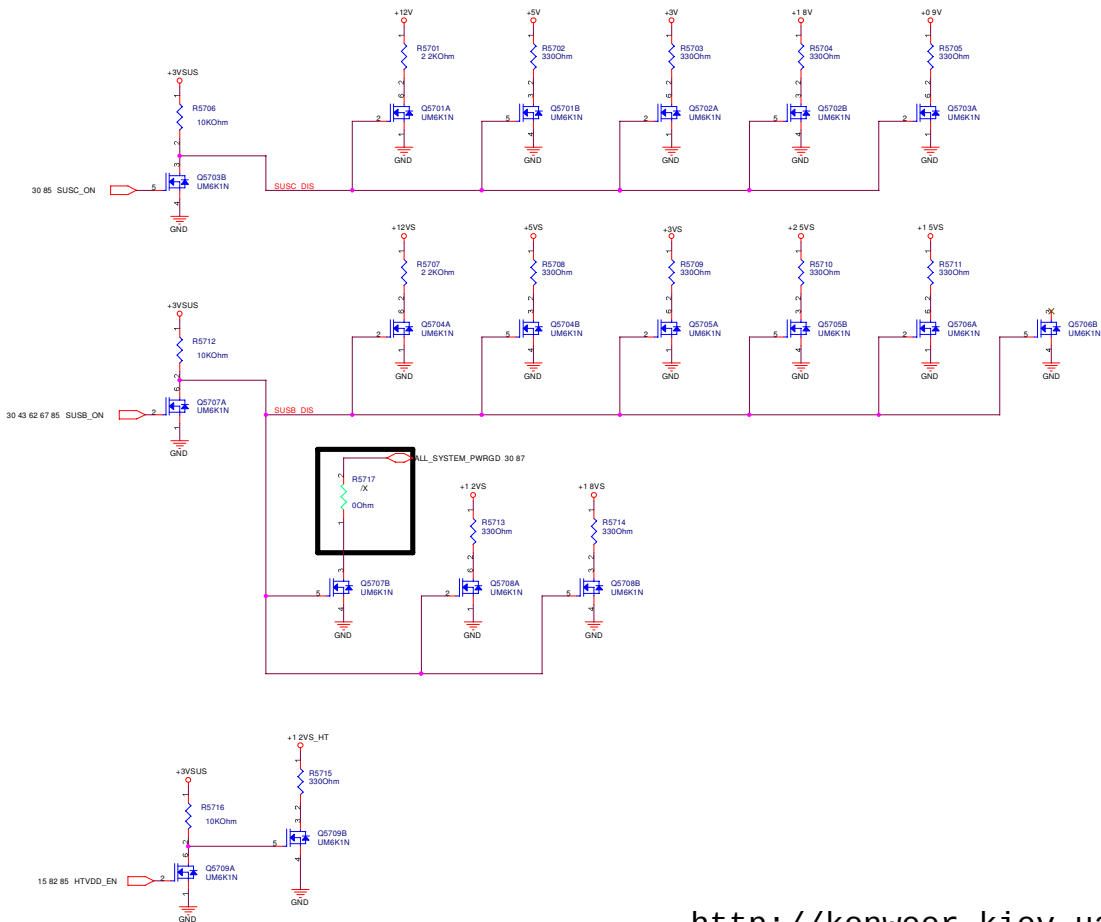
For BATTERY LED



ASUS		Title : FSU	
ASUS Taiwan Computer Inc.		Engineer: He Wang	
Rev	1.0	Rev	1.0
Rev	1.0	Rev	1.0

<< Kennedy_Zhang >>

DISCHARGE CIRCUIT



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ASUS		Title : FSU	
ASUSTek Computer NC		Engineer: He_Wang	
Size	Project Name	Rev	
Custom	FSU	1.0	
Date	2014.09.25	Sheet	37 of 38

<< Kennedy_Zhang >>

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<< Kennedy_Zhang >>

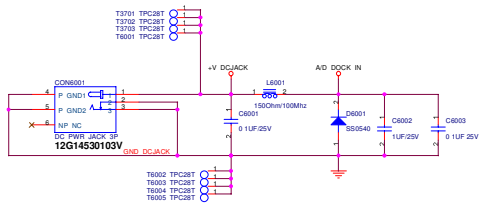
		Title : FSU	
ASUSTek Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 01 of 01		

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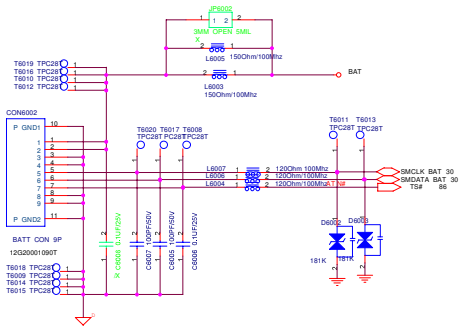
<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 01 of 01		

Adaptor IN Circuit



BATTERY IN CIRCUIT

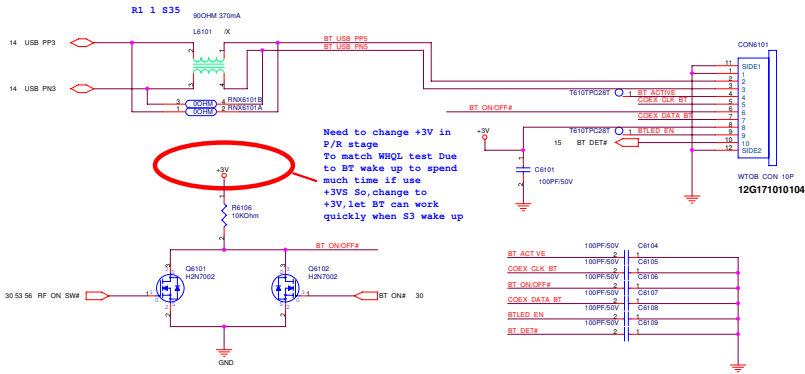


ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Size	P	Sheet Name	Rev
C	FSU		1.0
Date	2023-05-26 10:01	Sheet	65 of 65

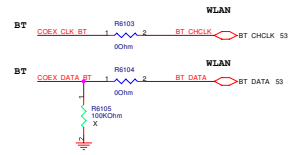
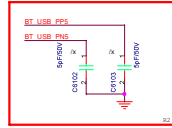
<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

BLUETOOTH CONNECTOR



Signal direction
CLK: BT > WLAN;
DATA: WLAN > BT

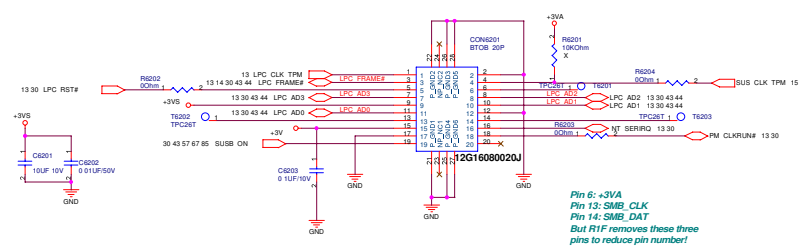


<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Size	1	Rev	1.0
File	FSU	Sheet	1 of 1

TPM CONN.



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<< Kennedy_Zhang >>

ASUS		Title : FSU	
ASUS T4000 Computer PC		Engineer: He Wang	
Size	1	Sheet Name	Rev
C	FSU		1.0
Date: 2007-05-29 10:00		Sheet: 65	of 80

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«Kennedy_Zhang»

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 61 of 61		

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<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 44 of 45		

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«« Kennedy_Zhang »»

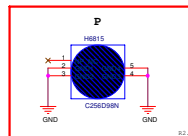
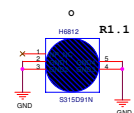
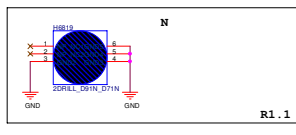
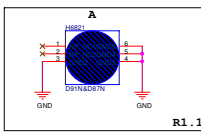
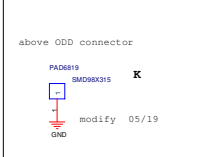
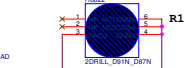
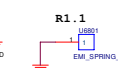
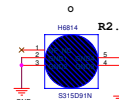
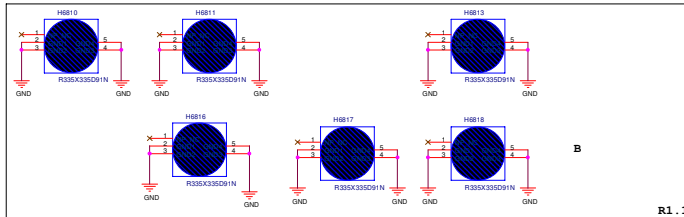
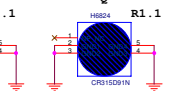
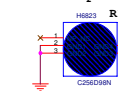
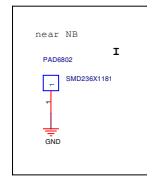
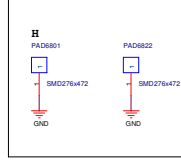
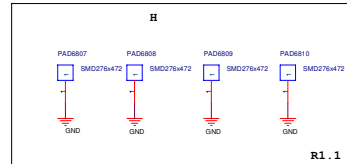
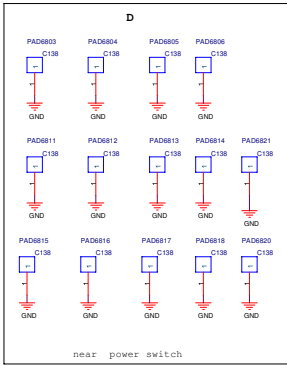
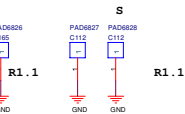
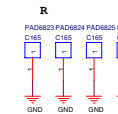
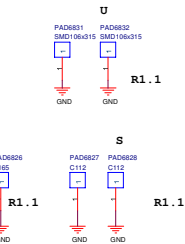
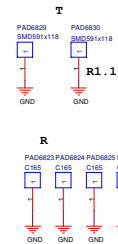
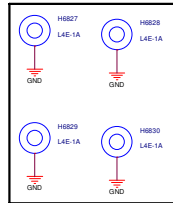
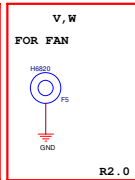
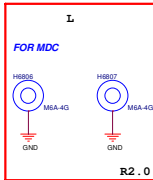
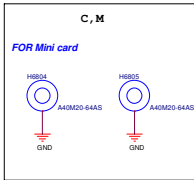
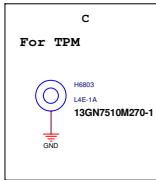
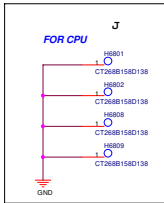
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ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 65 of 65		

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« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 65 of 65		

FOR Thermal
R2.0



H8B25
C150C150N
H8B26
C150C150N

ASUS		Title : FSU	
ASUS T400 Computer PC		Engineer: He Wang	
Rev	Project Name	Rev	
1	FSU	1.0	
Date: 2019-05-20	Drawn: 68	By: 68	

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« Kennedy Zhang »

<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:01	Sheet: 68 of 68		

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<< Kennedy_Zhang >>

		Title : FSU	
ASUS T400 Computer (PC)		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-10-26 12:00	Sheet: 10 of 10		

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<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 11 of 25	

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«Kennedy_Zhang»

		Title : FSU	
KOLCHAK Computer INC		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 15 of 25	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 11 of 25	

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« Kennedy_Zhang »

		Title : FSU	
KOLCHAK Computer INC		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 14 of 25	

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<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 15 of 25	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 15 of 25	

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

		Title : FSU	
ASUSTek Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 11 of 25	

<http://konweer.kiev.ua>

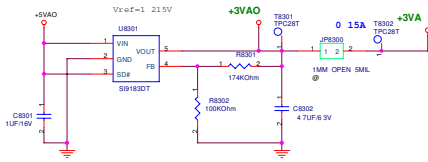
<< Kennedy_Zhang >>

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KOLSTEK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 15 of 25	

<http://konweer.kiev.ua>

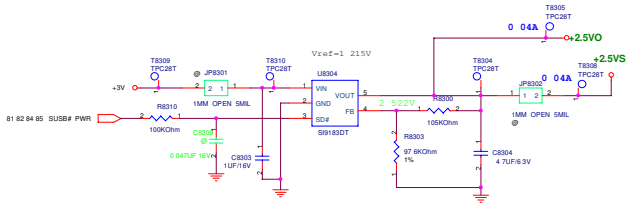
<< Kennedy_Zhang >>

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Size	Project Name		Rev
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Date: 2012-10-26 12:01	Sheet: 35 of 35		

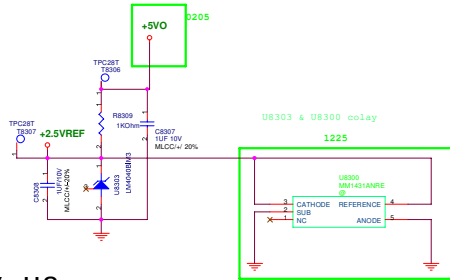


+3VA

+2.5VS



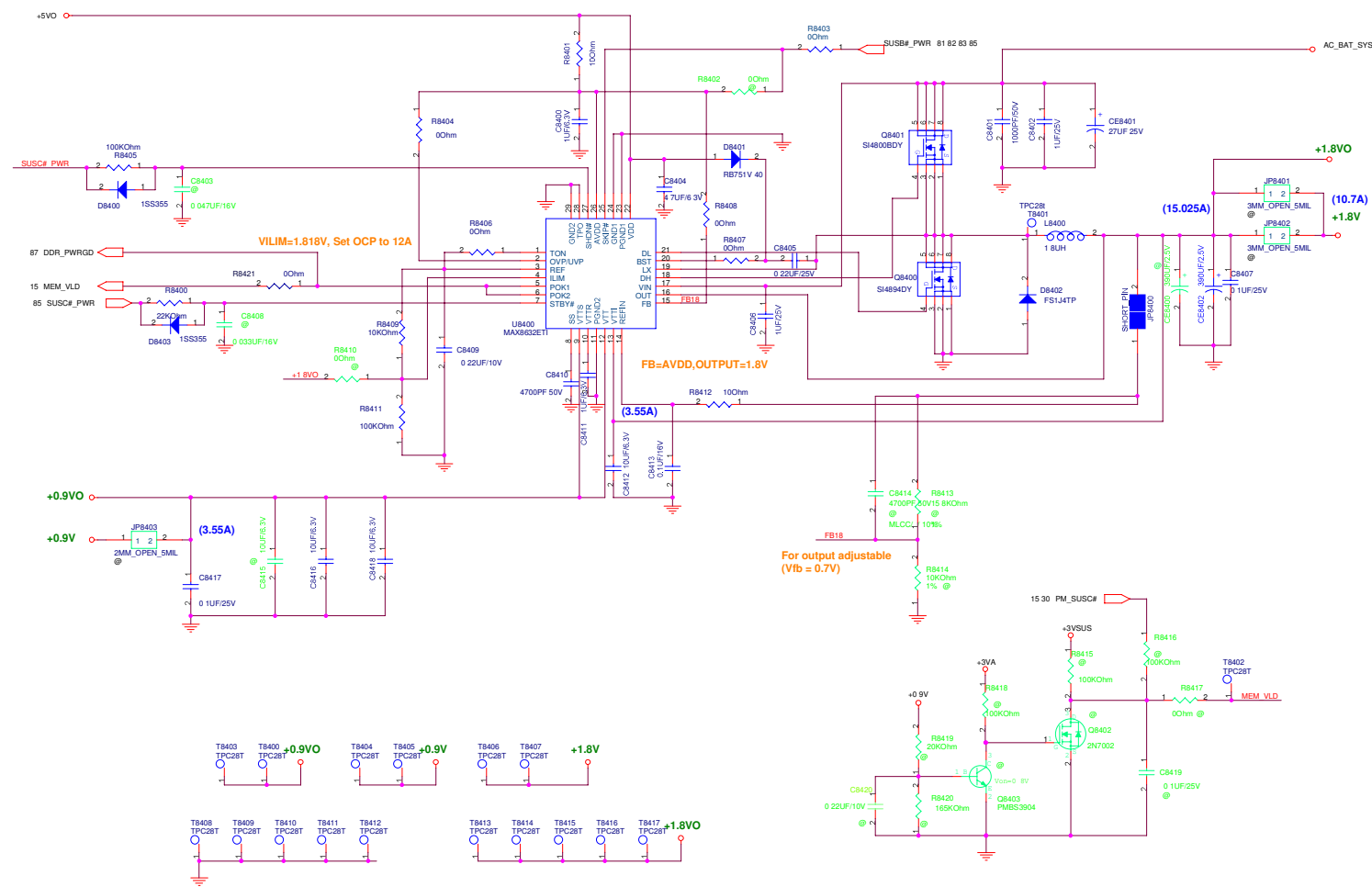
+2.5VREF



<http://konweer.kiev.ua>

« Kennedy_Zhang »

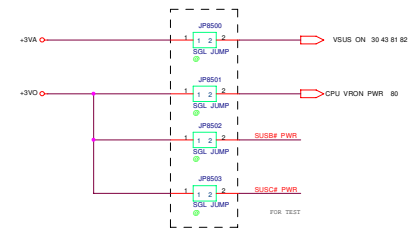
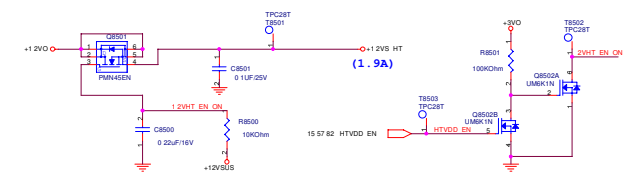
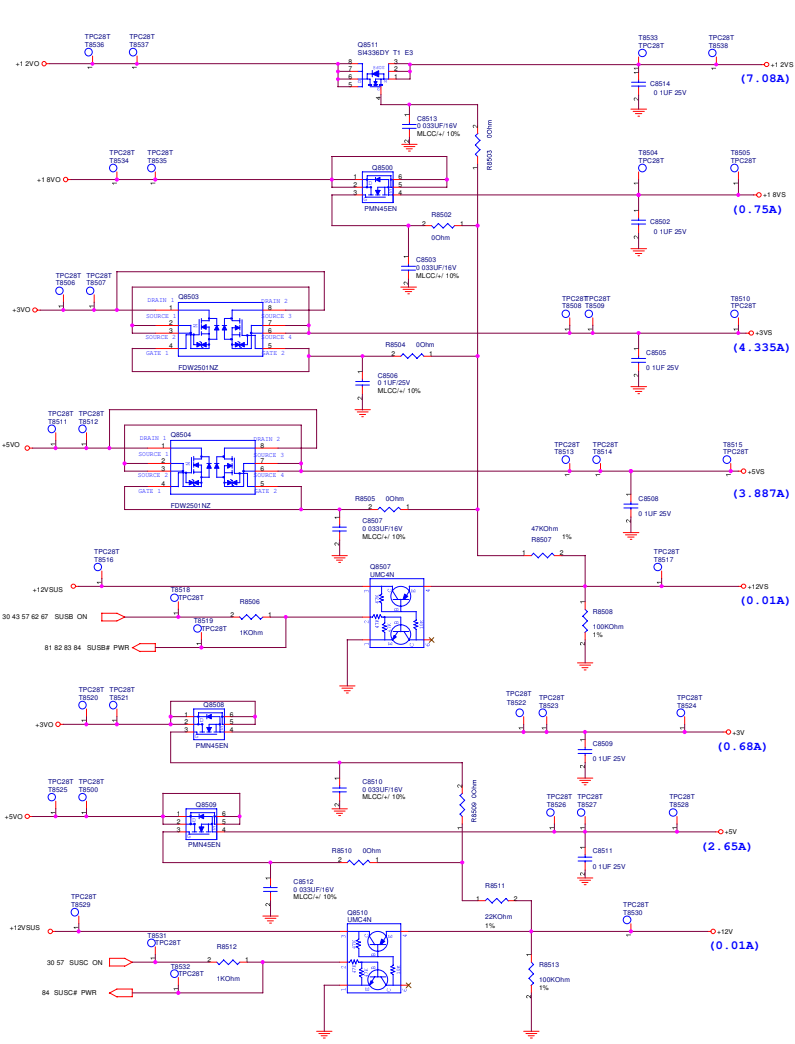
ASUS		Title: POWER_IO_LDO	
ASUSTeK		Engineer: li mei_chen	
Size	Project Name	Rev	
C	FSN	1.1	
Date: 2012-05-25 2012	Sheet: 61	of	65



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<< Kennedy_Zhang >>

ASUS		Title : POWER_IQ_D0R1	
ASUSTECH	Project Name	Engineer: li-mei_chen	
Custom	F5N	Rev	1.1
Date: 10/09/2007	Sheet	84	of 88

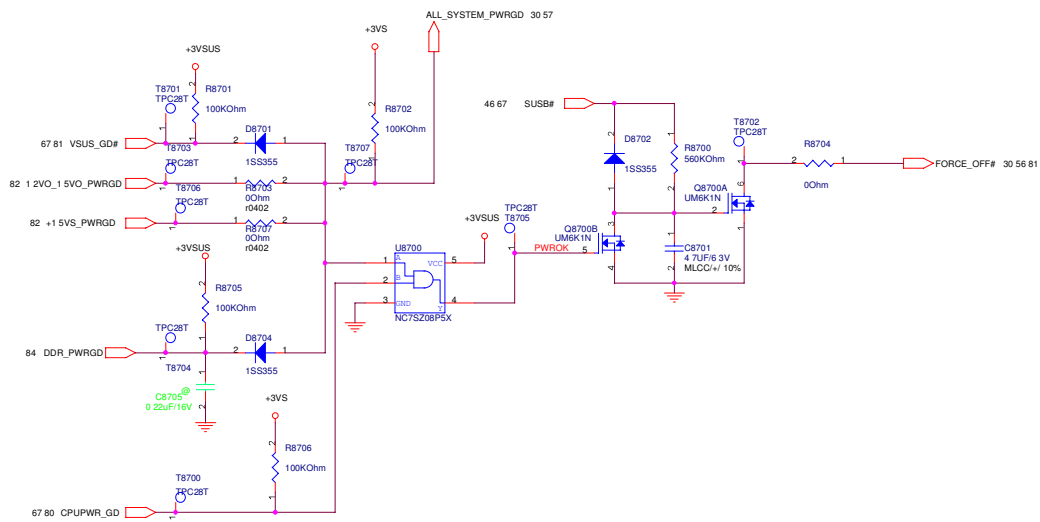


+3VA	15 30 46 58 62 67 82 83 84
+5V0	56 81 82 83 84 86
AC BAT SYS	5 80 81 82 84 86
+VCORE	5 80
+5VAD	81 82 83 86
+3V0	81
+2V	30 34 43 46 53 57 61 62 83
+12VUS	81
+1.2VUS HT	3 5 10 15 57 82
+2.5V0	83
+1.2VUS	12 16 82
+1.2VUS	11 14 16 57
+3VS	3 7 8 10 11 12 13 14 15 16 30 36 37 43 44 45 46 50 51 53 56 57 62 67 80 82 87
+3VUS	3 12 14 15 16 30 33 34 43 53 56 57 67 81 82 84 87
+0.9V	5 9 57 84
+1.2V	37 52 57
+1.2VUS	37 45 46 57
+5V	37 40 43 46 52 57
+5VUS	15 30 36 37 45 50 51 56 57 80
+2.5VS	3 57 83
+1.8V0	84
+1.8V	3 5 7 8 9 10 15 57 82 84
+1.8VS	12 57
BAT	60 86
+2.5VREF	82 83 86

<http://konweer.kiev.ua>

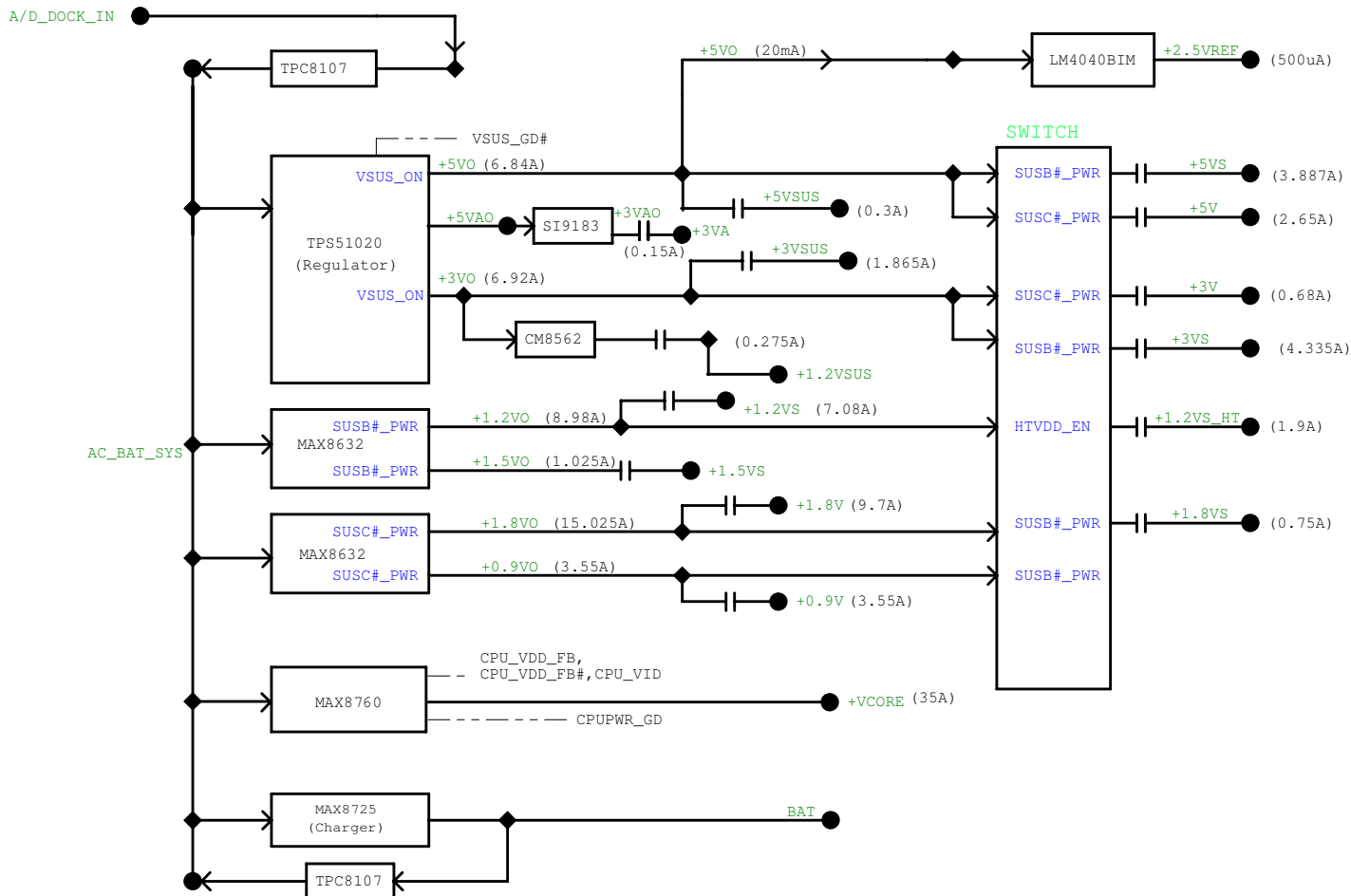
« Kennedy_Zhang »

ASUS		Title : POWER_LOAD_SYSTEM	
ASUSTeCH		Engineer: li mei chen	
Size	1	Sheet Name	Rev
1	1	F5N	1.1
Date: 2007-05-29	2007-05-29	Sheet: 65	65



ASUS		Title : POWER_PROTECT	
ASUSTECH		Engineer: <u>li-mei_chen</u>	
S.Zs	Project Name		Rev
Custom	F5N		1.1
Date: 2007-09-09		Sheet 87 of 88	

« Kennedy_Zhang »



ASUS		Title : POWER DIAGRAM	
ASUSTECH		Engineer: li-mei_chen	
Size	Project Name		Rev
Custom	F5N		1.1
Date	10/09/2007	Sheet	88 of 88

<< Kennedy_Zhang >>