



Compal Confidential

PICASSO M Schematics Document

Nvdia(T30S) + LPDDRII

QAJ70-LA-8022P

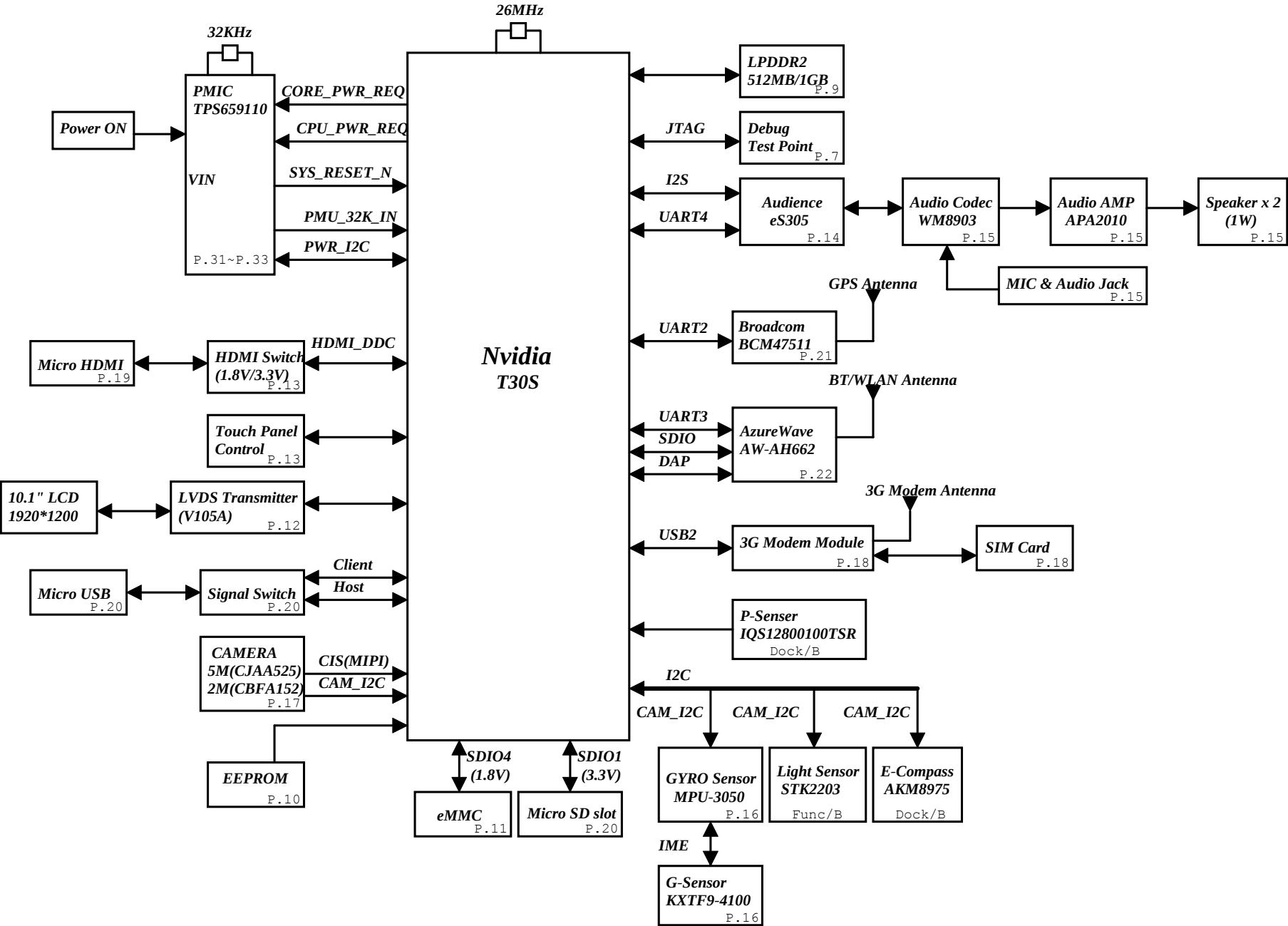
2012-03-28 REV: 1.0

※The content in this document contains confidential information of Compal Electronics, Inc. that is protected under all applicable trade secrets laws and regulations.
If you are not the intended recipient or otherwise authorized to receive such information, please do not copy, distribute or otherwise use the information contained herein and please destroy this communication accordingly.

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2011/06/20	Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		4019FQ		Rev B	
		Size C	Document Number		SCHEMATIC MB A8022
		Date: Tuesday, July 10, 2012		Sheet 1 of 38	

Compal Confidential

Model Name : NVIDIA T30S System Block Diagram



Voltage Rails

Power Plane	Description
VIN	Adapter power supply (19V)
B+	AC or battery power rail for power circuit.
+VDD_1V2_RTC_TEGRA	Power for RTC and always-on core logic.
VDD_1V2_SOC	Power for remainder of core logic
+AVDD_1V1_PLL_TEGRA	AVDD_PLL power rail
+VDD_1V8_PMU_VRTC	RTC power rail
+VDD_1V8_SYS_TEGRA	System power rail
+AVDD_3V3_USB_TEGRA	USB power rail
+VDD_2V85_EMMC	Core voltage for EMMC
+VDD_1V8_CAM_TEGRA	Core voltage for CAMERA
+AVDD_3V3_HDMI_S	HDMI power rail
VDD_1V8_GEN	1.8V switched power rail for standby mode
+VDD_3V3_DDR_RX_TEGRA	DDR RX power rail
+3VS	3.3V switched power rail for standby mode
+5VS	5V switched power rail for standby mode
+VDD_LED_BL	LED power rail
+VDD_3V3_SDMMC1_TEGRA	Micro SD power rail
+VDD_VCM_3V3	CAMERA power rail
+VDD_1V2_DDR_MEM	DDR power rail

GEN1_I2C <+VDD_1V8_SENSOR >

Device	Address<Write,Read>
Gyro	0xD0 , 0xD1
E Compass	0x18 , 0x19
Light Sensor	0x38 , 0x39

GEN2_I2C / TS_I2C < +VDD_3V3_GMI_TEGRA >

Device	Address<Write,Read>
Touch Screen	0x98 , 0x99

CAM_I2C < +VDD_1V8_CAM_TEGRA >

Device	Address<Write,Read>
Camera 5M	0x78 , 0x79
Camera 2M	0x20 , 0x21
Flash LED	0x66 , 0x67

PWR_I2C < +VDD_1V8_SYS_TEGRA>

Device	Address<Write,Read>
Thermal Senser	0x98 , 0x99
ES305	0x3E
Codec	0x34 , 0x35
PMU	0x2D
TPS62361	0x60
BATT Conn	0xAA , 0xAB
EEPROM (Low level)	0xA0 , 0xA1
EEPROM (High level)	0xA2 , 0xA3

Board ID

PICASSO 2

EVT	QA150(2-6-2)		QA170(3-4-3)			
SKU	SKU1(3G only)	SKU1-1(WIFI&3G)	SKU2(3G only)	SKU2-1(WIFI&3G)		
WIFI/BT	NH660	AH662	NH660	AH662		
PCB_ID0	L	L	H	H	L : 262	H : 343
PCB_ID1	L	H	L	H	L : AH663	H : NH660
PCB_ID2	L	L	L	L	L: NON LTE	H: LTE
BOARD_ID0	L	L	L	L		
BOARD_ID1	L	L	L	L	00:EVT	

DVT1	QA150(2-6-2)		QA170(3-4-3)			
SKU	SKU1(WIFI only)	SKU2(WIFI only)	SKU3((WIFI&3G))	SKU4(WIFI&3G)	SKU5(WIFI<E)	
WIFI/BT	A W-AH663	A W-NH660	A W-NH660	A W-AH663	A W-AH663	
PCB_ID0	L	L	H	H	H	L : 262
PCB_ID1	L	H	H	L	L	L : AH663
PCB_ID2	L	L	L	L	H	L: NON LTE
BOARD_ID0	H	H	H	H	H	
BOARD_ID1	L	L	L	L	L	01:DVT1

DVT2	QA150(2-6-2)									
SKU	SKU1(WIFI only)	SKU2(WIFI only)	SKU3(WIFI only)	SKU4(WIFI only)	SKU5(WIFI only)	SKU6(WIFI only)	SKU7(WIFI only)	SKU8(WIFI only)		
WIFI/BT	A W-AH663	A W-AH663	A W-AH663	A W-AH663	A W-AH663	A W-AH663	A W-AH663	A W-AH663		
PCB_ID0	L	L	L	L	L	L	L	L	L : 262	H : 343
PCB_ID1	L	L	L	L	L	L	L	L	L : AH663	H : NH660
PCB_ID2	L	L	L	L	L	L	L	L	L: NON LTE	H: LTE
U1_I37	L	L	L	L	L	L	L	L	L: Picasso 2	H: Picasso M
BOARD_ID0	L	L	L	L	L	L	L	L		
BOARD_ID1	H	H	H	H	H	H	H	H	10:DVT2	
DVT2	QA170(3-4-3)									
SKU	SKU9(WIFI&3G)	SKU10(WIFI&3G)	SKU11(WIFI&3G)	SKU12(WIFI&3G)	SKU13(WIFI<E)	SKU14(WIFI<E)				
WIFI/BT	A W-AH663	A W-AH663	A W-AH663	A W-AH663	A W-AH663	A W-AH663				
PCB_ID0	H	H	H	H	H	H	L : 262	H : 343		
PCB_ID1	L	L	L	L	L	L	L : AH663	H : NH660		
PCB_ID2	L	L	L	L	H	H	L: NON LTE	H: LTE		
U1_R37	L	L	L	L	L	L	L: Picasso 2	H: Picasso M		
BOARD_ID0	L	L	L	L	L	L				
BOARD_ID1	H	H	H	H	H	H	10:DVT2			

PICASSO M

EVT	QA1A0(2-6-2)		
SKU	SKU1(Wifi only)		
PCB_ID0	L	L : 262	H : 343
PCB_ID1	H	L : AH663	H : NH660
PCB_ID2	L	L: NON LTE	H: LTE
U1_I37	H	L: Picasso 2	H: Picasso M
BOARD_ID0	L		
BOARD_ID1	L	00:EVT	

DVT	QA1A0(2-6-2)		
SKU	SKU1(Wifi only)		
PCB_ID0	L	L : 262	H : 343
PCB_ID1	H	L : AH663	H : NH660
PCB_ID2	L	L: NON LTE	H: LTE
U1_I37	H	L: Picasso 2	H: Picasso M
BOARD_ID0	L		
BOARD_ID1	L	00:EVT(R01)	

DVT2	QA1A0(2-6-2)		
SKU	SKU1(Wifi only)		
PCB_ID0	L	L : 262	H : 343
PCB_ID1	H	L : AH663	H : NH660
PCB_ID2	L	L: NON LTE	H: LTE
U1_I37	H	L: Picasso 2	H: Picasso M
BOARD_ID0	1		
BOARD_ID1	L	01:DVT(R02)	

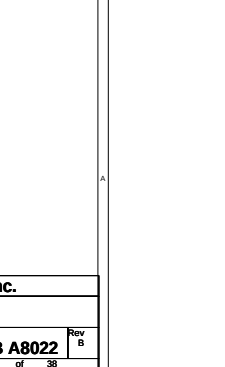
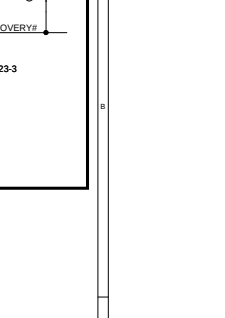
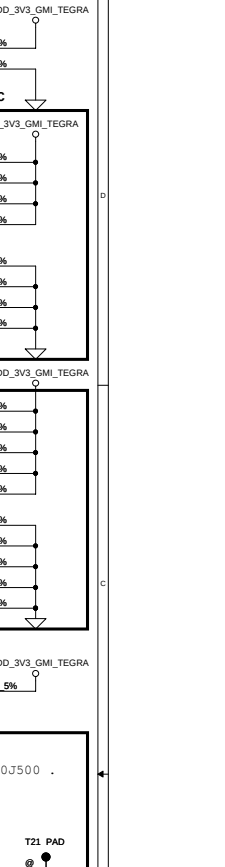
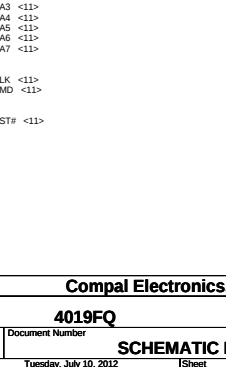
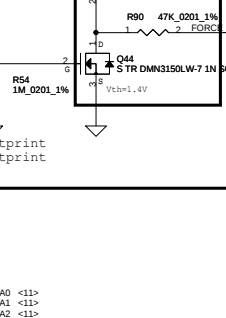
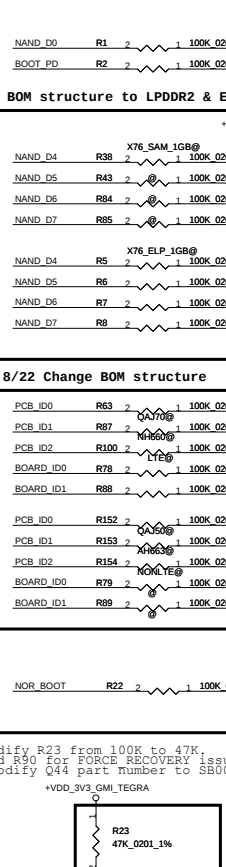
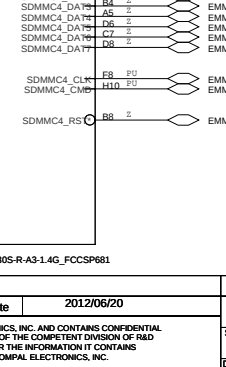
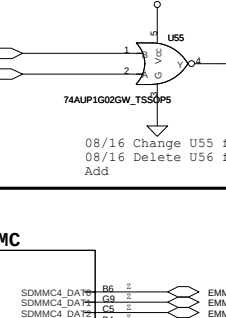
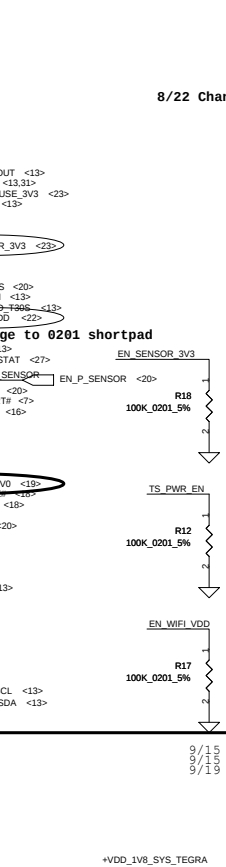
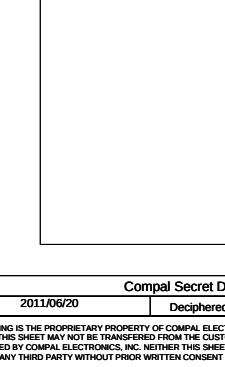
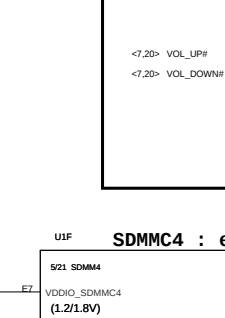
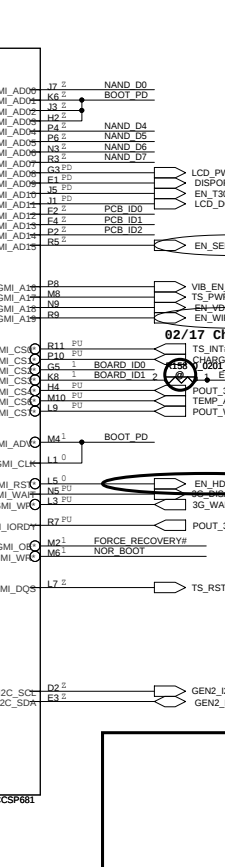
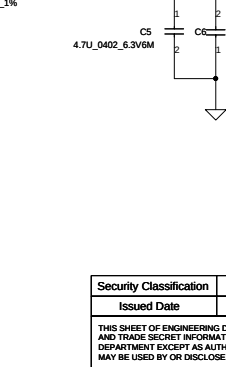
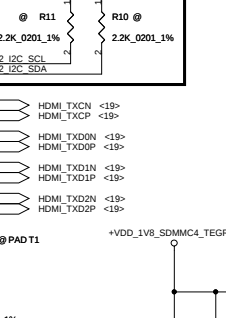
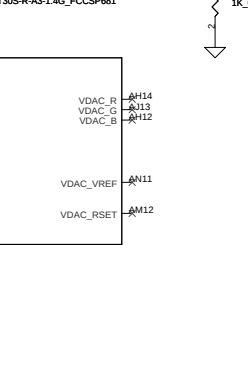
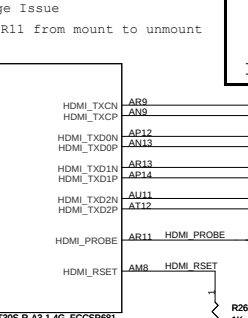
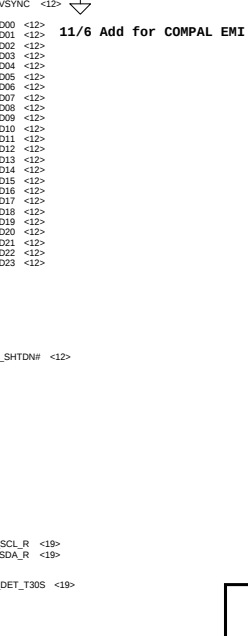
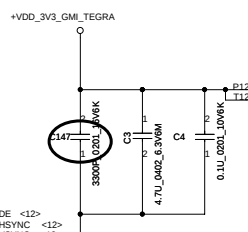
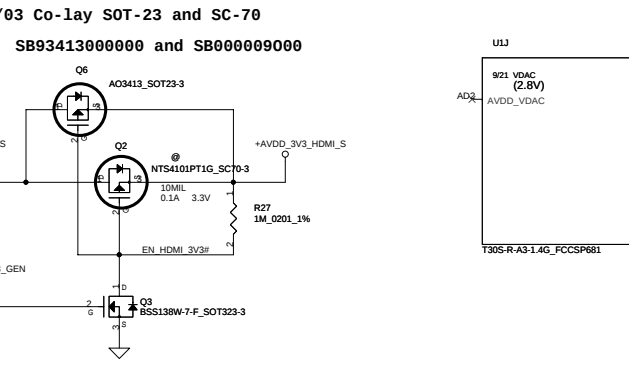
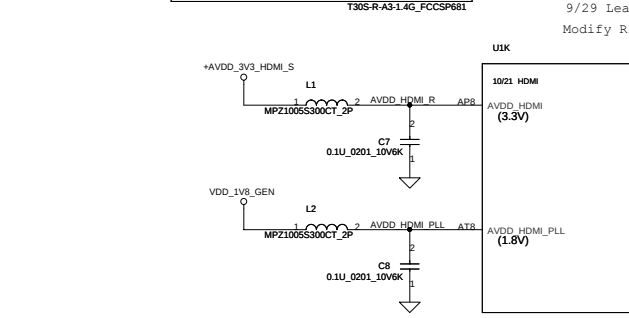
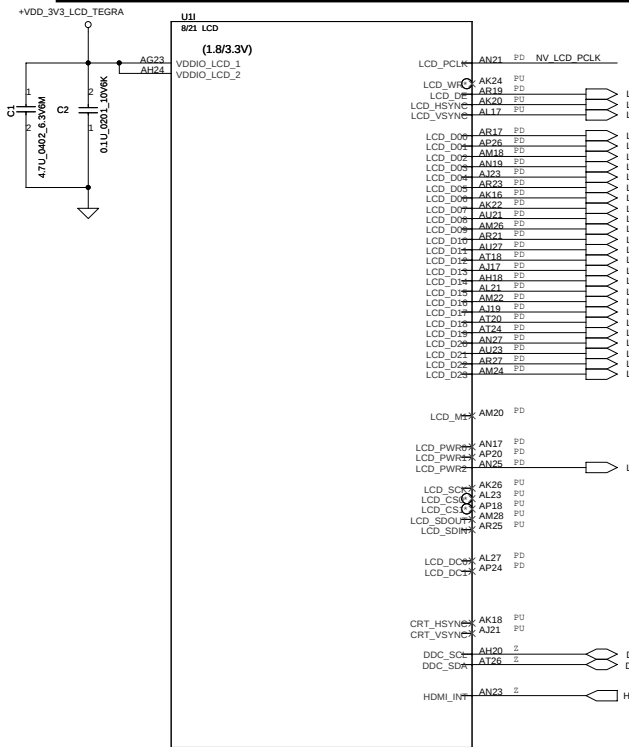
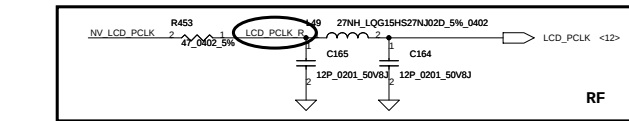
LPDDR2

	Vendor PN	Acer PN	Compel PN	Assign Bit	NAND_D4	NAND_D5	
LPDDR2	EDB8132B2MA-1D-F	IC.0080E.002	SA000050Z10	00	0	0	ELF_1GB@
	K4P8G304EB-R9C2	IC.00809.002	SA00004TY20	01	1	0	SAM_1GB@

LVDS strap pin

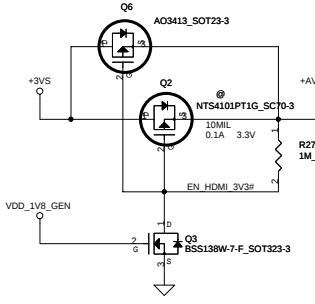
	LVDS_CTRL2	LVDS_CTRL1	LVDS_CTRL0	LVDS_MAP	LCD_PCLK	LVDS_ACLK
PicassoM	L	H	H	H	70.34Mhz	71.56Mhz
Picasso2	L	H	L	L	152.28Mhz	76.14Mhz

Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size C	Document Number	Rev B
				4019FQ SCHEMATIC MB A8022		
				Date:	Tuesday, July 10, 2012	Sheet 3 of 38



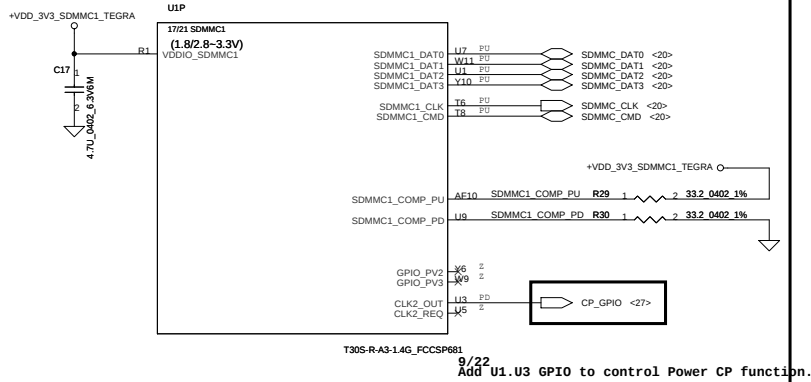
10/03 Co-lay SOT-23 and SC-70

SB934131000000 and SB000009000

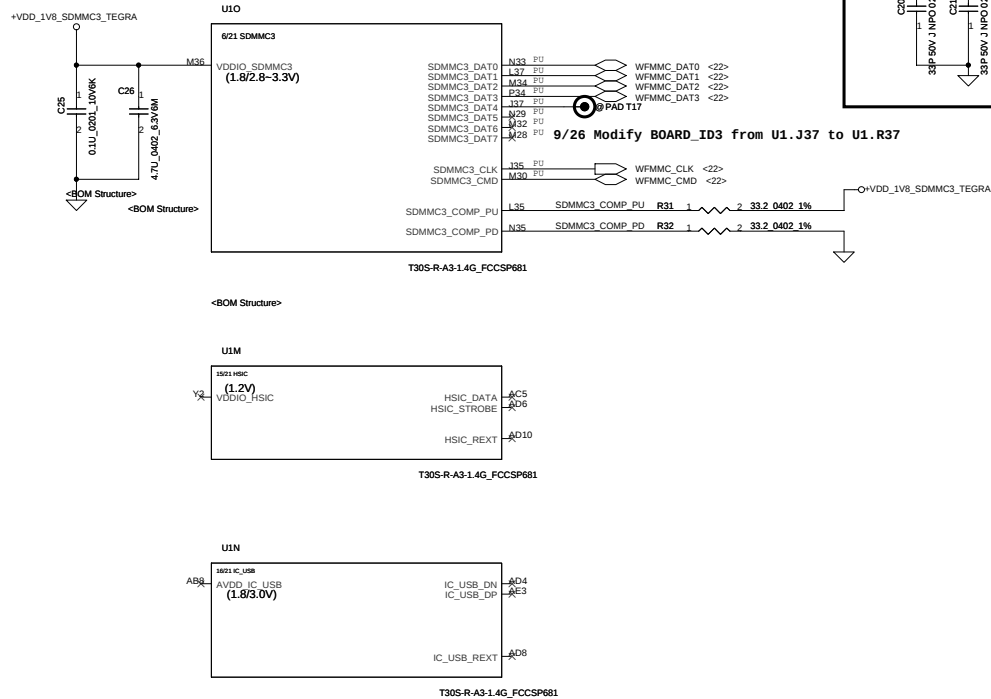


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2011/06/20		4019FQ	
Deciphered Date		2012/06/20		SCHEMATIC MB A8022	
Title		Size		Rev	
This sheet of engineering drawing is the proprietary property of Compal Electronics, Inc. and contains confidential and trade secret information. This sheet may not be transferred from the custody of the competent division of R&D Department except as authorized by Compal Electronics, Inc. Neither this sheet nor the information it contains may be used by or disclosed to any third party without prior written consent of Compal Electronics, Inc.		Date: Tuesday, July 10, 2012		Sheet 4 of 38	

SDMMC1 : SD card

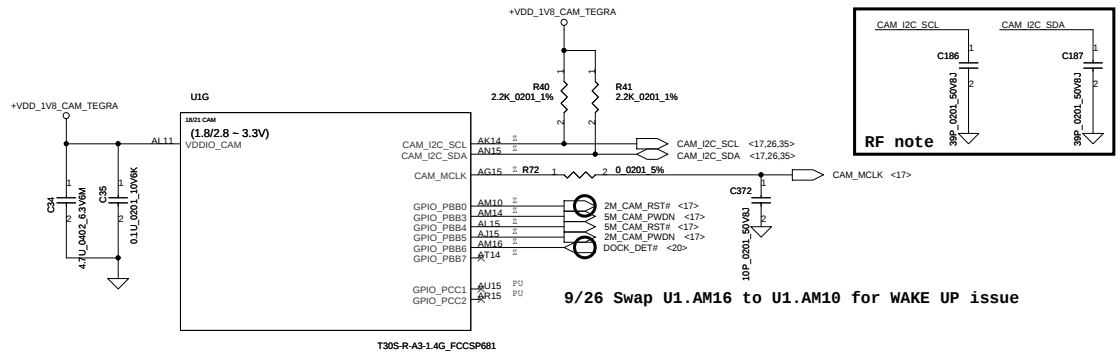
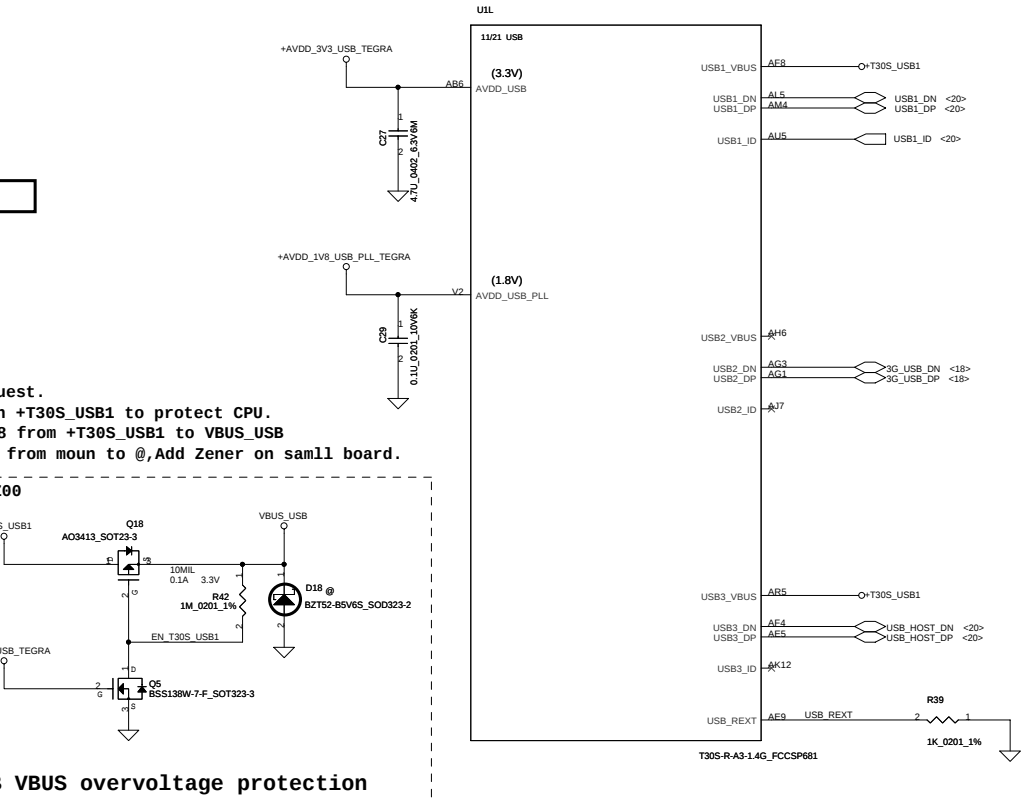
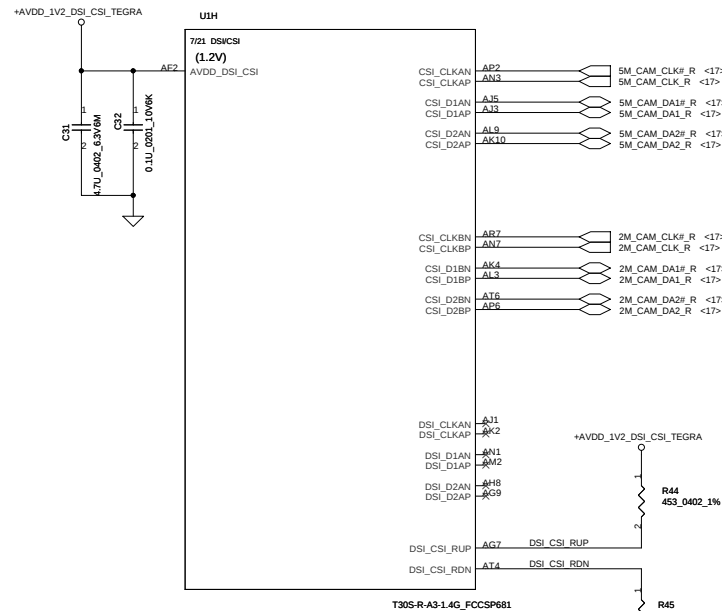
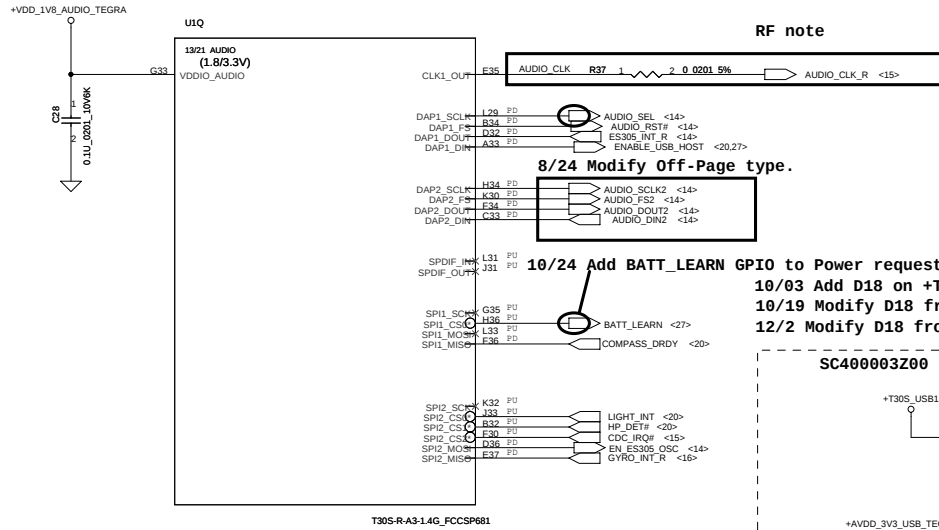


SDMMC3 : WIFI

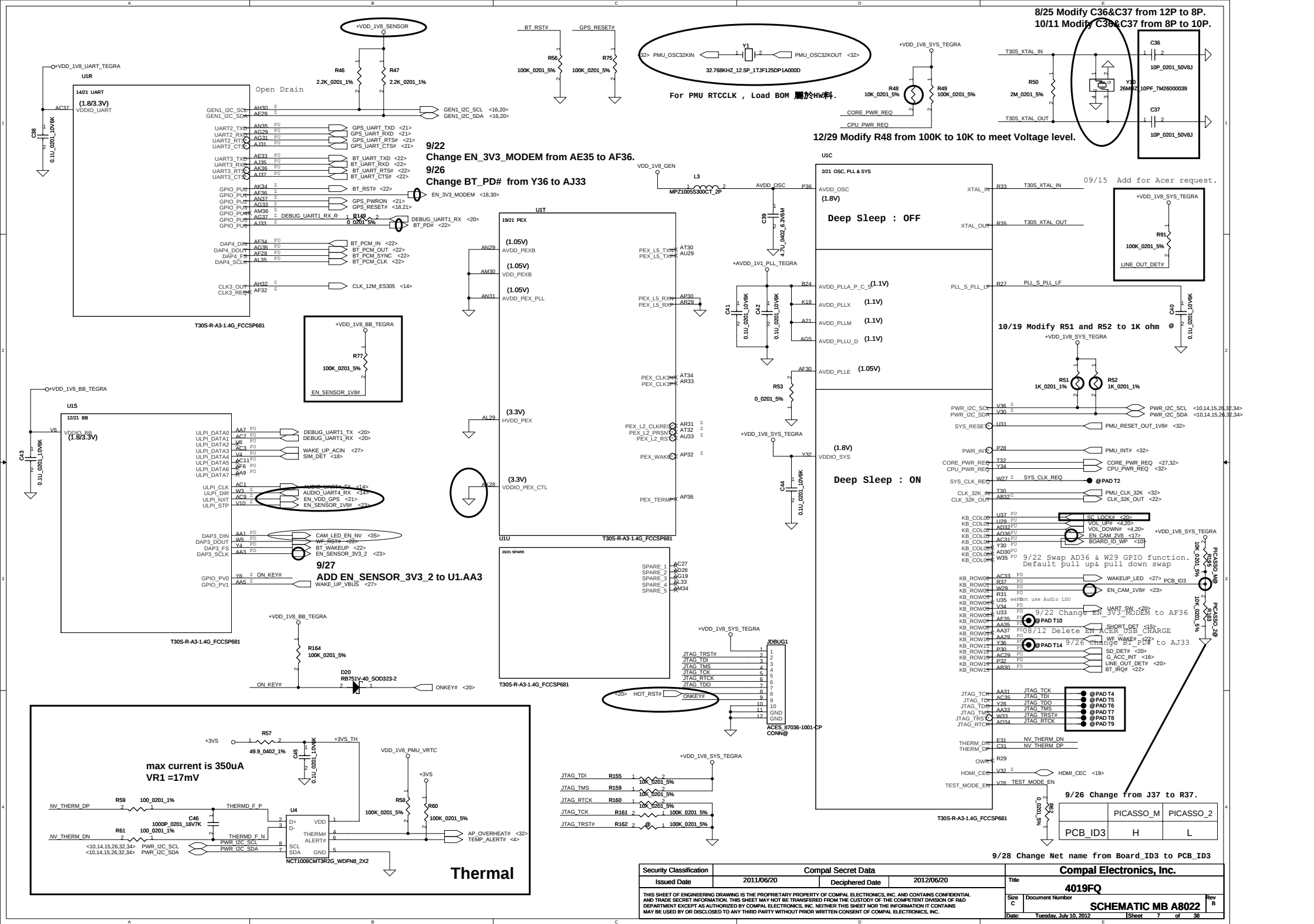


Security Classification	Compal Secret Data	Title	4019FQ
Issued Date	2011/06/20	Deciphered Date	2012/06/20
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size	Document Number
		C	SCHEMATIC MB A8022
		Date	Tuesday, July 10, 2012
		Sheet	5 of 38

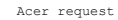
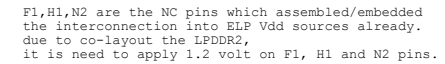
U1Q is not place on grid , need check connection



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				4019FQ
Size	C	Document Number	SCHEMATIC MB A8022	Rev
Date:	Tuesday, July 10, 2012	Sheet	6	of 38

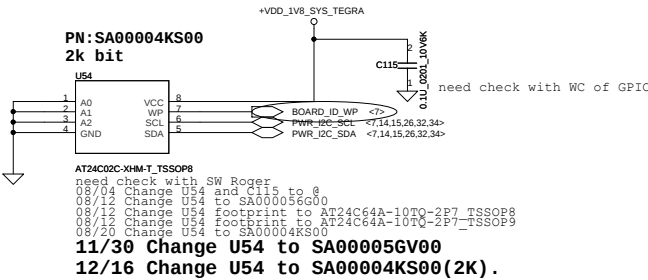


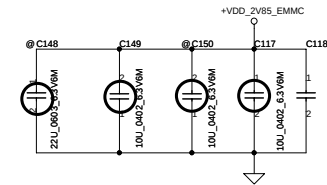
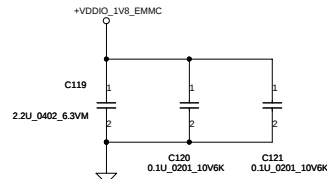
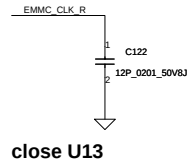
U7  X76_ELP_1GB@ (1GB Elpida)
SA000050Z10



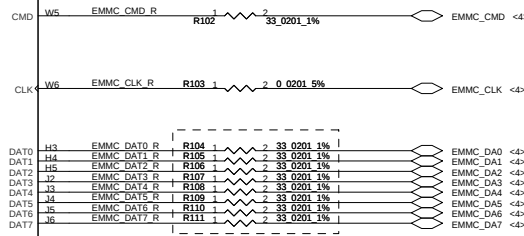
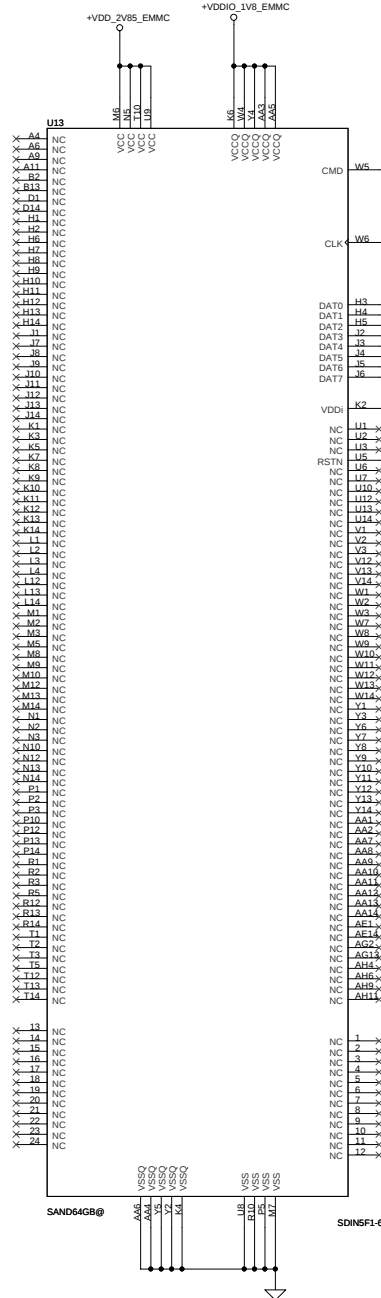
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Remove EC

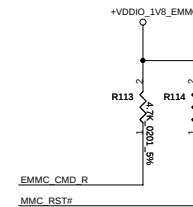




11/23 Reserver C148,C149 and C150 for Acer HW request.
 11/23 Modify C117 from 2.2U 0402 to 10U 0402.
 12/7 Modify C148,C149,C150 from @ to mount
 12/16 Modify C148 and c150 from mount to @ for LDO solution.



02/17 Change to 0201 shortpad



U13 KING16GB@
 SA000040610
 U13 SAND16GB@
 SA00004XA00
 U13 SAM16GB@
 SA00004FN20

U13 KING32GB@
 SA00004TD00
 U13 SAM32GB@
 SA00004FO40
 U13 SAND32GB@
 SA000052910

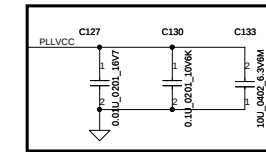
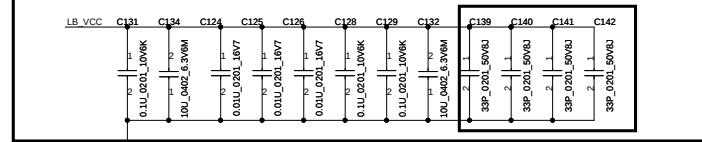
U13 SAND432GB@
 SA000042X20
 U13 SAND64GB@
 SA00004XQ10

8/26 ADD for SKU control.

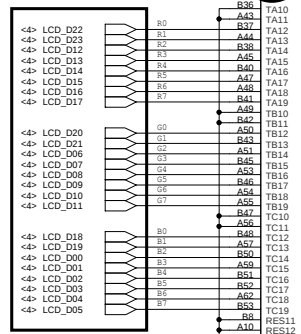
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	
2011/06/20		2012/06/20		4019FQ	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size		Document Number	
		C		SCHEMATIC MB A8022	
Date:		Tuesday, July 10, 2012		Sheet 11 of 38	

10/03 Modify C125,C126,C128,C129,C132,C139,C140,C141 and C142 from +VDD_LVDS to LB_VCC power domain.

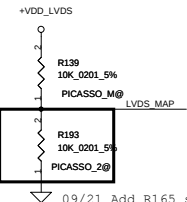
08/09 Add C139-C140 for +VDD_LVDS



10/19 Modify U20 PN from SA000050500 to SA000059A00



09/21 swap LCD_D00-LCD_D23 for colay.

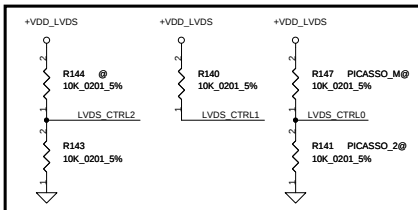


09/21 Add R165 select pin for Picasso M.

LVDS_MAP configuration:
High: Picasso M
Low: Picasso 2

<4> LCD_VSYNC
<4> LCD_HSYNC
<4> LCD_DE
<4> LCD_PCLK

need check for single in single out



08/12 Add R148 PD LVDS_SHTDN# to GND

CTRL[2:0] = LHL	Single-In, Dual-Out (DDR Off)
CTRL[2:0] = HHL	Single-In, Dual-Out (DDR On)
CTRL[2:0] = LHH	Single-In, Single-Out (Distribution Off)
CTRL[2:0] = HHH	Single-In, Single-Out (Distribution On)

Input control

DE

CLKIN

MAP

CTRL0

CTRL1

CTRL2

RF

RS

PD#

RSND_1

RSND_2

RSND_3

RSND_4

RSND_5

RSND_6

RSND_7

RSND_8

RSND_9

RSND_10

RSND_11

RSND_12

RSND_13

RSND_14

RSND_15

RSND_16

RSND_17

RSND_18

RSND_19

RSND_20

RSND_21

RSND_22

RSND_23

RSND_24

RSND_25

RSND_26

RSND_27

RSND_28

RSND_29

RSND_30

RSND_31

RSND_32

RSND_33

RSND_34

RSND_35

RSND_36

RSND_37

RSND_38

RSND_39

RSND_40

RSND_41

RSND_42

RSND_43

RSND_44

RSND_45

RSND_46

RSND_47

RSND_48

RSND_49

RSND_50

RSND_51

RSND_52

RSND_53

RSND_54

RSND_55

RSND_56

RSND_57

RSND_58

RSND_59

RSND_60

RSND_61

RSND_62

RSND_63

RSND_64

RSND_65

RSND_66

RSND_67

RSND_68

RSND_69

RSND_70

RSND_71

RSND_72

RSND_73

RSND_74

RSND_75

RSND_76

RSND_77

RSND_78

RSND_79

RSND_80

RSND_81

RSND_82

RSND_83

RSND_84

RSND_85

RSND_86

RSND_87

RSND_88

RSND_89

RSND_90

RSND_91

RSND_92

RSND_93

RSND_94

RSND_95

RSND_96

RSND_97

RSND_98

RSND_99

RSND_100

RSND_101

RSND_102

RSND_103

RSND_104

RSND_105

RSND_106

RSND_107

RSND_108

RSND_109

RSND_110

RSND_111

RSND_112

RSND_113

RSND_114

RSND_115

RSND_116

RSND_117

RSND_118

RSND_119

RSND_120

RSND_121

RSND_122

RSND_123

RSND_124

RSND_125

RSND_126

RSND_127

RSND_128

RSND_129

RSND_130

RSND_131

RSND_132

RSND_133

RSND_134

RSND_135

RSND_136

RSND_137

RSND_138

RSND_139

RSND_140

RSND_141

RSND_142

RSND_143

RSND_144

RSND_145

RSND_146

RSND_147

RSND_148

RSND_149

RSND_150

RSND_151

RSND_152

RSND_153

RSND_154

RSND_155

RSND_156

RSND_157

RSND_158

RSND_159

RSND_160

RSND_161

RSND_162

RSND_163

RSND_164

RSND_165

RSND_166

RSND_167

RSND_168

RSND_169

RSND_170

RSND_171

RSND_172

RSND_173

RSND_174

RSND_175

RSND_176

RSND_177

RSND_178

RSND_179

RSND_180

RSND_181

RSND_182

RSND_183

RSND_184

RSND_185

RSND_186

RSND_187

RSND_188

RSND_189

RSND_190

RSND_191

RSND_192

RSND_193

RSND_194

RSND_195

RSND_196

RSND_197

RSND_198

RSND_199

RSND_200

RSND_201

RSND_202

RSND_203

RSND_204

RSND_205

RSND_206

RSND_207

RSND_208

RSND_209

RSND_210

RSND_211

RSND_212

RSND_213

RSND_214

RSND_215

RSND_216

RSND_217

RSND_218

RSND_219

RSND_220

RSND_221

RSND_222

RSND_223

RSND_224

RSND_225

RSND_226

RSND_227

RSND_228

RSND_229

RSND_230

RSND_231

RSND_232

RSND_233

RSND_234

RSND_235

RSND_236

RSND_237

RSND_238

RSND_239

RSND_240

RSND_241

RSND_242

RSND_243

RSND_244

RSND_245

RSND_246

RSND_247

RSND_248

RSND_249

RSND_250

RSND_251

RSND_252

RSND_253

RSND_254

RSND_255

RSND_256

RSND_257

RSND_258

RSND_259

RSND_260

RSND_261

RSND_262

RSND_263

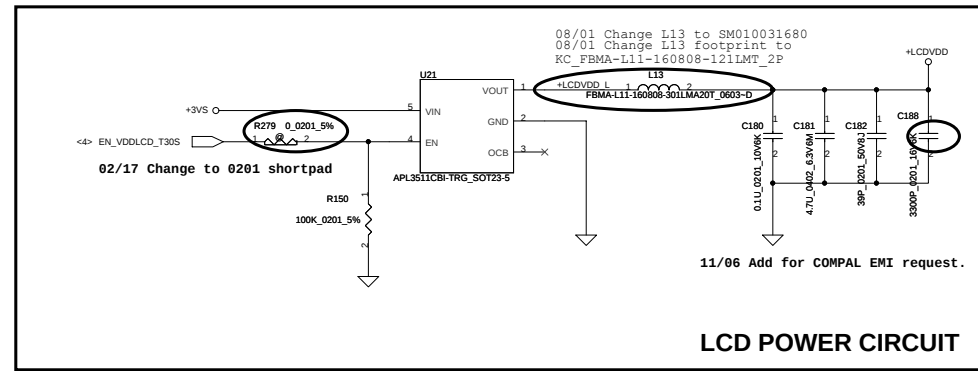
RSND_264

RSND_265

RSND_266

RSND_267

RSND_268



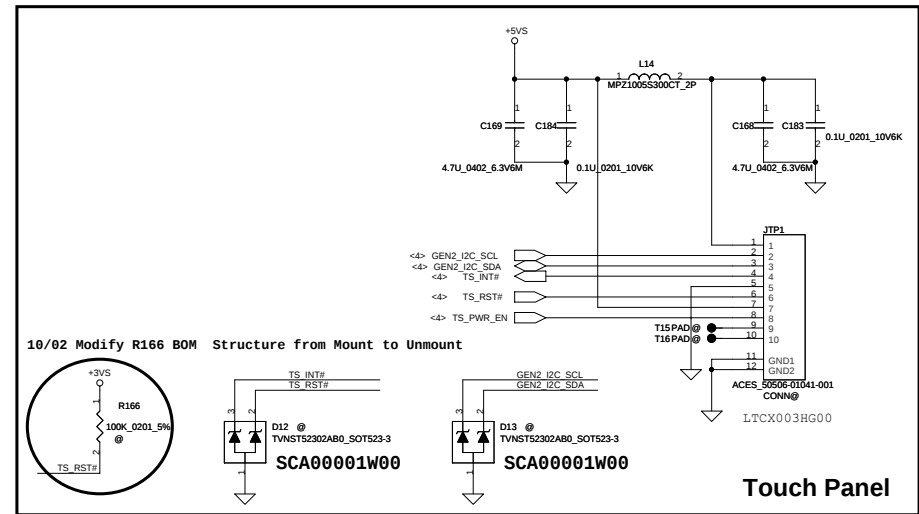
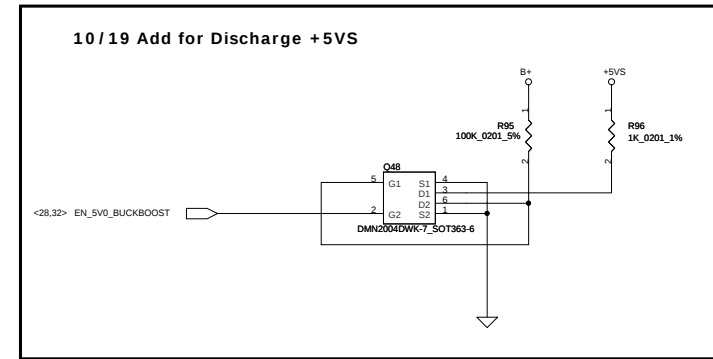
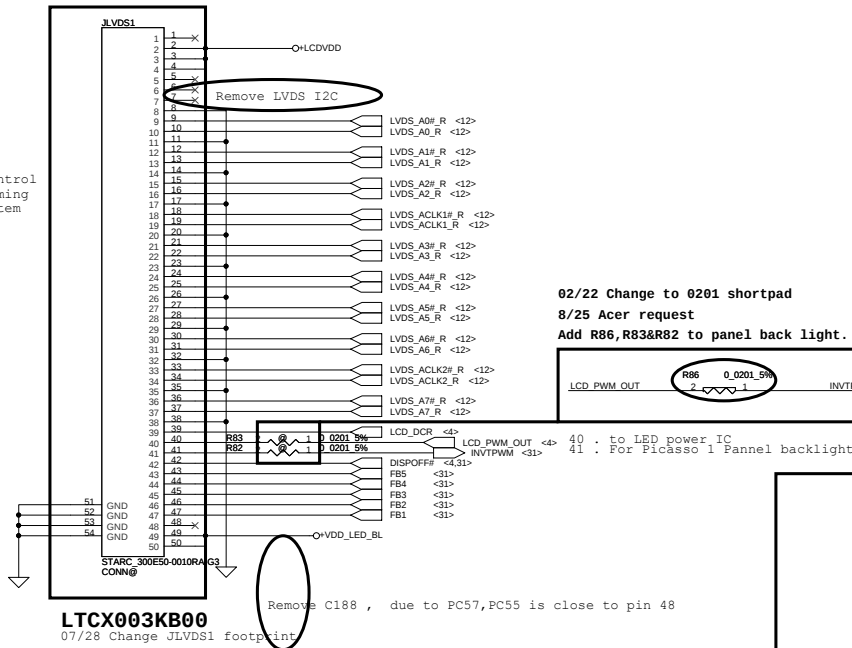
Panel SPEC

- 2 VDD Power Supply +3.3V
- 3 VDD Power Supply +3.3V
- 4 VDDDEDID EDID +3.3V

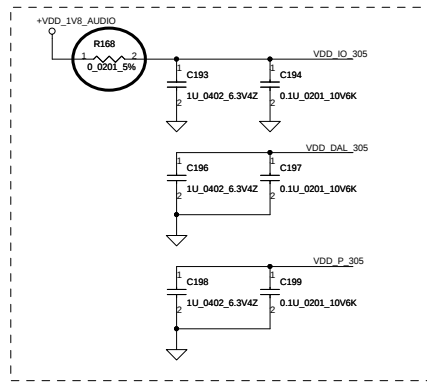
- 38 DCR_EN (CABC_EN) Dynamic backlight control
- 39 PWM_IN System PWM signal input for dimming
- 40 PWM_OUT Panel PWM signal output to system

- 43 LED_CA5 LED Cathode 5
- 44 LED_CA4 LED Cathode 4
- 45 LED_CA3 LED Cathode 3
- 46 LED_CA2 LED Cathode 2
- 47 LED_CA1 LED Cathode 1

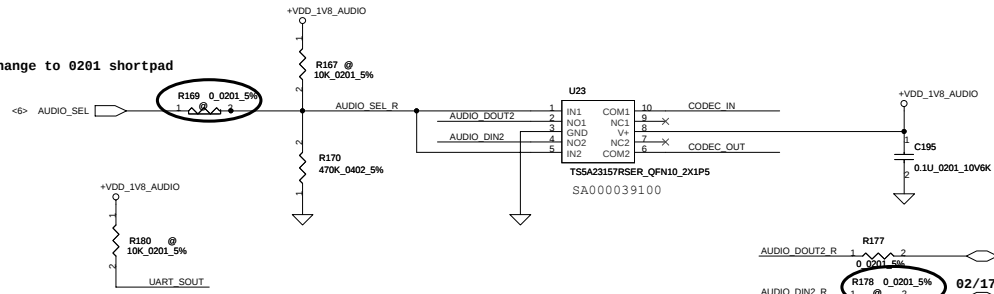
- 49 VLED Output LED Backlight power
- 50 VLED Output LED Backlight power



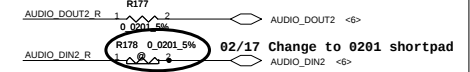
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				4019FQ
				Size C Document Number
				SCHEMATIC MB A8022
				Rev B
				Date: Tuesday, July 10, 2012
				Sheet 13 of 38



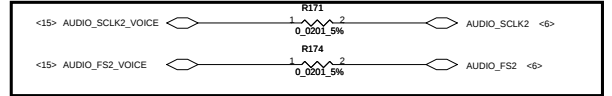
02/17 Change to 0201 shortpad



Support for input clock frequencies of 24 MHz and 26 MHz requires a 10KΩ pull-up resistor on the UART_SOUT pin.

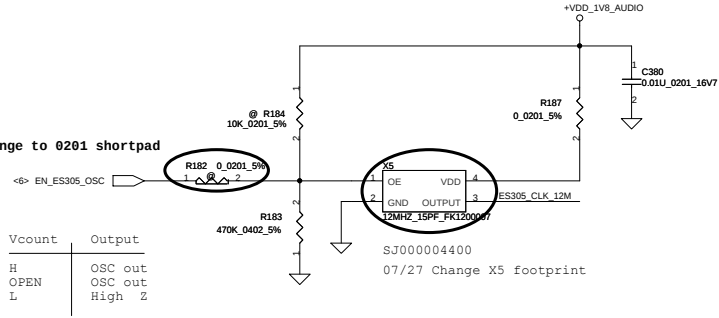


02/17 Change to 0201 shortpad



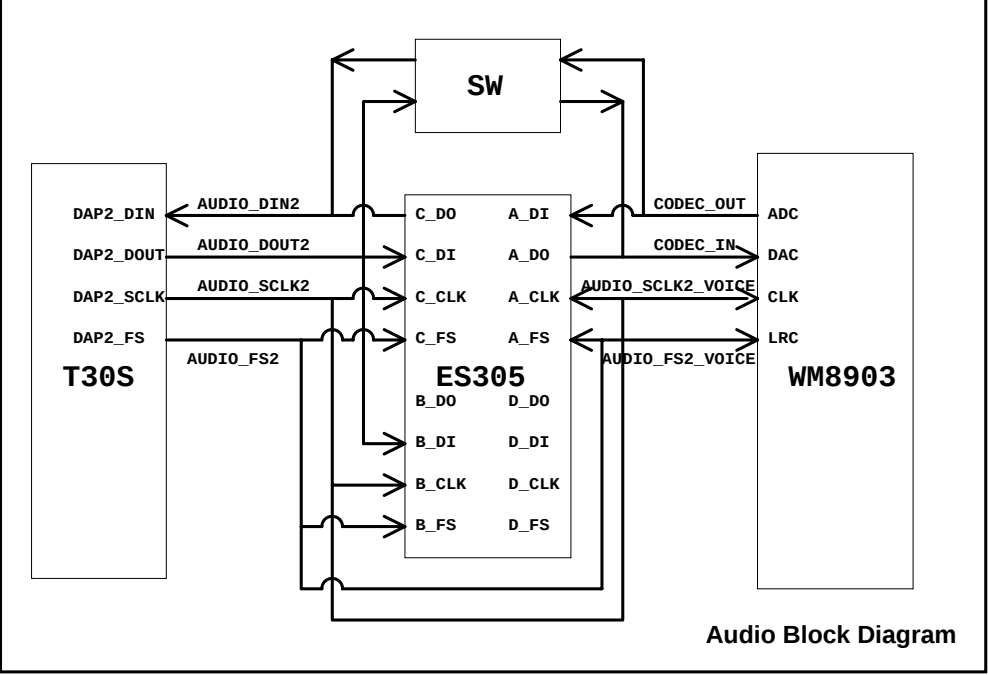
20110823 Modify for Acer request
AUDIO_FS2_VOICE connect to portA and CODEC,
AUDIO_FS2 connect to portB, portC and CPU.
AUDIO_SCLK2_VOICE connect to portA and CODEC
AUDIO_SCLK2 connect to portB, portC and CPU.

02/17 Change to 0201 shortpad

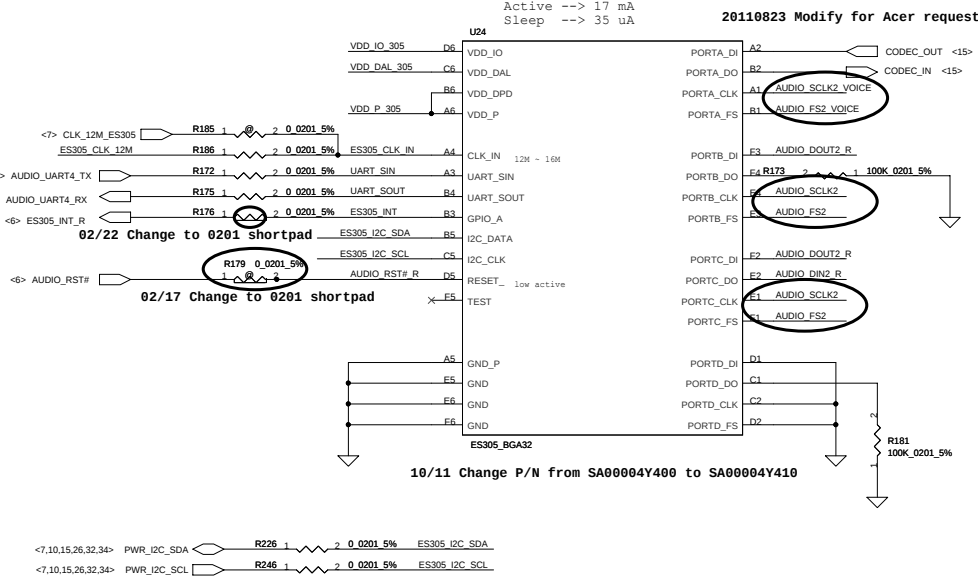


Vcount	Output
H	OSC out
OPEN	OSC out
L	High Z

SJ000004400
07/27 Change X5 footprint

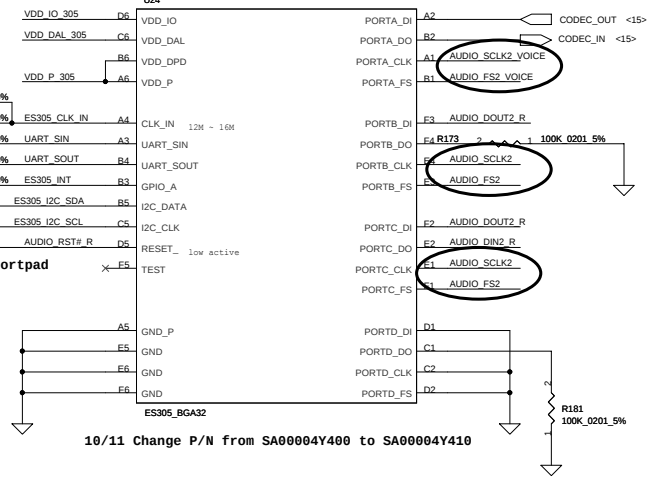


Audio Block Diagram



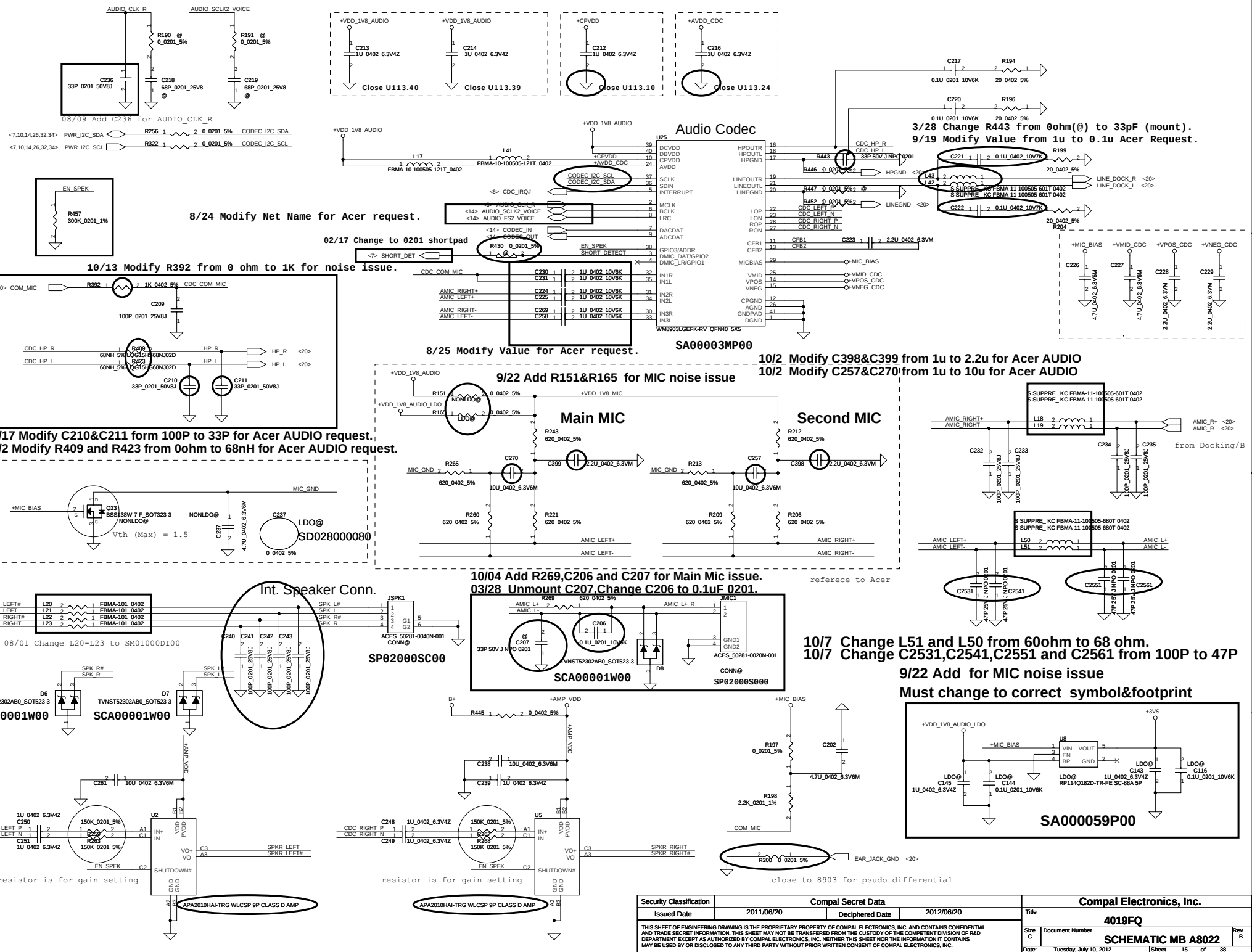
Active --> 17 mA
Sleep --> 35 uA

20110823 Modify for Acer request

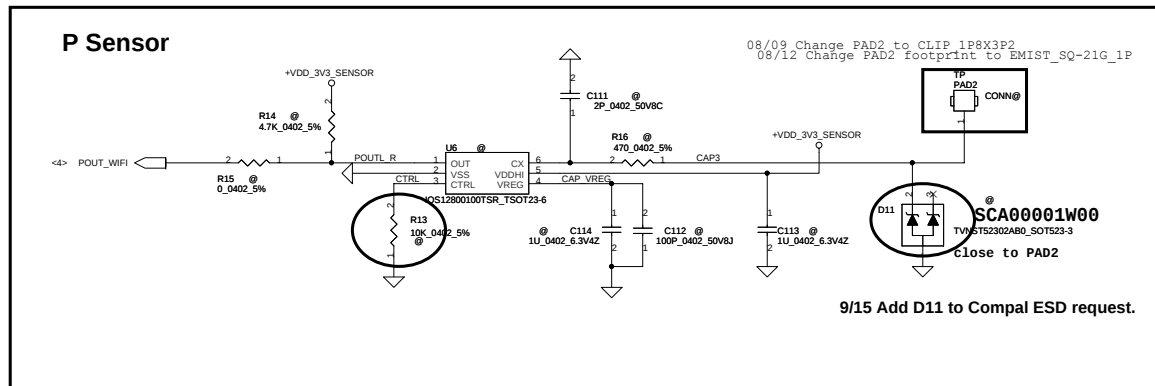
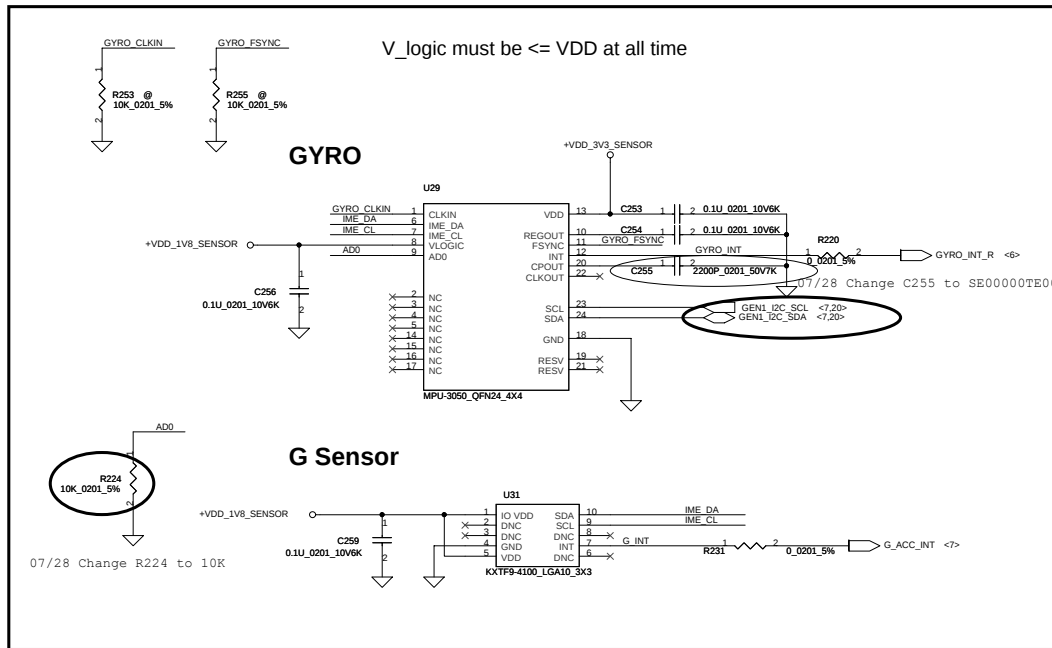


10/11 Change P/N from SA00004Y400 to SA00004Y410

Security Classification	Compal Secret Data	Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Title	4019FQ
Size	C	Document Number	SCHEMATIC MB A8022
Date	Tuesday, July 10, 2012	Sheet	14 of 38



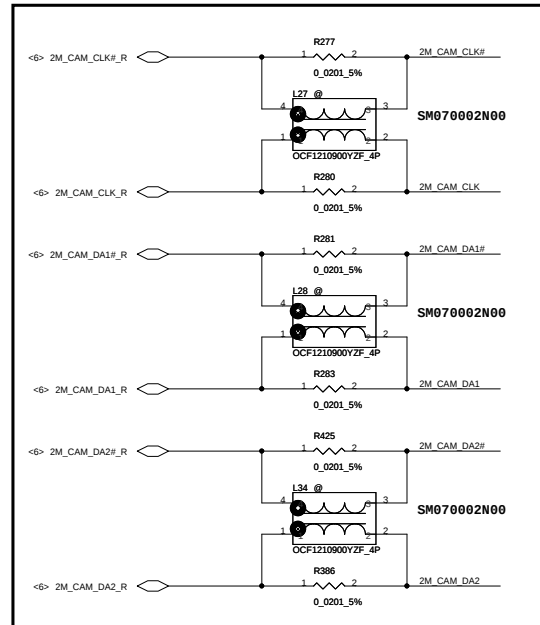
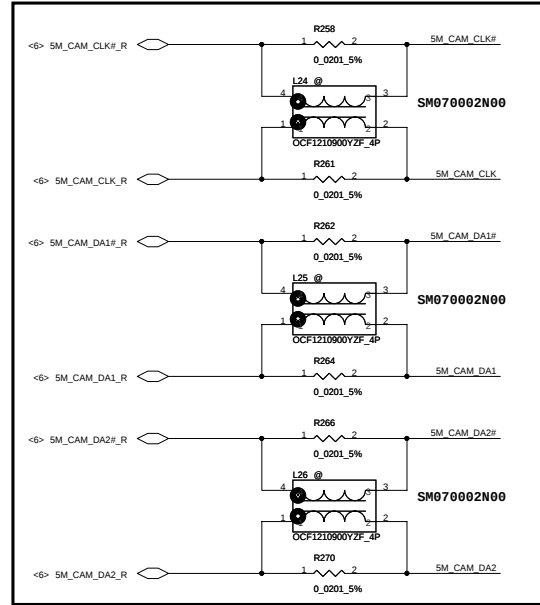
Security Classification		Compal Secret Data			Compal Electronics, Inc.			
Issued Date		2011/06/20	Deciphered Date	2012/06/20	Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					4019FQ			
					Size C	Document Number		Rev B
					SCHEMATIC MB A8022			
Date:		Tuesday, July 10, 2012		Sheet	15	of 38		



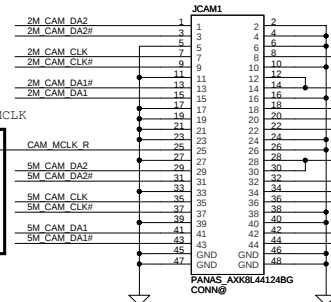
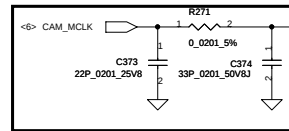
10/13 Modify R14,U6,C111,R16,D11,C113,C112 and C114 from WIFI @ to @ .

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				4019FQ
Size	C	Document Number	SCHEMATIC MB A8022	
Date:	Tuesday, July 10, 2012	Sheet	16	of 38

10/17 Change L24-L28,L34 to 0 ohm
08/05 Change L24-L28,L34 to mount
08/05 Change L24-L28,L34 to SM070002N00



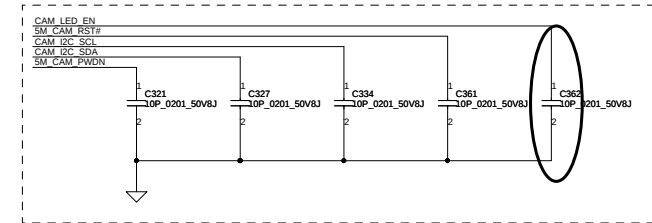
08/09 Add C373,C374,R271 for CAM_MCLK



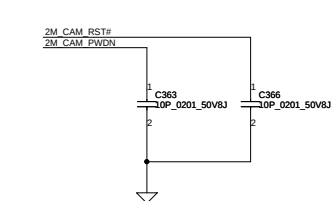
LTCX003I300

Camera Board

reference Acer schematic

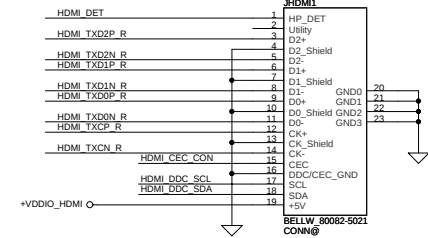
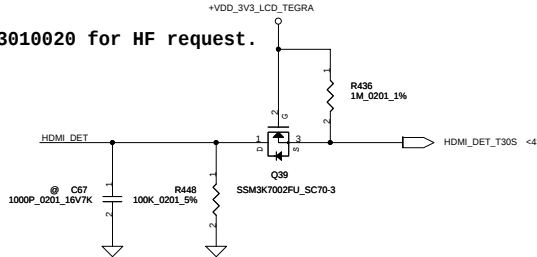
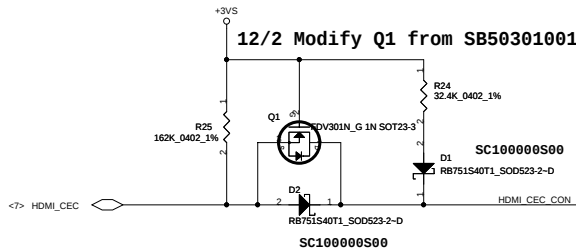
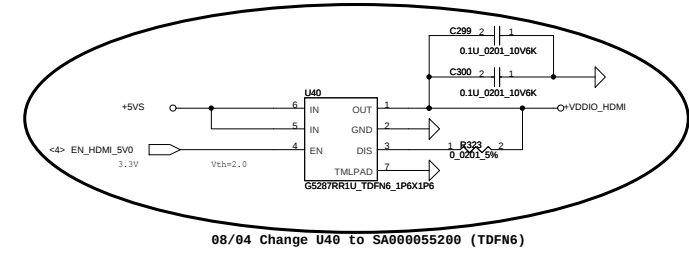
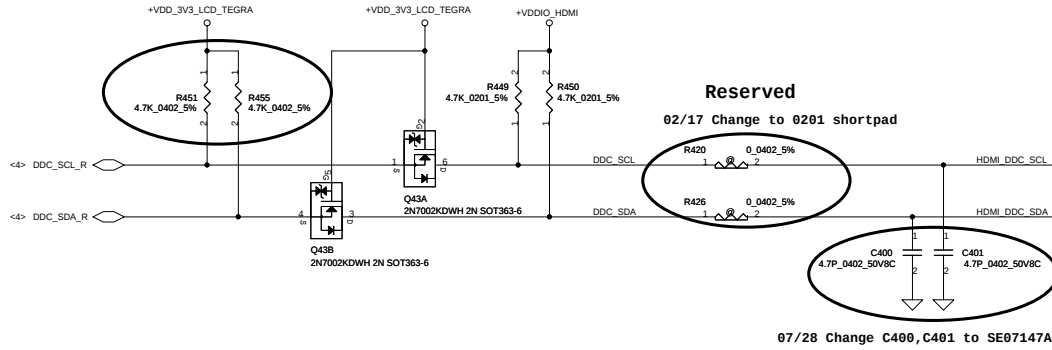


reference Acer schematic



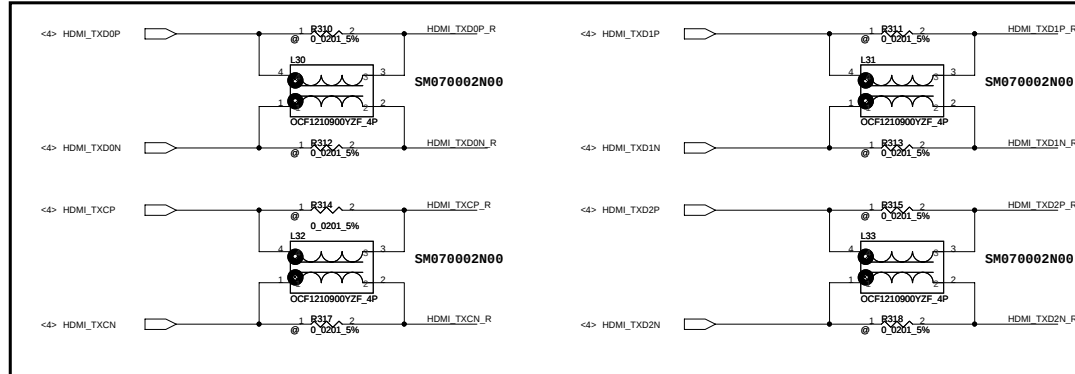
Security Classification	Compal Secret Data	Compal Electronics, Inc.
Issued Date	2011/06/20	Deciphered Date
Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		4019FQ
Size	C	Document Number
Document Number	SCHEMATIC MB A8022	Rev
Date	Tuesday, July 10, 2012	Sheet
Sheet	17	of
of	38	

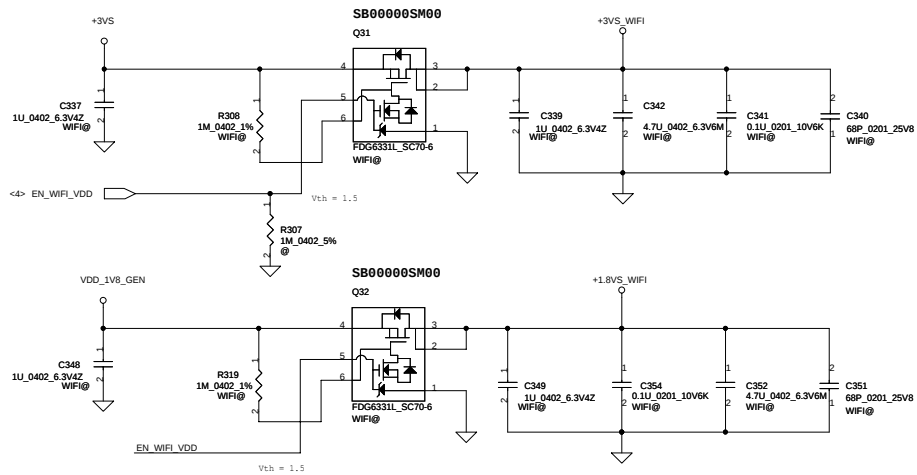
8/25 Modify R451&R455 from 47K 1% to 4.7K 5%



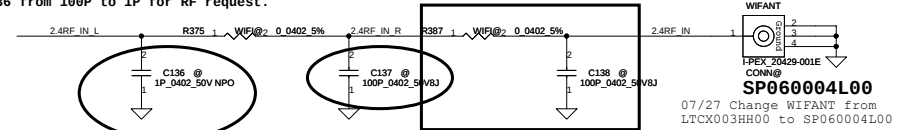
DC232001M10
HDMI Type D Connector

08/05 Change L30-L33 to SM070002N00



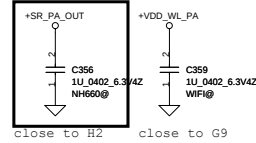


dual-Band wifi RF matching , Reserve " 2 Pi " filter
9/10 Modify C136 from 100P to 1P for RF request.



07/27 Change WIFANT from LTCX003HH00 to SP060004L00

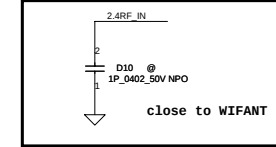
AH662 use 1U, AH663 use 0.1U



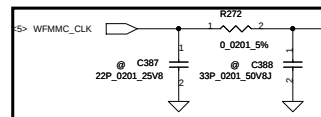
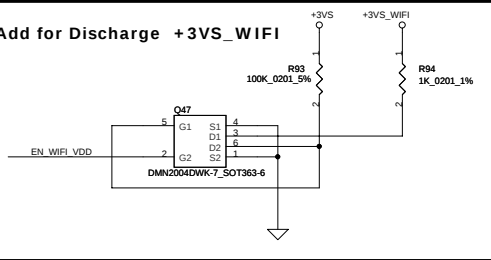
8/23 Add for AH663.



9/21 Add D10 to Compal ESD request.



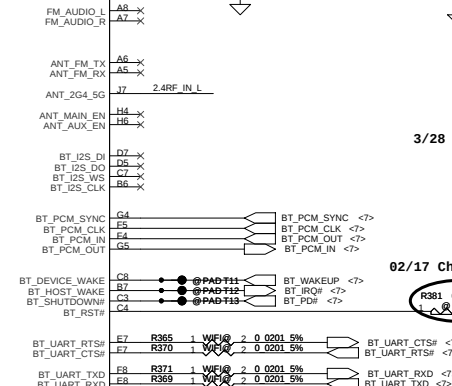
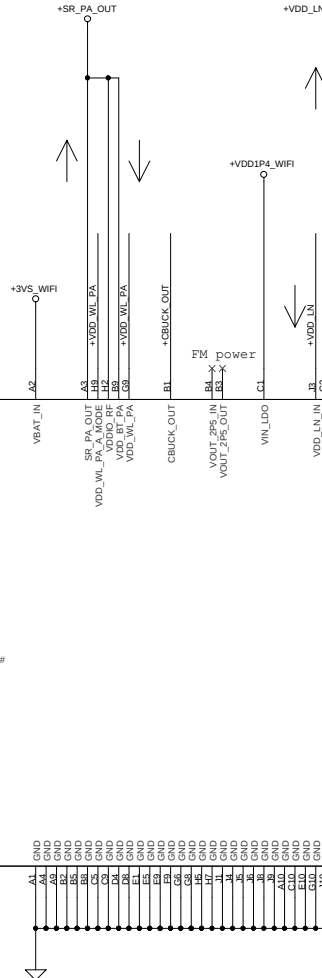
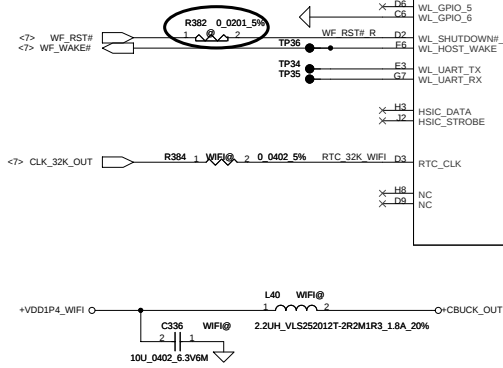
10/19 Add for Discharge +3V5_WIFI



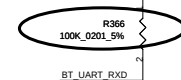
08/09 Add C387,C388,R272 for WFMCM_CLK
08/25 Modify C387&C388 from mount to unmount

F1,F2,F3,G2,G3 internal weak pull up resistor to VDDIO

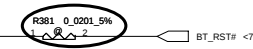
02/17 Change to 0201 shortpad



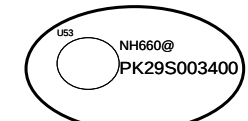
3/28 Change R366 from unmount to mount.



02/17 Change to 0201 shortpad



8/22 Change BOM structure



8/22 Change BOM structure

AH663@ AW-AH663_86P
PK29S003200
Footprint use AH662 co-lay

Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2011/06/20				4019FQ			
Deciphered Date				2012/06/20				SCHEMATIC MB A8022			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Date: Tuesday, July 10, 2012				Sheet 22 of 38			

```

VDD_1V8_GEN --- R395 1 2 0 0201 5% --->VDD_1V8_SDMCMC3_TEGRA
R397 1 2 0 0201 5% --->VDD_1V8_AUDIO_TEGRA
R399 1 2 0 0201 5% --->VDD_1V8_USB_PLL1_TEGRA
R401 1 2 0 0201 5% --->VDD_1V8_CAM_TEGRA
R404 1 2 0 0201 5% --->VDD_1V8_BB_TEGRA
R406 1 2 0 0201 5% --->VDD_1V8_SYS_TEGRA
R398 1 2 0 0402 5% --->VDD_1V8_DDR_MEM
R101 1 2 0 0402 5% --->VDDIO_1V8_EMMC
R157 1 2 0 0402 5% --->VDD_1V8_AUDIO
R390 1 2 0 0201 5% --->VDD_1V8_SDMCMC4_TEGRA
R402 1 2 0 0201 5% --->VDD_1V8_UART_TEGRA

```

12/08 Add U51, C365, C385 and R437 for EMMC

Device	Pin	Signal	Frequency	Current
VDD_PMU_LD03	R442	2 0 0.4002 5%	→VDD_3V3_SDMCM1_TEGRA	10mA
VDD_PMU_LD08	R412	2 0 0.4002 5%	→VDD_1V0_DDR_HS_TEGRA	30mA
VDD_PMU_LD06	F10	2 0 0.4002 5%	→AVDD_1V2_DSI_CSI_TEGRA	56mA
VDD_PMU_LD07	R411	2 0 0.4002 5%	→AVDD_1V1_PL3_TEGRA	54mA
VDD_PMU_LD04	R407	2 0 0.4002 5%	→VDD_1V2_RTC_TEGRA	20mA
VDD_PMU_LD01	R422	2 0 0.4002 5%	→VDD_2V8S_EMMC	200mA
VDD_PMU_LD05	R408	2 0 0.4002 5%	→VDD_VCM3_V3	135mA
VDD_PMU_LD02	R441	2 0 0.4002 5%	→VDD_3V_3SD	475mA
VDD_1V2_MEM	R444	2 0 0.4002 5%	→VDD_1V2_DDR_MEM	45mA

The schematic diagram shows the PMU LDO1 circuit. It starts with a +3V5 input connected to the VIN pin (pin 1) of the US1 chip. A 2.2uF 0.402 6.3V6M capacitor (C365) is connected between the VIN pin and ground. The GND pin (pin 2) of the US1 chip is connected to ground. The EN pin (pin 3) of the US1 chip is connected to the VDD_PMU_LDO1 signal. The VOUT pin (pin 5) of the US1 chip is connected to a node that has a 1uF 0.402 6.3V4Z capacitor (C365) connected to ground. This node is also connected to a 0.0402 5% resistor (R437), which is then connected to the +VDD_2V8S_EMMC output.

10/04 Add R19 and Q46 to Discharge +VDD_CAM_1V8.

Key components and values shown in the schematic:

- Q26:** ME2301A-G_SOT23-3
- Q46:** TR DMN315QLW-7 1N SOT-323
- Q25B:** DMN2140WK-7
- R19:** 2.2K_0201_1%
- R300:** 100K_0201_5%
- R326:** 200K_0201_1%
- R3:** 100K
- C33:** 1U_0402_6.3V4Z
- C384:** 0.1U_0201_10V6K
- FUSE_3V3:** 3.3V

The diagram also shows a red oval highlighting a section of the circuit and a callout for the addition of R19 and Q46 to discharge +VDD_CAM_1V8.

Modify R330 to meet SPEC sequence

<?> EN_SENSOR_IVB#

R330
47K_0201_1%

C390 @
0.1U_0201_10V6K

Q35
ME2201A-G_SOT23-3

VDD_IVB_GEN

+VDD_IVB_SENSOR

S B 0 0 0 0 0 J L 0 0

C160
U1_0402_6.3V4Z

10/07 Modify C390 from m

9/27 Add EN_SENSOR_3V3_2 from U1.AA3
9/29 Modify R424 from unmout to mount
9/29 Modify R427 from mout to unmount

H8
H₂20N


FD1
FIDUCIAL_C40M80


FD2
FIDUCIAL_C40M80

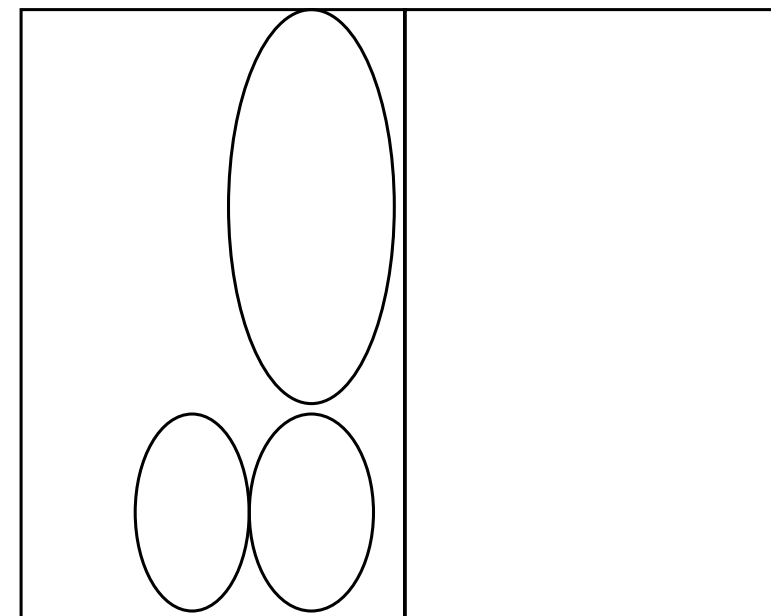

FD3
FIDUCIAL_C40M80


FD4
FIDUCIAL_C40M80

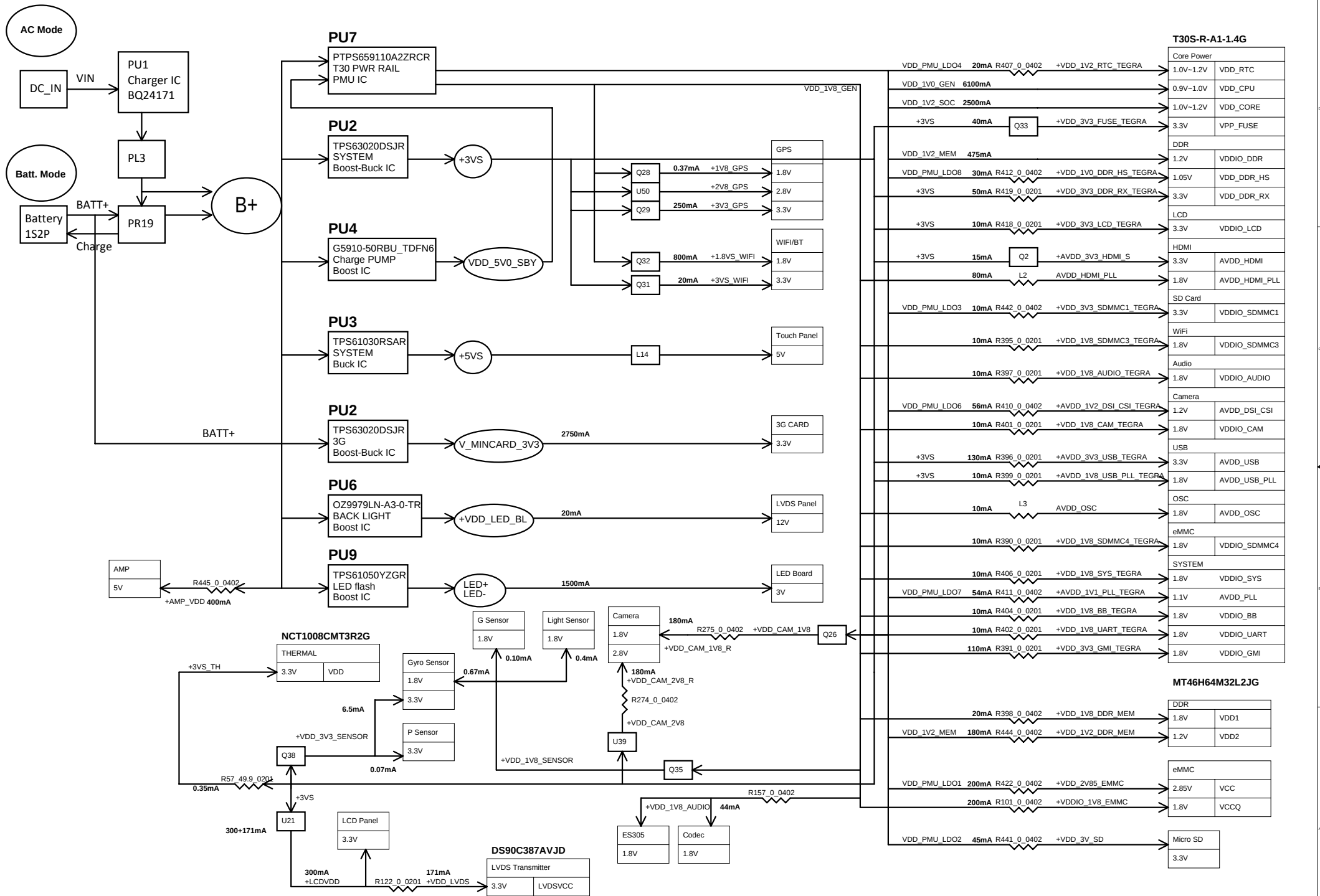

```

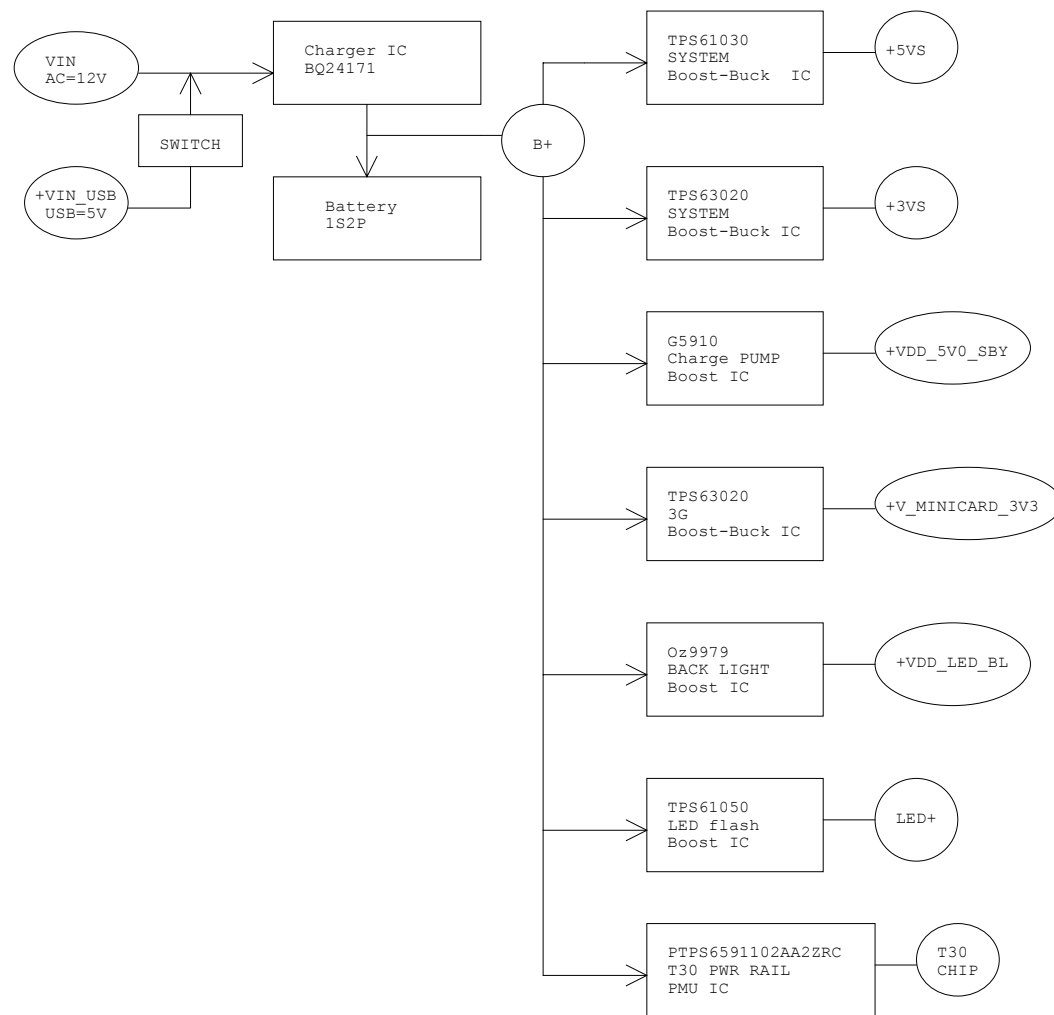
12/19 Del all CLIP for ME request.
9/2 Add CLP22-CLP27
9/2 Add CLP28,CLP29
11/8 Del CLP22-CLP27 for ME request
Clip type conn      BOTTOM

```



Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				4019FQ		
				Size	Document Number	
				SCHEMATIC MB A8022		
				Date:	Tuesday, July 10, 2012	Sheet 23 of 38





Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2011/06/20	Deciphered Date	2012/06/20	Title
					4019FQ
					Size C Document Number
					SCHEMATIC MB A8022 Rev B
					Date: Tuesday, July 10, 2012 Sheet 25 of 38

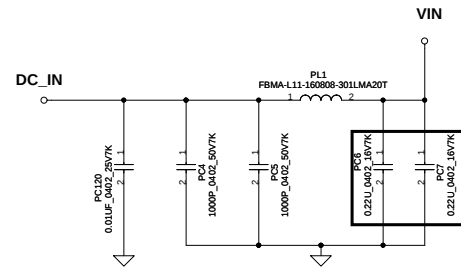
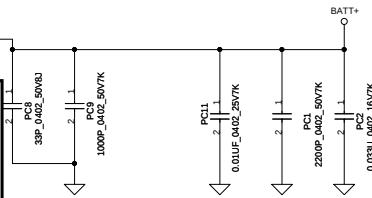
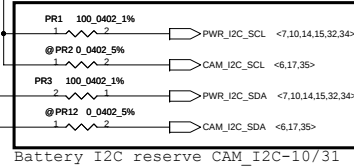
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

SP02000V310

ACES_88231-09001

GND
GND
11
10
9
8
7
6
5
4
3
2
1

@P3P1

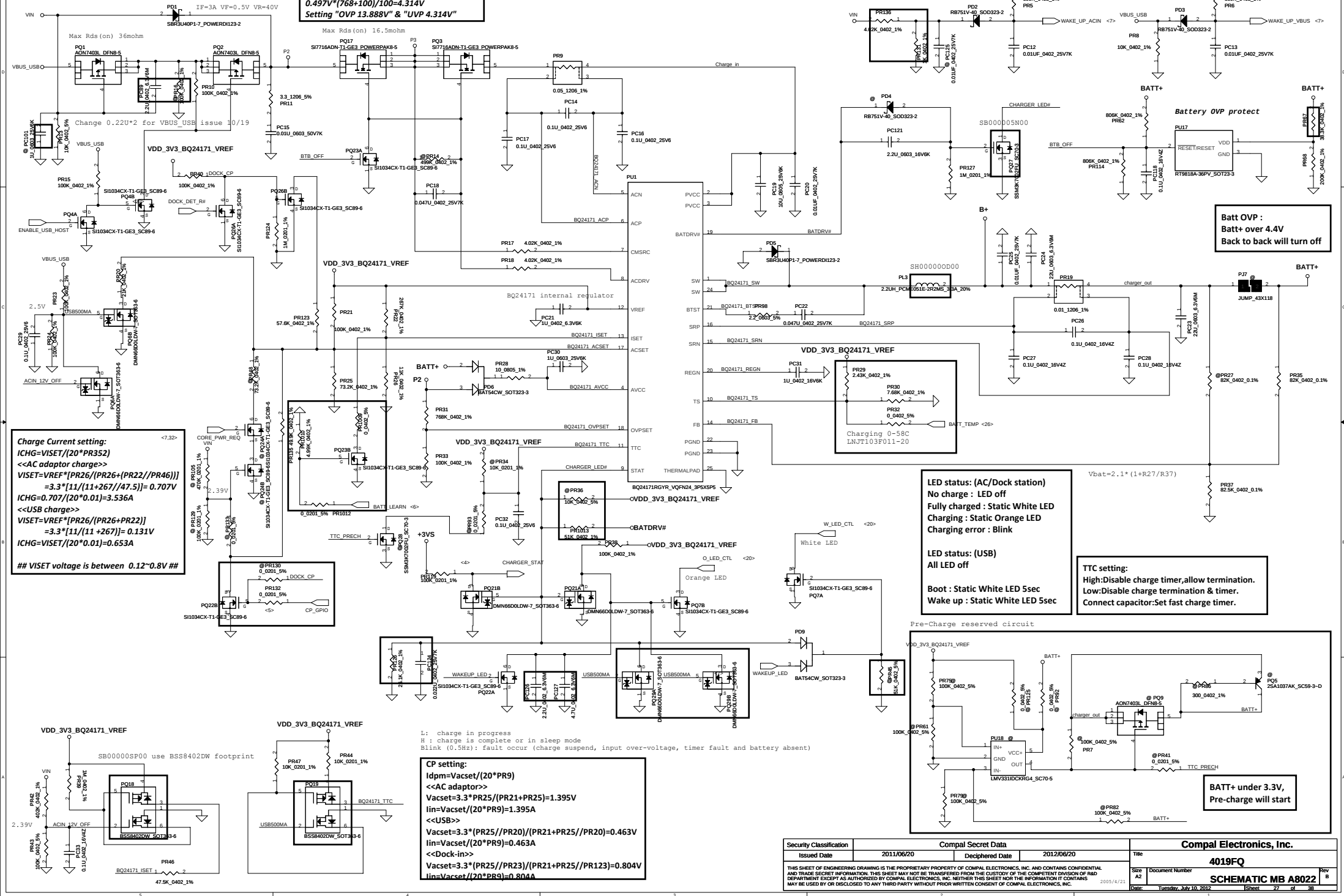


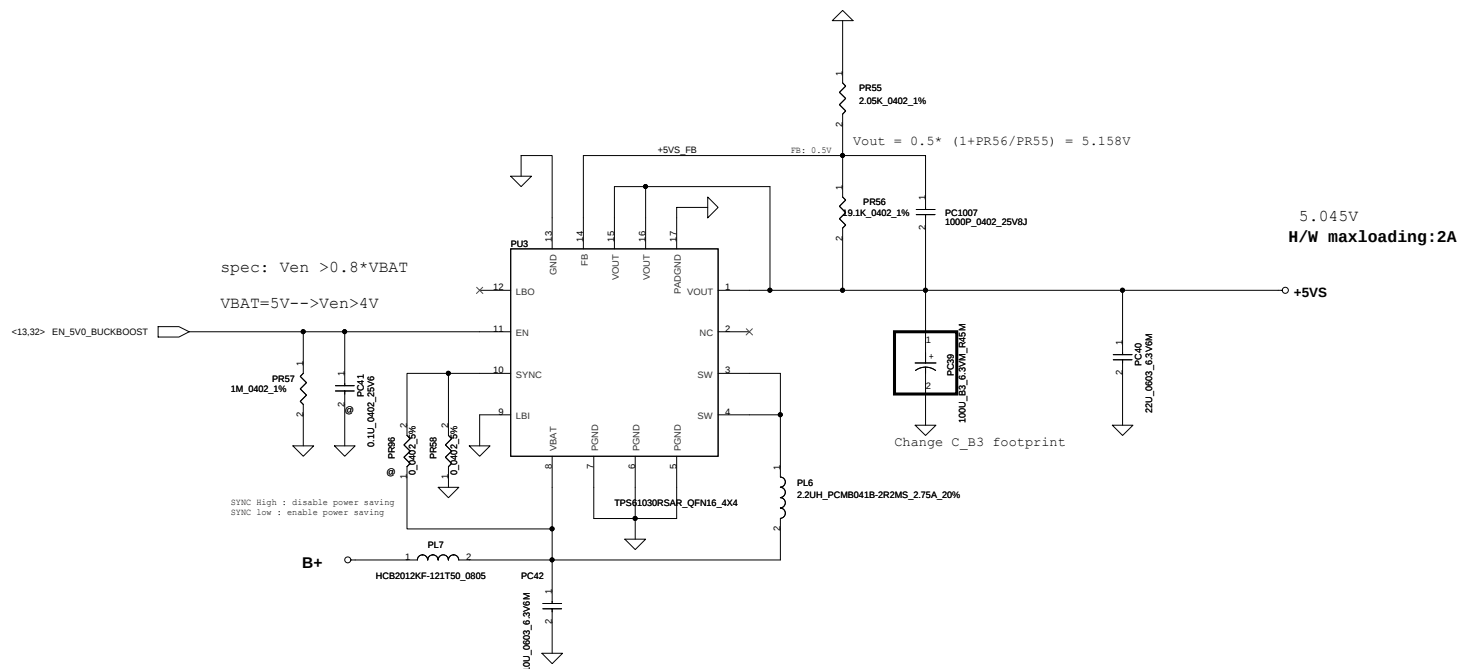
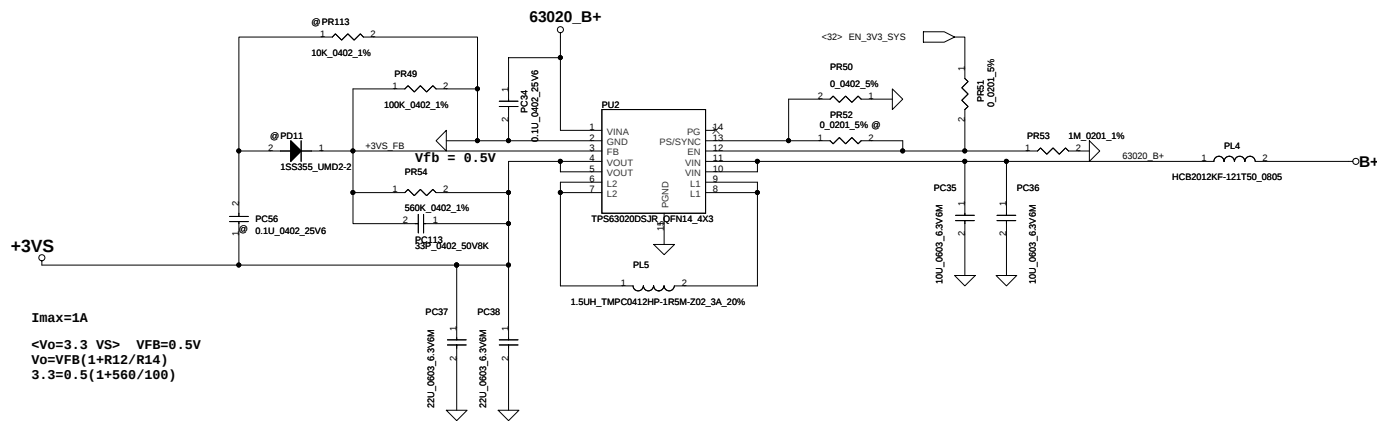
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				4019FQ
Size	C	Document Number	SCHEMATIC MB A8022	
Date:	Tuesday, July 10, 2012	Sheet	26	of 38

CHARGER BQ24171

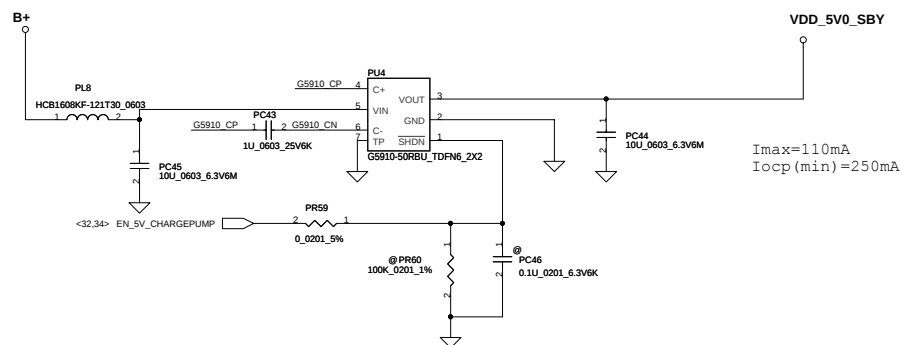
OVP UVP Range setting:
##OVPSET voltage is between 0.497~1.6V ##
1.6V*(768+100)/100=13.888V
0.497V*(768+100)/100=4.314V
Setting "OVP 13.888V" & "UVP 4.314V"

AC Insert or remove --> Wake up T30S
 USB Insert or remove --> Wake up T30S
 SW to check if Acer USB --> Change charge current to 1A



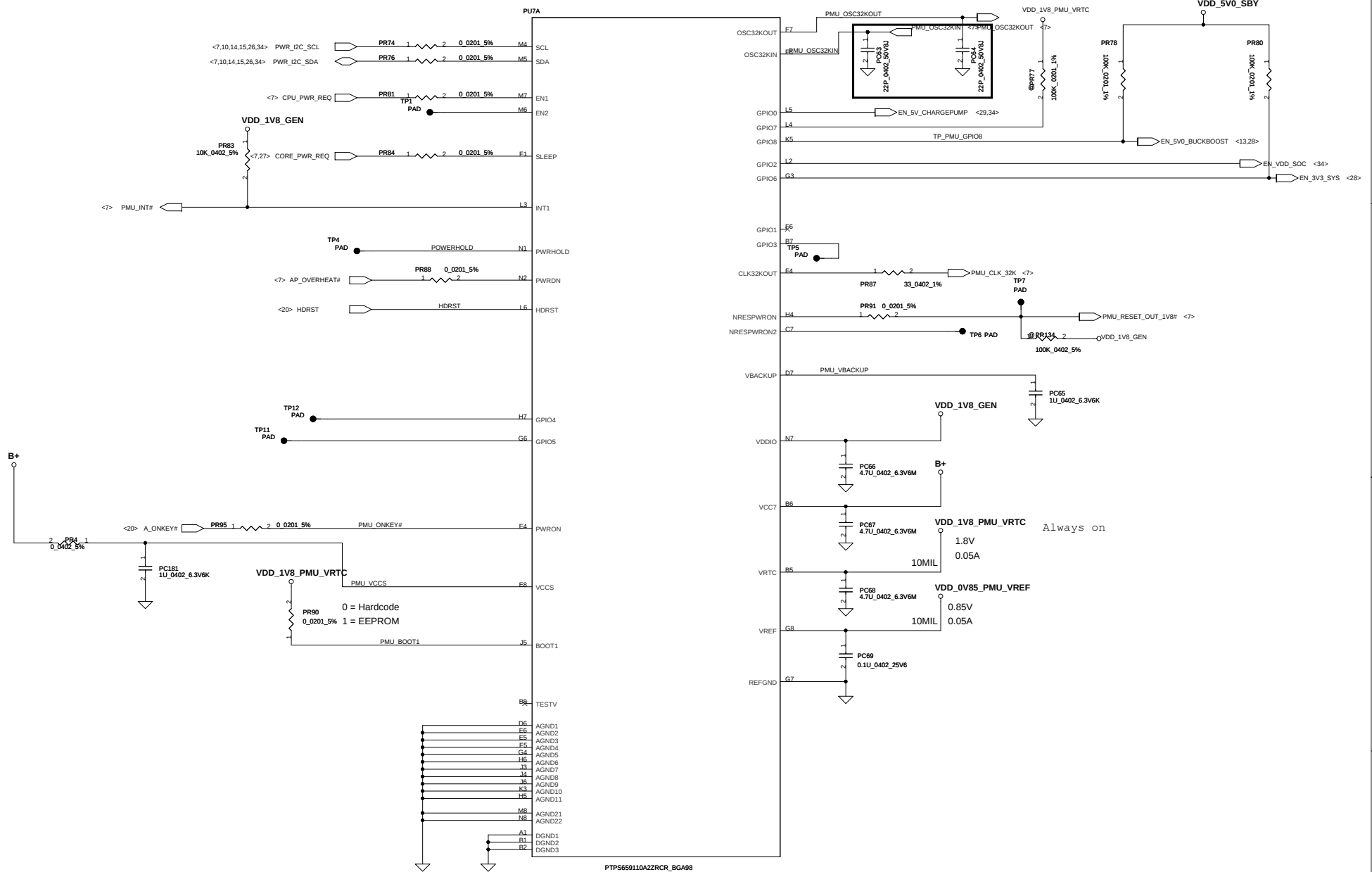
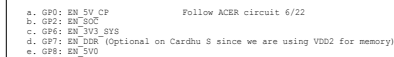


Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		2011/06/20	Deciphered Date	2012/06/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					4019FQ		
					Size	Document Number	Rev B
					SCHEMATIC MB A8022		
					Date:	Tuesday, July 10, 2012	
					Sheet	28 of 38	



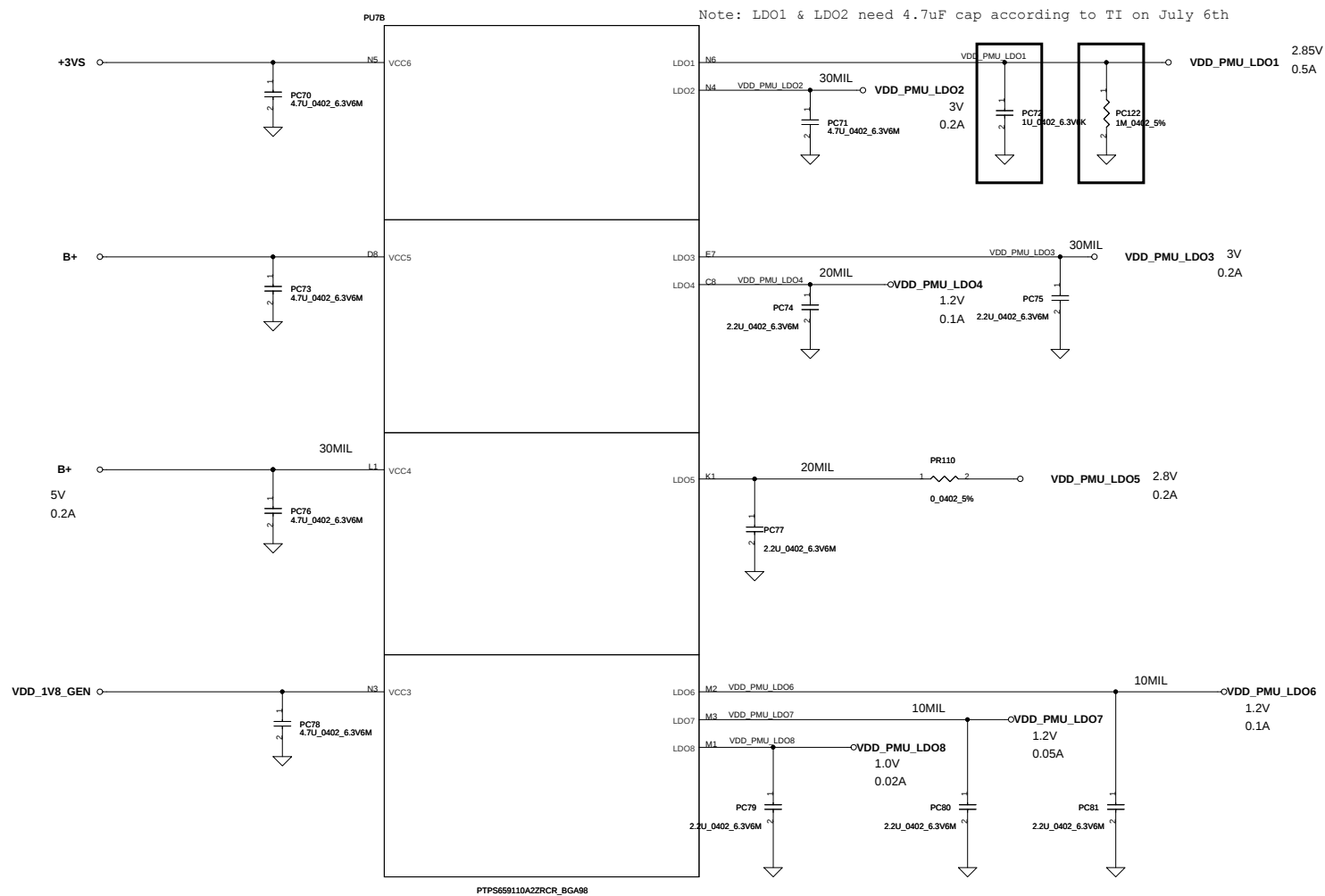
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2011/06/20	Deciphered Date	2012/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Document Number		4019FQ	Rev B
Date: Tuesday, July 10, 2012		Sheet 29 of 38		SCHEMATIC MB A8022	

PMU-TPS65911 1/3

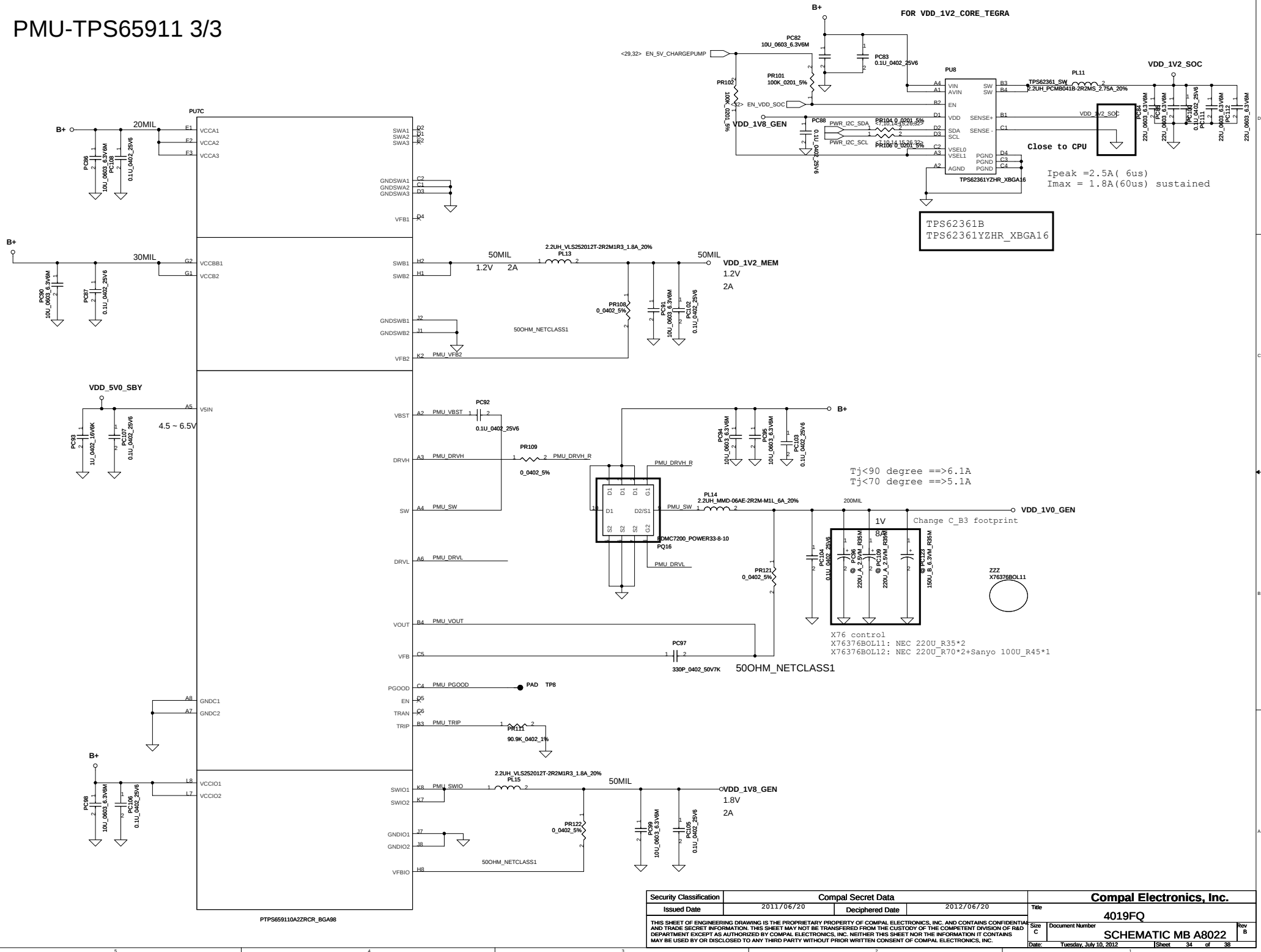


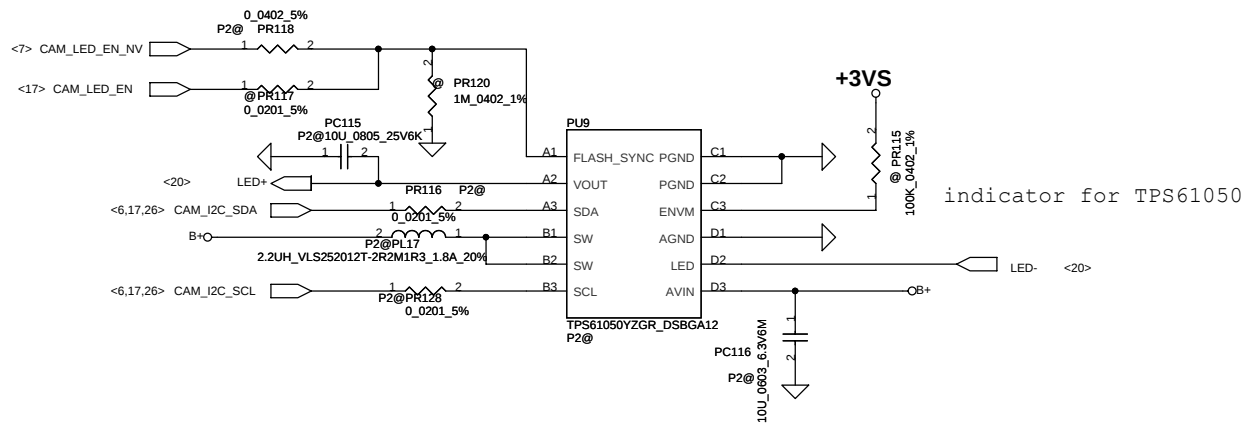
Security Classification		Compal Secret Data		Compal Electronics, Inc.					
Issued Date	2011/06/20		Deciphered Date	2012/06/20		Title	4019FQ		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number		Rev	B	
				SCHEMATIC MB A8022					
				Date	Tuesday, July 10, 2012		Sheet	32 of 38	

PMU-TPS65911 2/3



PMU-TPS65911 3/3





Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				4019FQ		
Size	A3	Document Number	SCHEMATIC MB A8022		Rev	B
Date:	Tuesday, July 10, 2012		Sheet	35	of	38

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	Date	Phase
1	Ripple un-stable	Loop stable for TPS63020	01	28 30	change PL5, PL9 from PCMB041B-2R2MS 2.75A to PCMB041B-1R5MS 3A,	20110806	EVT (QAJ70)
2		charger CP setting for adaptor protect	02	26	change PR9 form 0.01_1206_1% to 0.05_1206_1% change PR25 form 10K to 73.2K change PR20 form 3.83K to 21K Remove ACER USB setting circuit	20110809	DVT (QAJ50)
3		Net name error	02	35	change net name from VDD_1V8_SYS to VDD_1V8_GEN	20110809	DVT (QAJ50)
4		Material version change	02	35	change PQ16 from FDMC7200 2N POWER33-8 to FDMC7200S 2N POWER33-8	20110809	DVT (QAJ50)
5	GPIO only support 1.8V	Use low Vth dual N TR	02	26	change PQ4,PQ6,PQ7,PQ21,PQ21 from DMN66D0LDW-7_SOT363 to SI1034CX	20110809	DVT (QAJ50)
6		material change for common use	02	26	change PQ18 from BSS8402DW 1P/1N SOT363-6 to SI1553CDL-T1-GE3 1P/1N SC70-6	20110809	DVT (QAJ50)
7		No support Acer USB, delete related control circuit	02	26	Delete PQ19, PR40, PR44, PR47, PR41.	20110809	DVT (QAJ50)
8	Back-to-back MOS can't fully turn on	The BATDRV# pull low 1K will cause ACDRV abnormal	02	26	Change PR16, PR12, PD4 to reserve	20110809	DVT (QAJ50)
9	Charger will terminate	The TTC pull high 100K will cause wrong voltage level	02	26	Change PR34 from 100K to 10K	20110809	DVT (QAJ50)
10	Orange LED can't turn off	STAT pull high 100K can't drive TR	02	26	Change PR36 from 100K to 10Kohm	20110809	DVT (QAJ50)
11		Modify Battery OVP circuit to avoid AC turn off when Battery over discharge	02	26	Add PC118, PR144,PQ23. Delete PD7	20110809	DVT (QAJ50)
12	Pre-charge circuit can't work normally	Modify Pre-charge circuit	02	26	Change PR86,PR92,PR93 to PNP 2SA1037K	20110816	DVT (QAJ50)
13	Sub/B +5VS will drop to under 4.75V	Modify +5VS voltage setting to 5.15V	02	28	Change PU3 to TPS61030, PR56 to 19.1K, PR55 to 2.05K	20110824	DVT (QAJ50)
14		TPS63020 add 33P on feedback resistor for loop stable	02	28 30	Add PC113, PC114 33P	20110824	DVT (QAJ50)
15		V_MINCARD_3V3 sequence	02	30	Change PR65 from 1M_0402_1% to 100K_0402_1% Add PC1006 1U_0402_6.3V6K	20110824	DVT (QAJ70)
16		modify crystal cap	02	32	Change PC63, PC64 from 12P_0402_50V8J to 22P_0402_50V8J	20110825	DVT (QAJ70)
17		Add LED control circuit for wake up/boot define	02	26	Add PQ22 to turn off O_LED	20110825	DVT (QAJ70)

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title	4019FQ
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number SCHEMATIC MB A8022
				Date: Tuesday, July 10, 2012	Rev B Sheet 36 of 38

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	Date	Phase
1		Add for pre-charge and fast charge timeout	02	27	Add PQ19 ,PR44,PR47	20110826	DVT (QAJ70)
2		BOM control	02	27,31	Modify PD1, PD5, PD8 from SS3P4-M3-84A_SMP2 to SBR3U40P1	20110826	DVT (QAJ70)
3		footprint control	02	34,35	Modify PL13, PL15, PL17 footprint to TOKO_1239AS-H-2R2N-P2_2P	20110826	DVT (QAJ70)
4		Change CP setting for adaptor protect	02	27	Add PQ23	20110826	DVT (QAJ70)
5		Add VDD_1V0_GEN and VDD_1V8_GEN Fb resistor for test request	02	34	Add PR121, PR122	20110826	DVT (QAJ70)
6		EMI request	03	27	Change PL1 to 300ohm bead, add PC120, PR11, PC15, PC20, PR98, PC1,PC2 for EMI request	20110920	DVT2 (QAJ50)
7		Cancel the function	03	27	Delete PR12, PR16, PD4	20110920	DVT2 (QAJ50)
8		Reserve the CP control for thermal improve	03	27	Add PR48, PQ24 for reserve	20110920	DVT2 (QAJ50)
9		Dock in CP setting	03	27	Add PQ26 PR93 PR96 PR124 PR123 PR40	20110920	DVT2 (QAJ50)
10		Solve White LED flash issue when AC plug in	03	27	Add PD4,PC121,PR126,PR127,PQ27	20110920	DVT2 (QAJ50)
11		Battery OTP change to 55C	03	27	Change PR29 to 2.74K, PR30 to 8.87K	20110920	DVT2 (QAJ50)
12		remove pre-charge function	03		Reserved PU18, PQ9, PR7, PR61, PR79, PR86, PR41	20111006	DVT2 (QAJ50)
13		solve leakage current	03	27,31	Change PD1,PD5,PD8 from SBR3U40 to SS3P4; PR62, PR114 to 806K	20111006	DVT2 (QAJ50)
14		Thermal over temperature	04	28	Change PC39 to 100U_B3_6.3VM_R45	20111027	DVT2 (QAJ70)
15	VBUS leakage	PQ1 will turn on when AC plug out	04	26,27	Change PC6, PC7 to 0.22U. Add PC89 2.2U	20111027	PVT (QAJ50)
16		OTP change to 58C	04	27	Change PR29 to 2.43K, PR30 to 7.68K	20111027	PVT (QAJ50)
17		Modify battery OVP to 4.4V	04	27	Change PR67 to 38.3K	20111105	PVT (QAJ50)

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title	4019FQ
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number SCHEMATIC MB A8022
				Date: Tuesday, July 10, 2012	Rev B
				Sheet 37 of 38	

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	Date	Phase
1		Turn off charge LED when USB in	04	27	Add PQ29, PC126, PC127	20111210	PVT (QAJ50)
2	White LED can't light when power on	Don't need the component	04	27	Change PR45 to reserve	20111210	PVT (QAJ50)
3		Adjust CP setting when power on for thermal issue	04	27	Add PQ22, PR132, PR135	20111210	PVT (QAJ50)
4	Battery can't be charged when dock in	Modify the battery capacity control for EN_USB_HOST to BATT_LEARN	04	27	Add PR1012, change P135 to 4.99K	20111210	PVT (QAJ50)
5	T30S WAKE_UP_ACIN damage	Avoid WAKE_UP_ACIN in-rush voltage	04	27	Add PR136, PR131	20111210	PVT (QAJ50)
6		Modify charge output voltage to improve battery capacity	04	27	Change PR35 to 82K and PR31 to 82.5K	20111210	PVT (QAJ50)
7		Avoid white LED will flash 1s when AC/USB plug in/out	04	27	Change PD4 to reserve, Add PR1013, PR126, PC124	20111210	PVT (QAJ50)
8		Modify crystal cap value	04	32	Change PC63, PC64 to 22P	20111210	PVT (QAJ50)
9	EMMC voltage drop	PMIC LDO1 compensation	04	33	Change PC72 to 1U	20111210	PVT (QAJ50)
10		Avoid EMMC external LDO floating	04	33	Add PC122 1M	20111219	PVT (QAJ50)
11							
12							
13							
14							
15							
16							
17							

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/20	Deciphered Date	2012/06/20	Title	4019FQ
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	SCHMATIC MB A8022
Date: Tuesday, July 10, 2012				Sheet	38 of 38

www.s-manuals.com