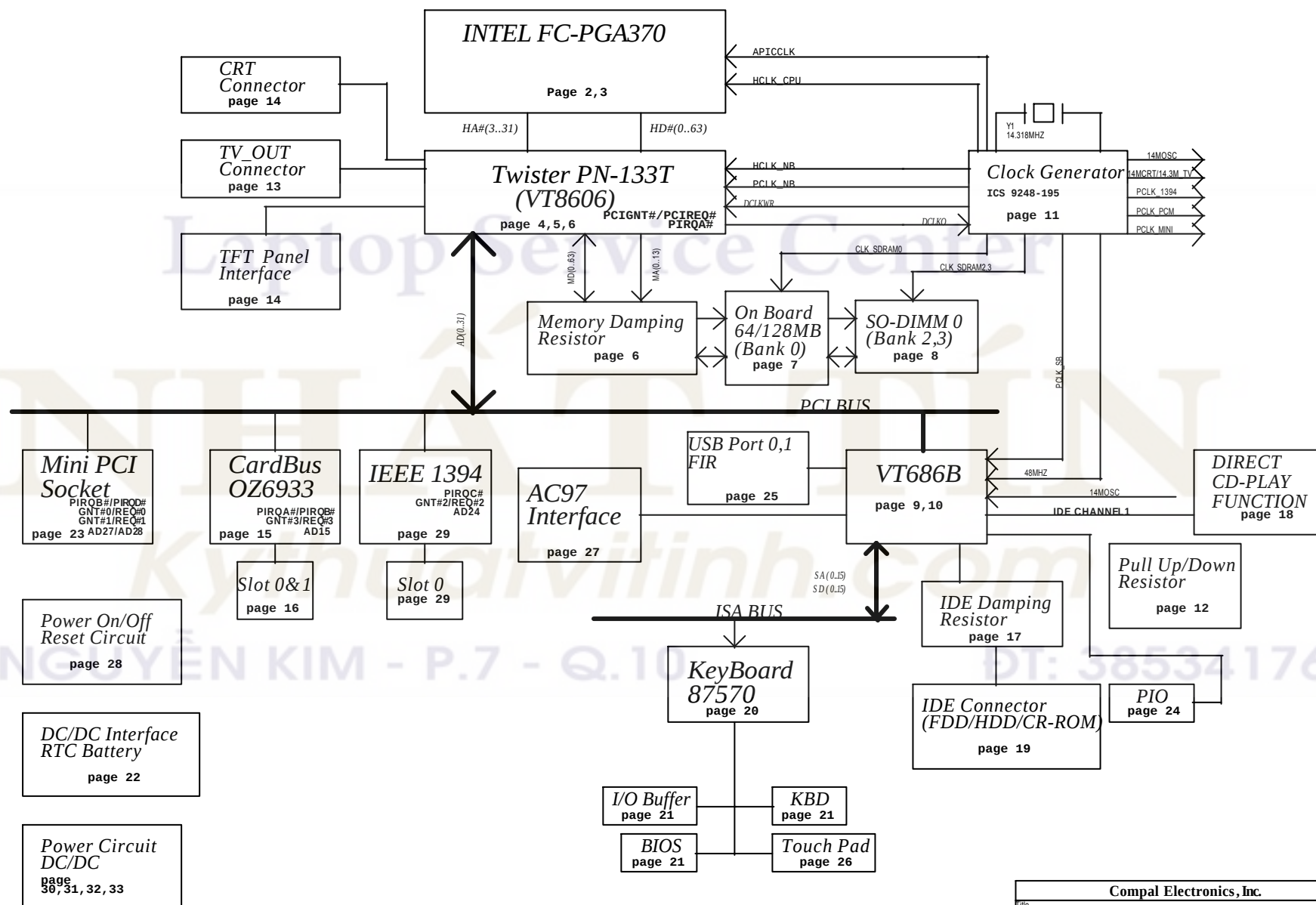


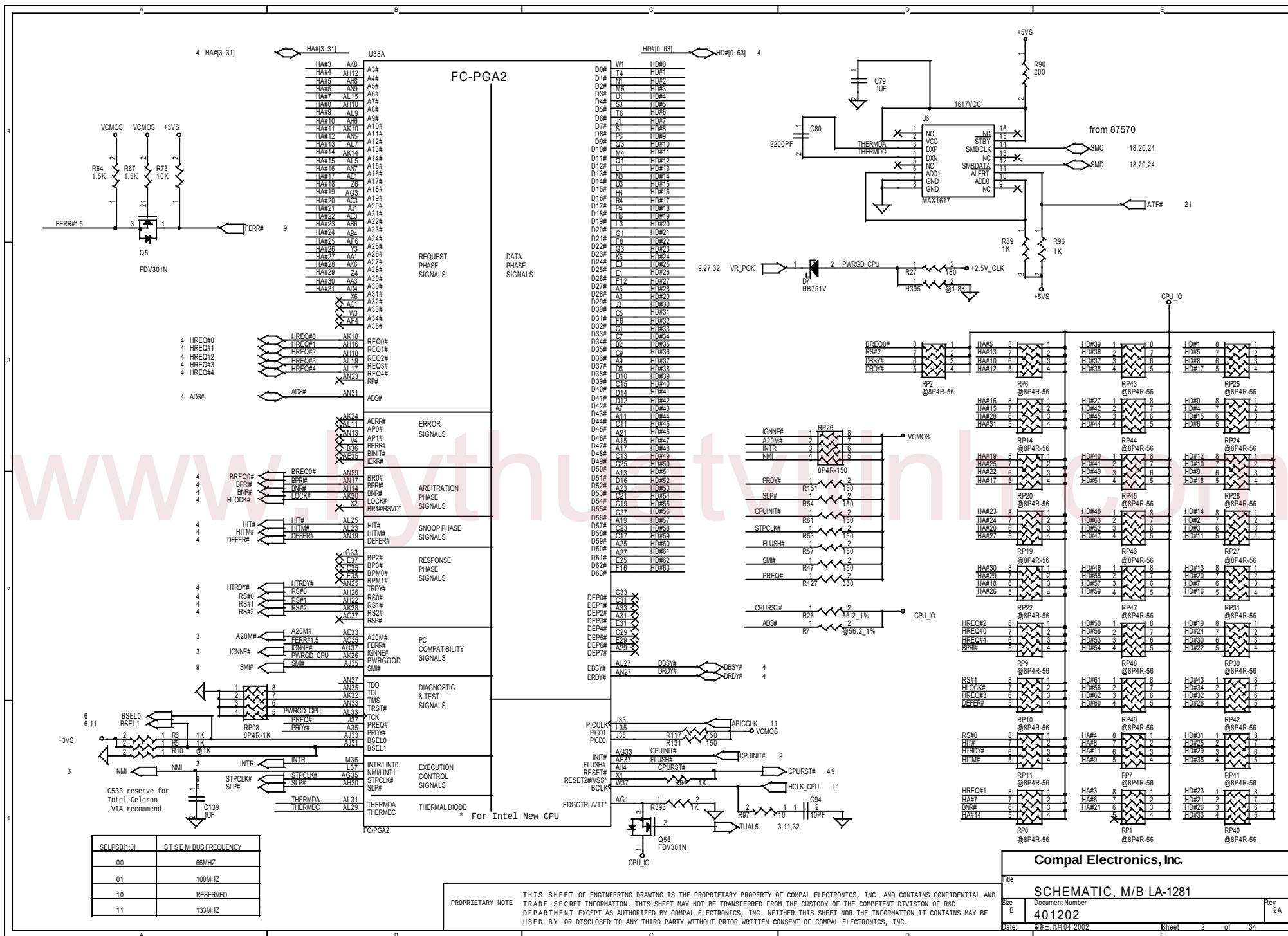
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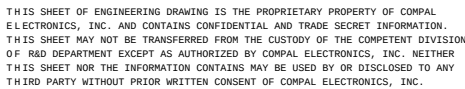


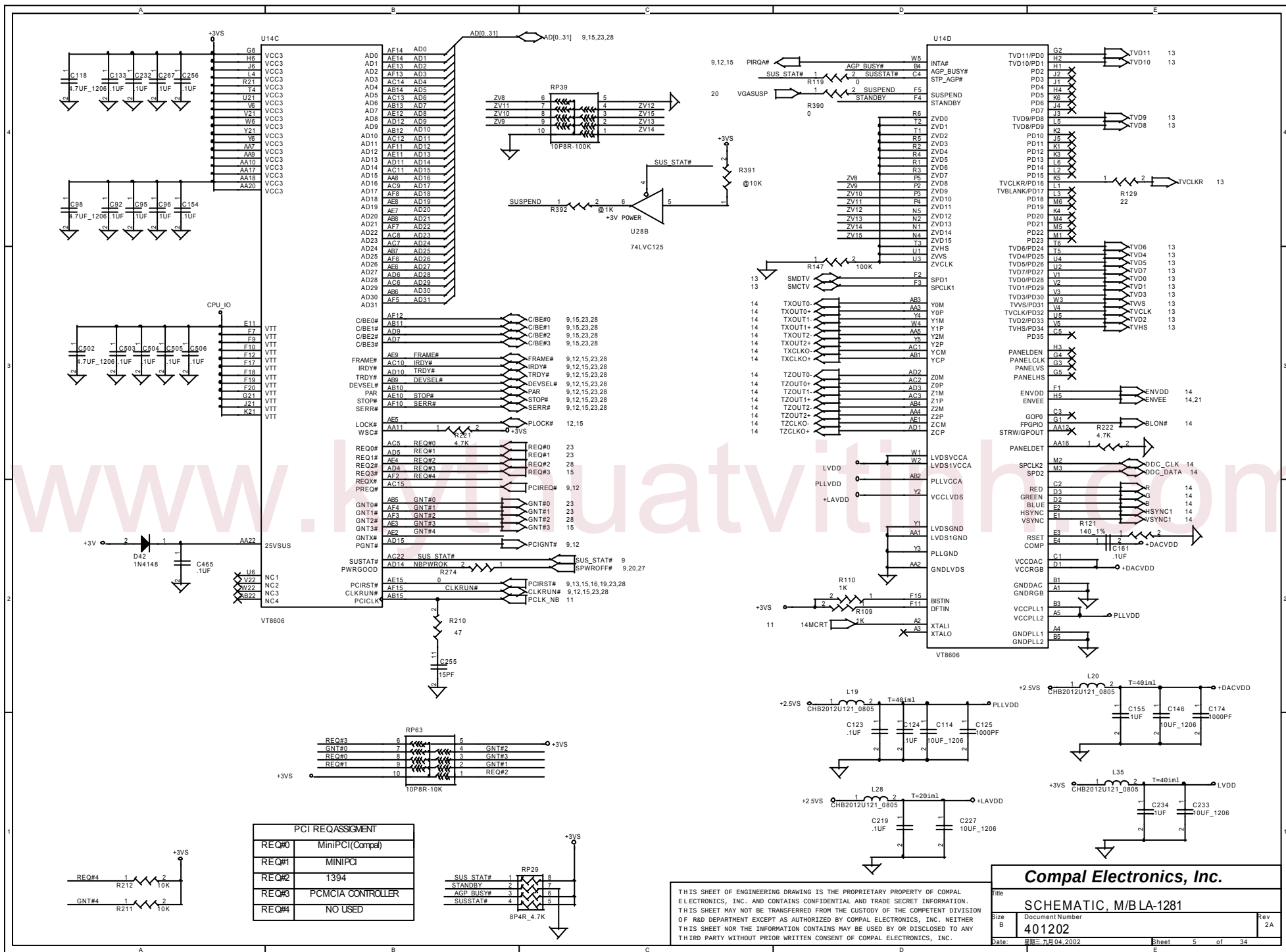
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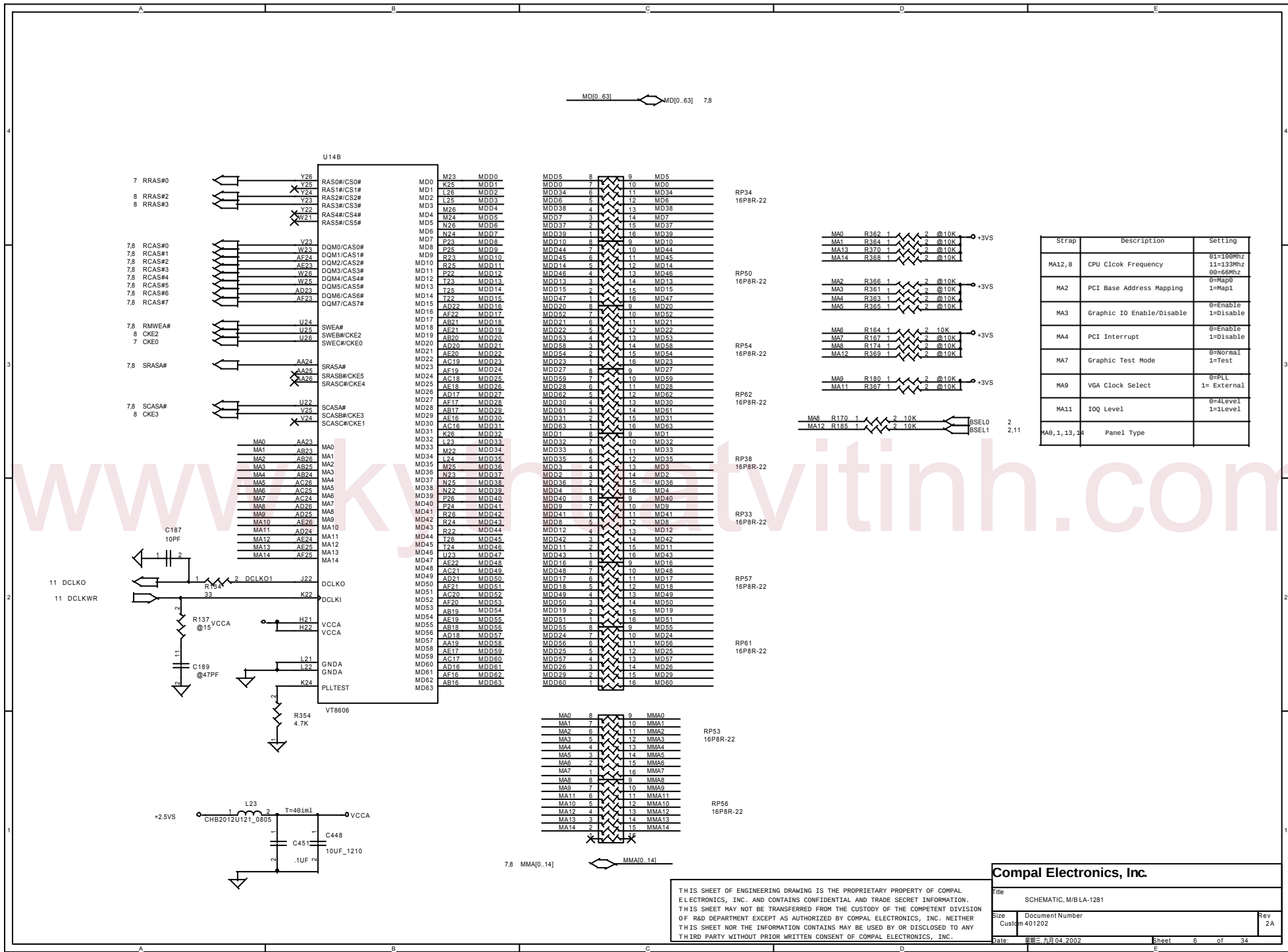


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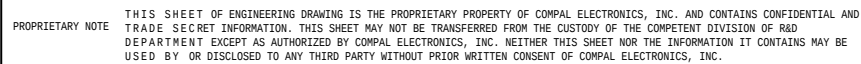
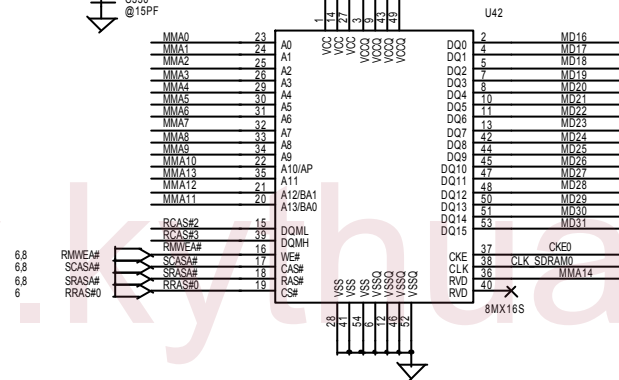
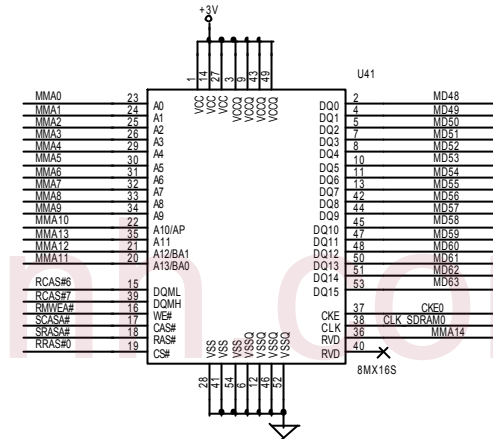


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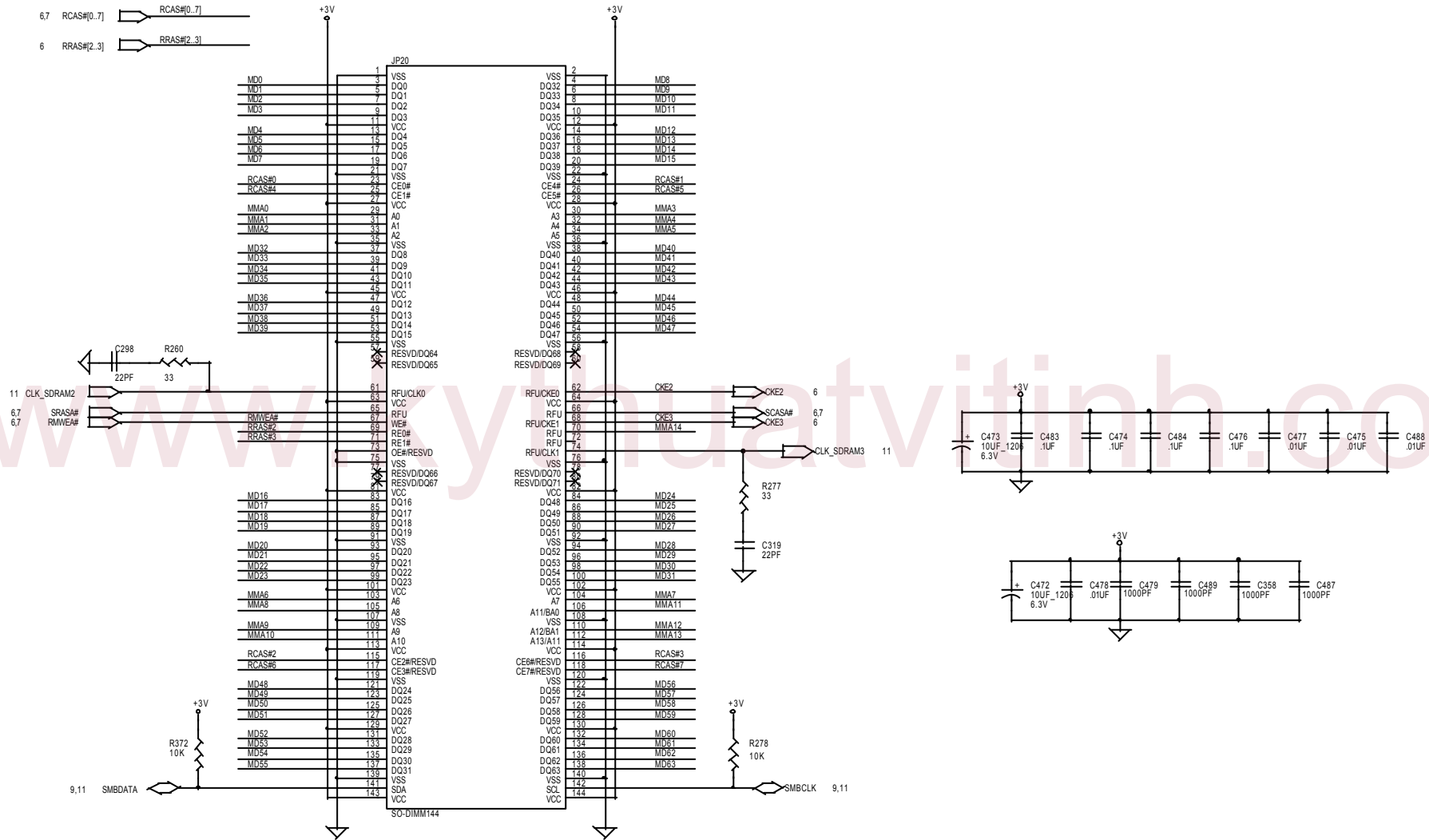
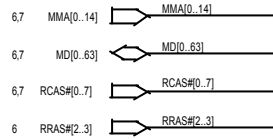
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64/128MB SDRAM



SO-DIM 144 PINS
RAM MODULE CONN.

BANK2/3



DIMM1

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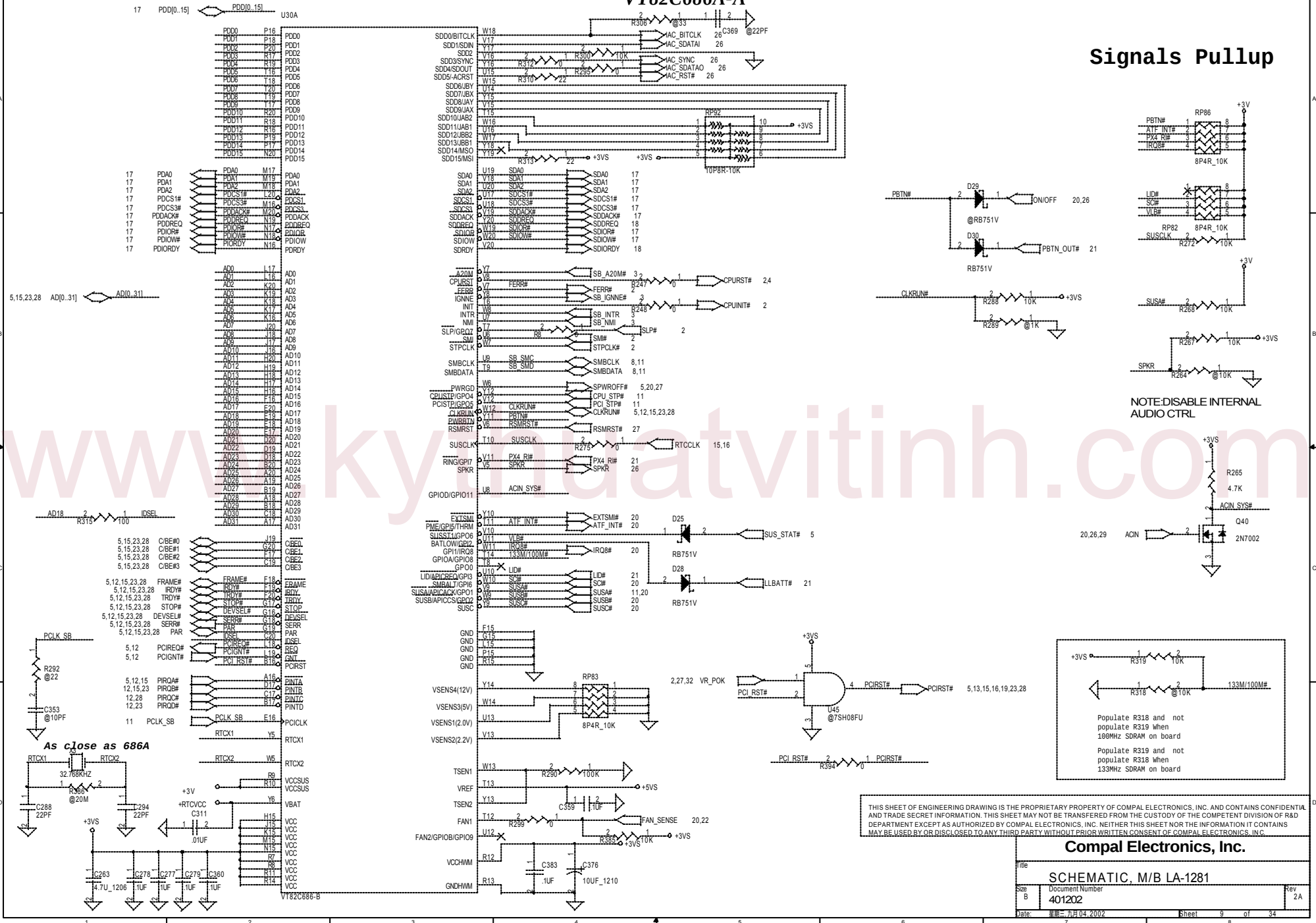
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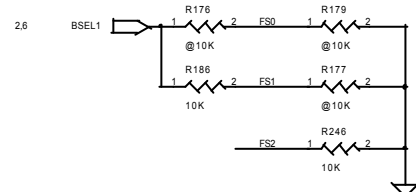
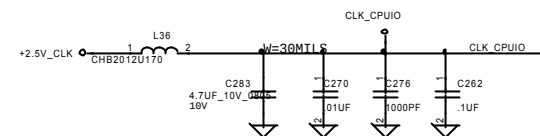
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Signals Pullup



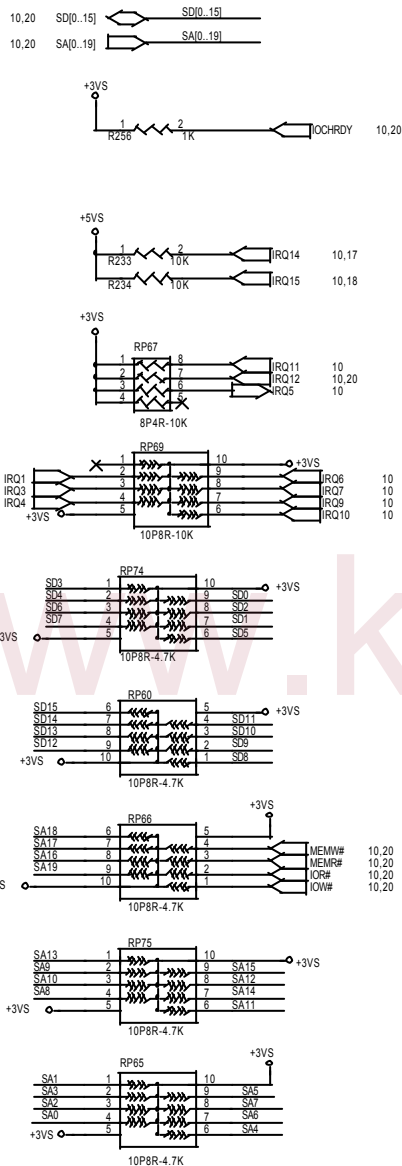
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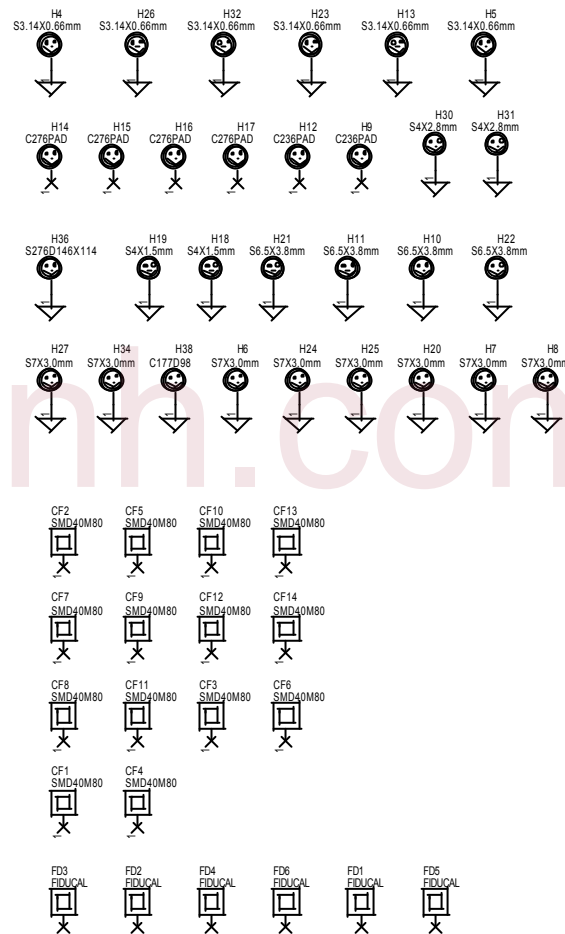
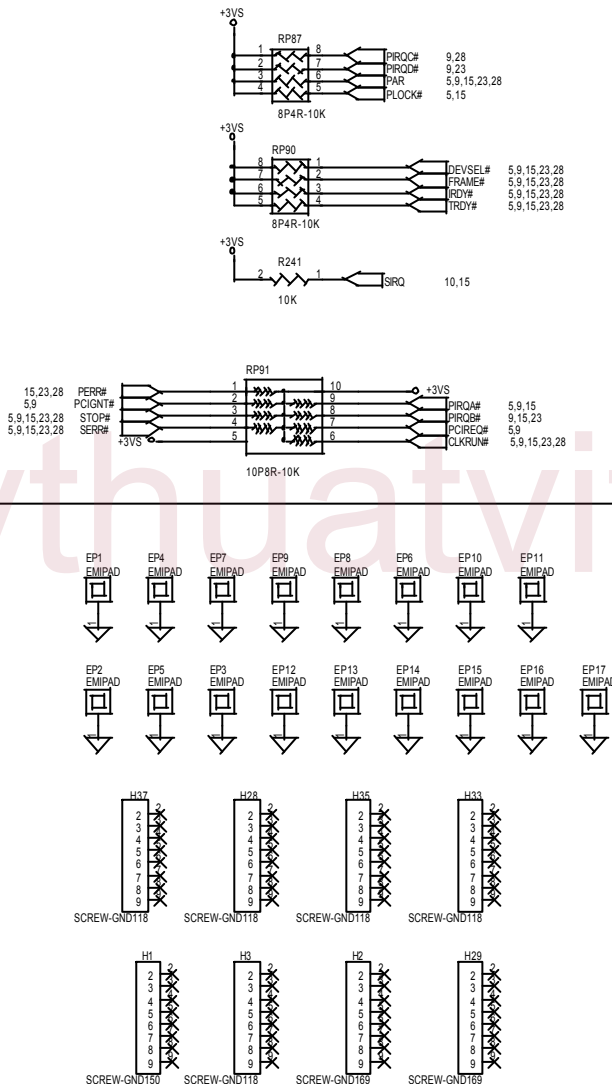


Timing diagram for the FDV301N device. The diagram shows three input signals: EARLY CLOCK, 3.3V CPU, and SPREAD SPECTRUM, each connected to a resistor (R220, R227, R218, R226) and then to the +3VS supply. The signals are labeled with their respective pin numbers and resistor values.

ISA BUS Pullup



PCI BUS Pullup



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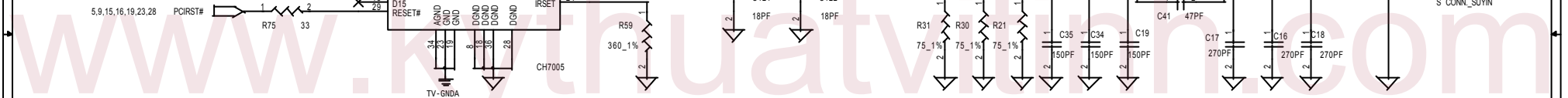
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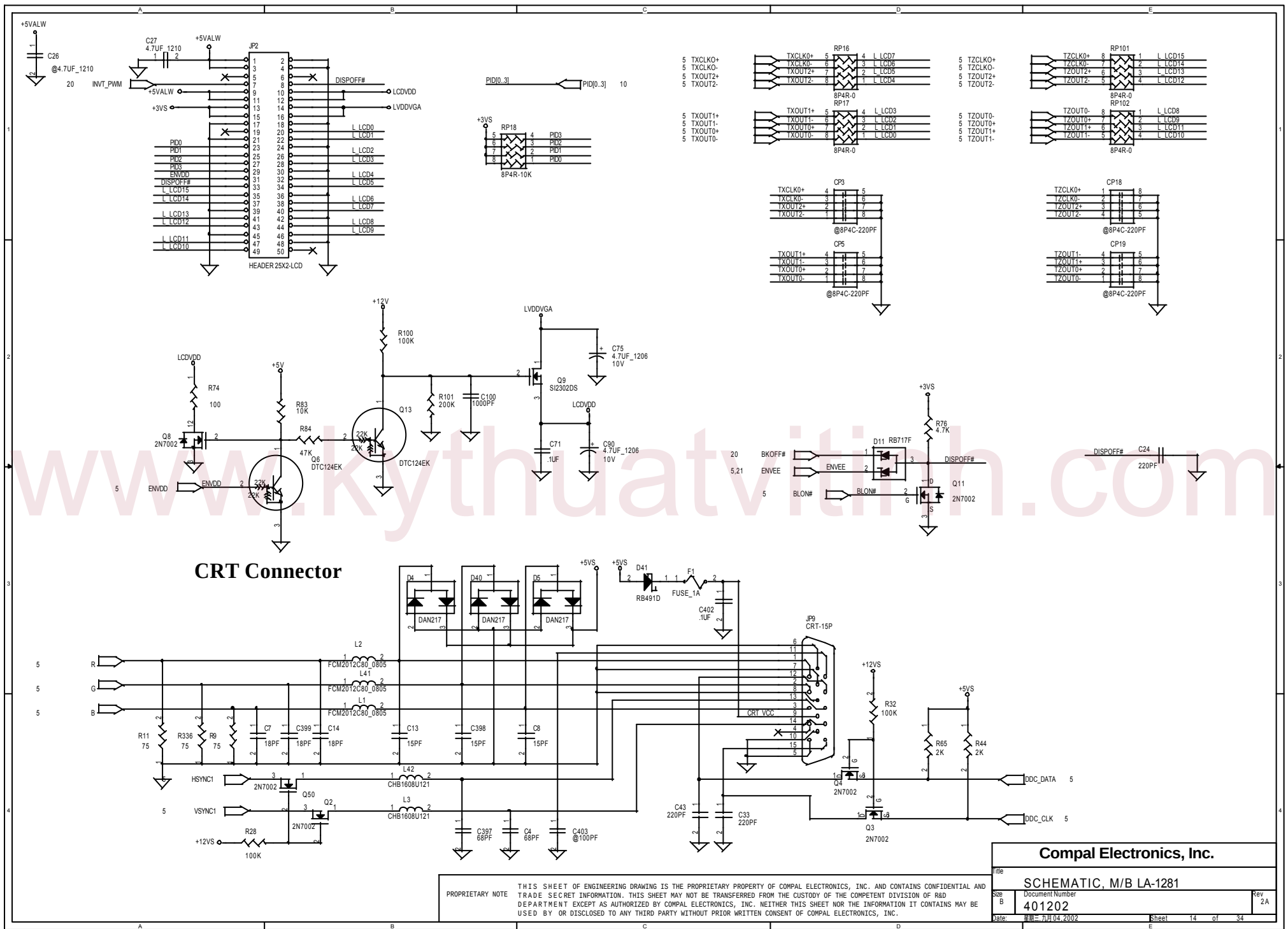
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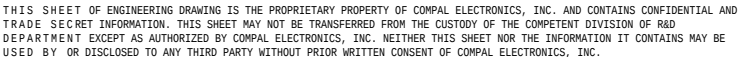
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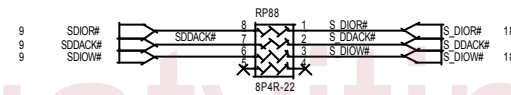
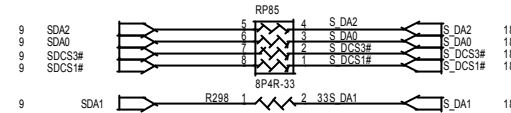
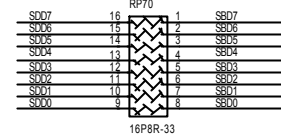
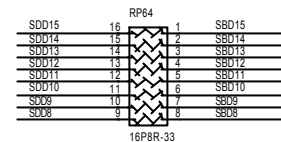
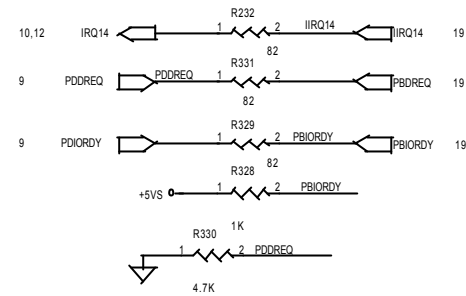
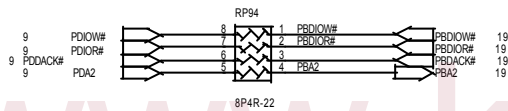
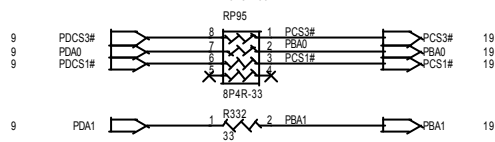
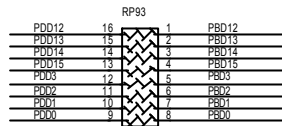
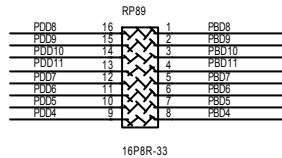
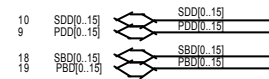


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IDE Series Resistor
Place them close SB



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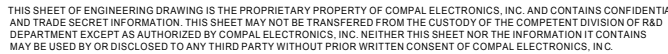
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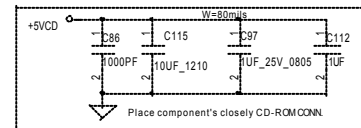
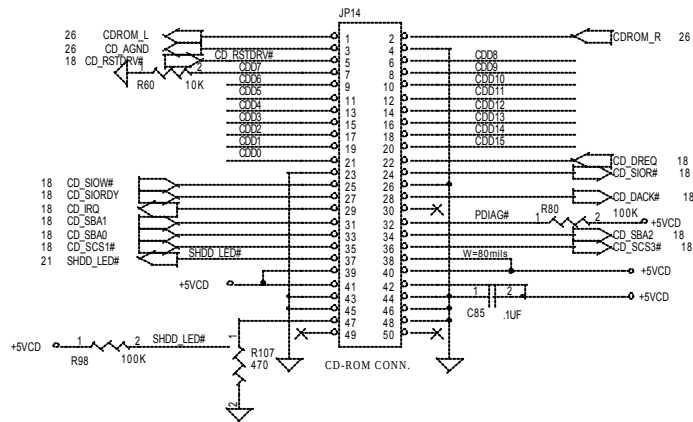
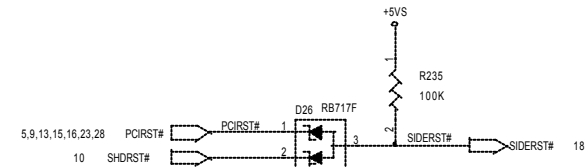
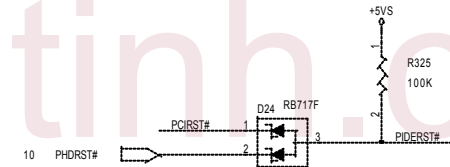
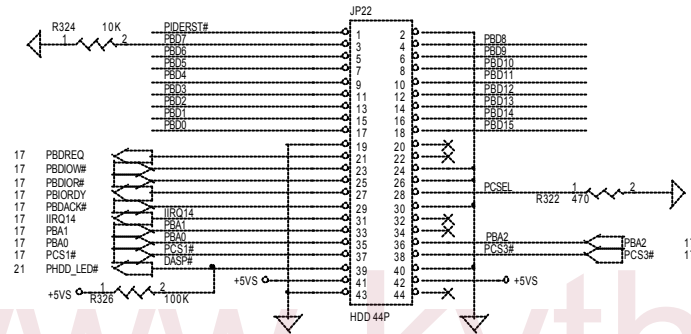
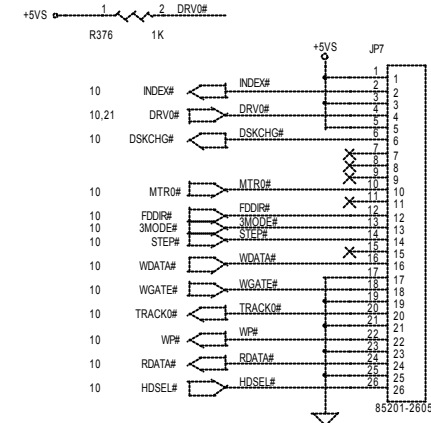
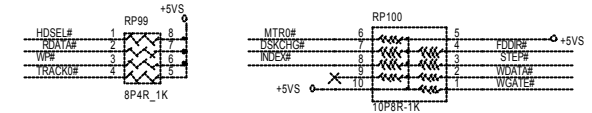
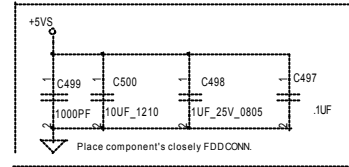
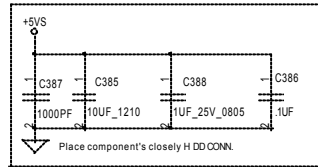
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IDE,CD-ROM & FDD Module CONN.



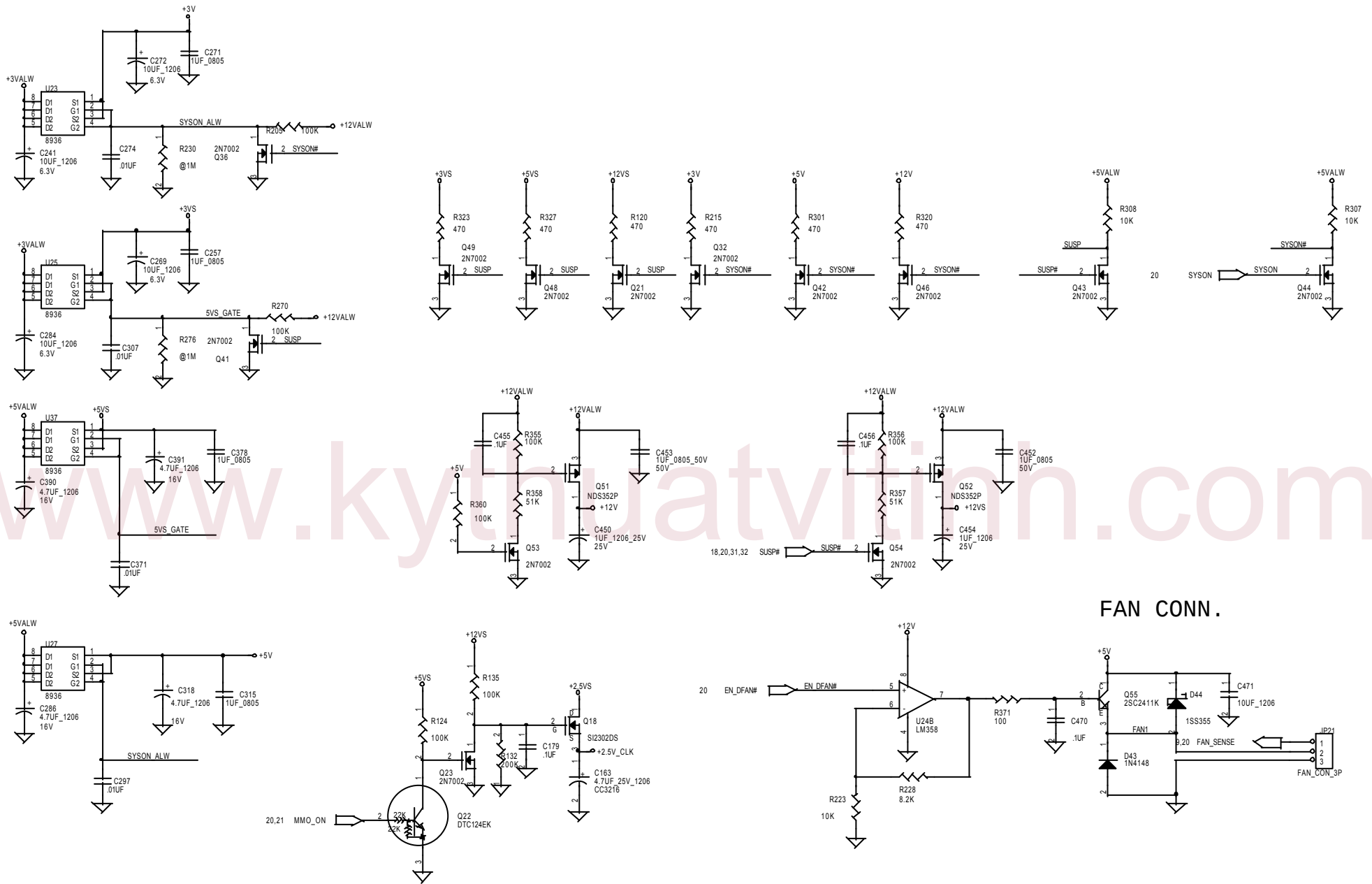
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FAN CONN.

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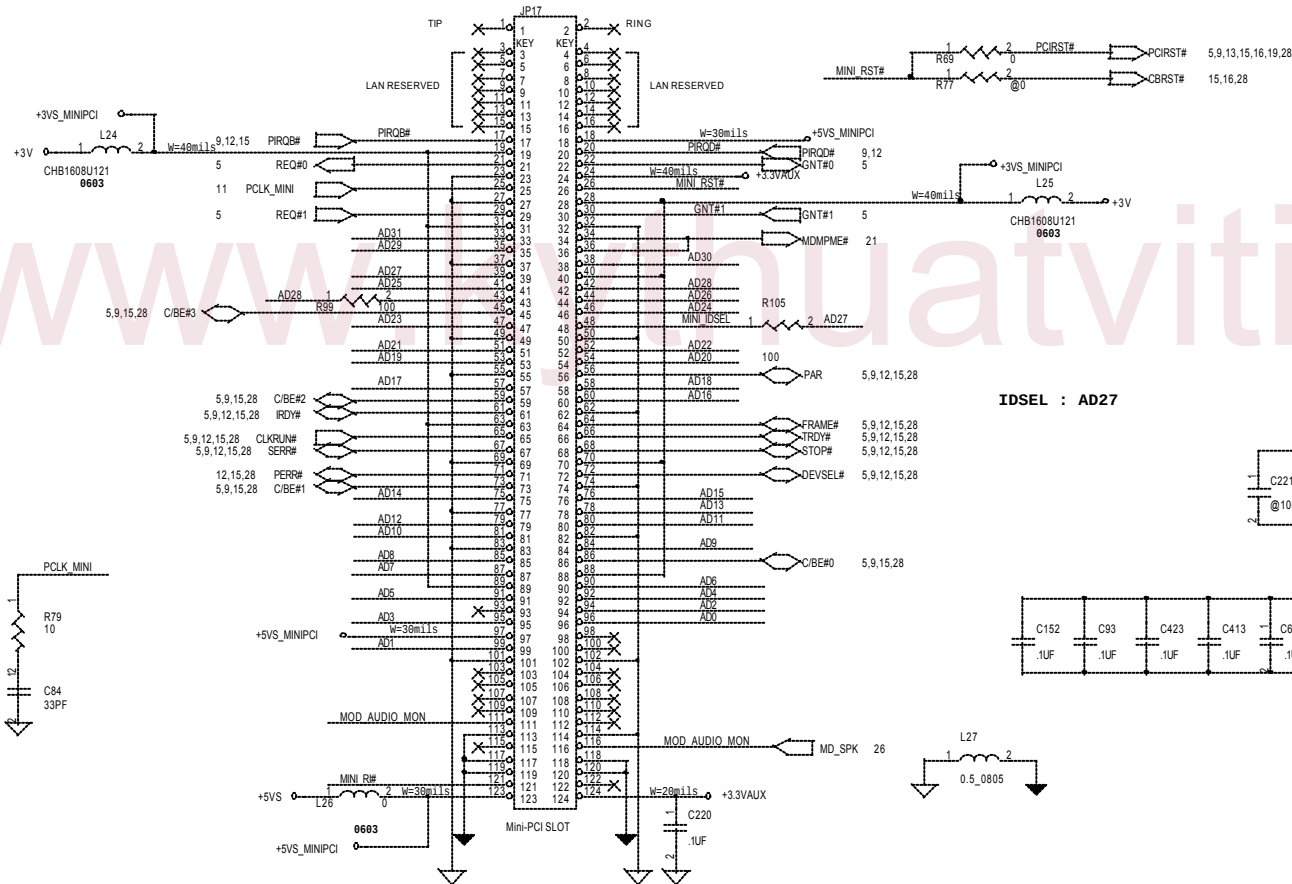
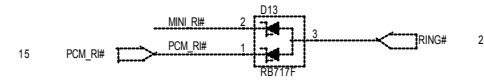
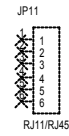
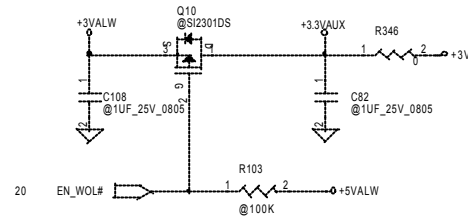
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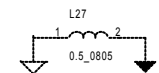
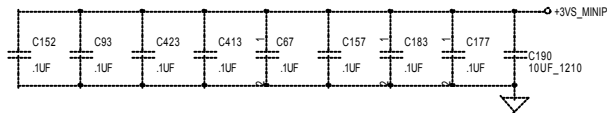
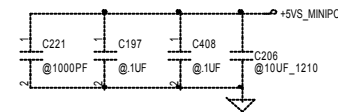
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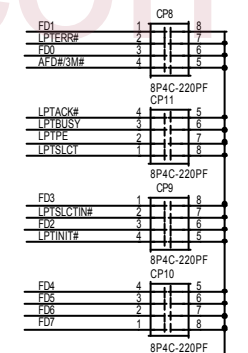
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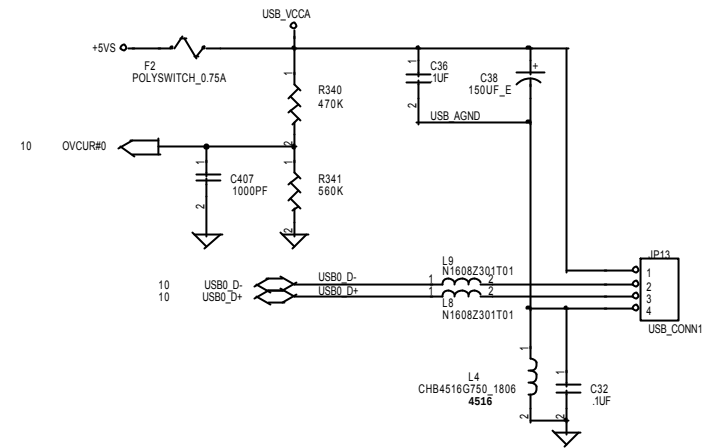
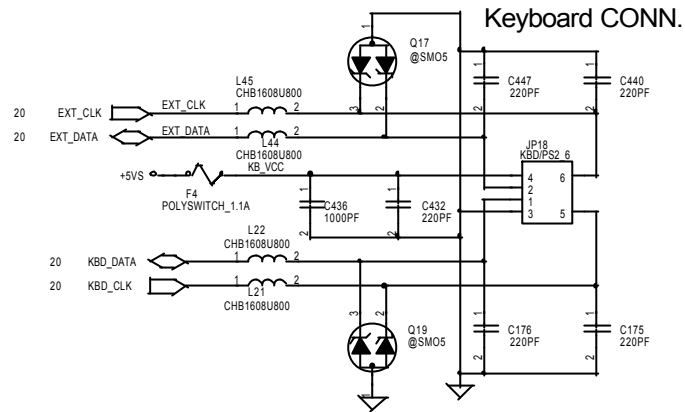
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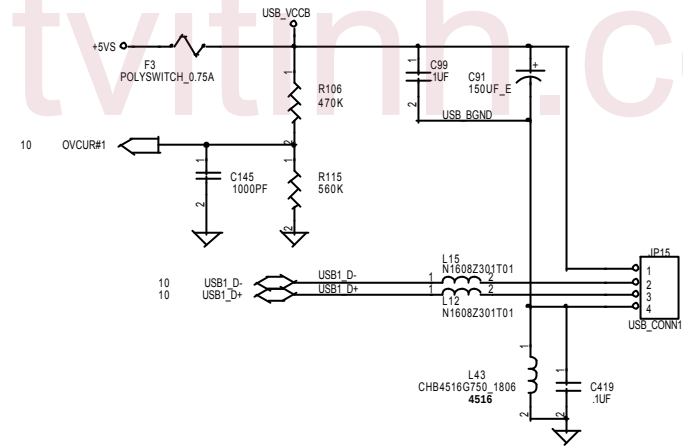
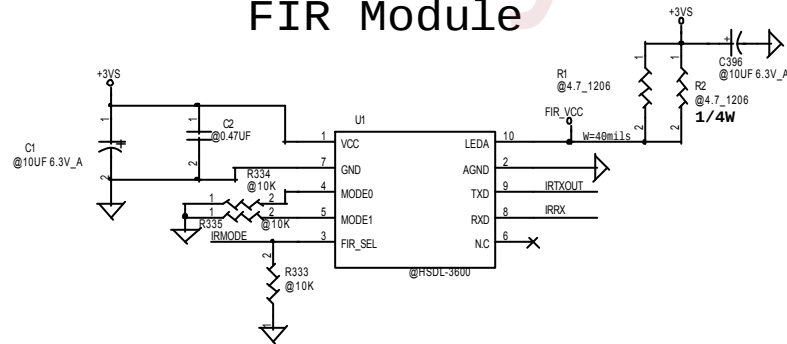
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FIR Module



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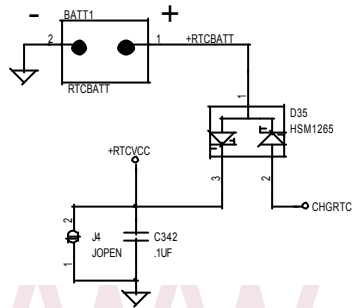
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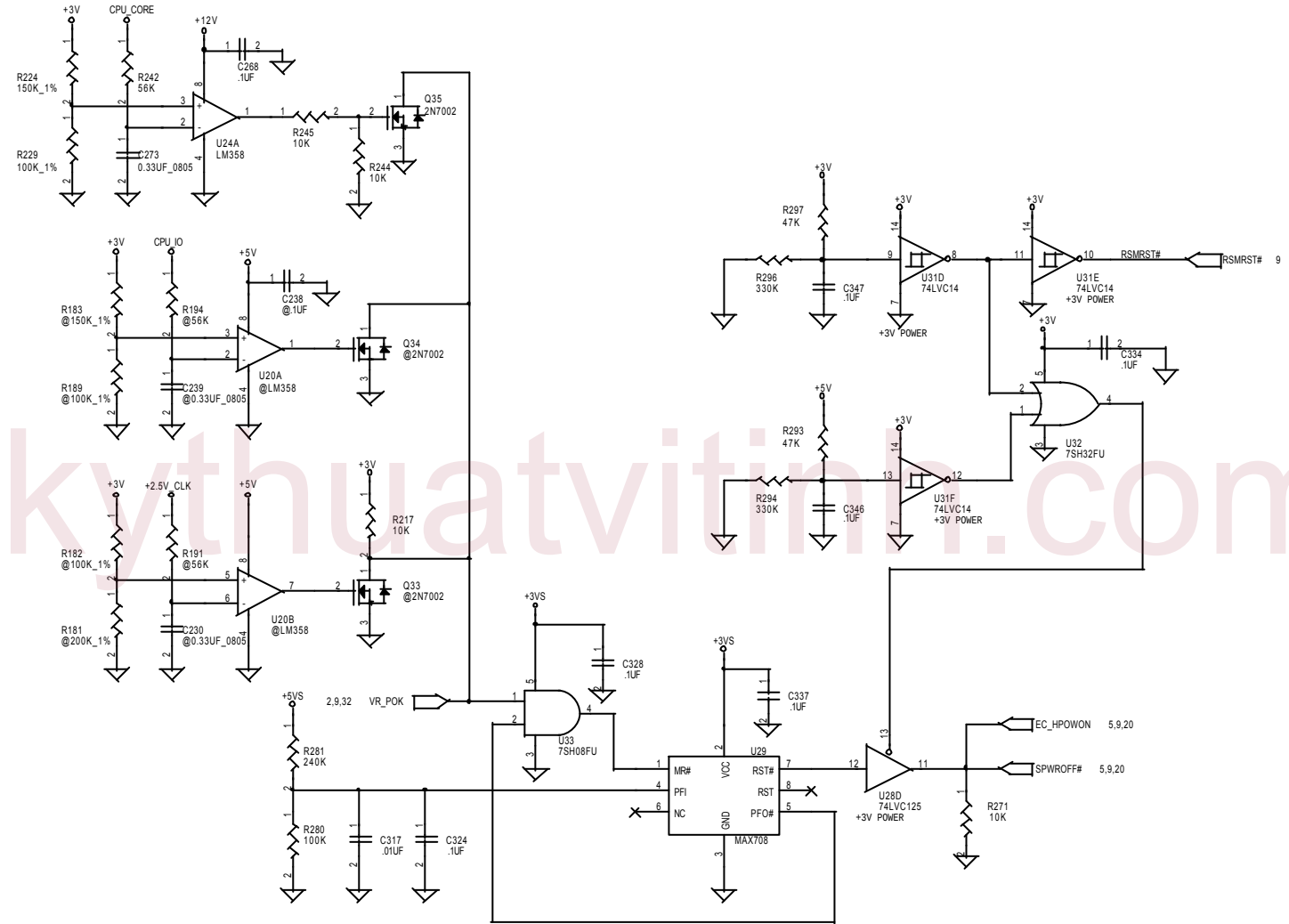
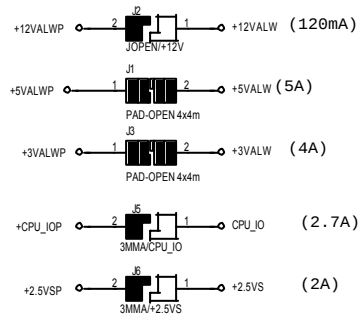
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RTC BATT



DC/DC INTERFACE



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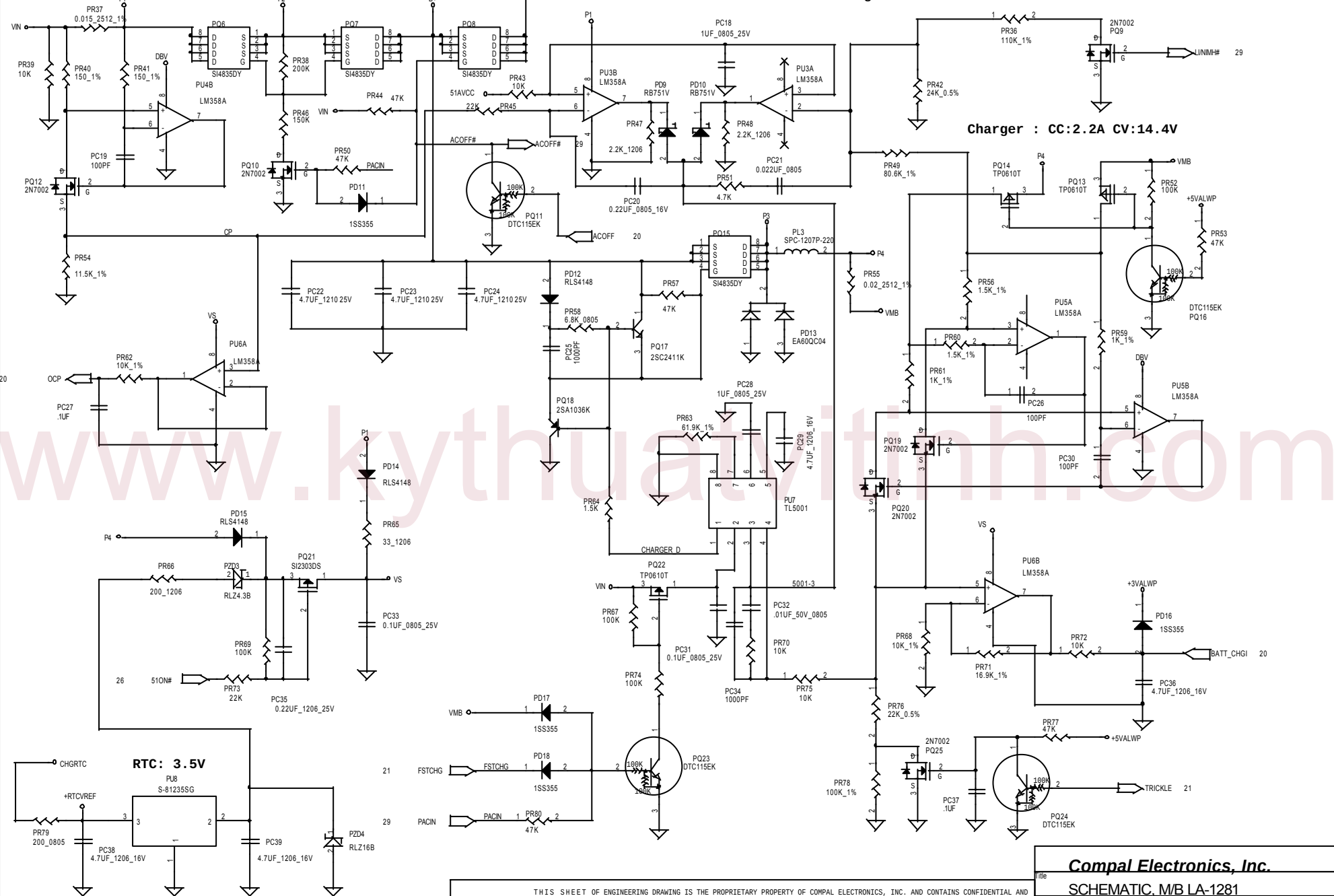
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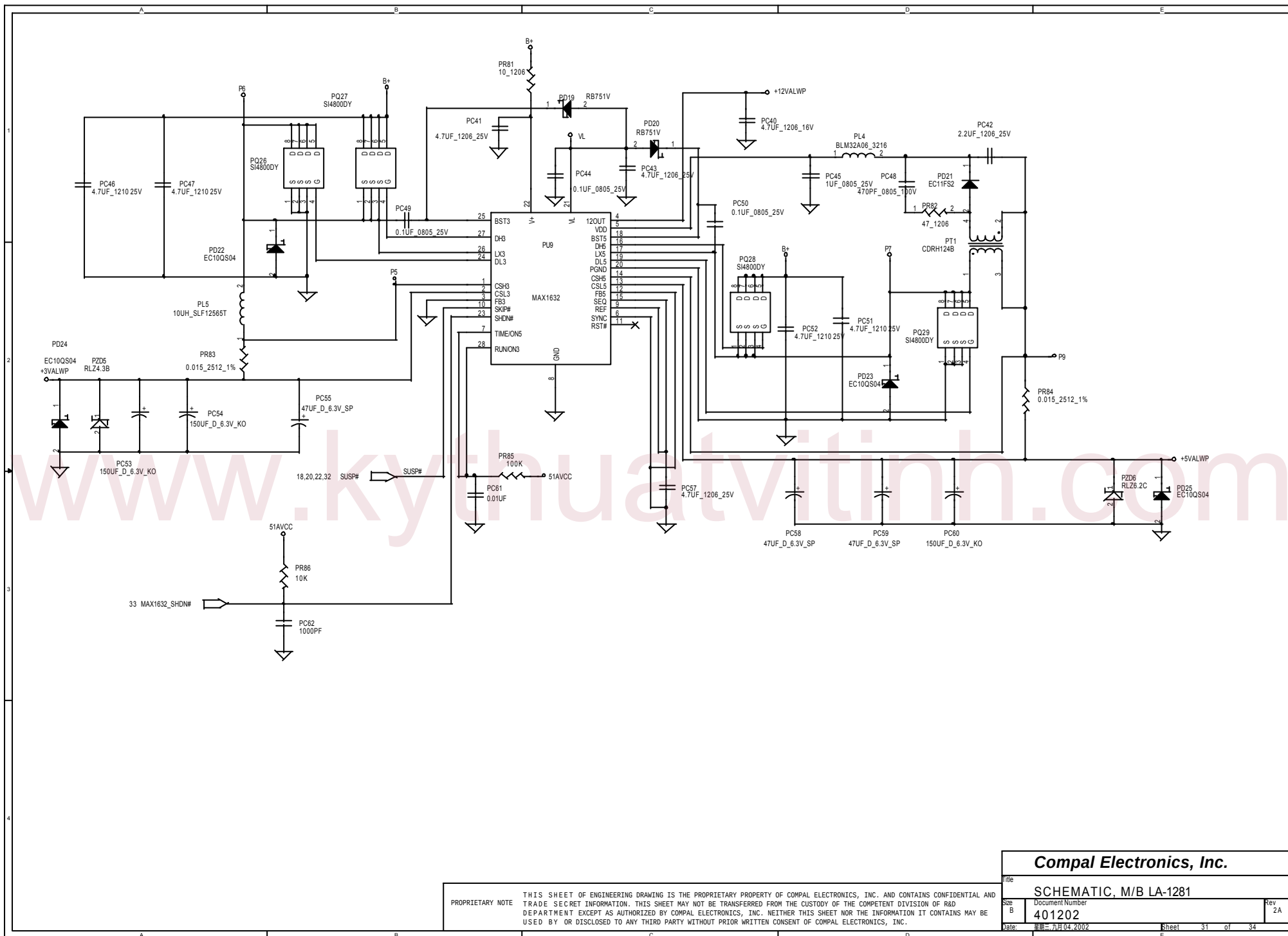
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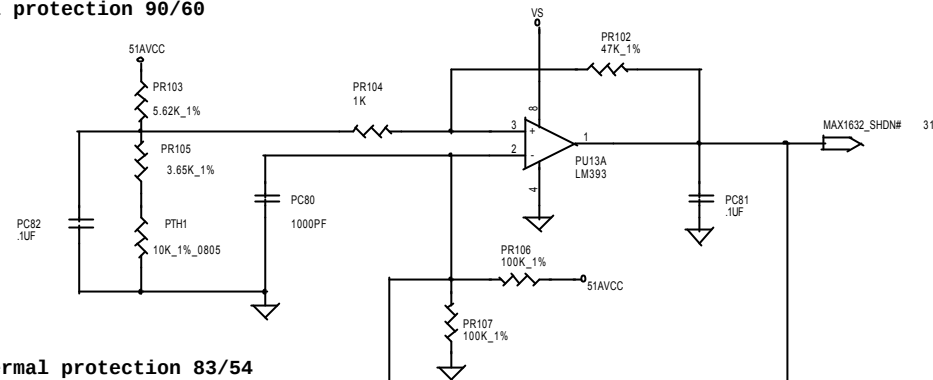
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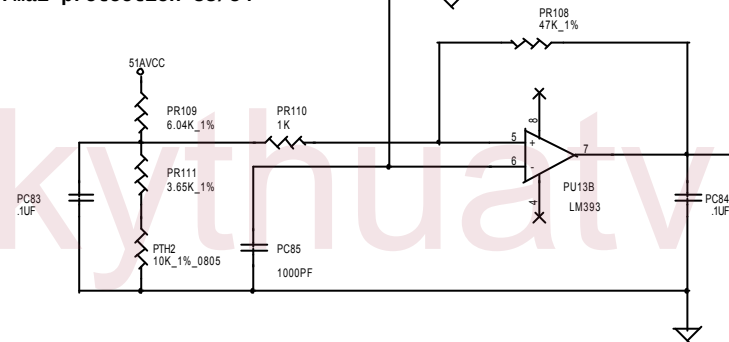
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For CPU thermal protection 90/60
度C



For Battery thermal protection 83/54
度C



CY23 PIR LIST

08/28/01 Written by Jerry

P11: Add Q61 and R407 for VTTWGRD

***** Rev1.0 PIR List *****

09/10/01 Written by Jerry

P26: Add @ at R309 and Q47

Add C507 and U46

Change value of R317 to 510K

P27: Remove @ at R224, R229, R242, R245, R244, C273, and Q35

P32: Add PC91, PC92, PC93, and PC94.

09/12/01 Written by Jerry

P06: Change value of RP34, RP50, RP54, RP62, RP38, RP33, RP57,

RP61,RP53, and RP56 to 16P8R-22

P26: Change value of R317 to 560K

09/19/01 Written by Jerry

P29: Change value of PR29 to 174K_1%

P32: Change value of PR123 to 52.3K_1%

***** Rev2.0 PIR List *****

10/03/01 Written by Jerry

P02: Change value of R27 to 100

Delete R395

R395's Pin 1 tie to RP90's pin5

P03: Change value of R353 to 56.2 1%

P28: Add R408 between +3V and pin 87 of U3

10/08/01 Written by Jerry

P11, P13, P28: Change footprint of Y1, Y2, and Y3

P23: Change footprint of JP11

P28: Change footprint of JP12

P31: Change footprint of PL5

P26: Change footprint of SW1

10/17/01 Written by Jerry

P02: Add @ at RP25, RP24, RP28, RP27, RP31, RP30, RP 42, RP41,

RP40, RP43, RP44, RP45, RP46, RP47, RP48, RP49, RP7, RP1, RP6,

RP14, RP20, RP19, RP22, RP9, RP10, RP11, RP8, RP2, and R7

P06: Remove @ at R164

P11: Add @ at R176

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