

Compal Confidential

Schematics Document

AMD S1/ ATI RS690MC / SB600
2007 / 4 / 10 Rev:0.4

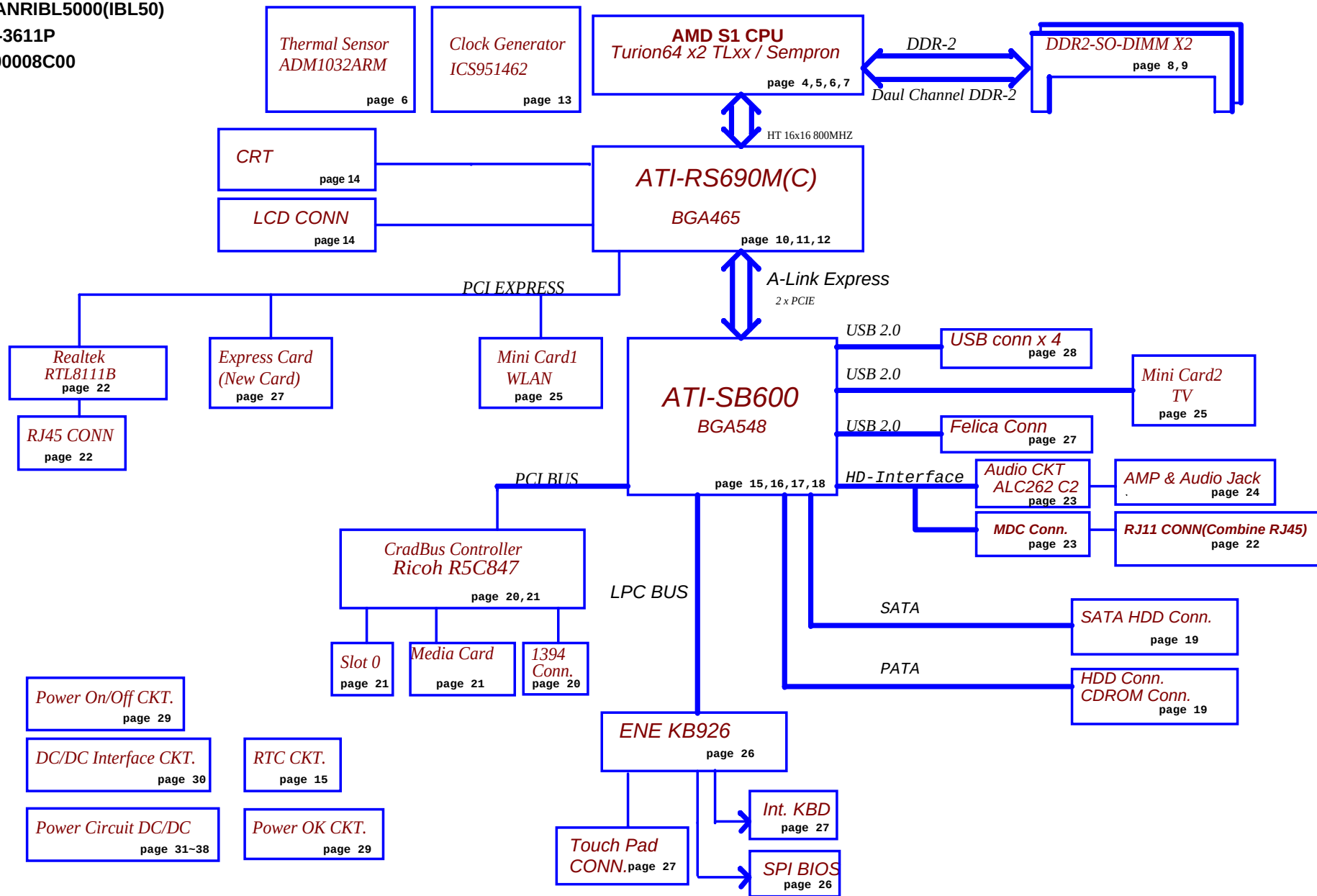
Security Classification	Compal Secret Data			Title	
Issued Date	2007/1/25	Deciphered Date	2008/01/25	Cover Sheet	
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Project Code: ANRIBL5000(IBL50)

File Name : LA-3611P

PCB P/N: DA800008C00



Security Classification	Compal Secret Data			Title	
Issued Date	2007/1/25	Deciphered Date	2008/01/25	Block Diagrams	
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Power Plane	Description	S1	S3	S4/ S5
VIN	Adapter power supply (19V)	ON	ON	ON
B+	AC or battery power rail for power circuit.	ON	ON	ON
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.2V_HT	1.2V switched power rail	ON	OFF	OFF
+0.9V	0.9V switched power rail for DDR terminator	ON	ON	OFF
+1.2VALW	1.2V always on power rail	ON	ON	ON
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+3VALW	3.3V always on power rail	ON	ON	ON
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON
+5VS	5V switched power rail	ON	OFF	OFF
+RTCVCC	RTC power	ON	ON	ON

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD21	0	PIRQE/PIRQF/PIRQG

Device	Address	Device	Address
Smart Battery	0001 011X b?	ADM1032	1001 110X b?
EEPROM(24C16/02)	1010 000X b?		
(24C04)	1011 000Xb?		

Device	Address
Clock Generator (ICS 951462AGT)	1101 001Xb?
DDRII DIMM0	1001 000Xb?
DDRII DIMM2	1001 010Xb?

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Vcc	3.3V +/- 5%			
Ra / Rc	100K +/- 5%			
Board ID	Rb / Rd	V_{AD_BID} min	V_{AD_BID} typ	V_{AD_BID} max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

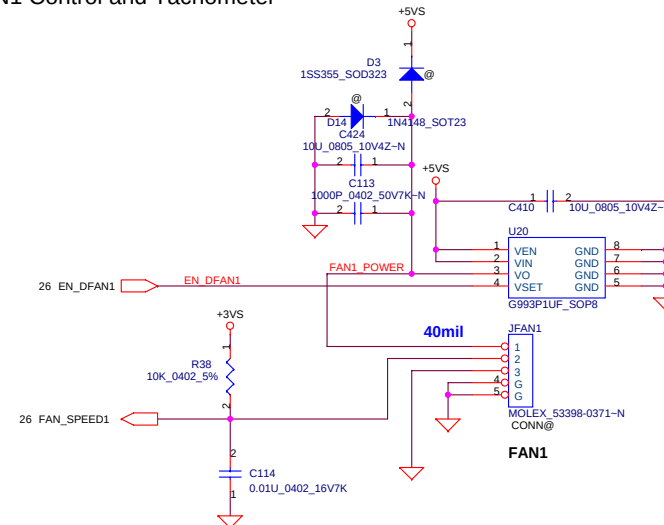
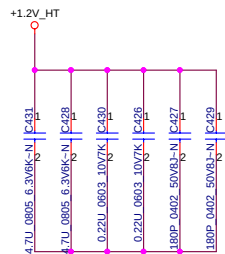
Board ID	PCB Revision
0	0.2 (KH4 ES1)
1	0.3 (KH4 ES2)
2	0.4 (KH4 PP)
3	
4	
5	
6	
7	

[illegible]

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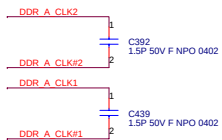
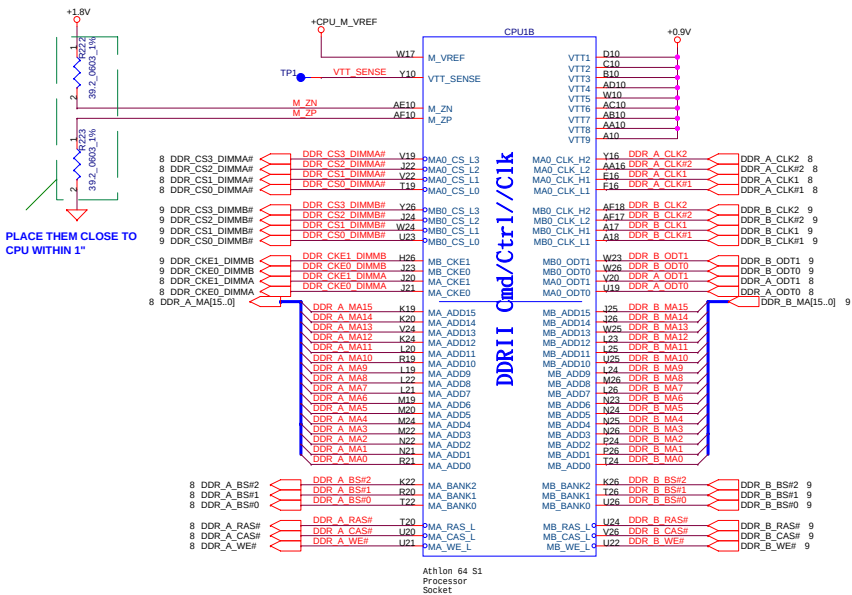
VLDT_Ax AND VLDT_Bx ARE CONNECTED TO THE LDT_RUN POWER SUPPLY THROUGH THE PACKAGE OR ON THE DIE. IT IS ONLY CONNECTED ON THE BOARD TO DECOUPLING NEAR THE CPU PACKAGE



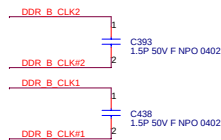
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Processor DDR2 Memory Interface

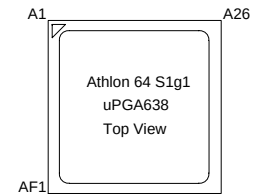
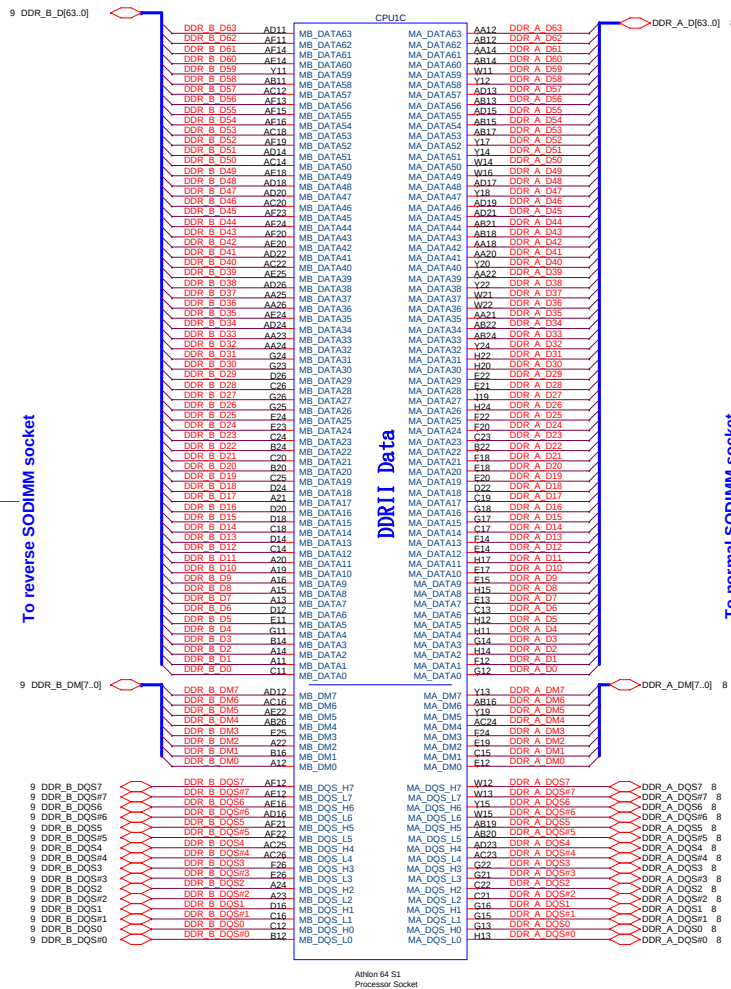
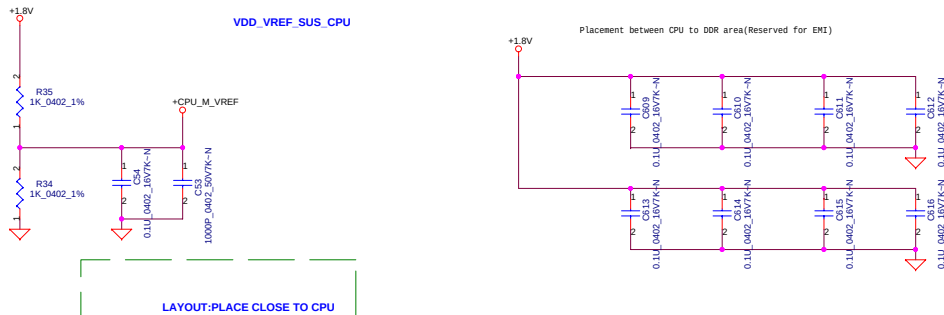
VDD_VTT_SUS_CPU IS CONNECTED TO THE VDD_VTT_SUS POWER SUPPLY THROUGH THE PACKAGE OR ON THE DIE. IT IS ONLY CONNECTED ON THE BOARD TO DECOUPLING NEAR THE CPU PACKAGE



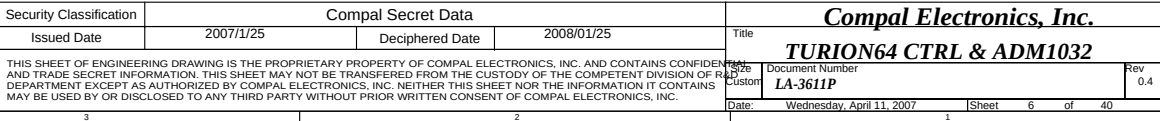
PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH



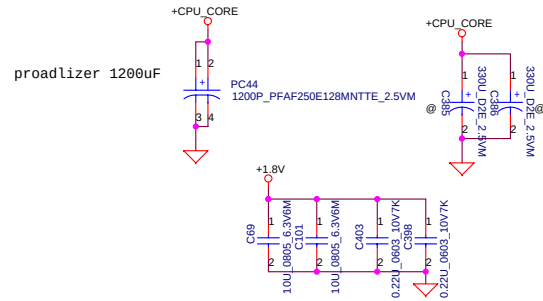
**PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH**



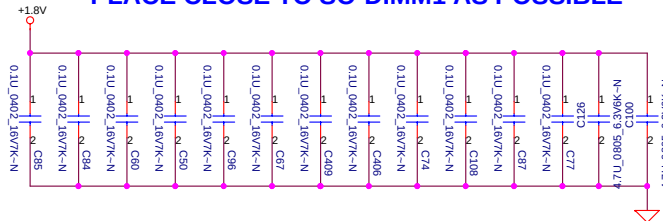
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 LAYOUT: ROUTE VDDA TRACE APPROX 50 MILS WIDE (USE 2x25 MIL TRACES TO EXIT BALL FIELD) AND 500 MILS LONG.

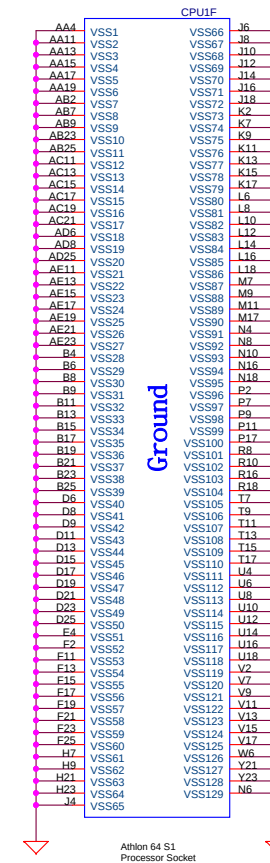
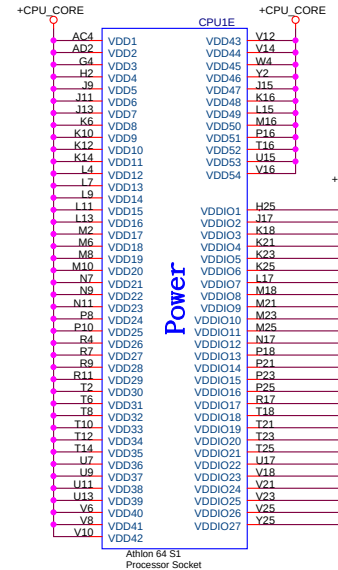
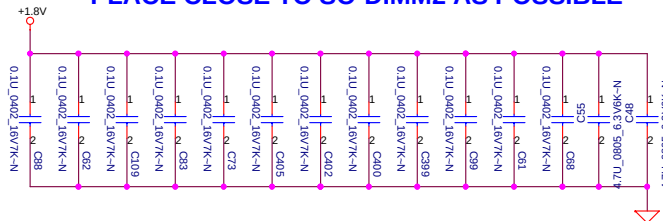
BOTTOMSIDE DECOUPLING



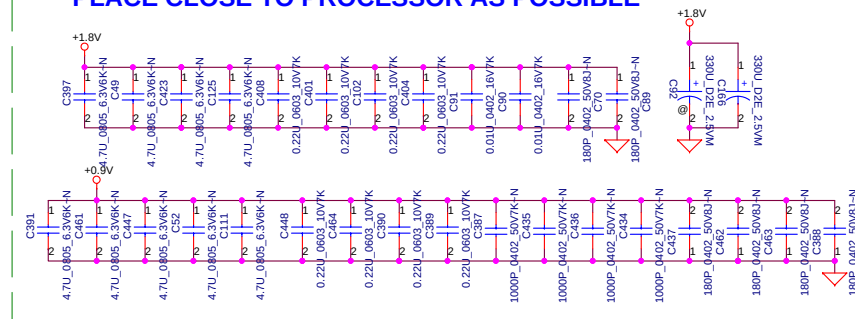
PLACE CLOSE TO SO-DIMM1 AS POSSIBLE



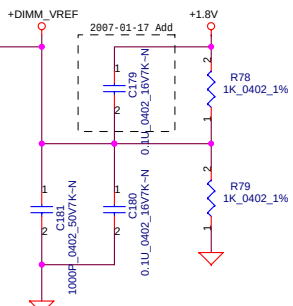
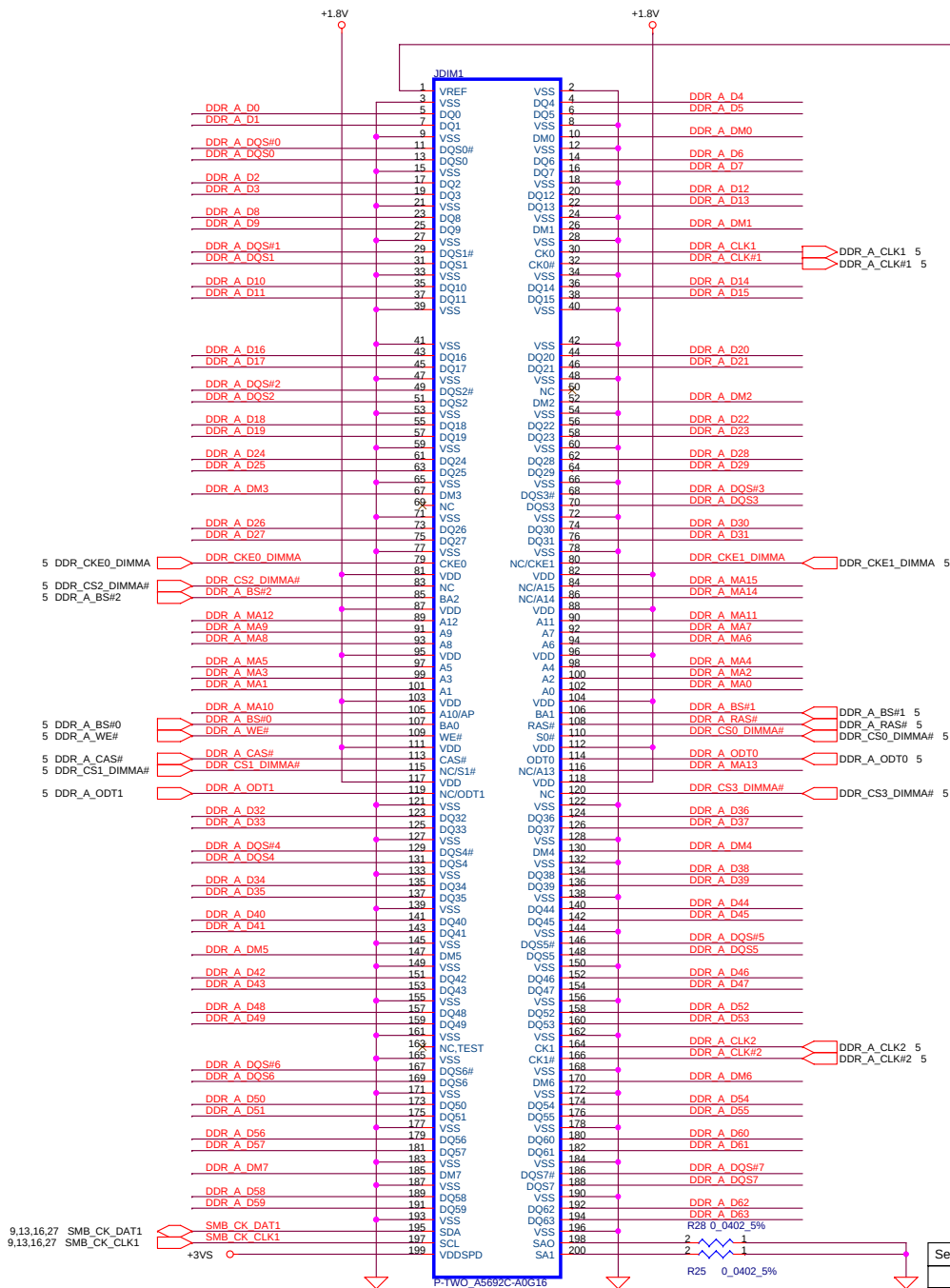
PLACE CLOSE TO SO-DIMM2 AS POSSIBLE



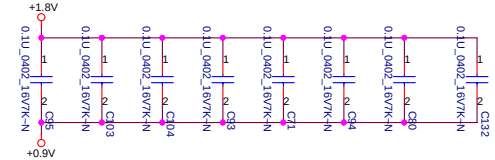
DECOUPLING BETWEEN PROCESSOR AND DIMMs
PLACE CLOSE TO PROCESSOR AS POSSIBLE



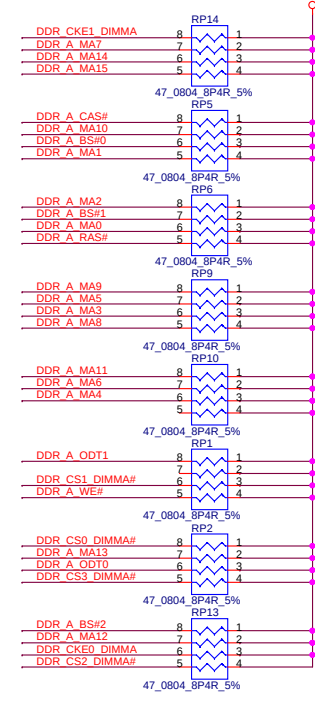
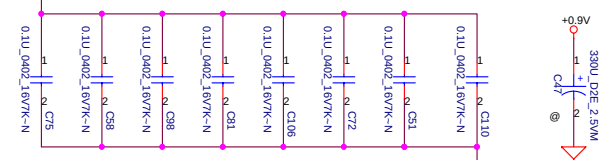
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Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V



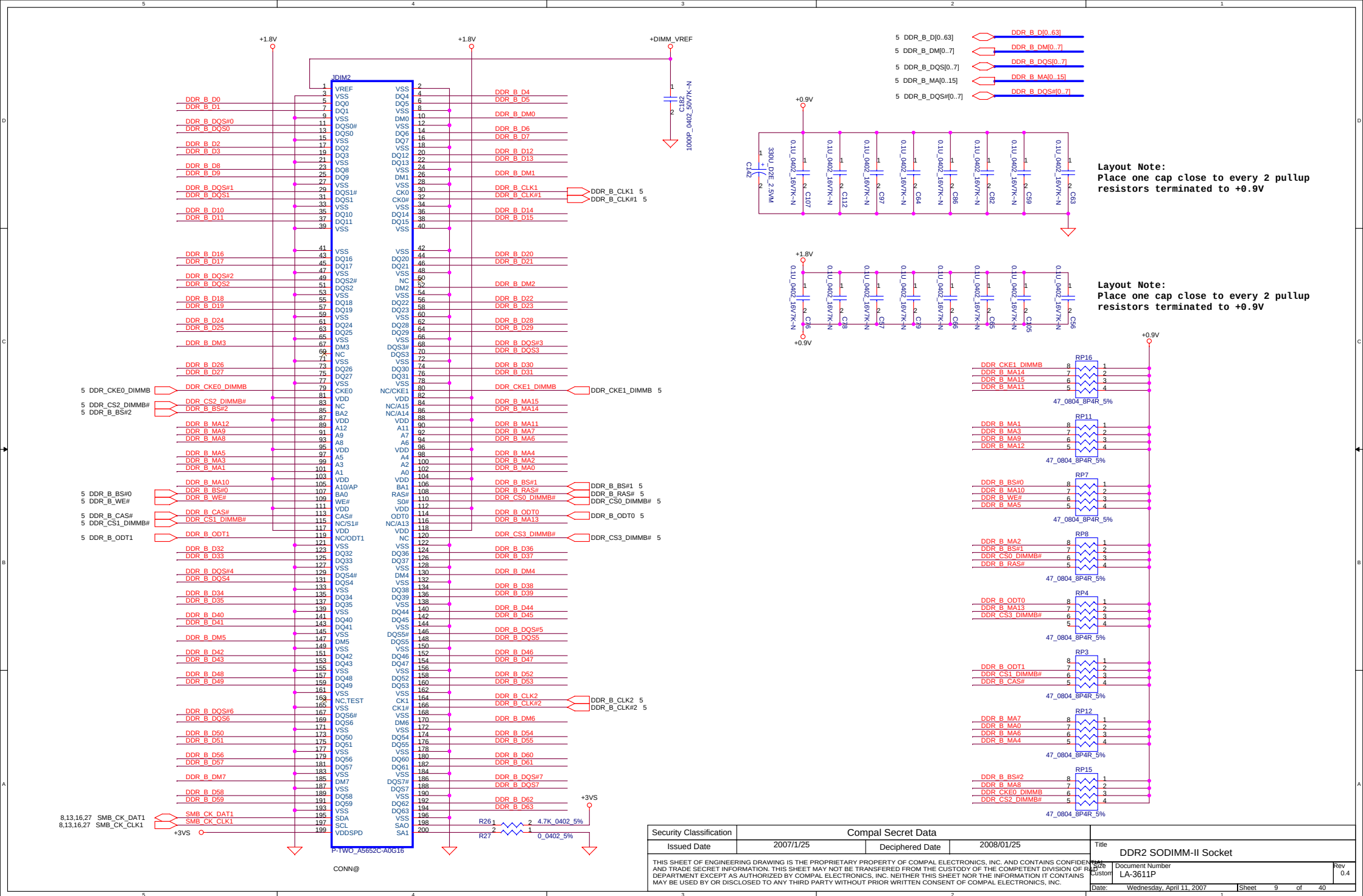
9.13.16.27 SMB_CK_DAT1
9.13.16.27 SMB_CK_CLK1

SMB CK DAT1
SMB CK CLK1

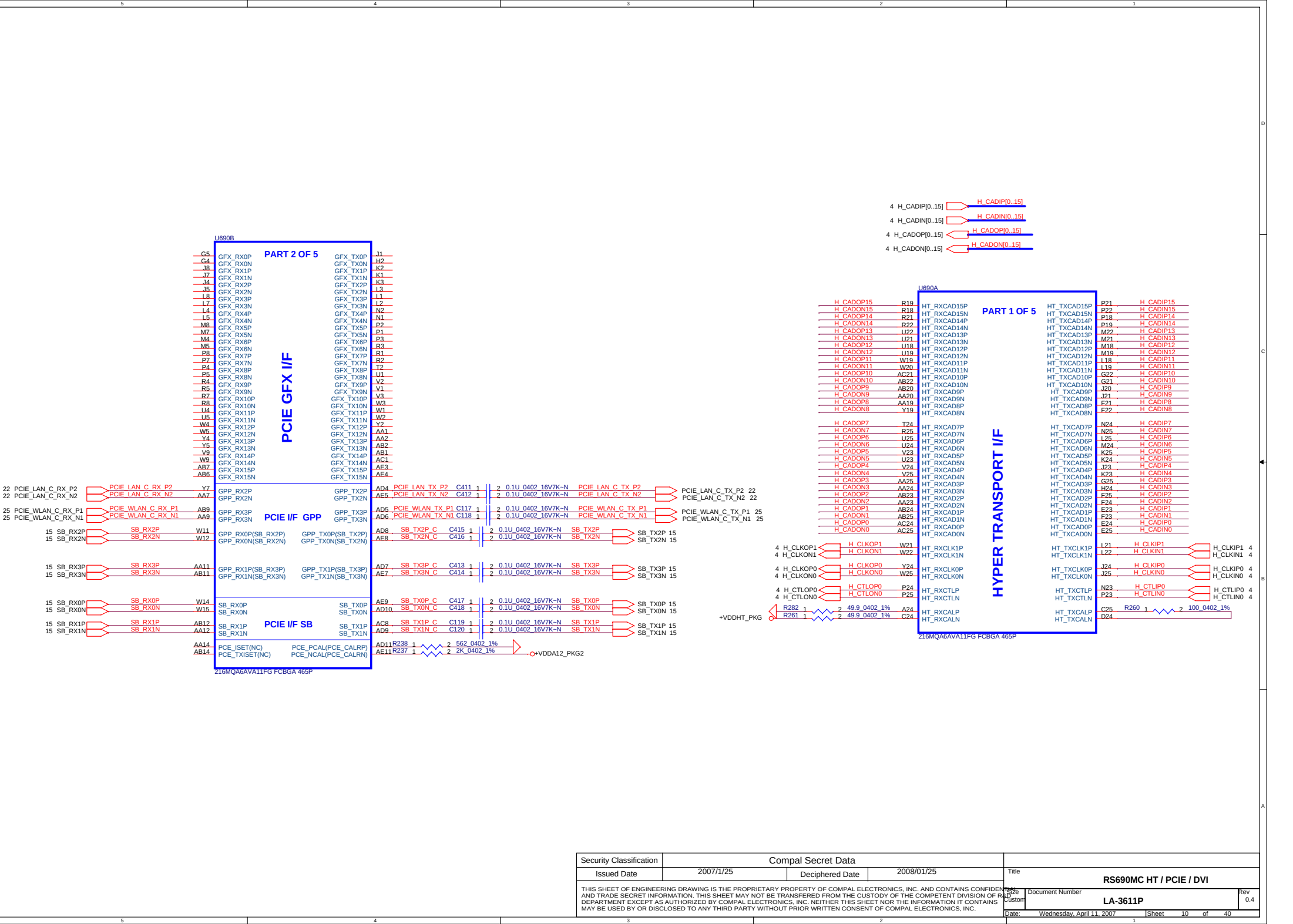
+3VS

CONN@

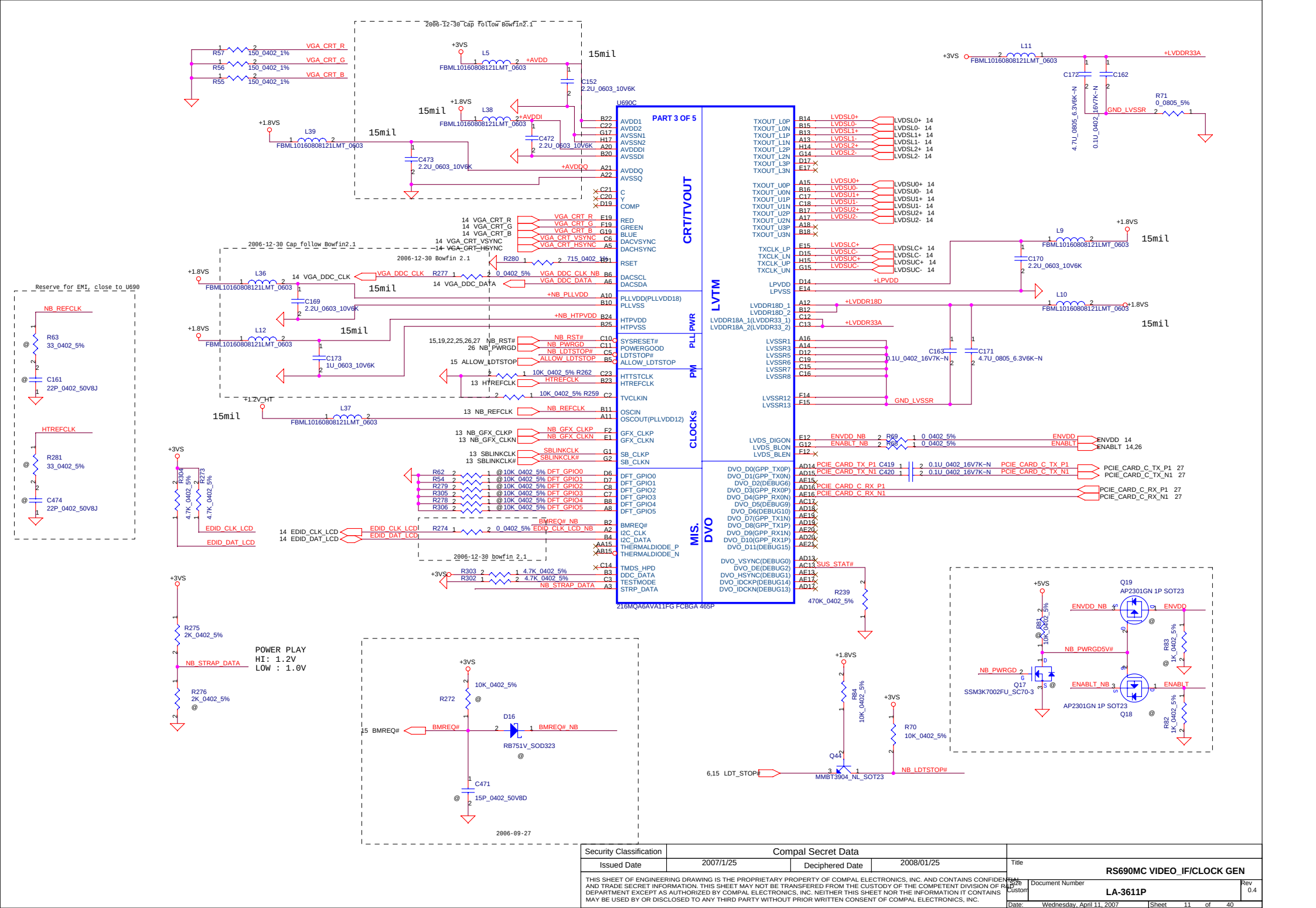
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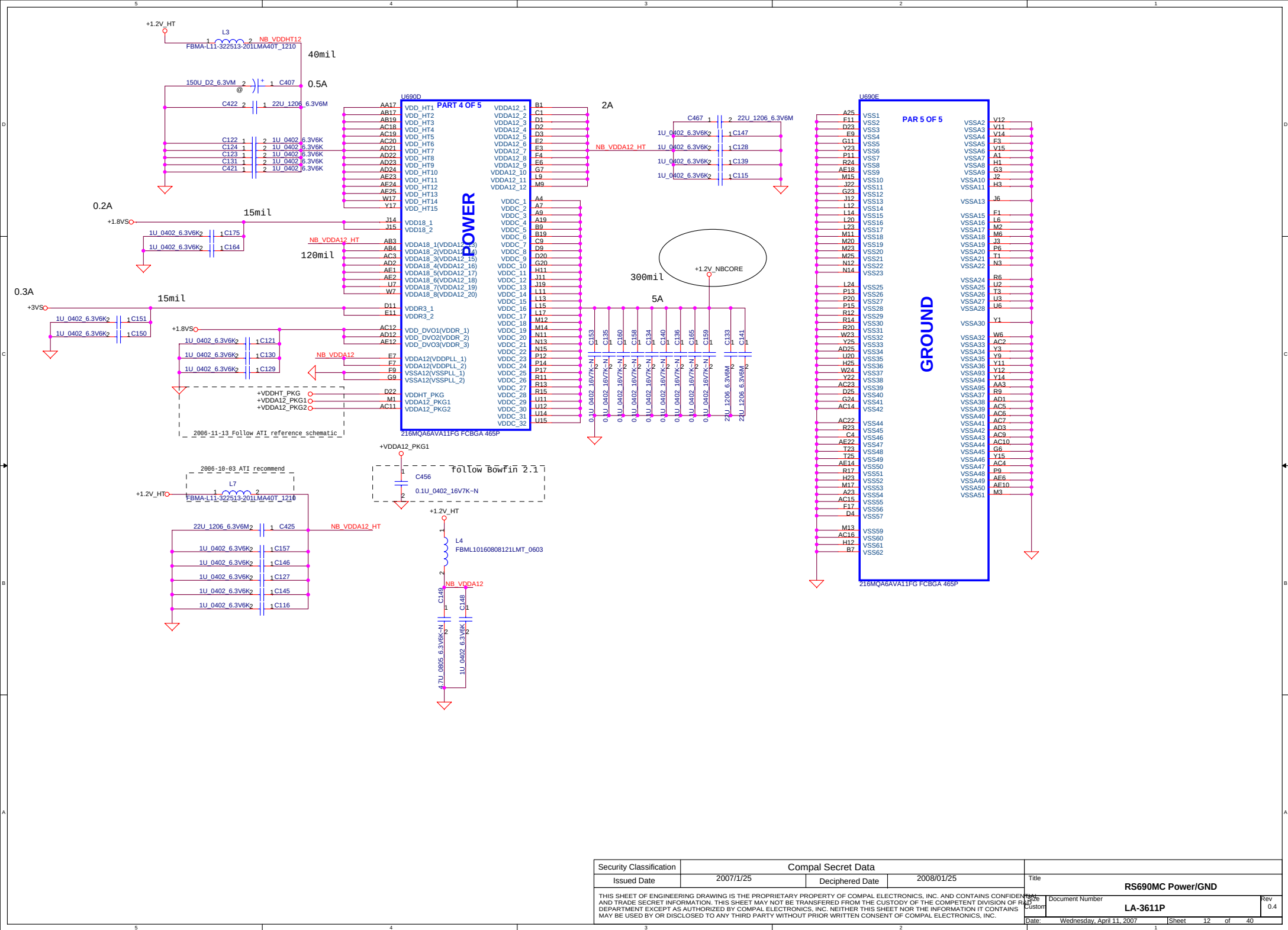


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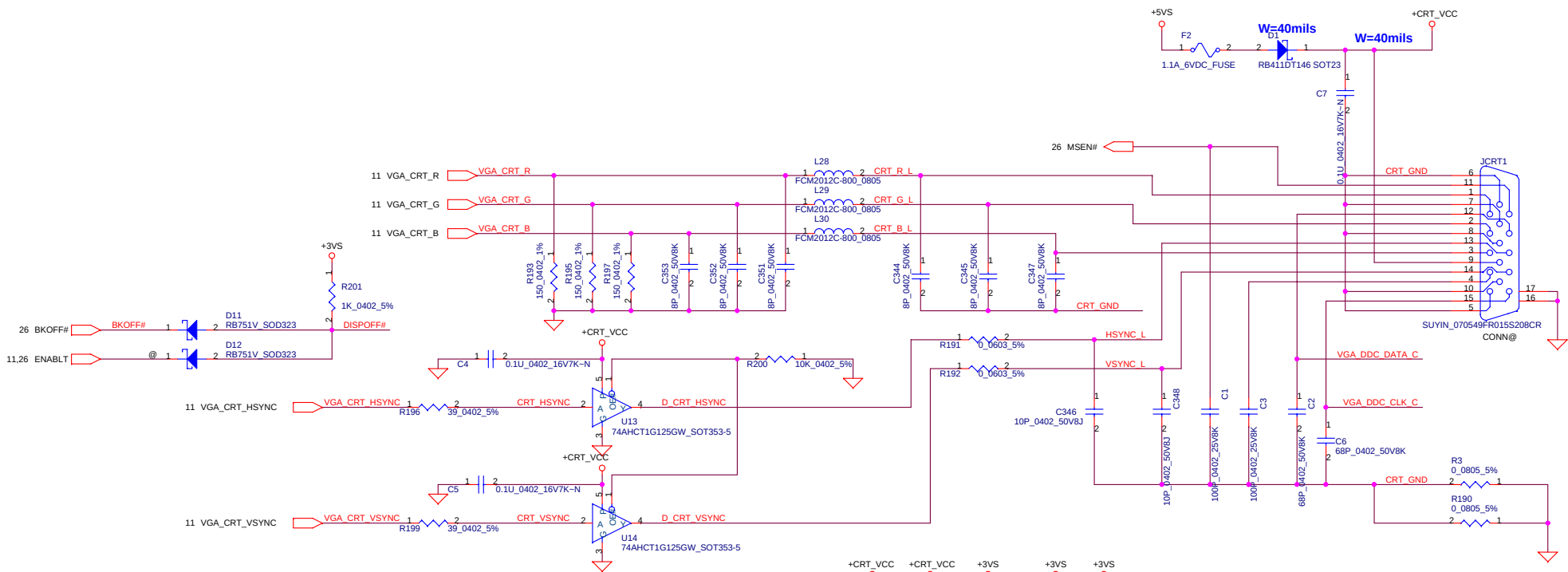


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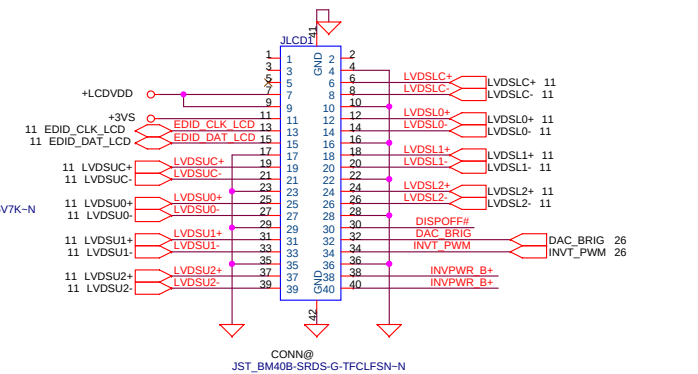
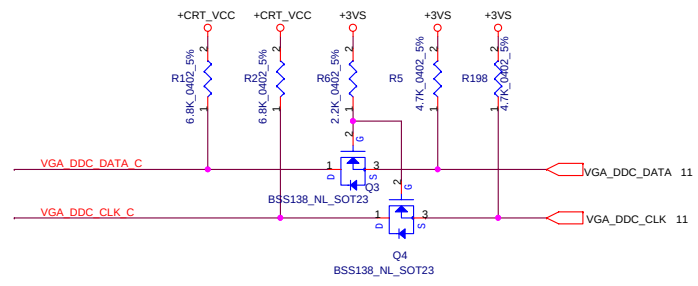
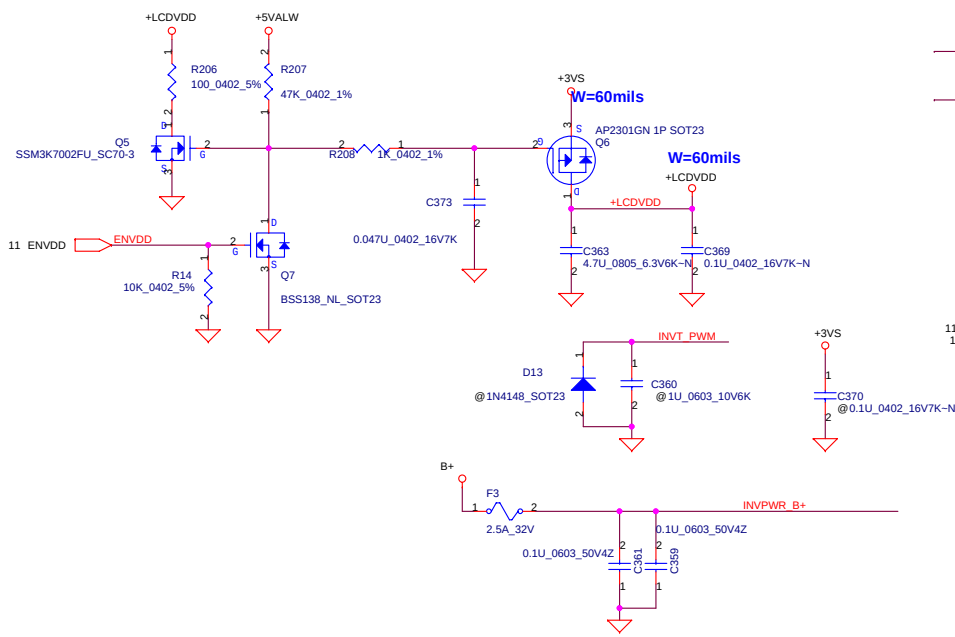




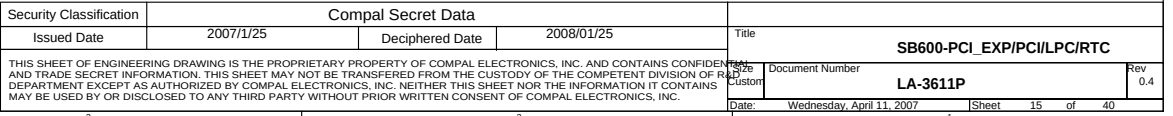
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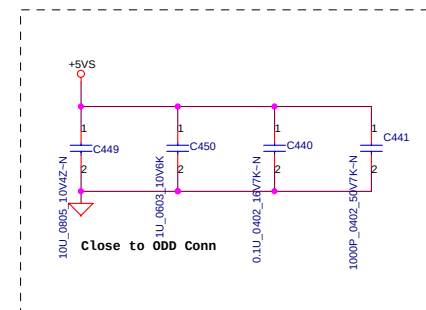


LCD POWER CIRCUIT

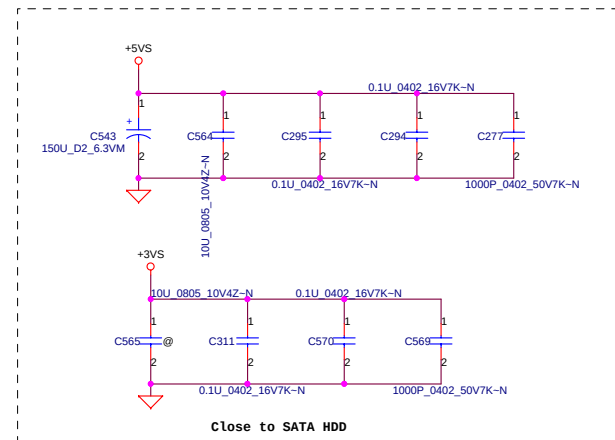


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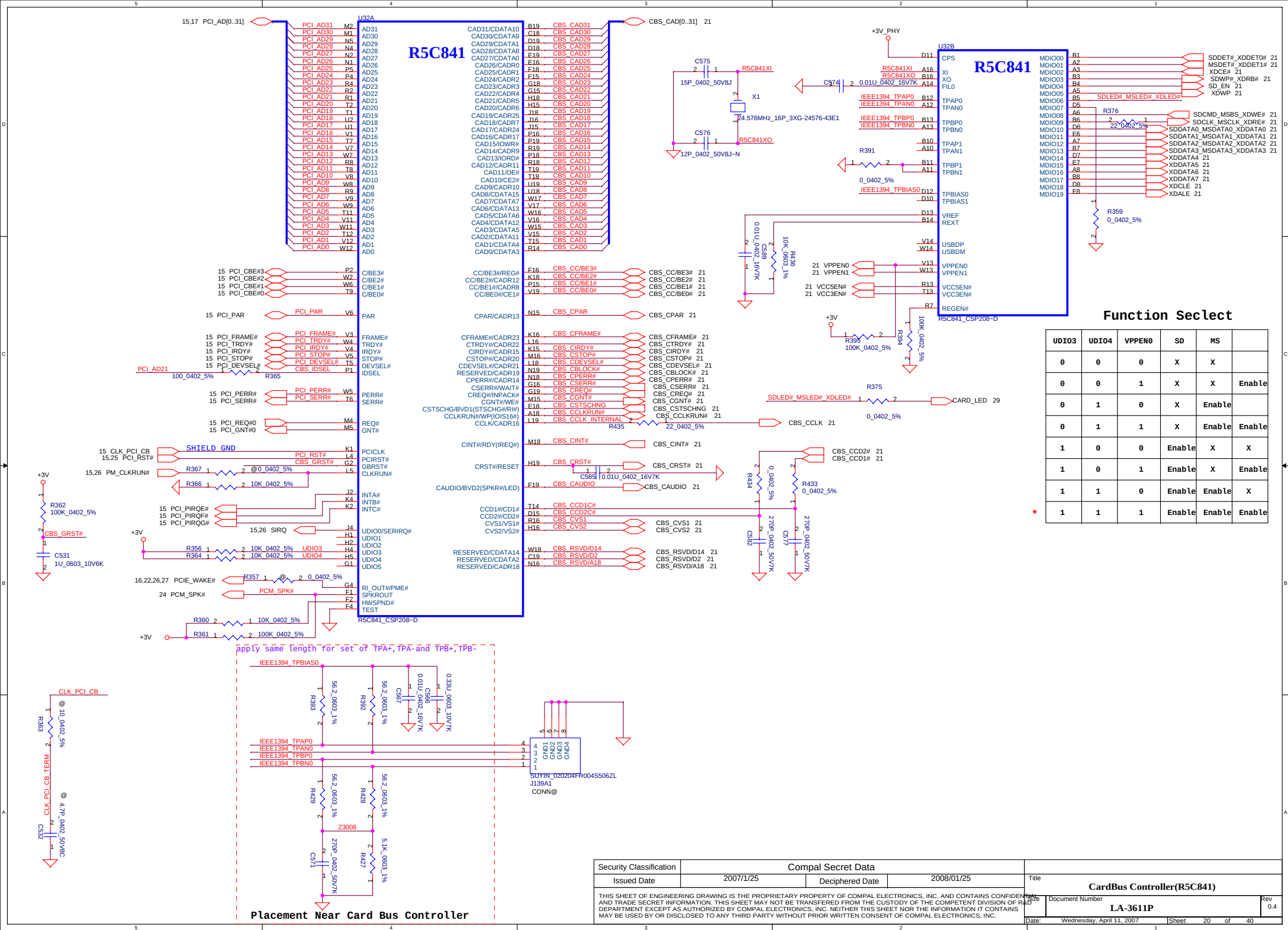


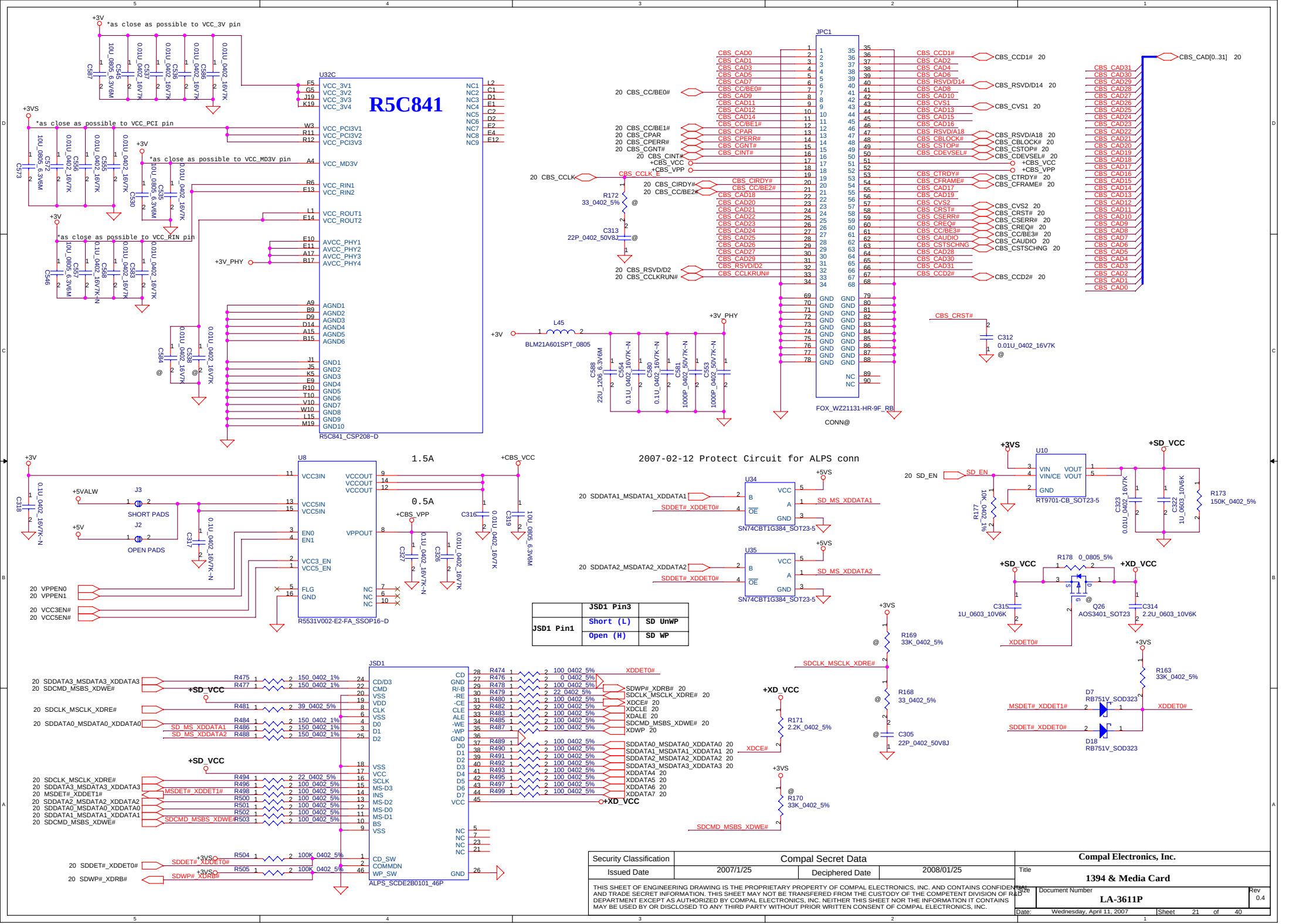


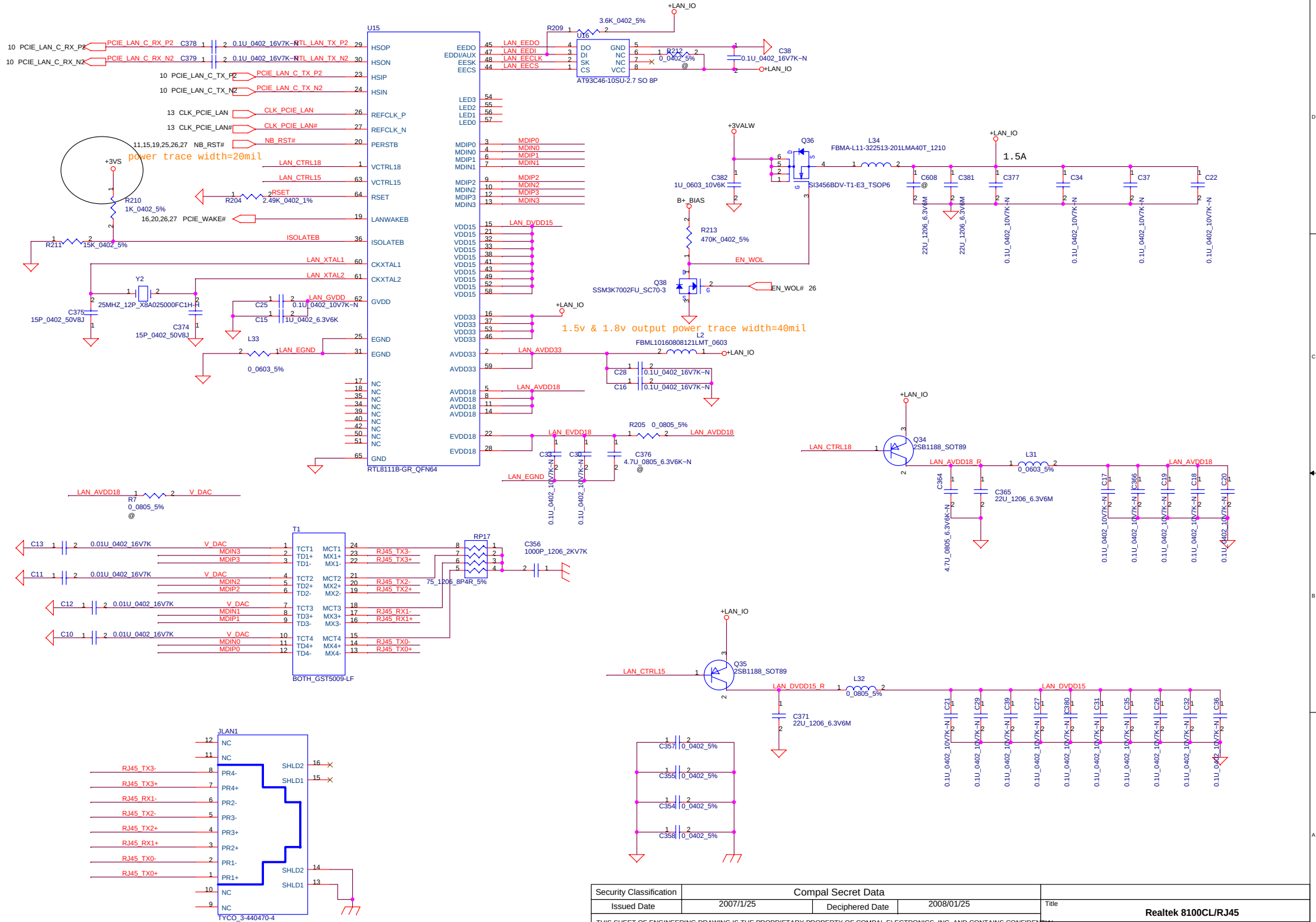
SATA HDD CONN



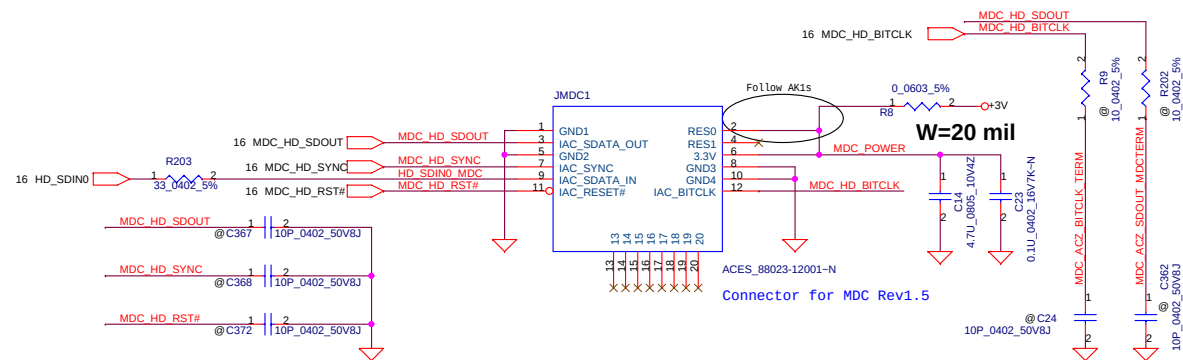
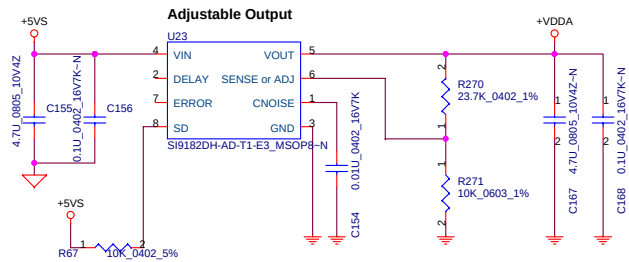
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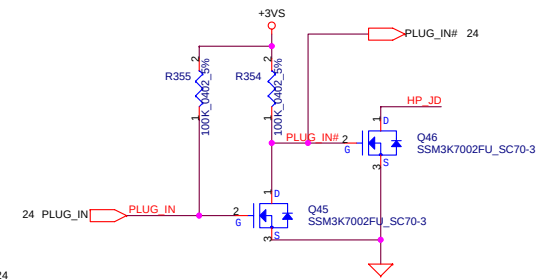
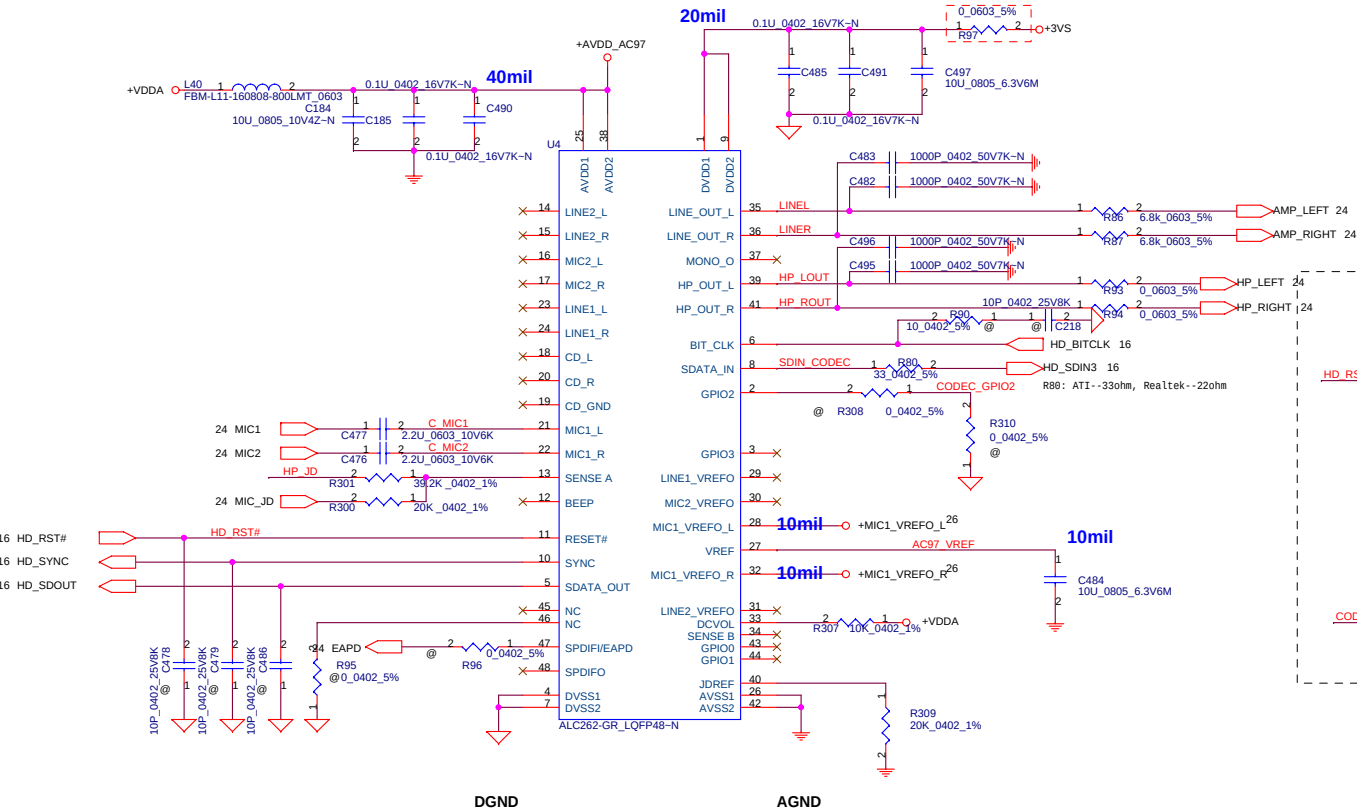




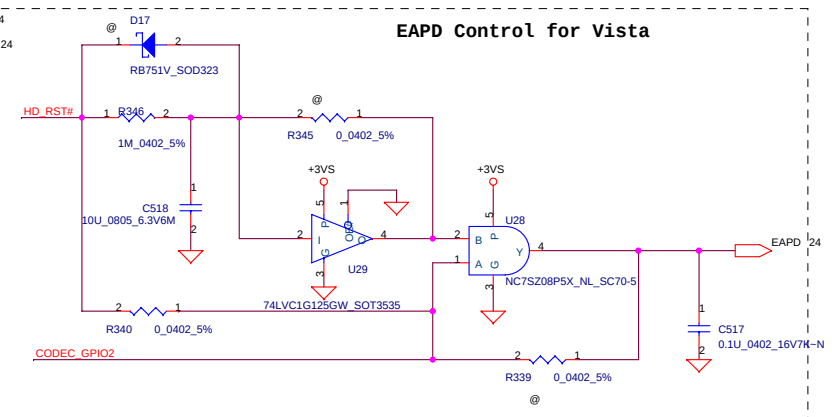
MDC CONN.



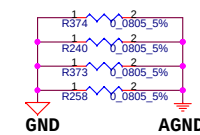
HD Audio Codec



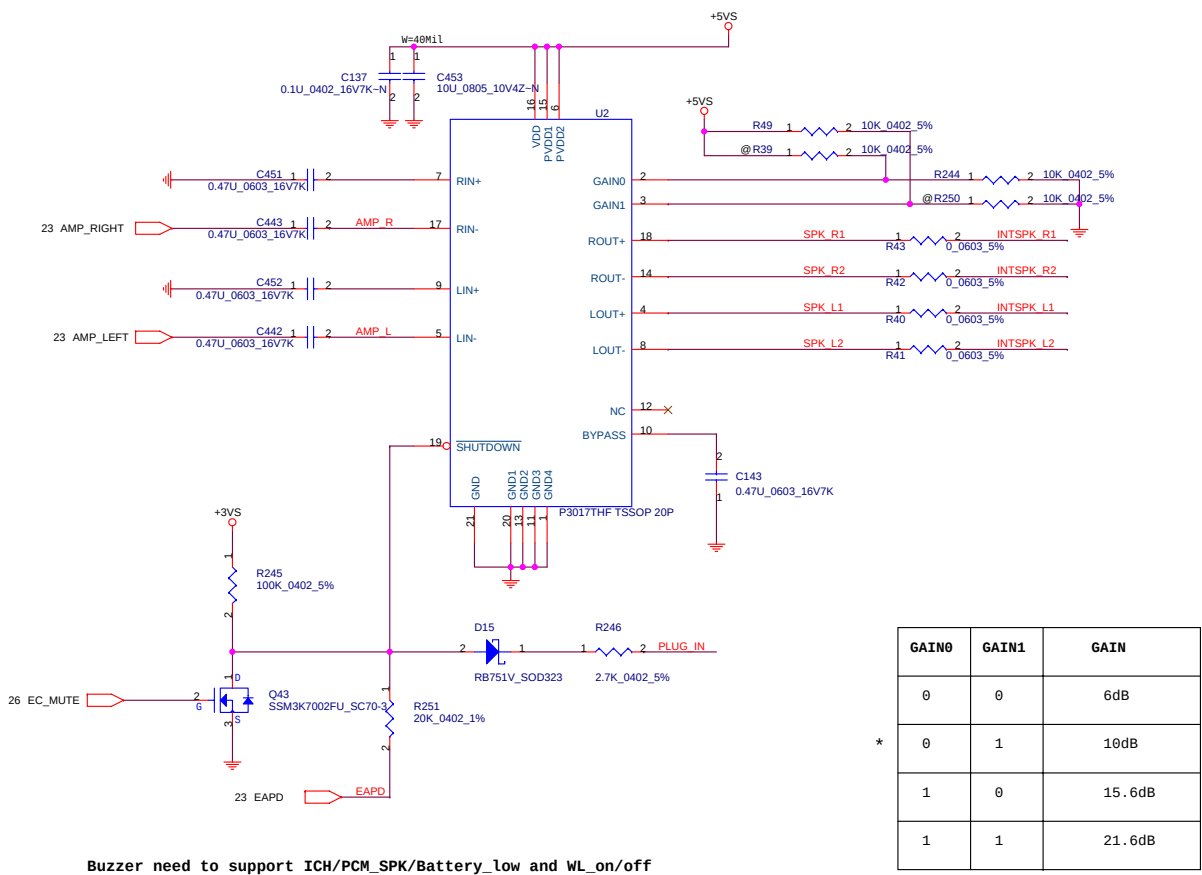
EAPD Control for Vista



Reserved for TEST



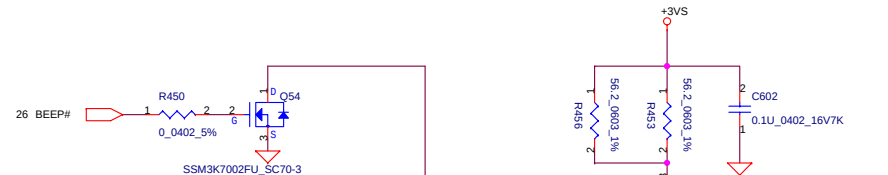
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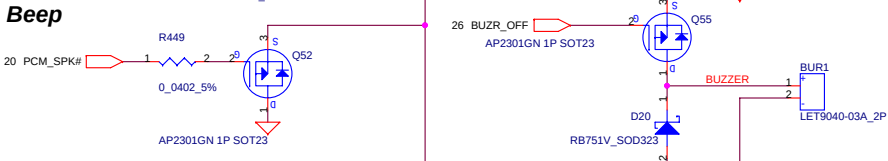
Buzzer need to support ICH/PCM_SPK/Battery_low and WL_on/off

Buzzer

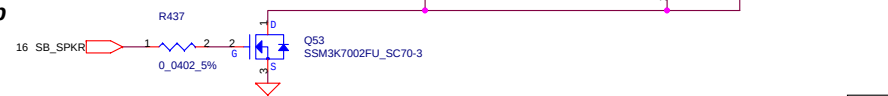
EC Beep



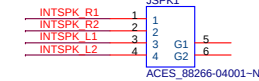
CardBus Beep



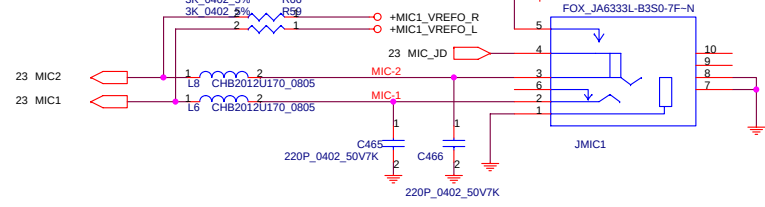
ICH Beep



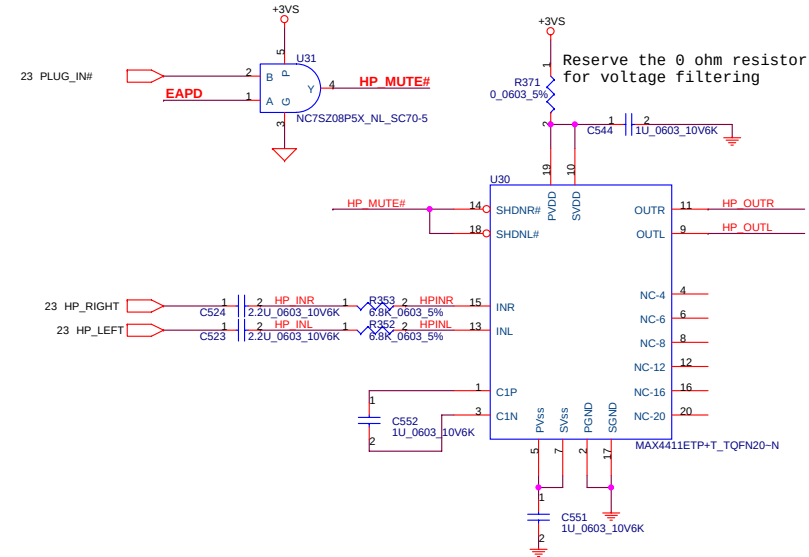
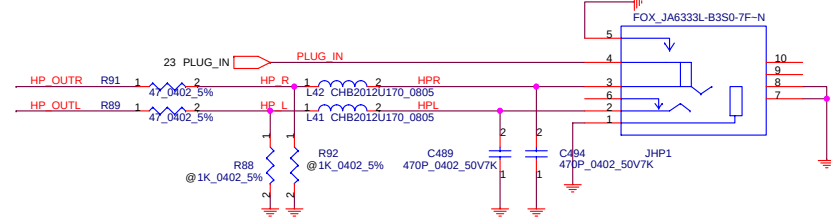
Speaker Connector

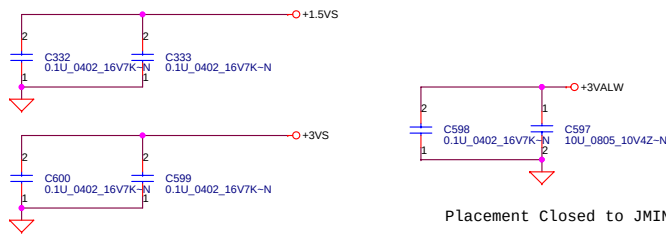


MICROPHONE IN JACK

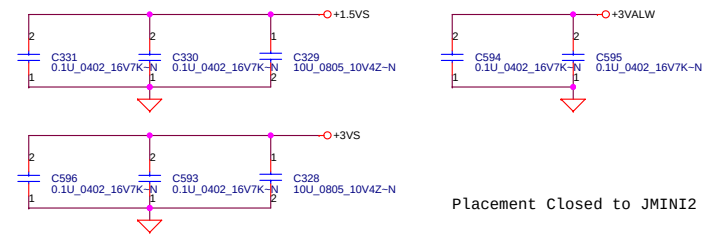
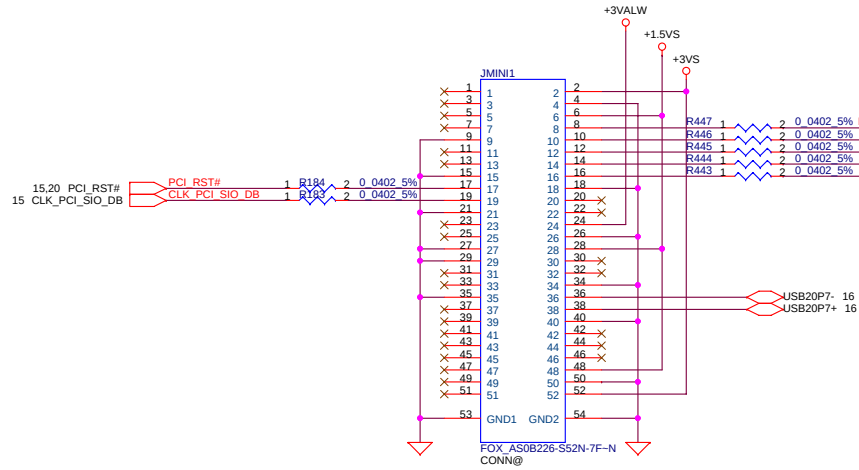


HEADPHONE OUT JACK

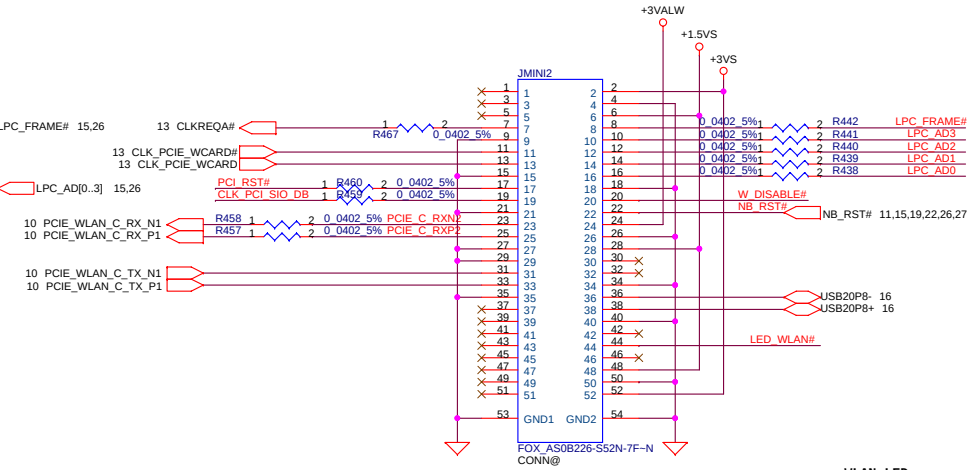




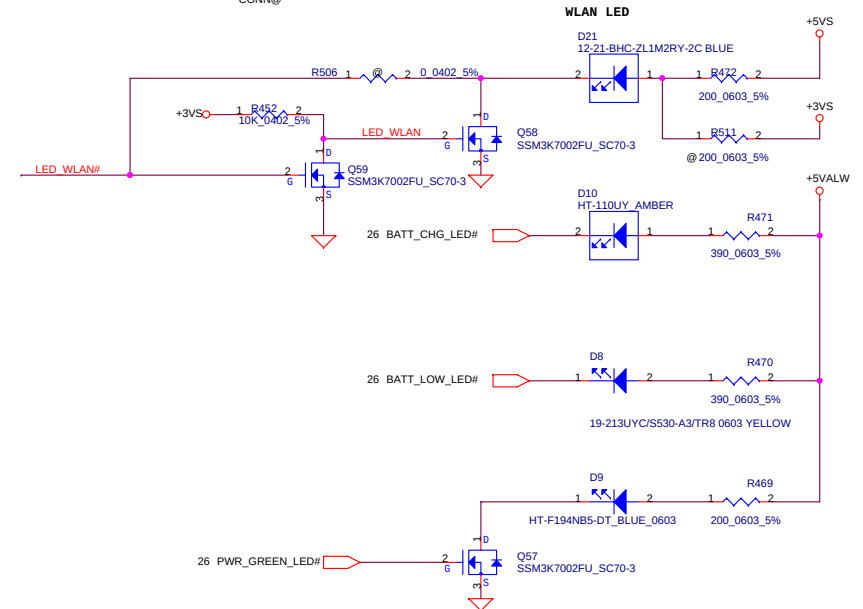
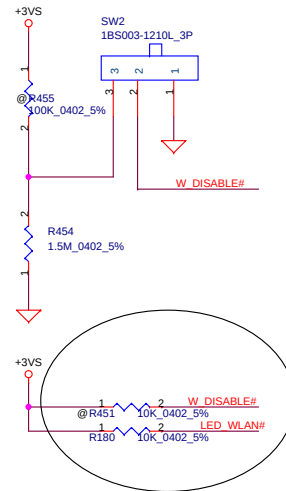
Mini-Express Card (Reserve for debug or TV)



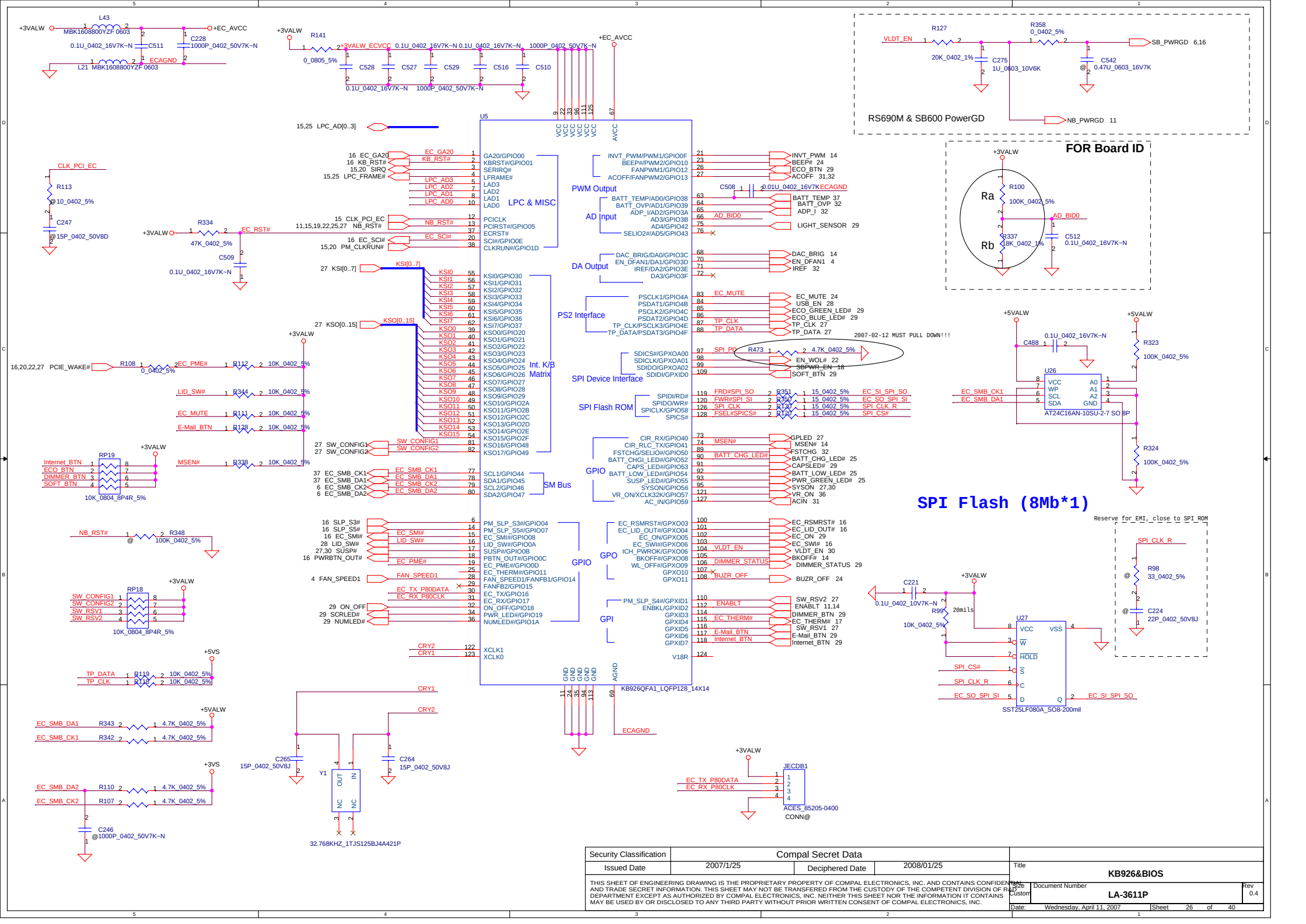
Mini-Express Card



Wireless_BTN Killer switch

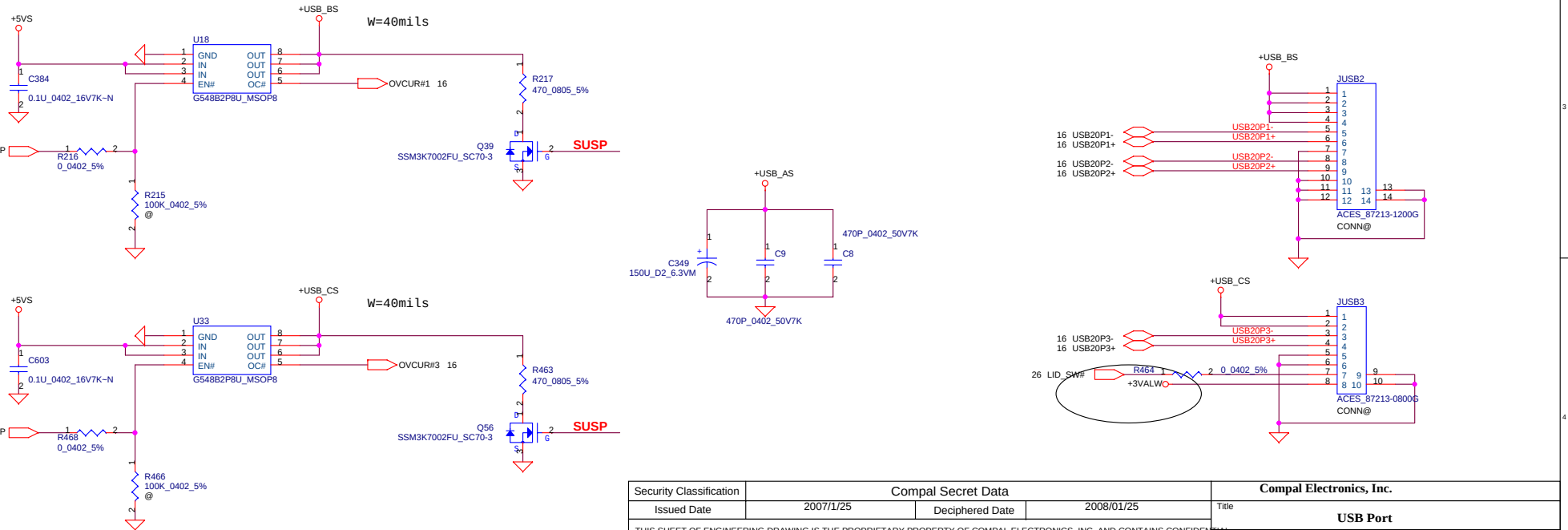
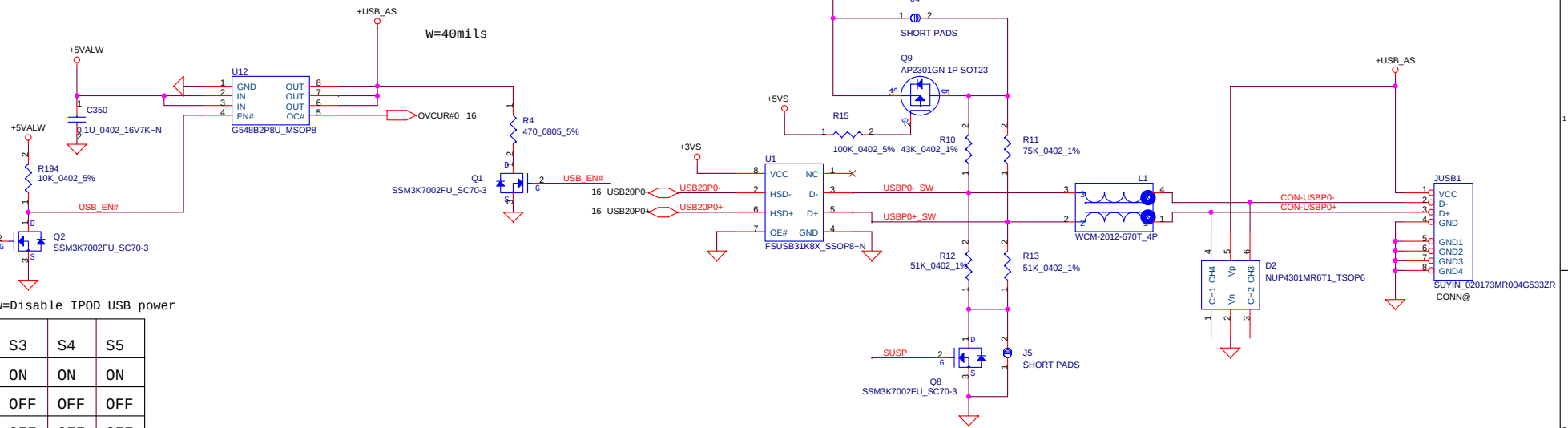


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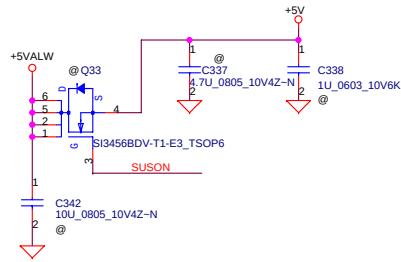
Security Classification	Compal Secret Data					
Issued Date	2007/1/25	Deciphered Date	2008/01/25	Title	MDA/BT/KBD/TP Conn	
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iPOD Charge when system shutdown

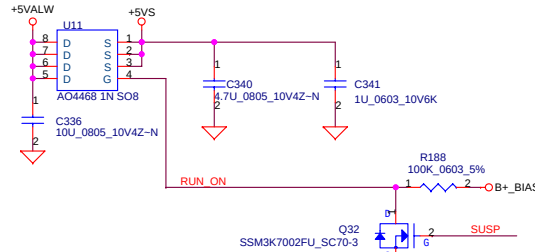


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Issued Date		2007/1/25		Deciphered Date		2008/01/25		Title							
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								Rev		Document Number		LA-3611P		Rev	
								Date		Wednesday, April 11, 2007		Sheet		28 of 40	
C		I		D		E									

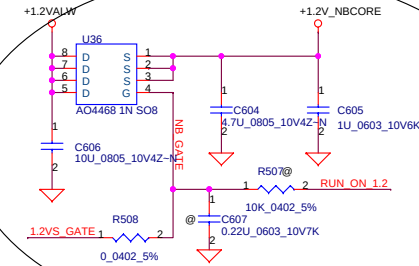
+5VALW TO +5V



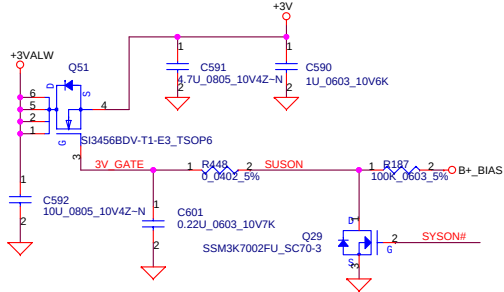
+5VALW TO +5VS



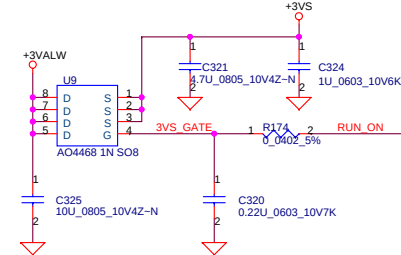
+1.2VALW TO +1.2V_NBCORE



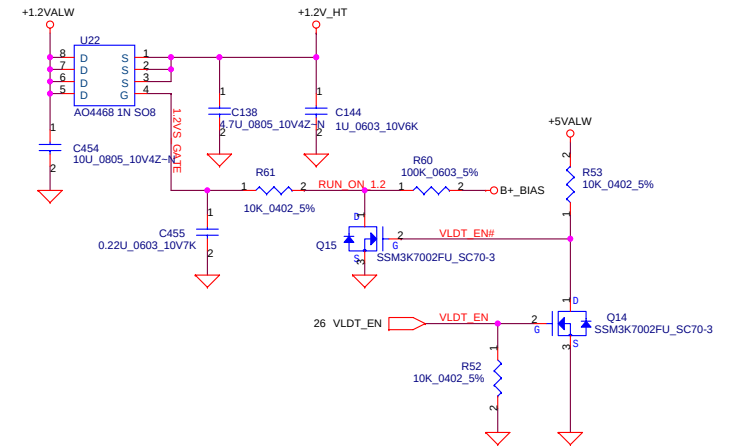
+3VALW TO +3V



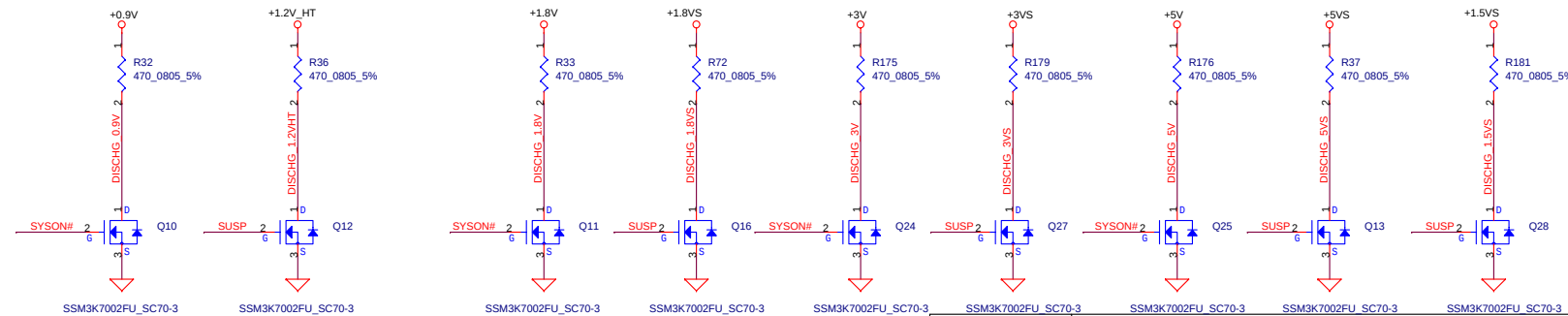
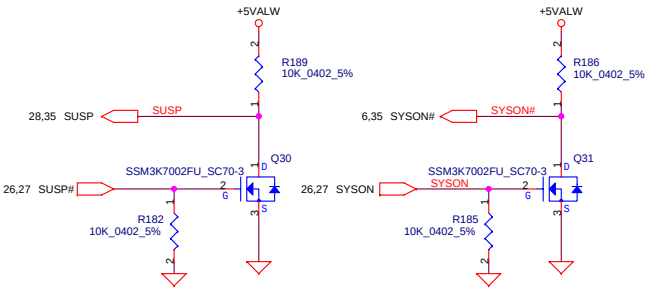
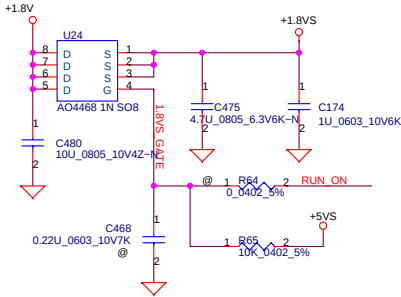
+3VALW TO +3VS



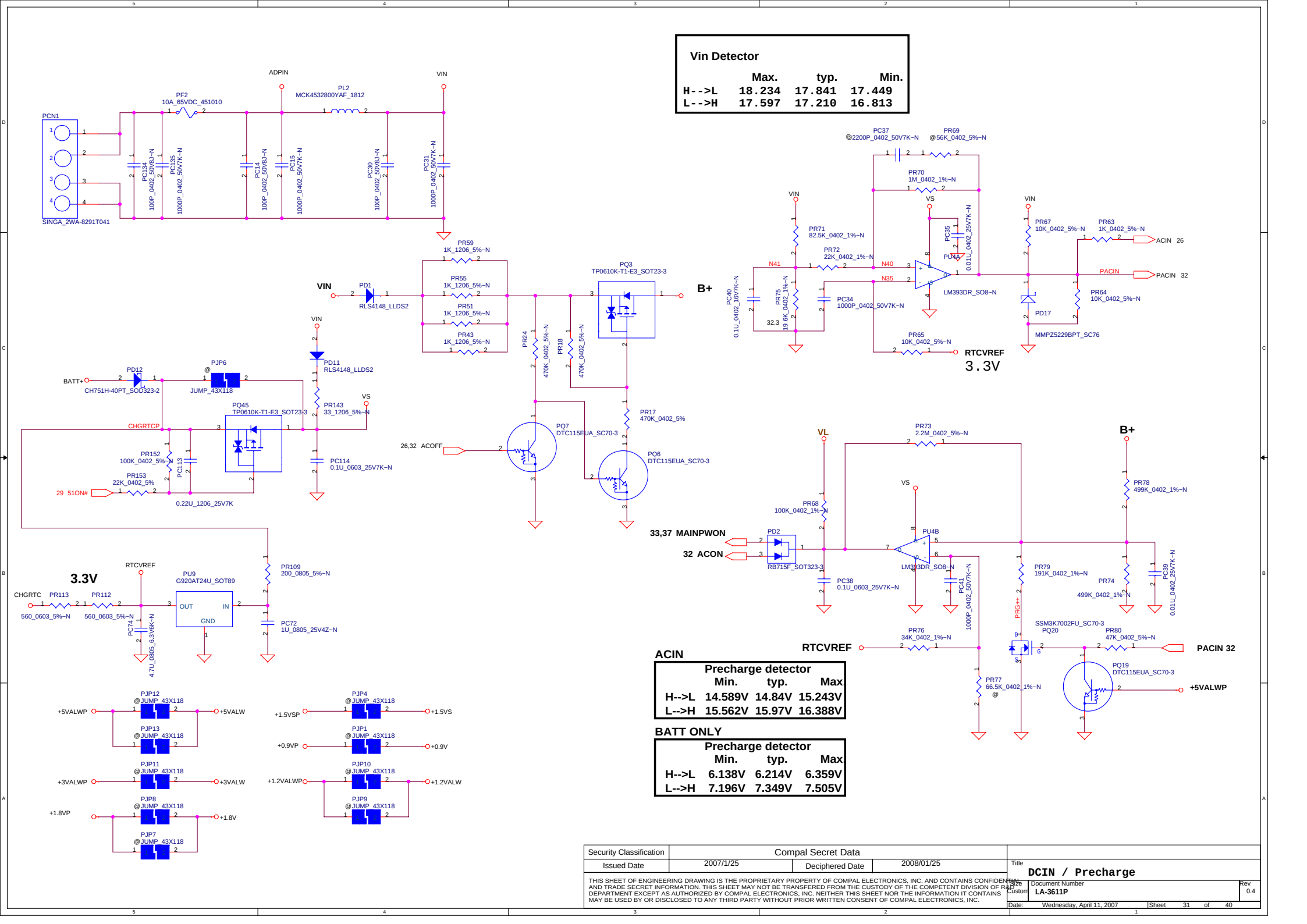
+1.2VALW TO +1.2V_HT



+1.8V TO +1.8VS



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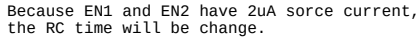
Vin Detector			
	Max.	typ.	Min.
H-->L	18.234	17.841	17.449
L-->H	17.597	17.210	16.813

ACIN			
Precharge detector			
	Min.	typ.	Max
H-->L	14.589V	14.84V	15.243V
L-->H	15.562V	15.97V	16.388V

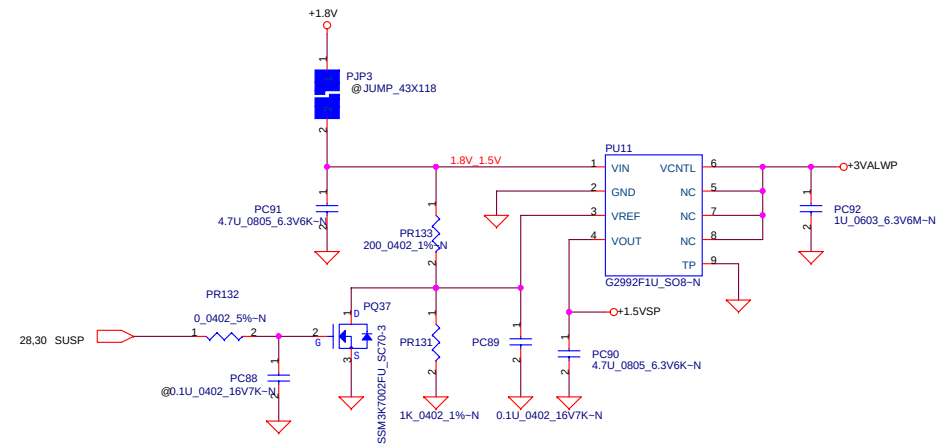
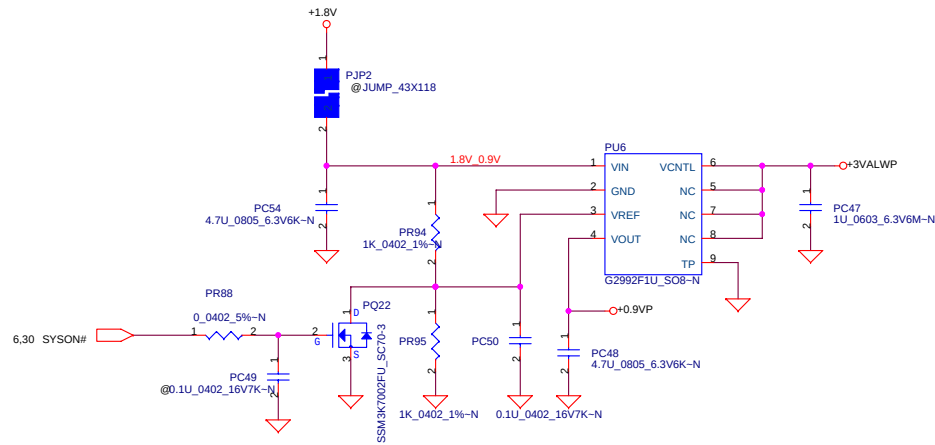
BATT ONLY			
Precharge detector			
	Min.	typ.	Max
H-->L	6.138V	6.214V	6.359V
L-->H	7.196V	7.349V	7.505V

$$F_{osc} = 14100 / R_t = 14100 / 47 = 300 \text{ KHz}$$

LI-4S :18V- - - BATT-0VP=1.498V

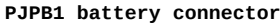


+1.5VSP/+0.9VSP



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Battery Connect/OTP



1.BAT+
2.BAT+
3.ID
4.B/I
5.TS
6.SMD
7.SMC
8.GND
9.GND

PH1 under CPU botten side :
CPU thermal protection at 85 degree C
Recovery at 70 degree C



Security Classification	Compal Secret Data			Title BATTERY CONN
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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	1	PWR-CHARGER	03/02	Compal	in order to slove battery plug in opposite make M/B damaged issue.	Add PR231 to 20ohm PR235 to 2.2 ohm PC142 pin1 to GND,PC143 0.1uF to 0.047uF, PR202 2.2ohm to 20ohm,PR204 18ohm to 20 ohm	0.1
2				Compal			0.1
3				Compal			0.1
4				Compal			0.2
5				Compal			0.3
6				Compal			0.3
7				Compal			0.3
8				Compal			0.3
10				Compal			0.4
11				Compal			0.4
12				Compal			0.4
13				Compal			0.4
14				Compal			0.5
15				Compal			0.5
16				Compal			0.6
17				Compal			

Next: PR238, PC202, PQ56, PD42, PJP21

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Compal Electronics, Inc.	
Title EE-PIR	
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Version change list (P.I.R. List)

Page 1 of 1 for HW

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List
1	EC can't boot by SPI	TP_SPI must pull down	0.3	26	Add R473
2	Can't wake on LAN	Isolate pin must pull high to +3VS	0.3	22	R210 connect to +3VS
3		Improve stable power for NB core	0.3	30	Add U36(+1.2V_NBCORE Control circuit)
4		ME/ID request	0.3	21	Change Media Card Conn
5	WLAN LED abnormal	Reverse LED_WLAN# signal	0.3	25	Add R506 and R511
6	New Card Can't Identified Issue	SB need check CPPE# and CPUSB# signal	0.3	16	New Card CPPE# and CPUSB# connect to U600.B4 (GEVENT6)
7		NECP request	0.4	16	only CPPE# of New Card connect to SB
8		WLAN LED always lighting if not install WLAN CARD	0.4	25	R180 pull high to +3VS
9		ME and ID request	0.4	29	Change JLED1 connertor to 14pin
10		NECP Audio Team request	0.4	23	Change R270 to 23.7K ohm
11					
12					
13					
14					
15					
16					
17					
18					
19					
20					
21					
22					
23					
24					
25					
26					

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