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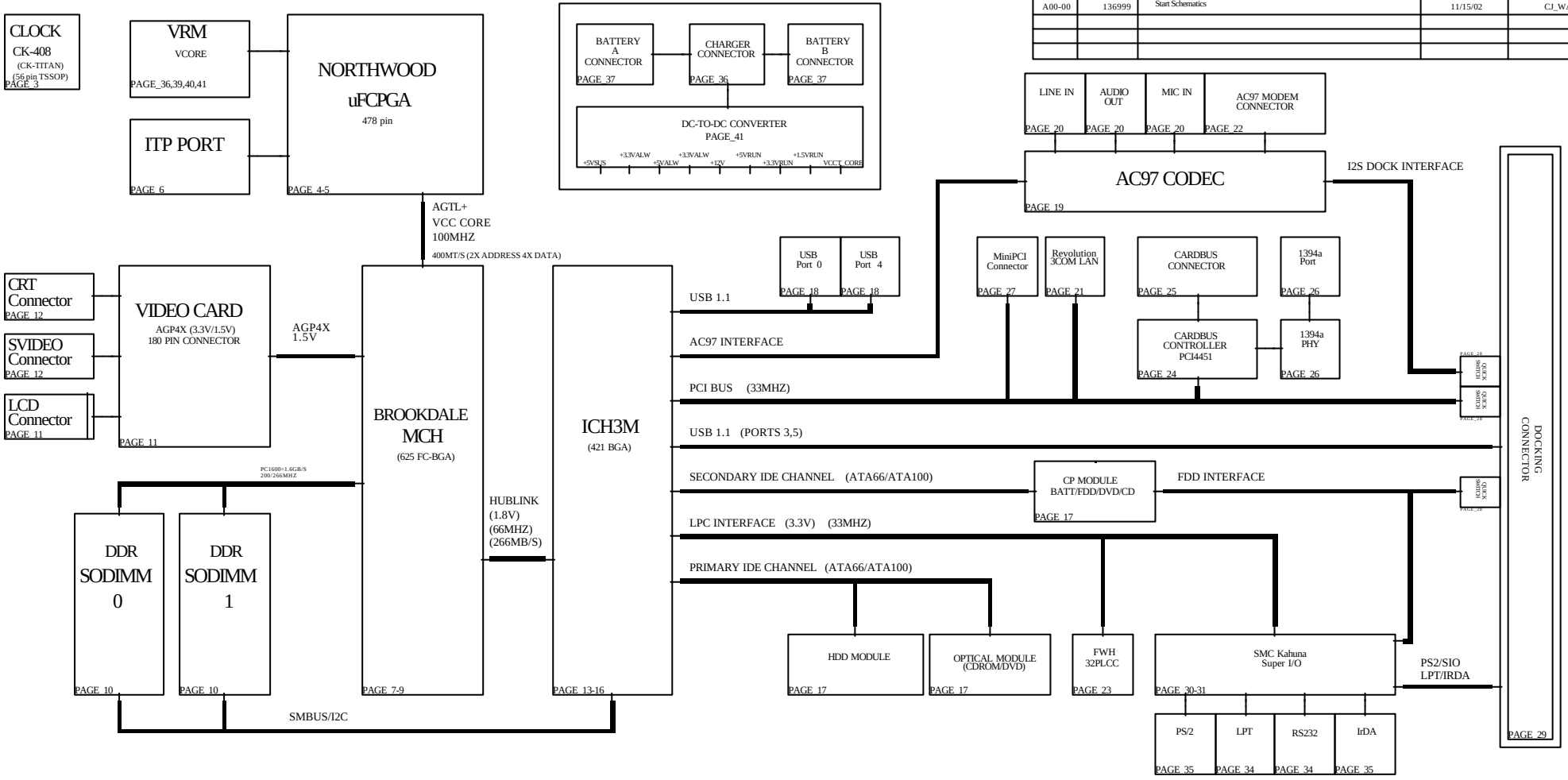
REVISIONS				
REV	ECO	DESCRIPTION	DATE	APPROVED
A00-00	136999	Start Schematics	11/15/02	CJ_WANG



PARTS

ASSY,PWA,MN,NV28,M50 (9Y422)
- PWA,PLN,MN,NV28,M50 (9Y422)
  - ASSEMBLY BARCODE LABEL (XXXXX)
  - ASSEMBLY REVISION LABEL (XXXXX)
  - ASSY DWG,PLN,MN,NV28,M50 (XXXXX)
  - PWB,PLN,MN,CABN (1Y802)
  - SCHEM,PLN,MN,NV28,M50 (C0370)

BOM ADDS APPEAR ON PAGE 42.

- This design requires a font mapping of Fixed to scaleable Arial with a scaling factor of 1.0
- Please remember when copying project files that you must include both the symbols and the schematics

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DELL COMPUTER CORPORATION
AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

DWG NO. 40I195 REV. 4A

DATE: 11/15/02 SHEET 52 OF 102

NEEDS TO BE UPDATED

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2. Index
3. System Clock, SMBUS Selector
4-5. CPU
6. CPU Voltage ID, CPU Temperature Sensor, ITP Debug Connector
7-9. GMCH
10. SODIMM Connectors
11-12. Video Connectors
13-16. ICH
17. CDROM, IDE Connectors
18. USB Hub
19. Audio Codec
20. Audio Connectors, Audio Amplifiers
21. 3COM Revolution (LAN On Motherboard)
22. LAN and Modem Connectors
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24. Cardbus Controller
25. Cardbus Connector
26. IEEE1394 PHY
27. MiniPCI Connector
28. Dock Buffers
29. Dock Connector
30-31. Super I/O Controller (Kahuna)
32. Keyboard/Touchpad Connectors
33. Fan Controllers and Tachometers
34. Parallel and Serial Ports
35. PS2 Connectors, LED Connector, Power Sequencing Logic
36. DC-IN Connector, Charger Board Connector
37. Battery Connectors, NiMH Support
38. Power Plane Enables
39. 1.8V/1.5V Switcher and Sequencer, RTC Power Source
40. VCORE
41. DC to DC Converter
42. Mounting Holes and EMI Clips
43. Schematic Change List
44. Power Rail Diagram
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46-50. REFDES Page Index

POWER MODES

Power Mode	Voltage	Page	Source	ON	OFF
RBAT	7.2V	32, 36		Always	
PBATT+	17V-13V	36, 37, CB-3		Always	
SBATT+	17V-13V	36, 37, CB-3		Always	
DOCK_DC_IN	20V	29, 36		Always	
DC_IN	20V	29, 36, CB-2, 3		Always	
SDC_IN	20V	CB-2, 3		Always	
V_CHG	15V-17.5V	CB-2		Note 1	
PWR_SRC	7.2V-20V	8, 11, 29, 36, 38, 39, 40, 41		Always	
RTCSRC	7.2V-20V	36, 39	For source power on all rails, See page 44	Always	
3VSUS	3.3V	4, 6, 10-16, 18, 21-25, 27, 30-31, 35, 38-41		S1-S3	S4-S5
RTC_PWR	5V	36, 38, 39, 41, 42		Always	
5VSUS	5V	8, 11, 16, 18-19, 22, 25, 29, 33-35, 38-42		S1-S3	S4-S5
2.5VSUS	2.5V	8-10, 39		S1-S3	S4-S5
VCC_CORE	0.6V-1.75V	4,5-9, 16, 23, 38,40		S1-S2	S3-S5
12V	12V	3, 11, 25, 29, 33, 36, 38, 41		S1-S2	S3-S5
DOCK_PWR_SRC	13V-20V	29		Docked ALW	
3VRUN	3.3V	3, 4, 6, 11, 13-14, 16-17, 19, 21-23, 26-28, 30-31, 33, 35, 38		S1-S2	S3-S5
VAUX	3.3V	27		Note 2	Note 2
1.8VSUS	1.8V	16, 38-39		S1-S3	S4-S5
1.8VRUN	1.8V	7, 8, 9, 13,16, 38		S1-S2	S3-S5
5VRUN	5V	6, 11-12, 16-17, 20, 27-28, 30, 32-35, 38, 42		S1-S2	S3-S5
5VHDD	5V	17, 38		S1-S2	S3-S5
5VMOD	5V	17, 38		S1-S2	S3-S5
1.5VRUN	1.5V	7, 9, 11, 38-39		S1-S2	S3-S5
STRB-_5V	5V	29, 34, 38		S1-S2	S3-S5
VREFCG	0.7V	11		S1-S2	S3-S5
V_1P25V_DDR_VTT	1.25V	8, 10, 11, 38, 39		S1-S2	S3-S5
CRT_VCC	5V	12		S1-S2	S3-S5
5V_QDOCK	5V	28, 30		S1-S2	S3-S5
5VFDDPP	5V	17, 31, 38		S1-S2	S3-S5
5VSUS_DOCKED	5V	29		Docked S1-S3	Docked S4-S5
USB_PWR_0/4	5V	18		S1-S2	S3-S5
VDDA	4.65V	19, 20		S1-S2	S3-S5
VDDA2	2.2V	19, 20		S1-S2	S3-S5
1.2V_VCCVID	1.2V	4, 40		S1-S3	S4-S5
5VALW	5V	11, 29, 30-32, 35-37, 39, 41		Note 4	Note 4
3.3VRTC	3.3V	16, 31, 36, 39, 41		Always	
3VALW	3.3V	6, 17, 30-32, 35, 36, 39, 41		Note 4	Note 4
VCC_RTC	3.3V	14, 16		Always	
ICH_VBIAS	3.3V	14, 16		S1-S2	S3-S5
3VPHYD	3.3V	26		S1-S2	S3-S5
3VPHYA	3.3V	26		S1-S2	S3-S5
CK_VDD_MAIN	3.3V	3		S1-S2	S3-S5
CK_VDD_CORE	3.3V	3		S1-S2	S3-S5
V_3P3_LAN	3.3V	21, 22		S1-S3	S4-S5
V_3P3_LAN_VDDTX	3.3V	21		S1-S3	S4-S5
V_3P3_LAN_VDDRX	3.3V	21		S1-S3	S4-S5
V_MCH_GTLREF	0.67xVCC_CORE	8		S1-S2	S3-S5
S1_VPP	0V-12V Note A	25		S1-S3	S4-S5
S2_VPP	0V-12V Note A	25		S1-S3	S4-S5
S1_VCC	0V-5V Note A	24, 25		S1-S3	S4-S5
S2_VCC	0V-5V Note A	24, 25		S1-S3	S4-S5
S1_VCCF	0V-5V Note A	24, 25		S1-S3	S4-S5
S2_VCCF	0V-5V Note A	24, 25		S1-S3	S4-S5
HL_REF	0.9V	8, 13		S1-S2	S3-S5
H_VCCA	1.2V	4		S1-S3	S4-S5
H_VCCIOPLL	1.2V	4		S1-S3	S4-S5
H_GTLREF	0.67xVCC_CORE	4		S1-S2	S3-S5
H_VSSA	1.2V	4		S1-S3	S4-S5
V_DDR_MCH_REF	1.25V	8, 10, 39		S1-S3	S4-S5

Note A Voltage delivered to these pins is determined from the type card inserted to the PCMCIA controller.

Note 1 Rail is available anytime AC and BATTERY are both present, and the BIOS is not inhibiting charge

Note 2	NB powered by AC	VAUX	S1-S3	S4 - S5
	NB powered by BATT	VAUX	S1-S2	S3 - S5

Note 3 Rail is available anytime the HEADPHONE jack is occupied

Note 4	NB powered by AC	3VALW	Always	
	NB powered by BATT	3VALW	S1-S3	S4 - S5
	NB powered by AC	5VALW	Always	
	NB powered by BATT	5VALW	S1-S3	S4 - S5

PCI TABLE

DEVICE	IDSEL	IRQ	REQ/GNT	DREQ/DGNT
DOCK	AD24	PIRQC*		REQ0*/GNT0*
3COM	AD16	PIRQC*		REQ4*/GNT4*
VIDEO	IDSEL	PIRQA* PIRQB*		
PCI4451	AD17	PIRQD*	REQB*/GNTB*	REQ1*/GNT1*
M3i	AD18	PIRQA*	REQA*/GNTA*	REQ2*/GNT2*
MINIPCI	AD19	PIRQB* PIRQD*		REQ3*/GNT3*

BOM BUILD OPTIONS

Option	Description
0	Base Build
1	FDD_PP

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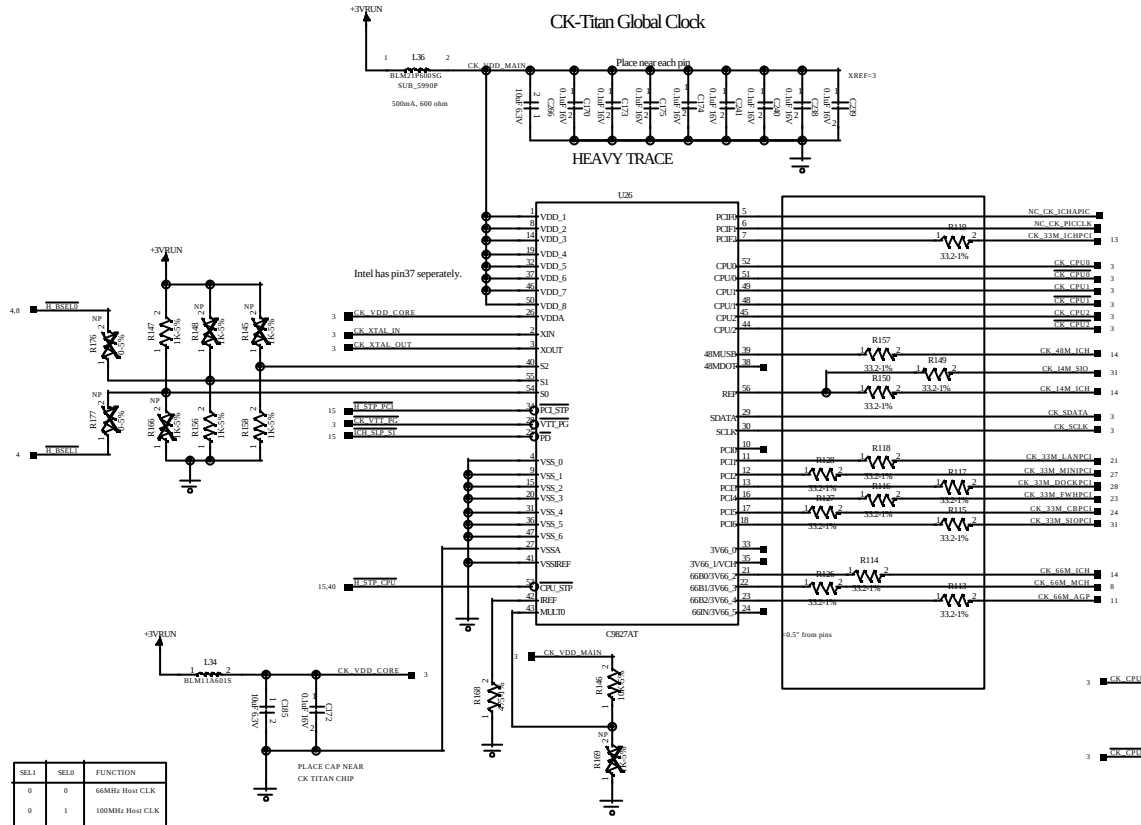
DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

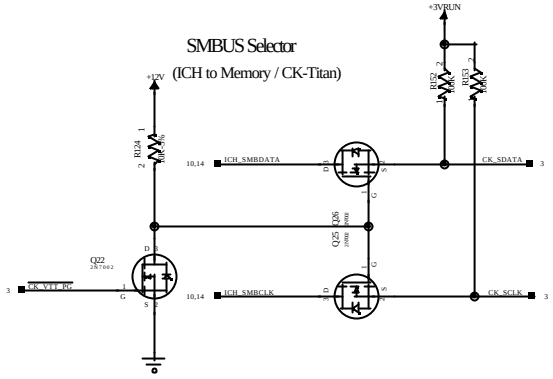
DWG NO. 401195 REV. 4A

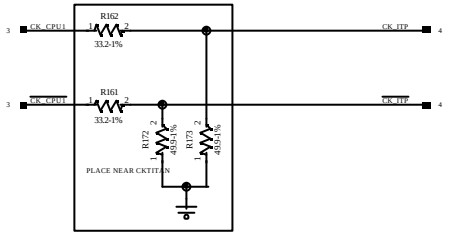
DATE 11/15/02 SHEET 53 OF 102

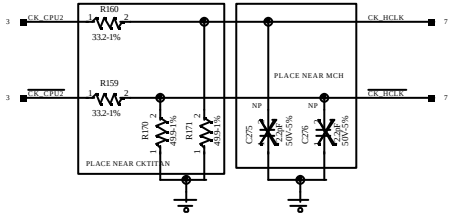


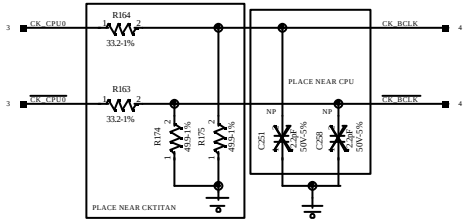
SEL1	SEL0	FUNCTION
0	0	66MHz Host CLK
0	1	100MHz Host CLK
1	0	200MHz Host CLK
1	1	133MHz Host CLK

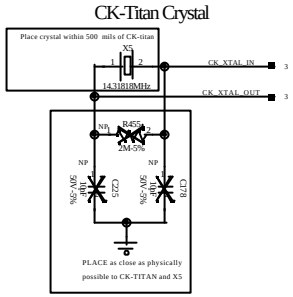
MULT	IREF	CLOCK SWING
1	475	0.7 Volts
0	221	1.0 Volts

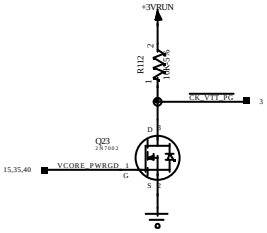












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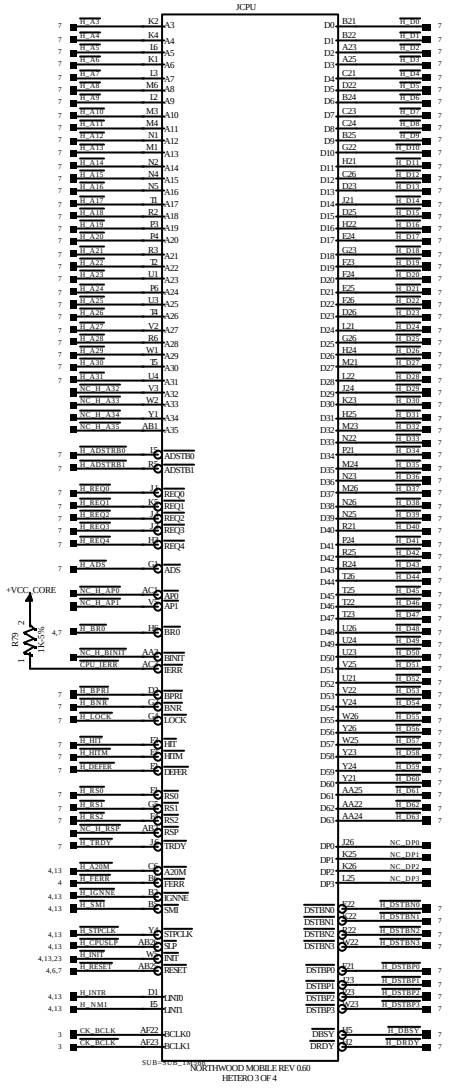
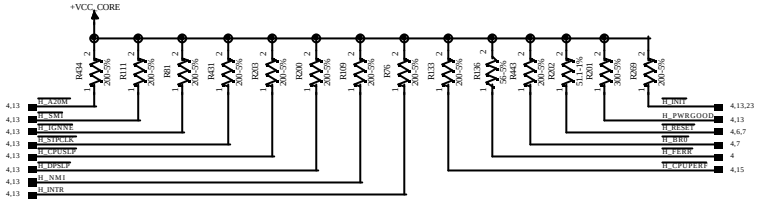
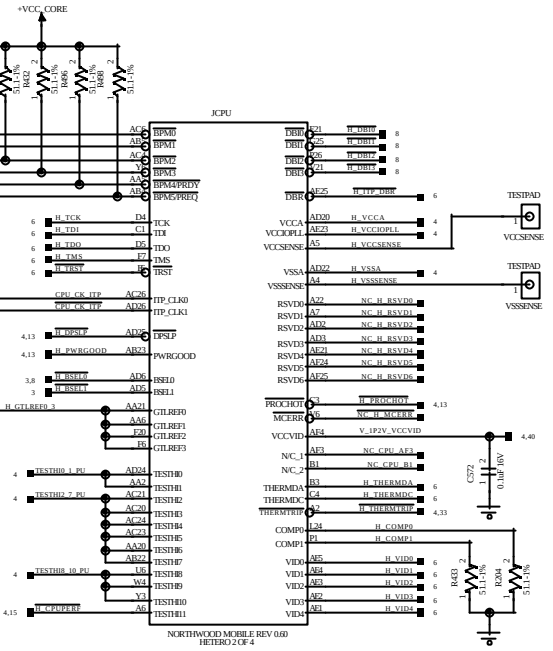
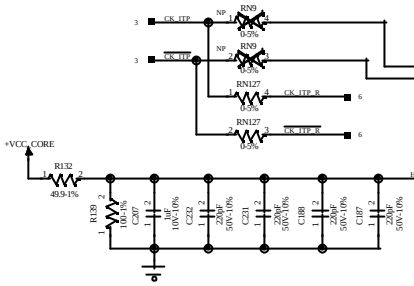
DWG NO. 401195 REV. 4A

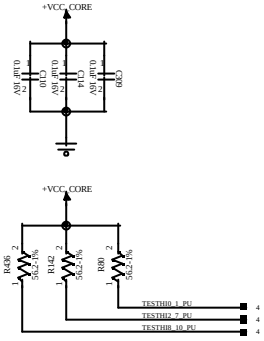
DATE: 11/15/02 SHEET 54 OF 102

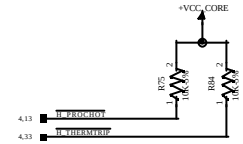


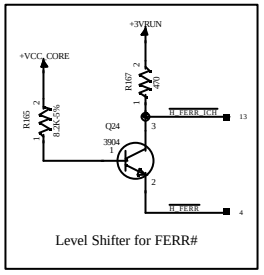


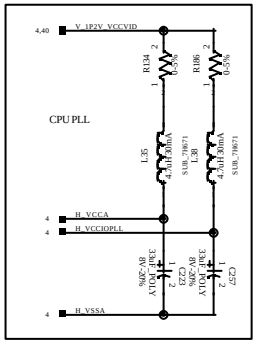












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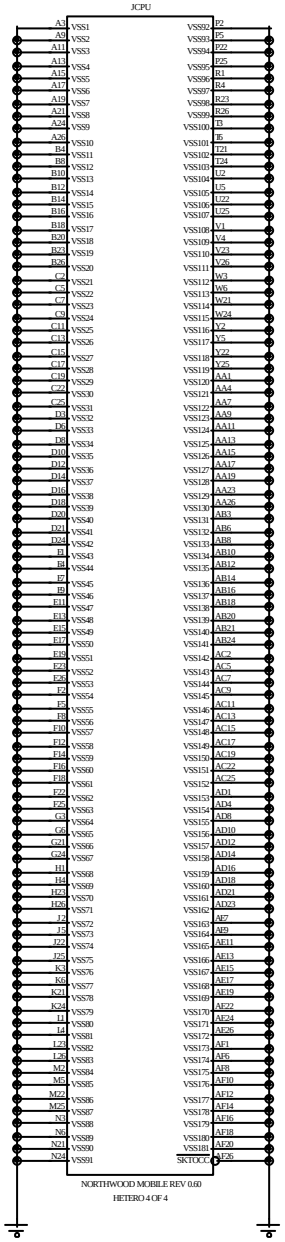
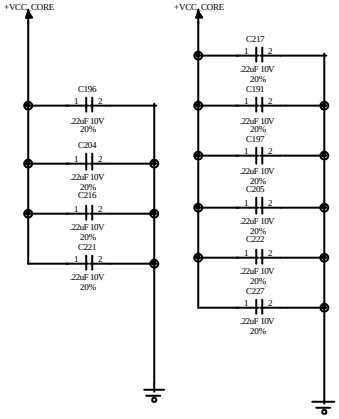
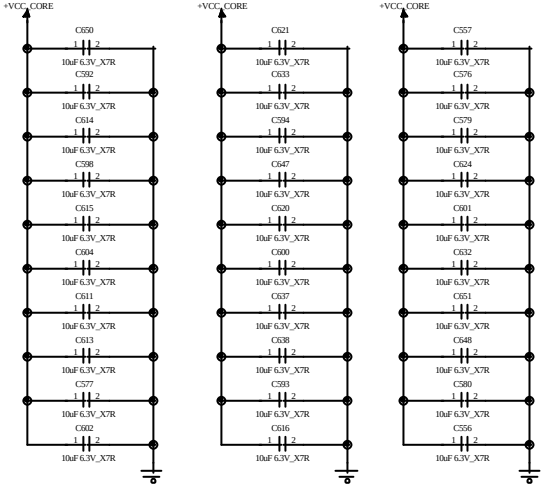
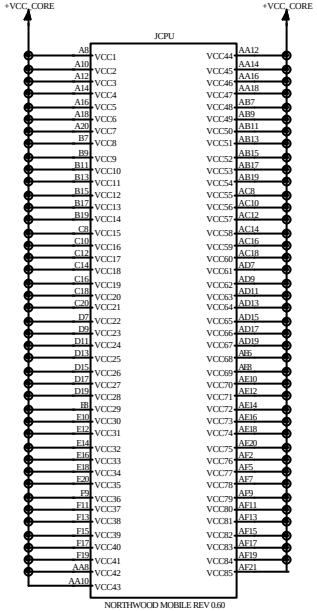


CPU High Frequency Decoupling



CPU Bulk Decoupling





Place caps close to JCPU
ALL CAPS NEED TO BE PLACED
DIRECTLY UNDER JCPU, WHEN
POSSIBLE USE TWO VIAS/PAD

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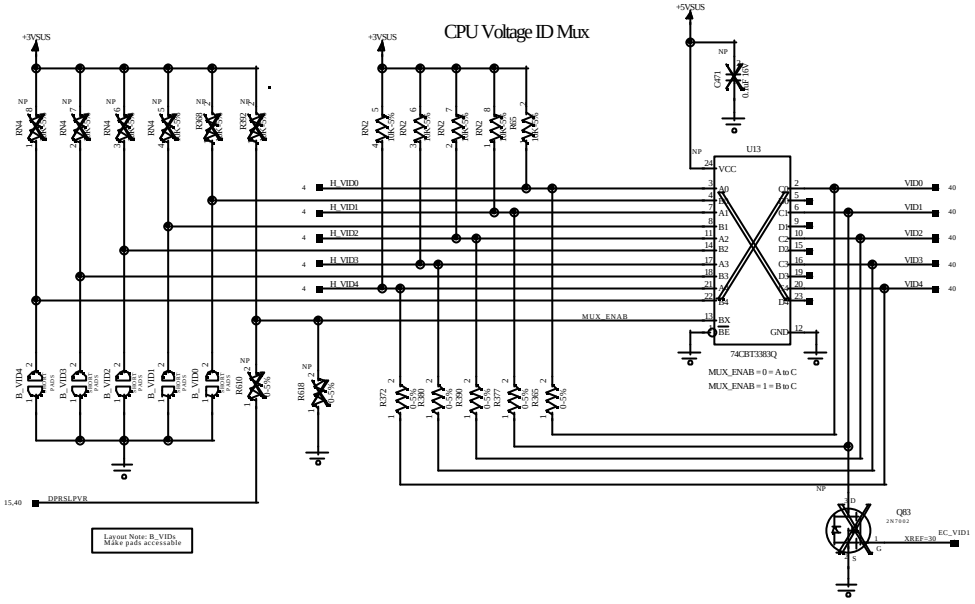
CPU Voltage ID Mux

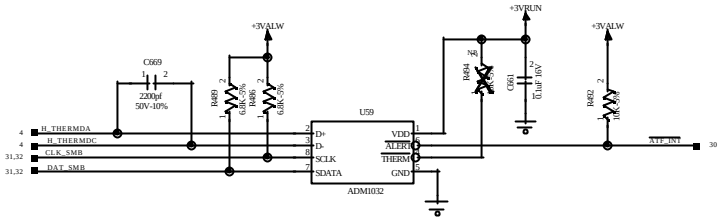


O = pads open
S = pads shorted

B_VID Setting						B_VID Setting					
4	3	2	1	0	VCC_CORE	4	3	2	1	0	VCC_CORE
S	S	S	S	S	1.750V	O	S	S	S	S	0.9750V
S	S	S	S	O	1.700V	O	S	S	S	O	0.950V
S	S	S	O	S	1.650V	O	S	S	O	S	0.925V
S	S	S	O	O	1.600V	O	S	S	O	O	0.900V
S	S	O	S	S	1.550V	O	S	O	S	S	0.875V
S	S	O	S	O	1.500V	O	S	O	S	O	0.850V
S	S	O	O	S	1.450V	O	S	O	O	S	0.825V
S	S	O	O	O	1.400V	O	S	O	O	O	0.800V
S	O	S	S	S	1.350V	O	O	S	S	S	0.775V
S	O	S	S	O	1.300V	O	O	S	S	O	0.750V
S	O	S	O	S	1.250V	O	O	S	O	S	0.725V
S	O	S	O	O	1.200V	O	O	S	O	O	0.700V
S	O	O	S	S	1.150V	O	O	O	S	S	0.675V
S	O	O	S	O	1.100V	O	O	O	S	O	0.650V
S	O	O	O	S	1.050V	O	O	O	O	S	0.625V
S	O	O	O	O	1.000V	O	O	O	O	O	0.600V

Layout Note: B_VIDs
Make pads accessable

CPU Temperature Sensor



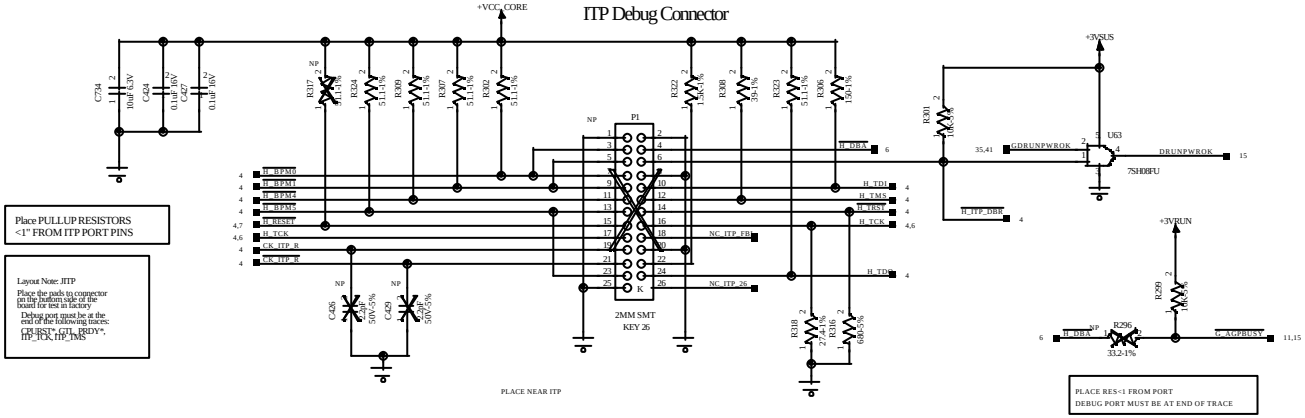
Layout Note: CPU Temperature Sensor
Place this circuit on the
bottom side of the mainboard

ITP Population Matrix

Ref. Des.	ITP Used	ITP Not Used
R264	510 ohms	1.5k
R255	pop	no pop
R257	pop	no pop
R265	no pop	1k pull-down
R258	pop	no pop
R140 (p.7)	pop	no pop
R249	pop	no pop
R248	pop	no pop
R280	no pop	no pop
R281	pop	no pop
R279	pop	no pop
R278	pop	no pop
R283	pop	no pop
R275	pop	no pop
R273	pop	no pop
R256	39 ohms	1k
R251	no pop	pop
R271	pop	no pop
R263	pop	no pop
R272	pop	pop

Circuit Not Complete. Pending input from Intel.

ITP Debug Connector



Place PULLUP RESISTORS
<1" FROM ITP PORT PINS

Layout Note: JITP
Place the pads to connector on the bottom side of the board for test in factory
Debug port must be at the end of the following traces:
CPURST, CPU_RSTDY*, ITP_TCK, ITP_TMS

PLACE NEAR ITP

PLACE RES<1 FROM PORT
DEBUG PORT MUST BE AT END OF TRACE

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TITLE: SCHEMATIC, M/B LA-1221

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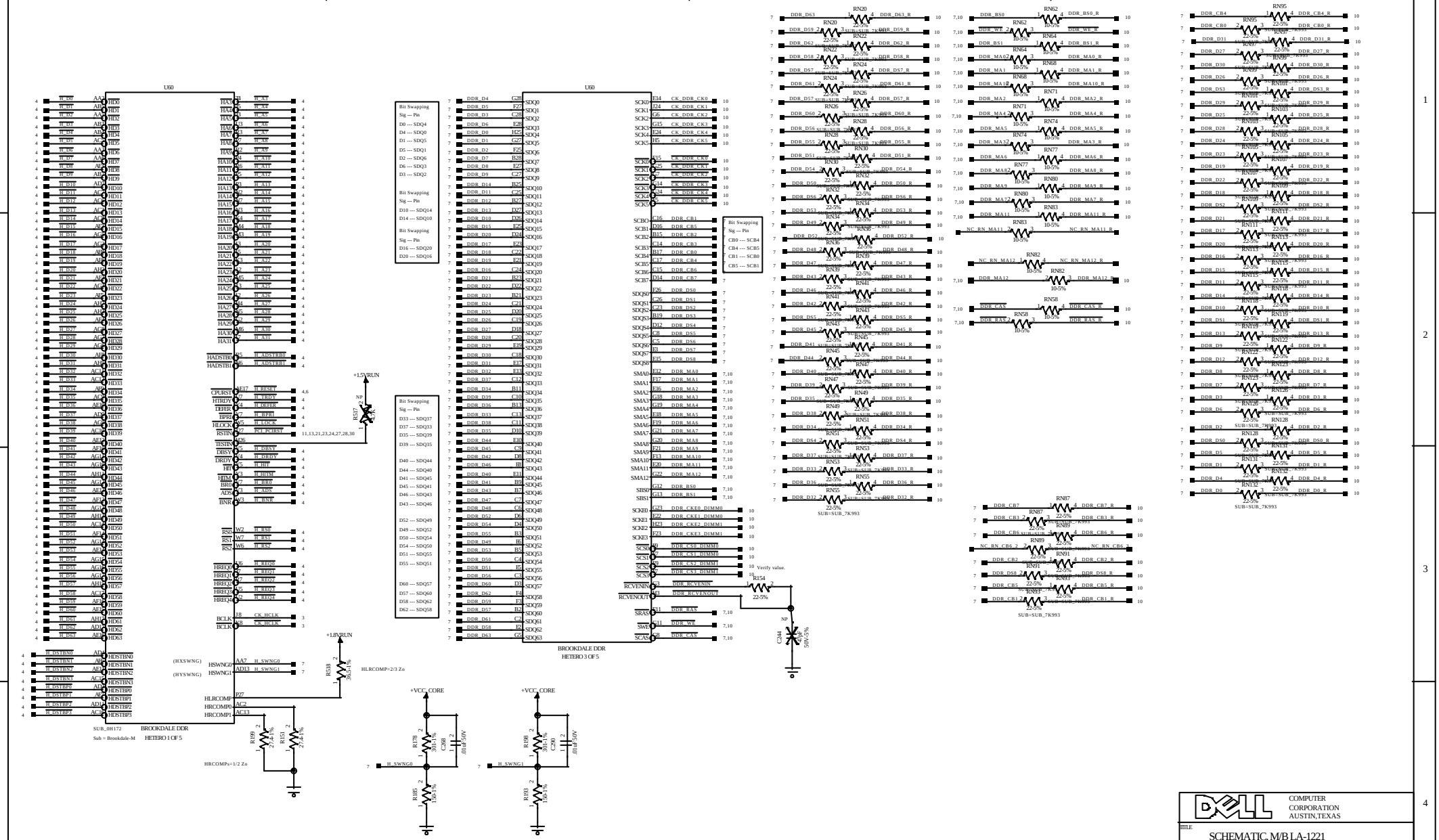

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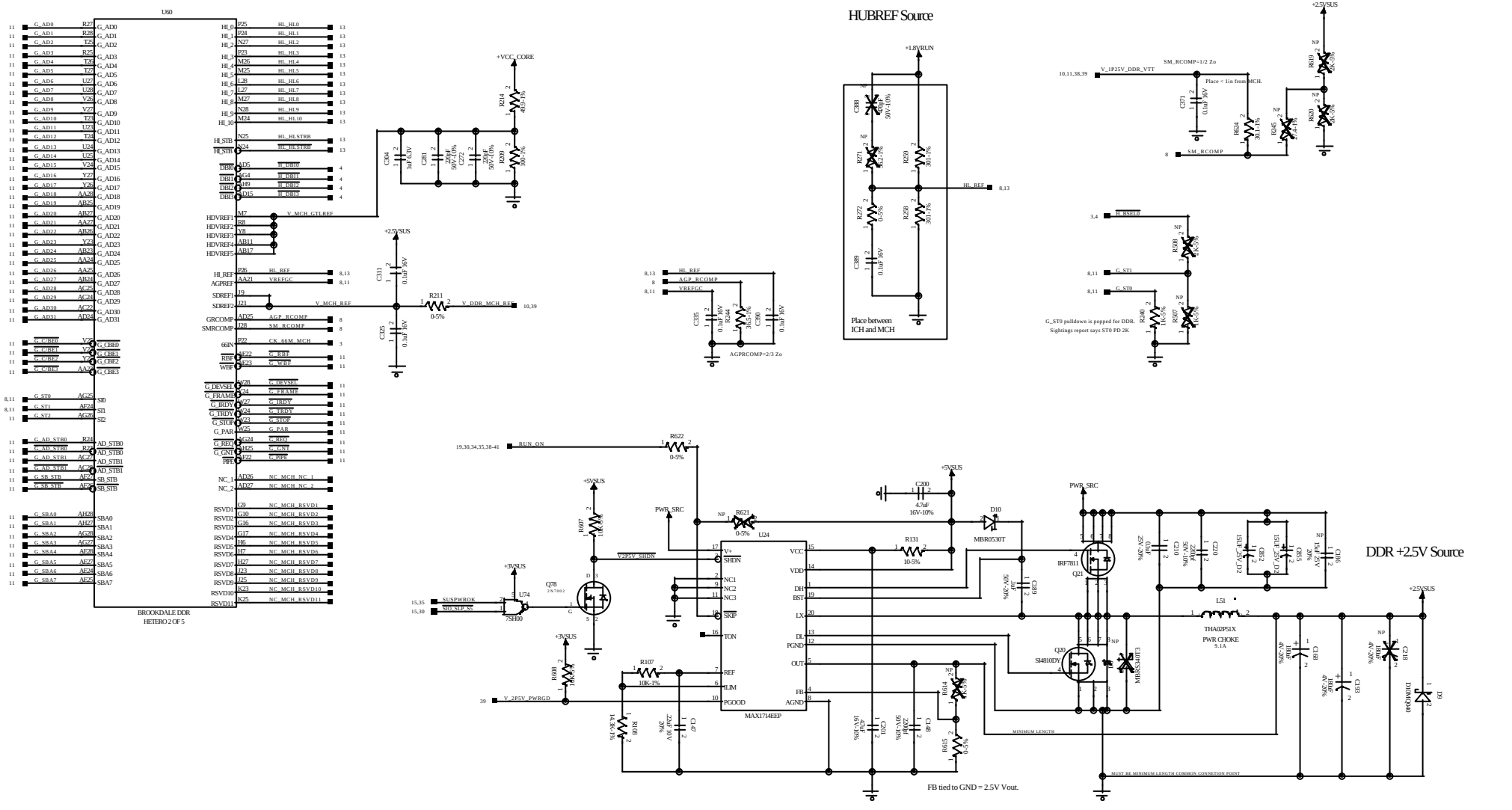

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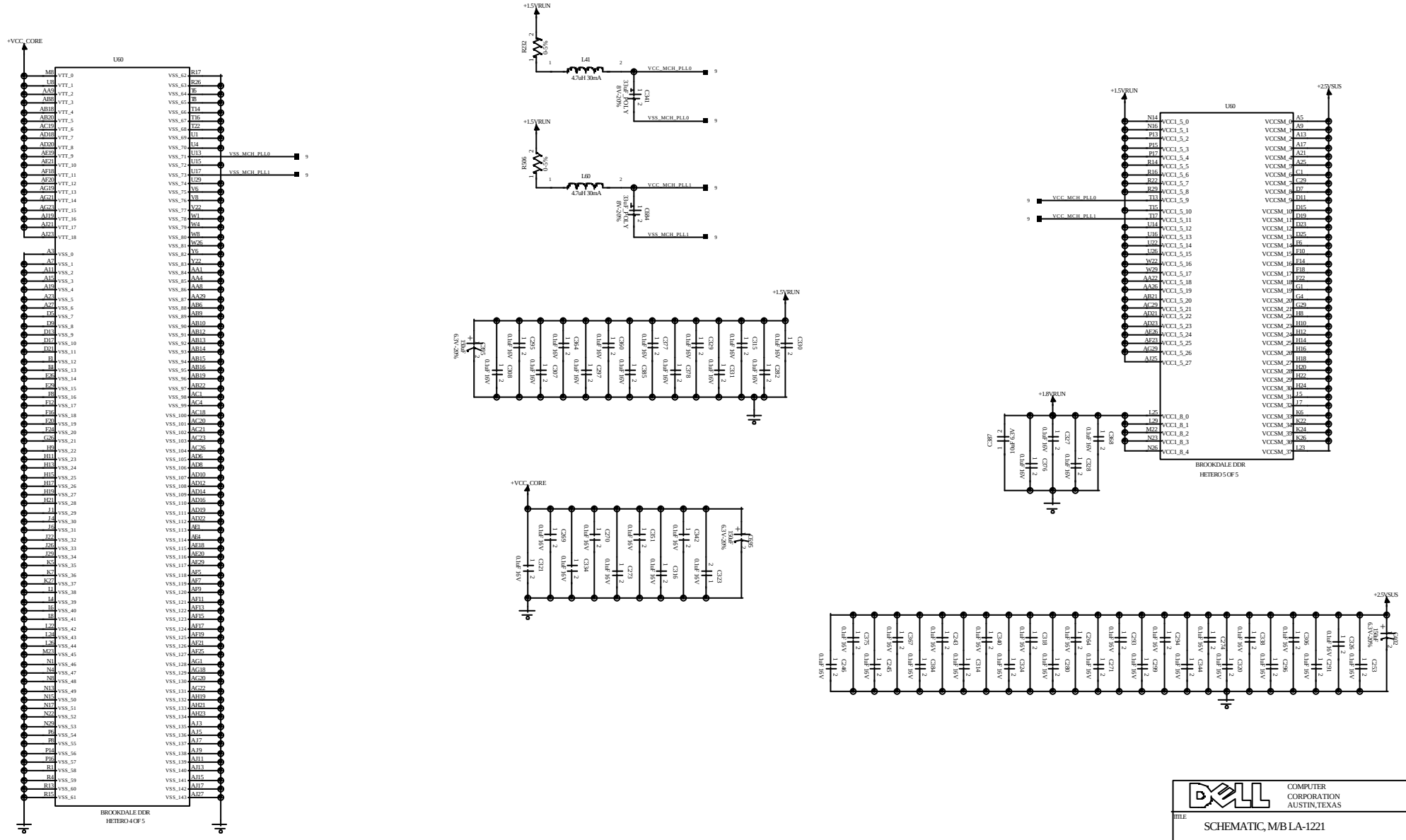

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

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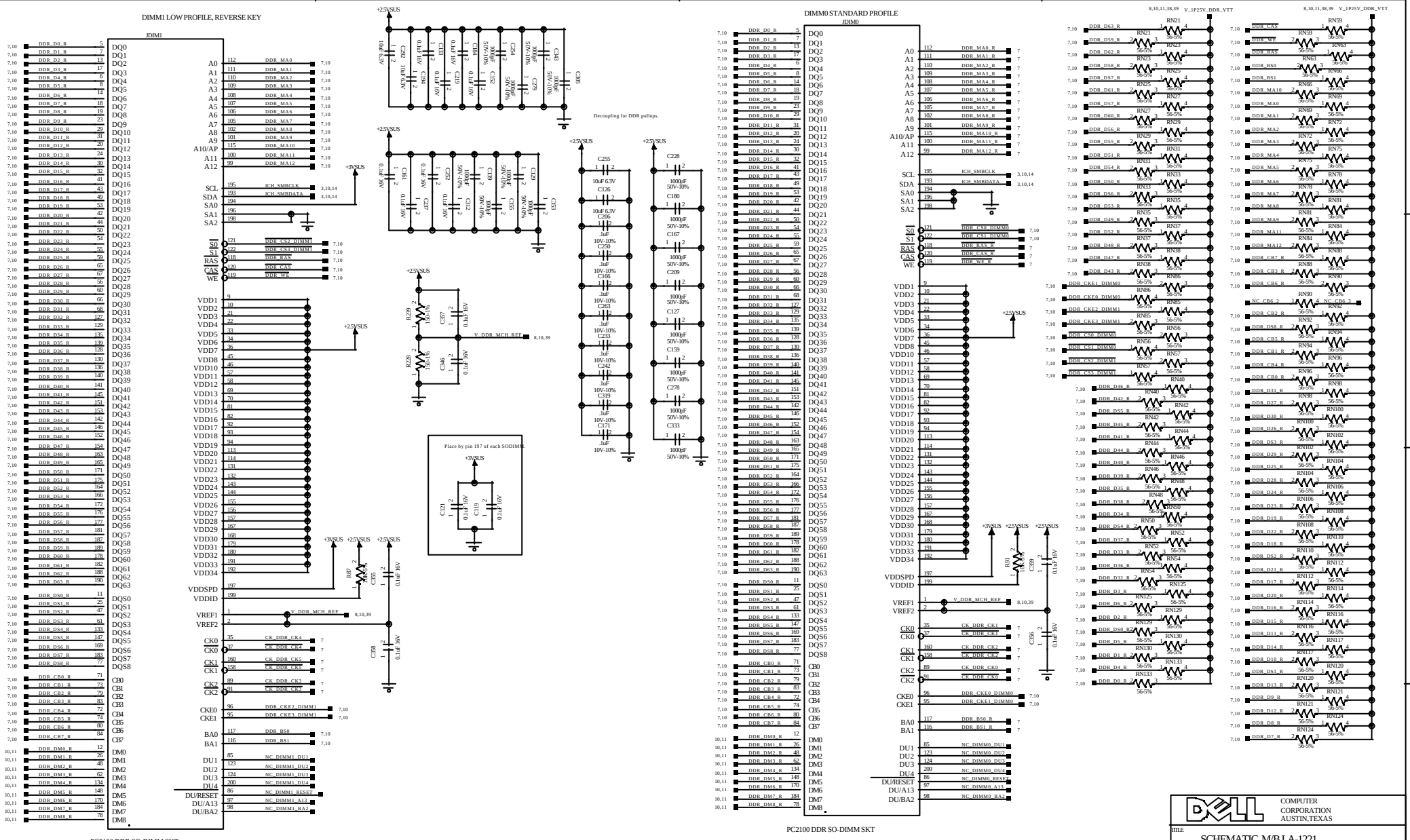
11-20-2002_22:38
DIMM1 LOW PROFILE, REVERSE KEY
JDIM1
PC2100 DDR SO-DIMM SKT
REVERSE KEY
Decoupling for DDR pullups.
Place by pin 197 of each SODIMM.
DIMM0 STANDARD PROFILE
JDIM0
PC2100 DDR SO-DIMM SKT
V_DDR_MCH_REF
V_1P25V_DDR_VTT
+2.5VSUS
+3VSUS
ICH_SMBCLK
ICH_SMBDATA
DDR_CS2_DIMM1
DDR_CS3_DIMM1
DDR_RAS
DDR_CAS
DDR_WE
CK_DDR_CK4
CK_DDR_CK5
CK_DDR_CK3
DDR_CKE2_DIMM1
DDR_CKE3_DIMM1
DDR_BS0
DDR_BS1
NC_DIMM1_DU1
NC_DIMM1_DU2
NC_DIMM1_DU3
NC_DIMM1_DU4
NC_DIMM1_RESET
NC_DIMM1_A13
NC_DIMM1_BA2
DDR_CS0_DIMM0
DDR_CS1_DIMM0
DDR_RAS_R
DDR_CAS_R
DDR_WE_R
DDR_CKE0_DIMM0
DDR_CKE1_DIMM0
CK_DDR_CK1
CK_DDR_CK2
CK_DDR_CK0
NC_DIMM0_DU1
NC_DIMM0_DU2
NC_DIMM0_DU3
NC_DIMM0_DU4
NC_DIMM0_RESET
NC_DIMM0_A13
NC_DIMM0_BA2
DELL
COMPUTER
CORPORATION
AUSTIN,TEXAS
TITLE
SCHEMATIC, M/B LA-1221
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REV. 4A
DATE 11/15/02
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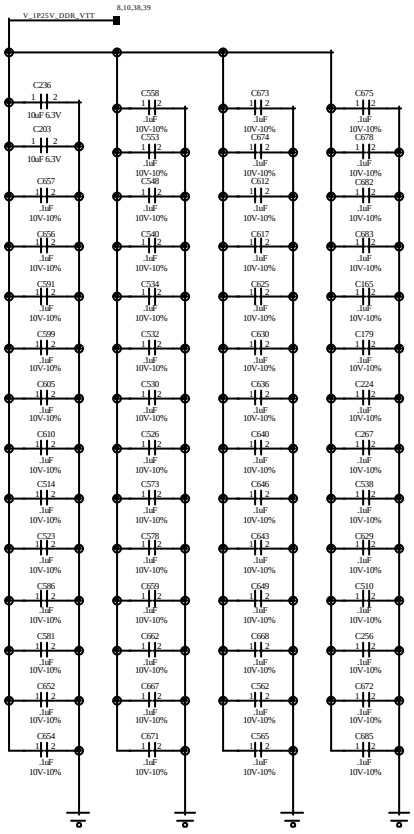
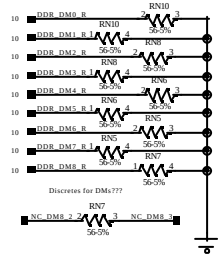
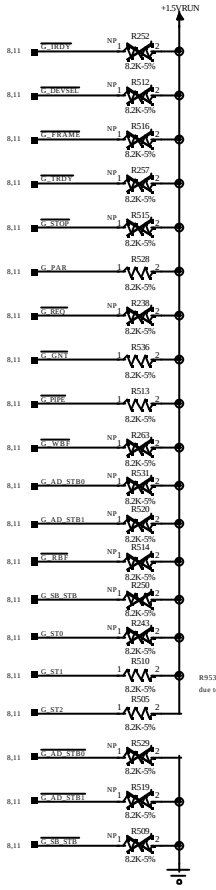
DDR VTT decoupling.



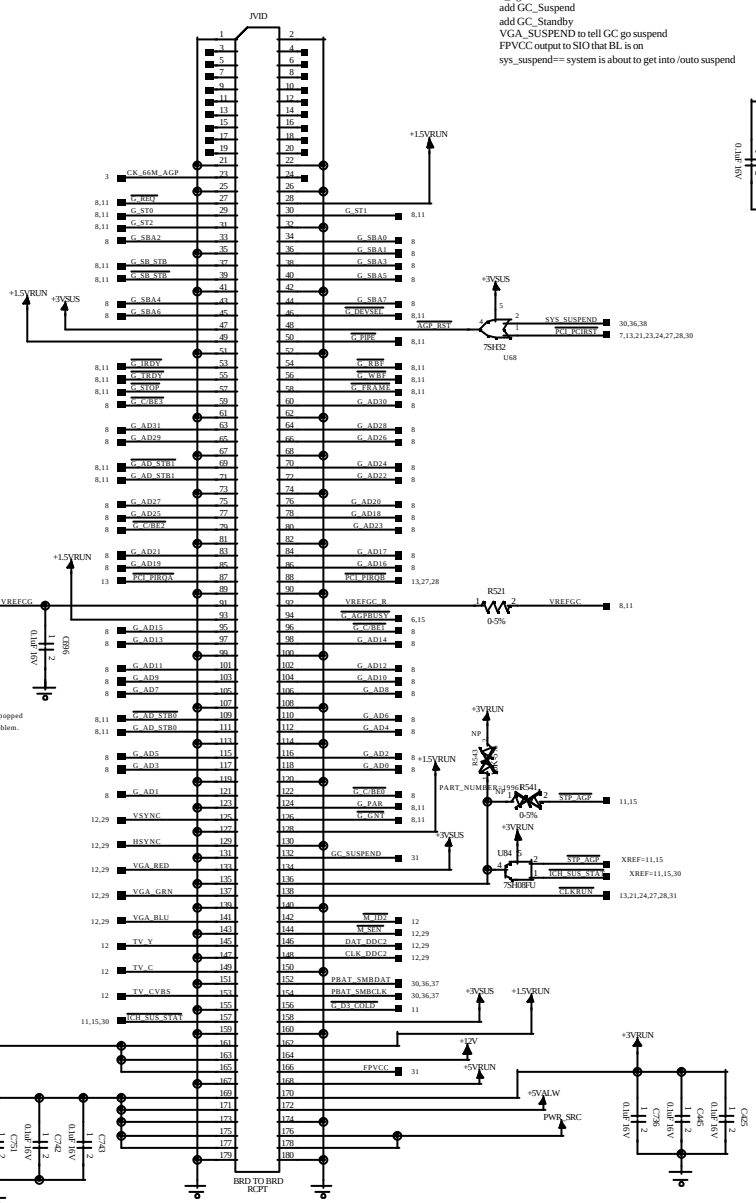


AGP pullups: matches Meteor. Verify pullups in Brookdale.

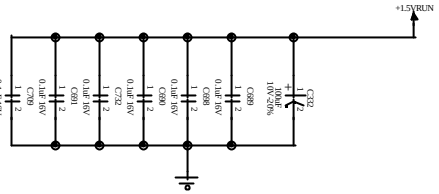


R953 and R898 are popped due to BD-M A0 problem.

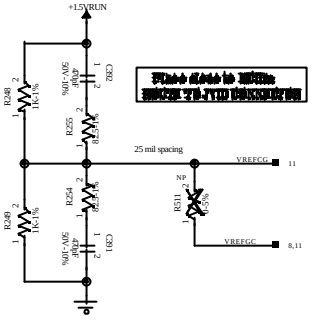
VGA Board Connector

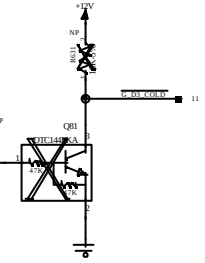


vga-off==vga_suspend
v_vga== comes from 3VSUS and 5vSus
add GC_Suspend
add GC_Standby
VGA_SUSPEND to tell GC go suspend
FPVCC output to SIO that BL is on
sys_suspend== system is about to get into /outo suspend



VREFCG Source





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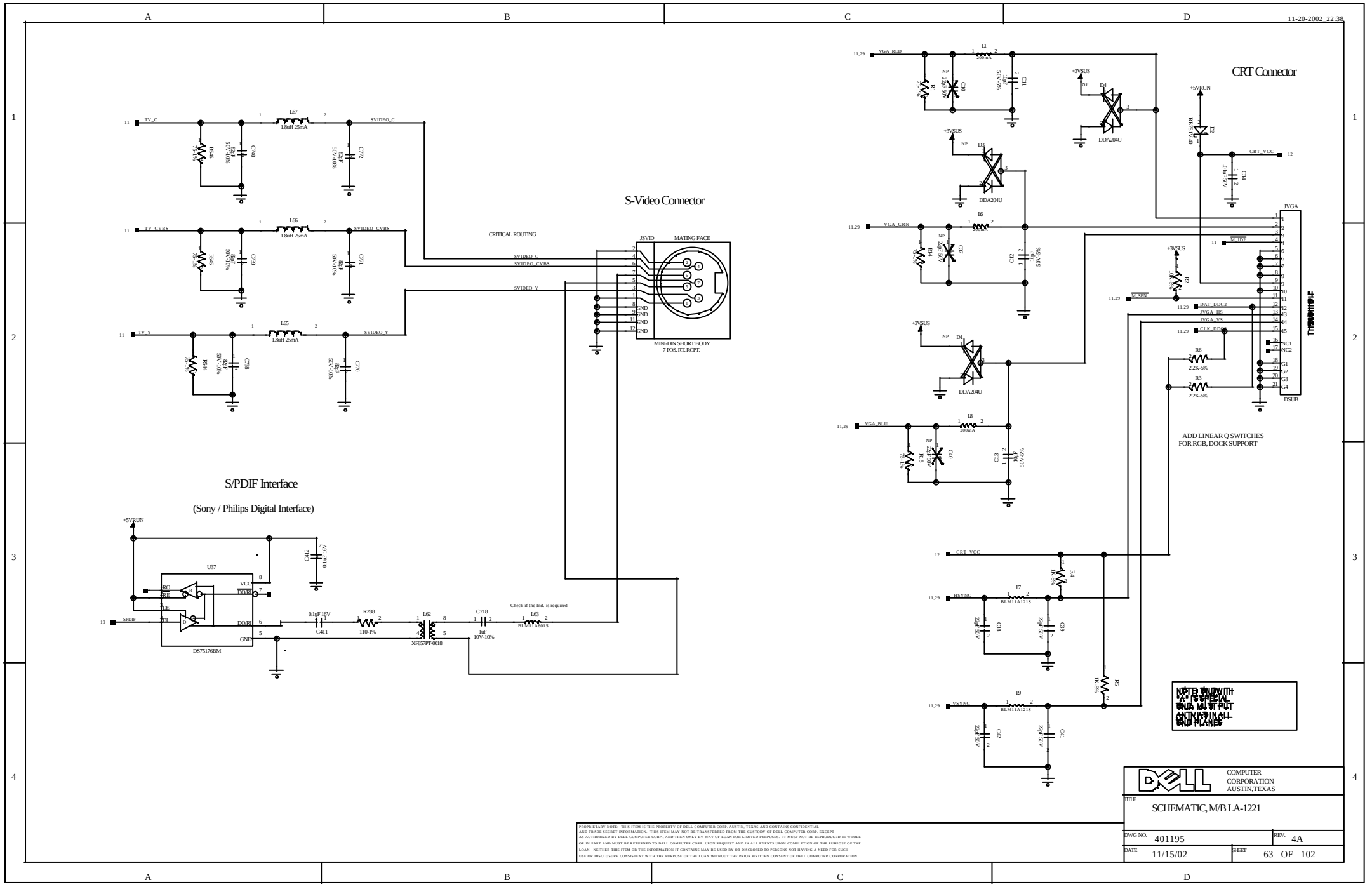

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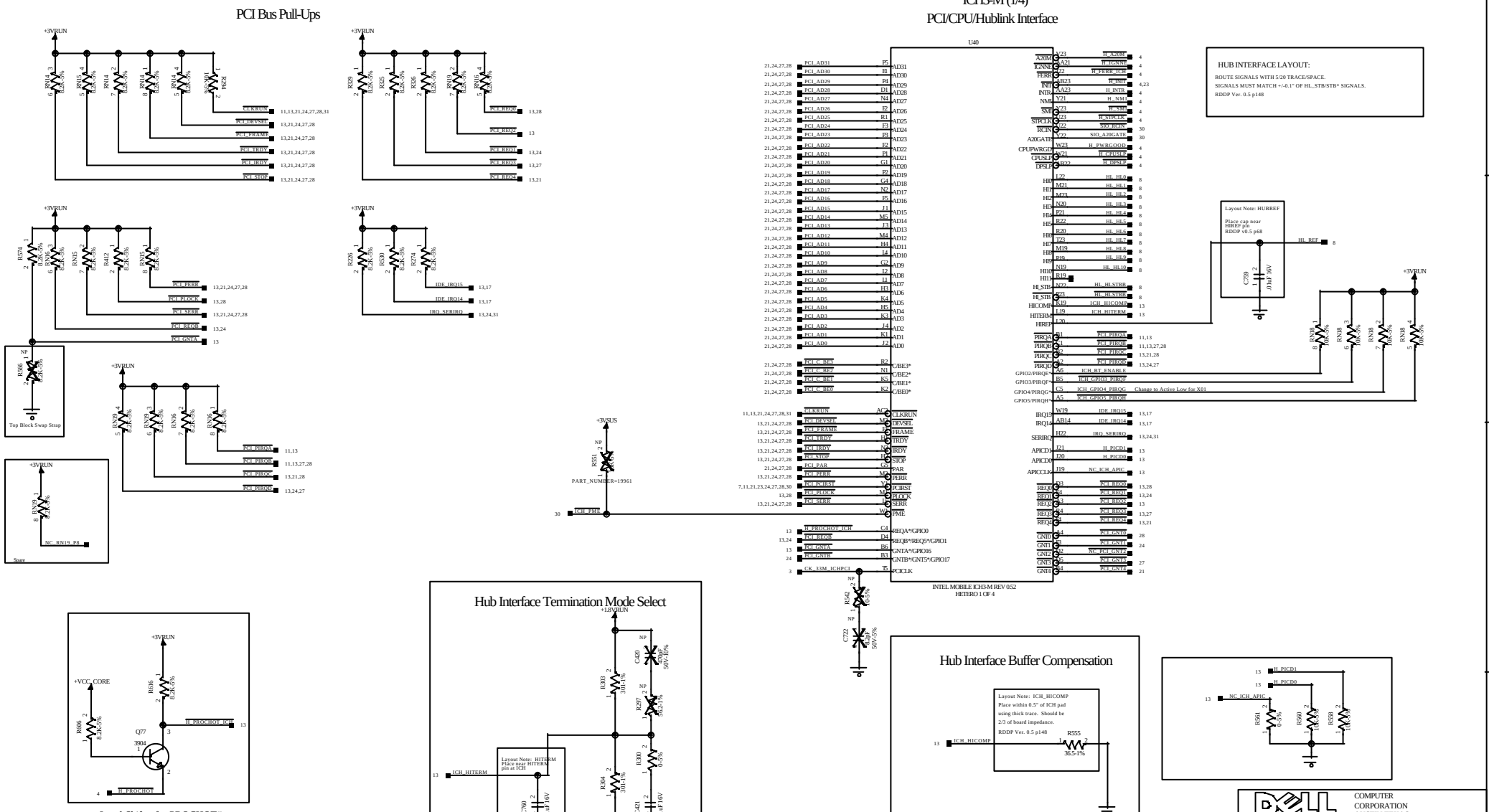
ICH3-M (1/4)
PCI/CPU/Hublink Interface



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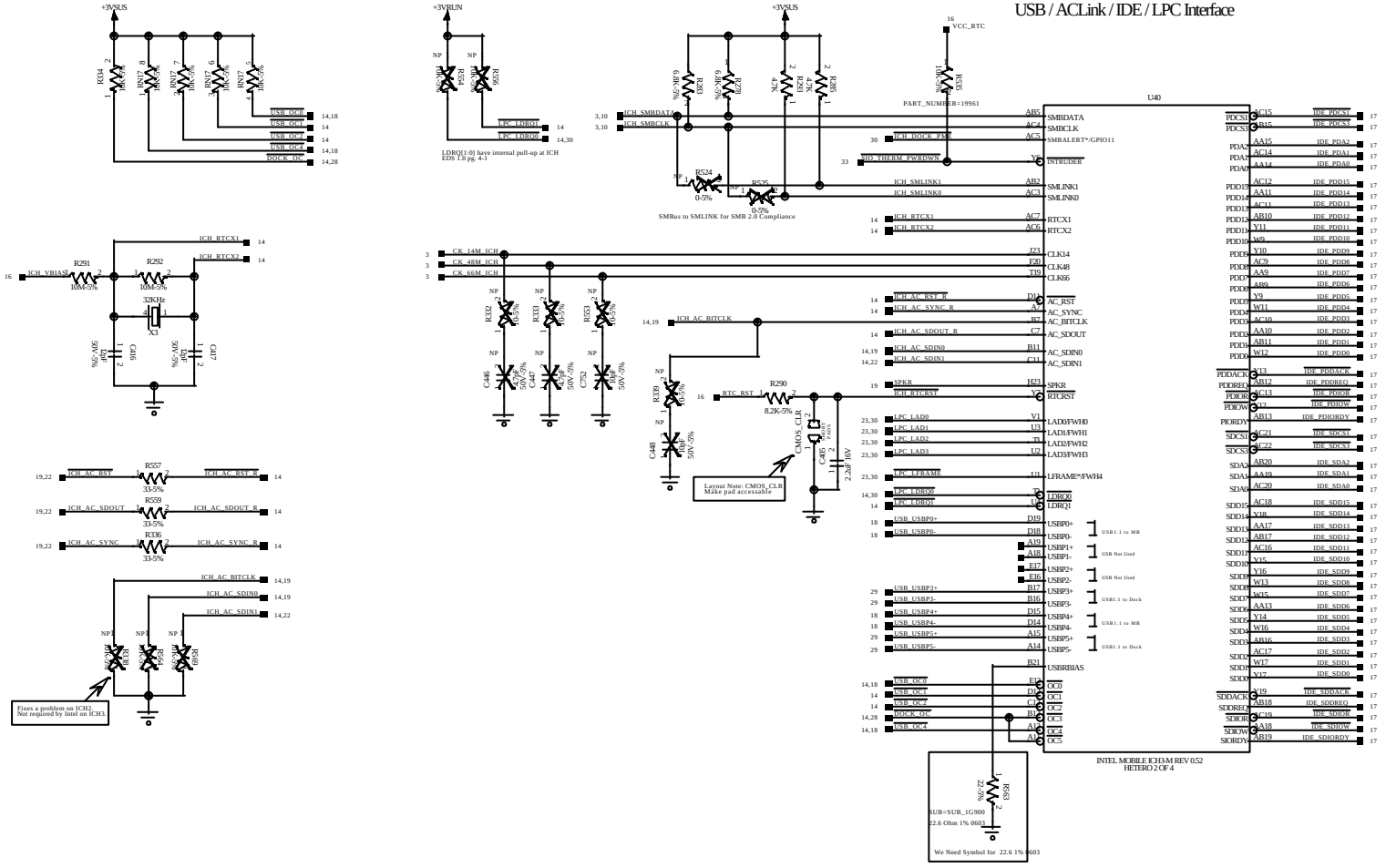
ICH3-M (2/4)
USB / ACLink / IDE / LPC Interface



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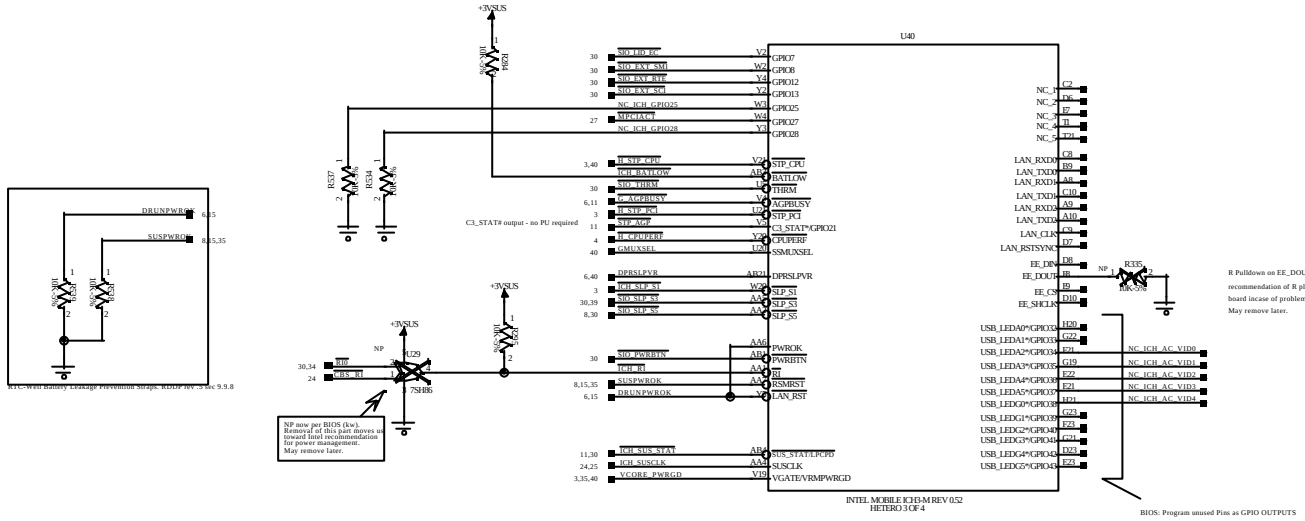
ICH3-M (3/4)
PWR MGMT / LAN / USBLED Interface



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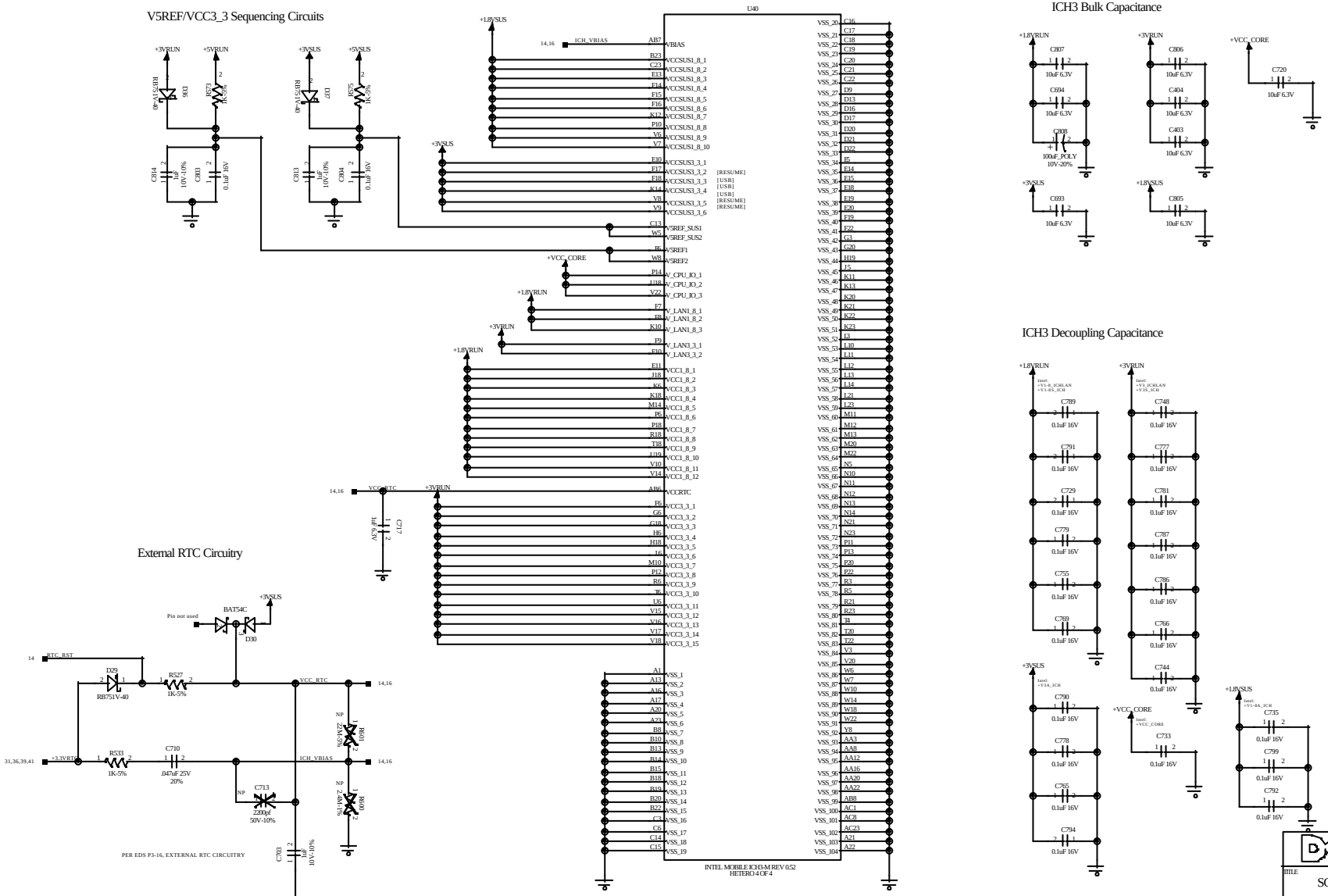
ICH3-M (4/4)
Power and Ground



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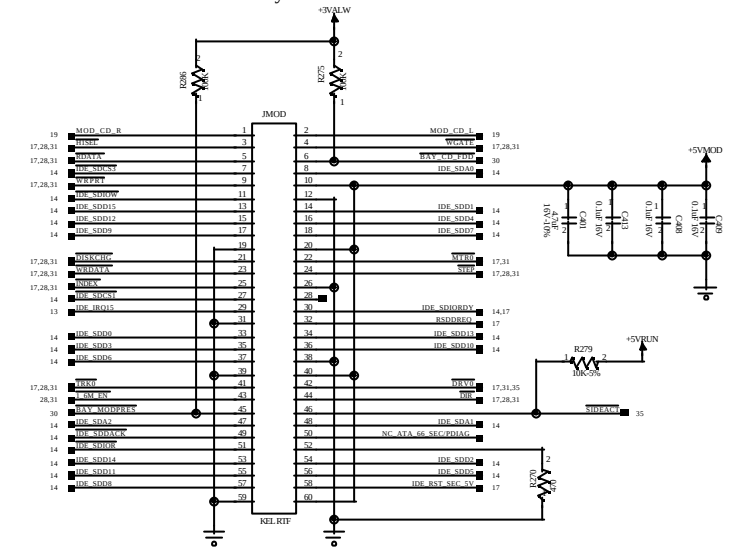
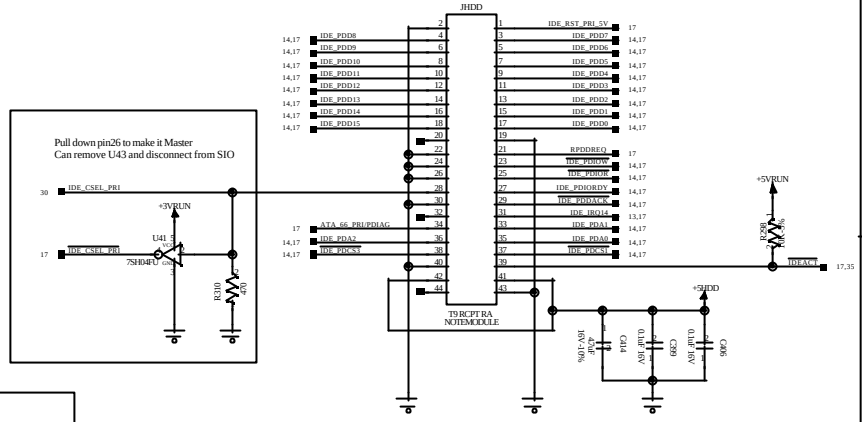
Modular Bay Connector



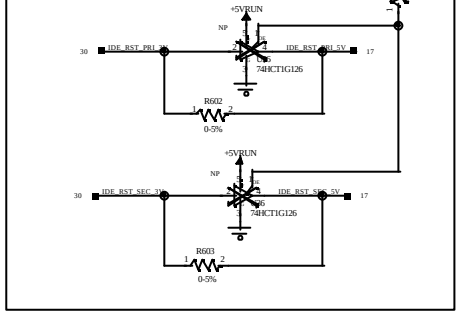
Hard Drive Connector



Pull down pin26 to make it Master
Can remove U43 and disconnect from SIO

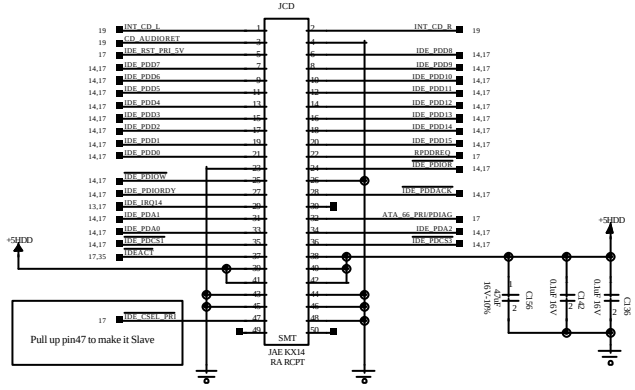
Level Shifting For IDE Reset Signals

Level shifting is required in order to support Toshiba DVD-ROM drive with VIH=3.5V (Resistor allows in-circuit testability for gates)

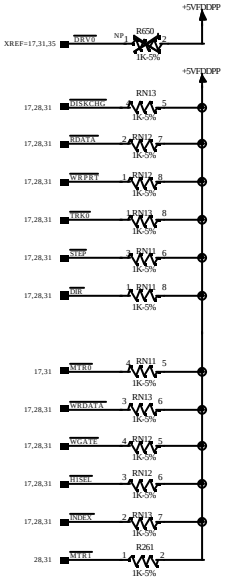


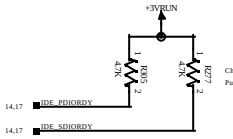
Optical Bay Connector

CD/DVD/etc.

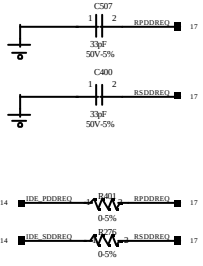


Pull up pin47 to make it Slave





Changed power plane per Intel RDDP Ver 0.5 p.67
Pullup to 3V instead of 5V for ATA100 per Intel



Still needed???
On Intel reference schematic for L2

Series resistance is now embedded in ICH3
Changing to 0 ohm

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

DWG NO. 401195 REV. 4A

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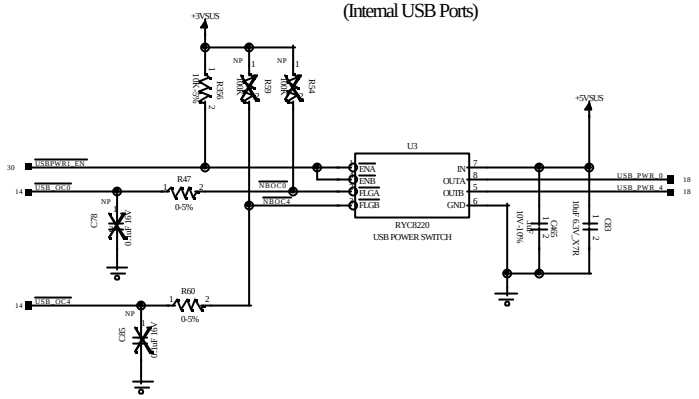
Overcurrent Detection
(Internal USB Ports)

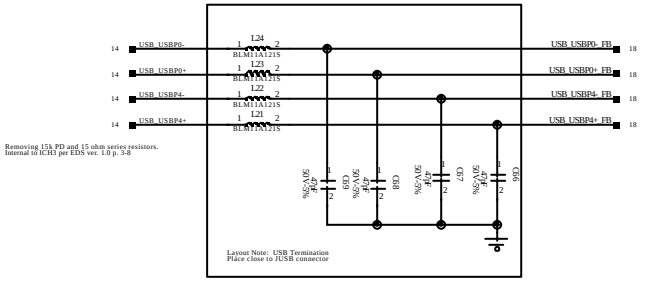


Layout Note: C72
Place a "+" character silkscreen on the board near the non-ground side of this capacitor.

C72 can be populated with either a ceramic (PN 2832P) or tantalum (PN 1033E) type capacitor

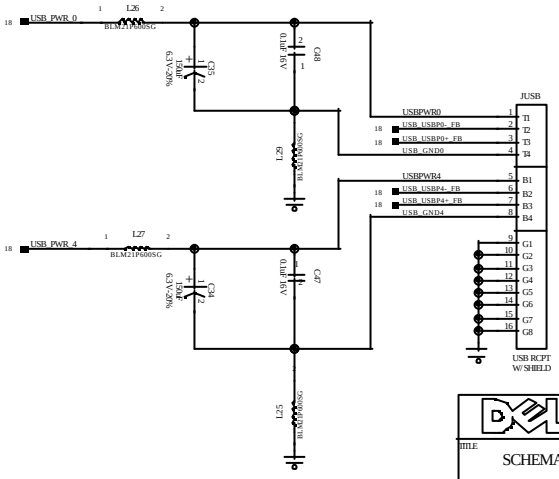
Layout Note: USB1.1
Follow RDDP's routing recommendations
INTEL USB GUIDELINE APPLIES

USB1.1 To System



Removing 15k PD and 15 ohm series resistors.
Internal t0 ICH3 per EDS ver. 1.0 p. 3-8

USB1.1 To System
System Board USB Connectors



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DELL COMPUTER CORPORATION AUSTIN,TEXAS

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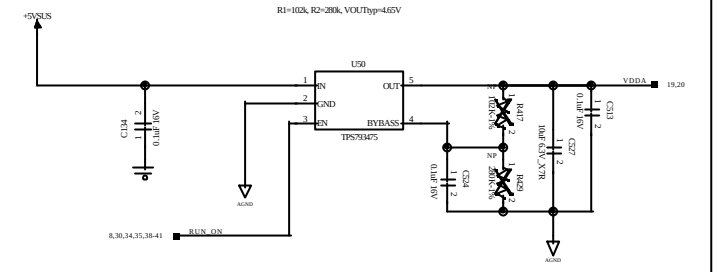
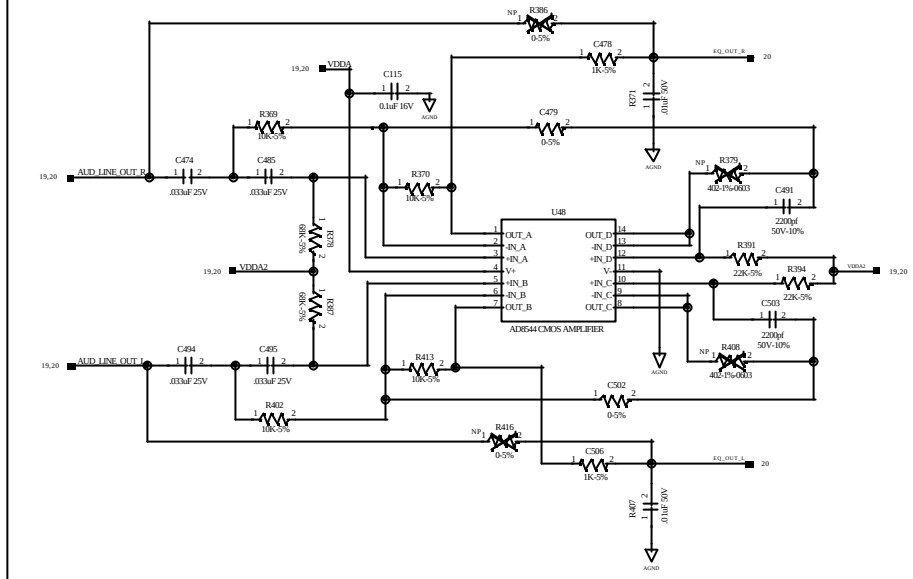
$$V_{out} = V_{set}\left(1 + \frac{R2}{R1}\right)$$

$$V_{set} = 1.242V$$

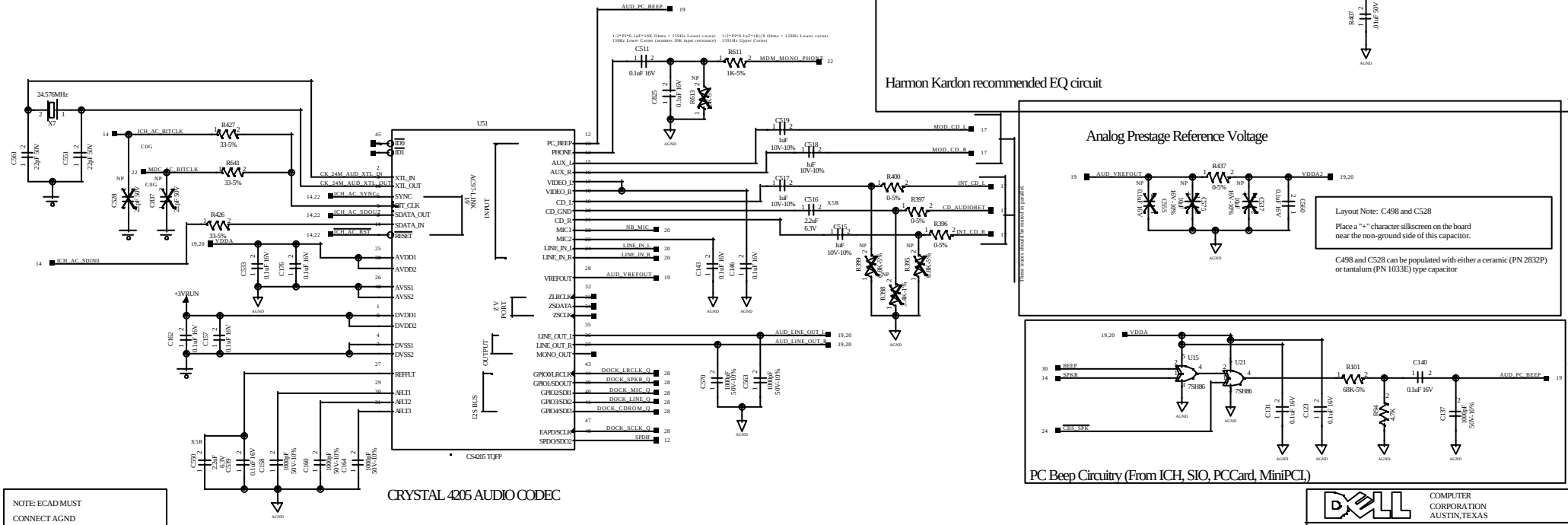
R1=102k, R2=280k, VOUTtyp=4.65V



VDDA Generation (via MIC5205)



Harmon Kardon recommended EQ circuit



CRYSTAL 4205 AUDIO CODEC

Analog Prestage Reference Voltage

Layout Note: C498 and C528

Place a "+" character silkscreen on the board near the non-ground side of this capacitor.

C498 and C528 can be populated with either a ceramic (PN 2832P) or tantalum (PN 1033E) type capacitor

PC Beep Circuitry (From ICH, SIO, PCCard, MiniPCI,)

NOTE: ECAD MUST
CONNECT AGND
TO GND (VIA DRAWBRIDGE)!!
60 MIL TRACE AT ONE POINT
ONLY UNDER THE CODEC

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

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Internal Speaker Amplifier

GAIN1	GAIN	Gain(dB)
0	0	3dB
0	1	10dB
1	0	15dB
1	1	25dB

Layout Note: C497
Place a "+" character silkscreen on the board near the non-ground side of this capacitor.

C497 can be populated with either a ceramic (PN 2832P) or tantalum (PN 1033E) type capacitor

NO VIAS EXCEPT DGND UNDER PART

Headphone/Line Out Amplifier

Layout Note: C529
Place a "+" character silkscreen on the board near the non-ground side of this capacitor.

C529 can be populated with either a ceramic (PN 2832P) or tantalum (PN 1033E) type capacitor

Line output circuitry

Line input circuitry

Microphone Input Circuit

DROP THIS VIA AT THE AUDIO CODEC - AT THE GROUND PIN NEAREST THE MIC INPUT (PIN 26)

PLACE CLOSE TO AUD JACKS on BOTTOM of Board

NOTE: ECAD MUST CONNECT AGND TO GND (VIA DRAWBRIDGE)!! 60 MIL TRACE AT ONE POINT ONLY UNDER THE CODEC

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

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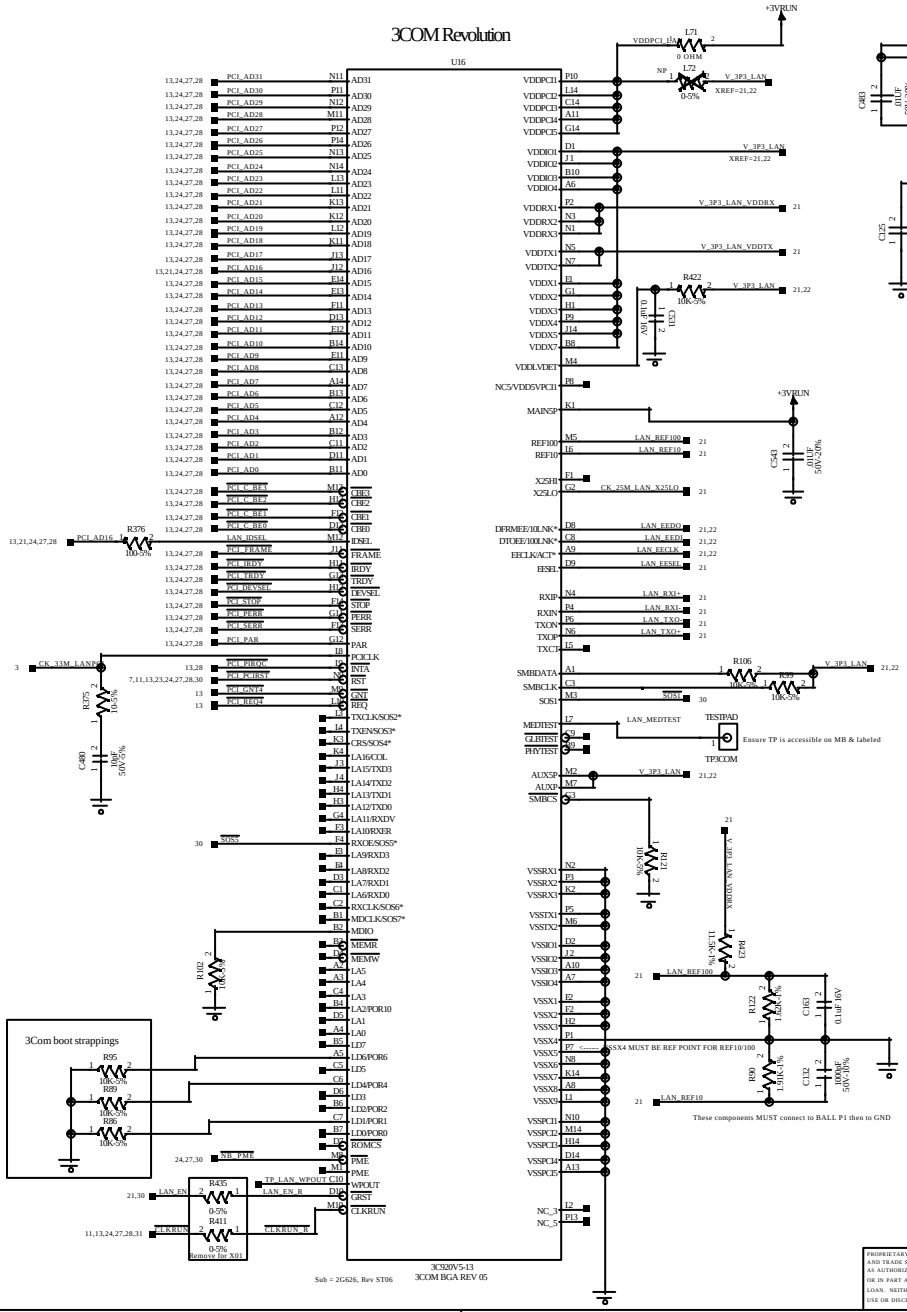
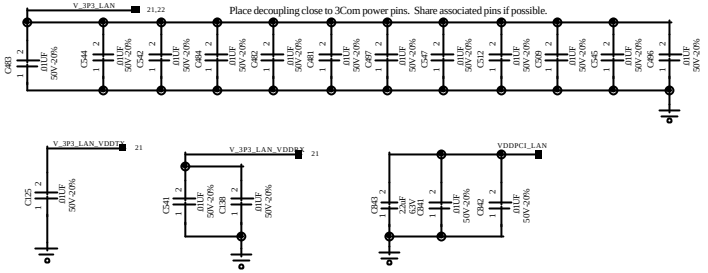
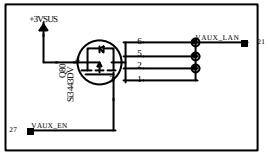
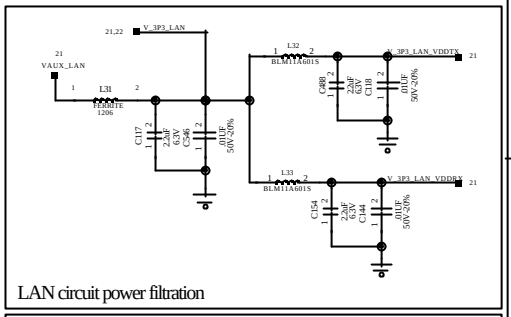
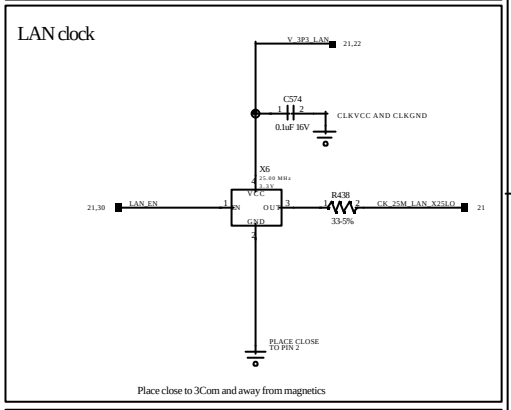






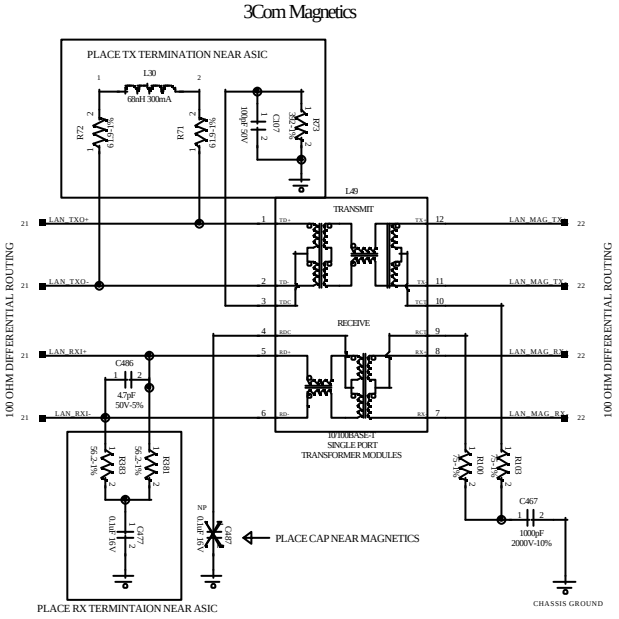


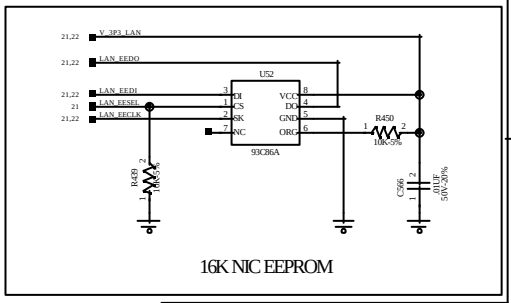
LAN circuit power filtration



LAN clock

Place close to 3Com and away from magnetics





16K NIC EEPROM

DELL	COMPUTER CORPORATION AUSTIN,TEXAS
TITLE	SCHEMATIC, M/B LA-1221
DWG NO.	401195
REV.	4A
DATE	11/15/02
SHEET	72 OF 102

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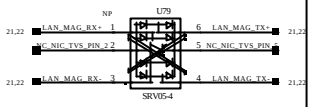
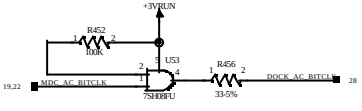
NIC ESD DIODE ARRAY

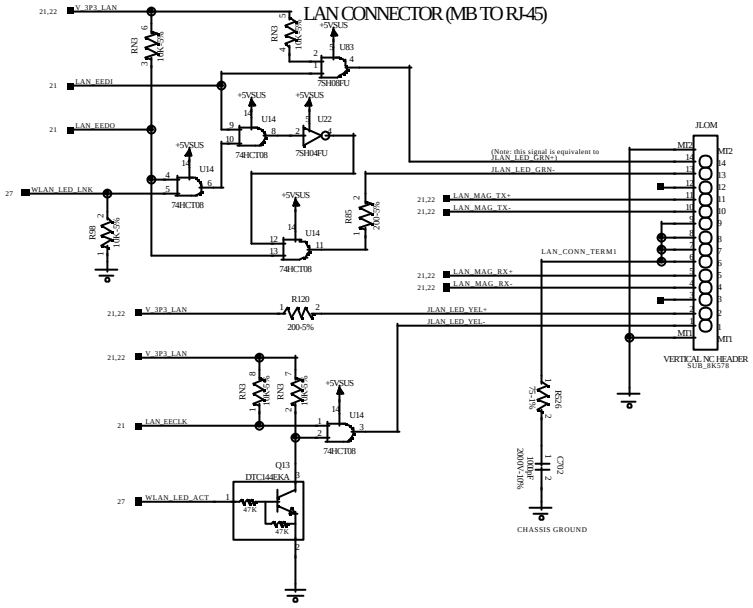


AC97 Bit Clock Buffer

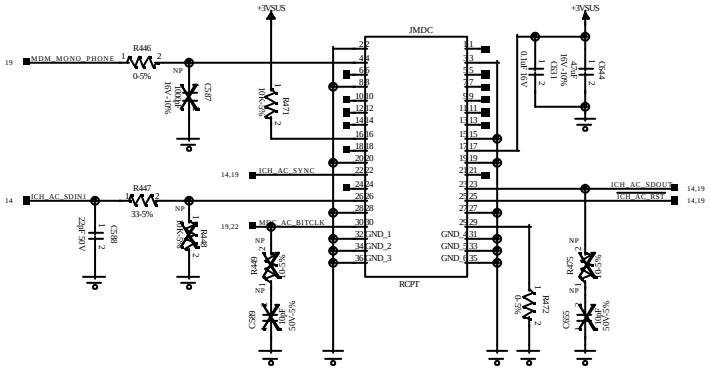


Place buffer near the MDC connector

LAN CONNECTOR (MB TO RJ-45)



Soft Modem MDC Connector



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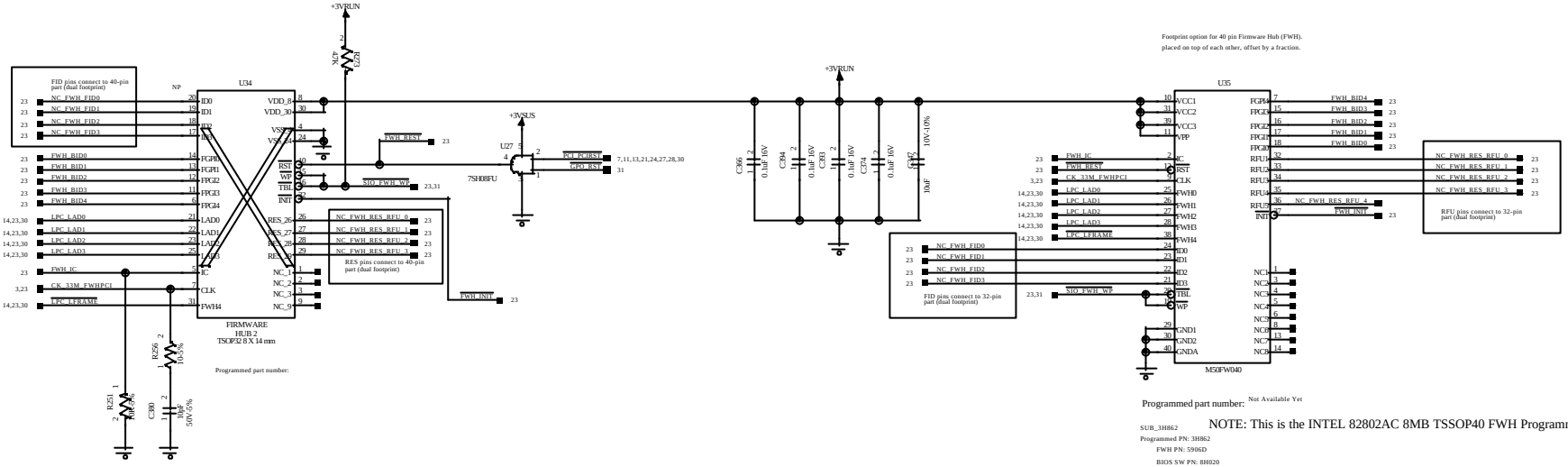
DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

DWG NO. 401195 REV. 4A

DATE: 11/15/02 SHEET 73 OF 102

Dual Footprint Firmware Hub (FWH) - System BIOS



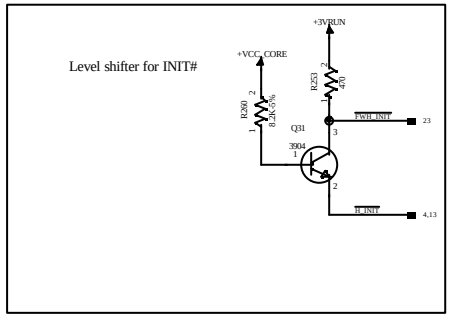
Programmed part number: Not Available Yet

NOTE: This is the INTEL 82802AC 8MB TSSOP40 FWH Programmed PN

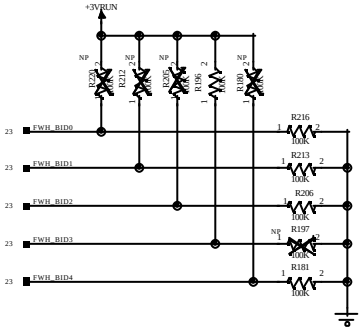
SUB_3H862
Programmed PN: 3H862
FWH PN: 5906D
BIOS SW PN: 8H020

4Mb Part
FWH PN: 6C739

Level shifter for INIT#



Board Rev Identifier



PWB REV.	BID4	BID3	BID2	BID1	BID0
x00-00	0	0	0	0	0
x01-00	0	0	0	0	1
x02-00	0	0	0	1	0
x03-00	0	0	0	1	1
X04-00	0	0	1	0	0
X05-00	0	0	1	0	1

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

DWG NO. 401195 REV. 4A

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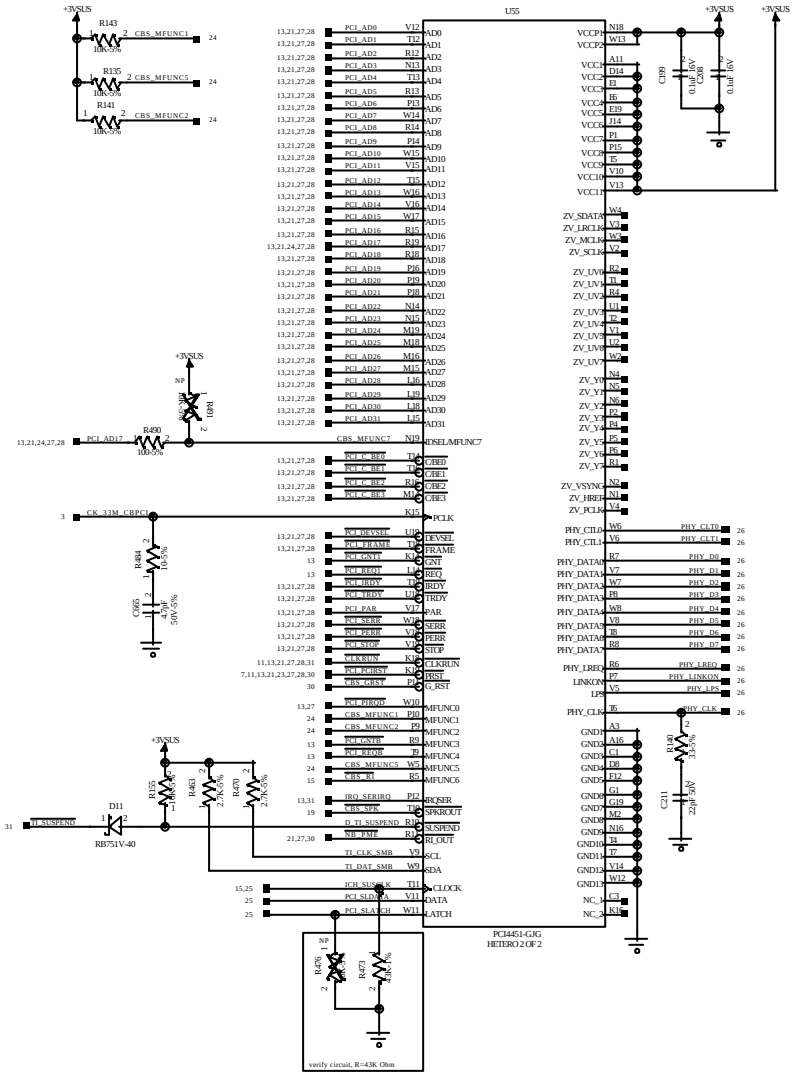
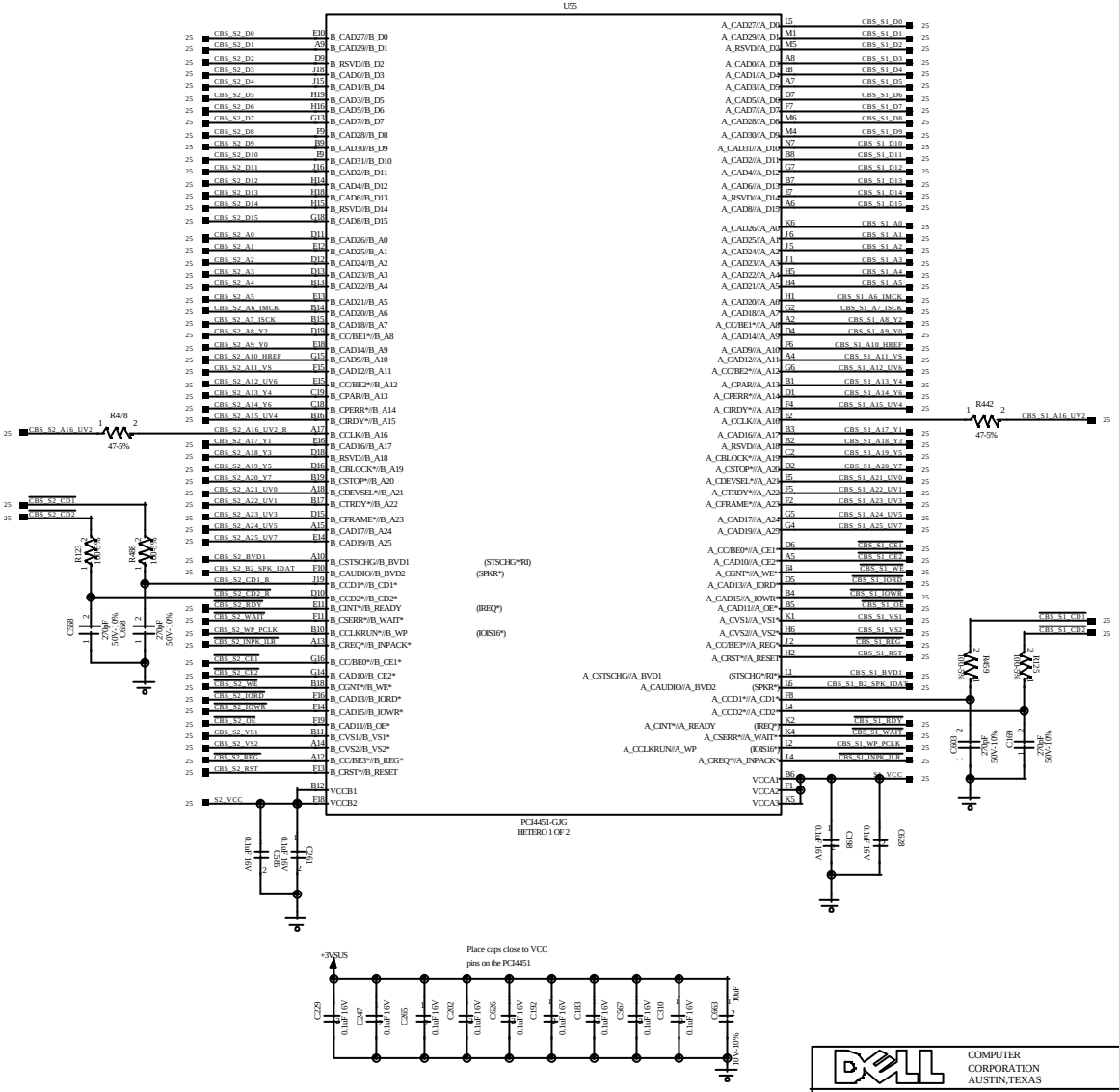
CardBus Controller (1/2)

PCI / PHY Interface



CardBus Controller (2/2)

PC Card Interface



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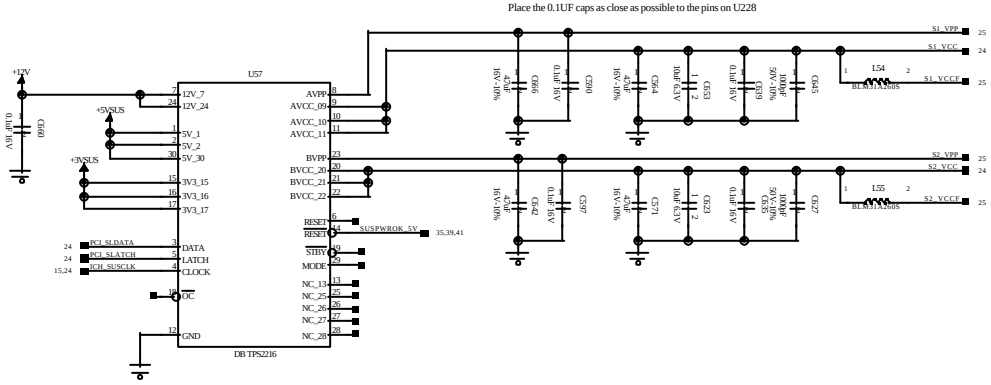
DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

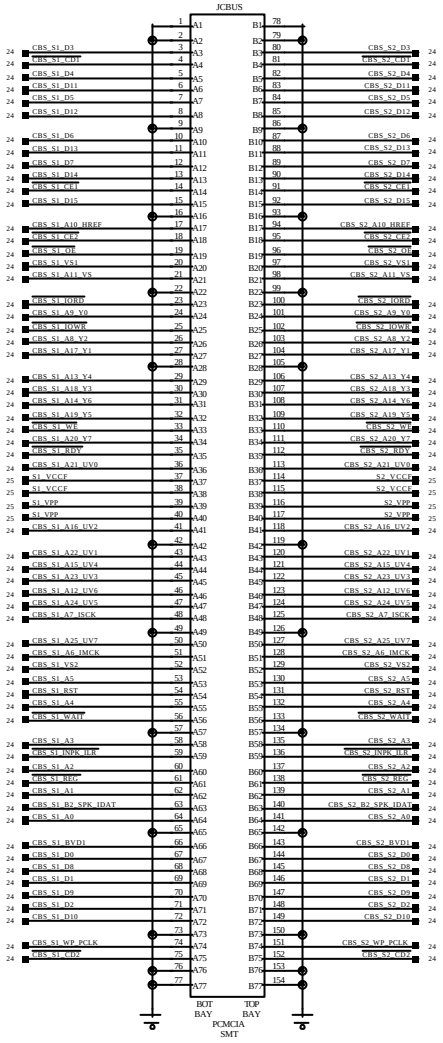
DWG NO. 401195 REV. 4A

DATE: 11/15/02 SHEET 75 OF 102

PC Card Slot Voltage Selector



PC Card Connectors



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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

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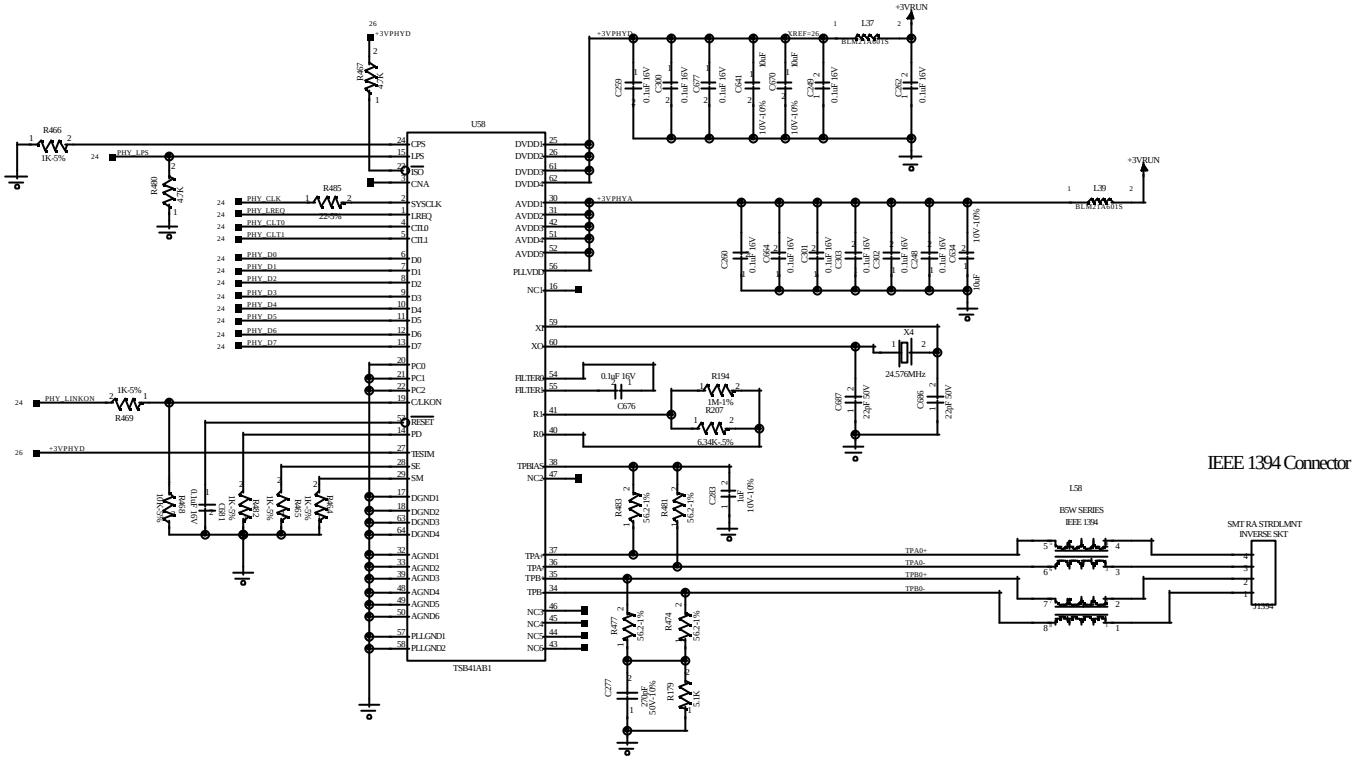
IEEE 1394 Phy



IEEE 1394 Connector

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

DWG NO. 401195 REV. 4A

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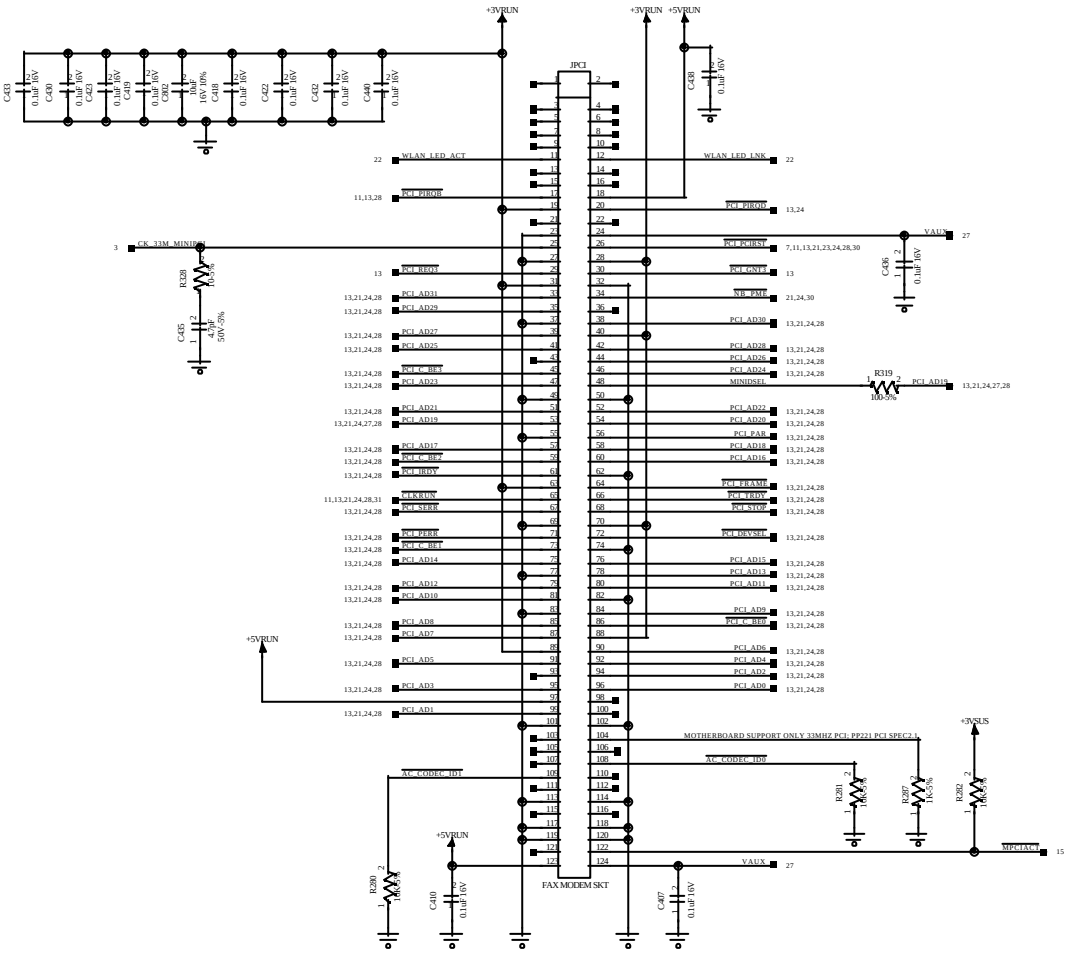
Layout Note: C699

Place a "+" character silkscreen on the board near the non-ground side of this capacitor.

C699 can be populated with either a ceramic (PN 2832P) or tantalum (PN 1033E) type capacitor

Mini PCI Connector

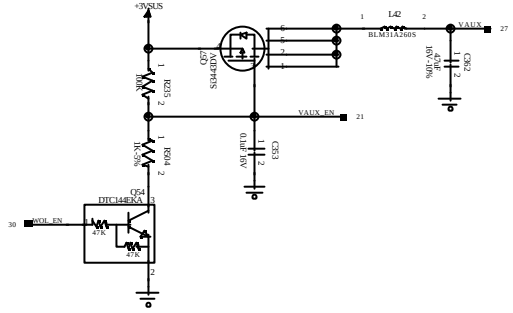


Note: Pins 3 through 16 of JPCI are not being used per the mini-pci spec.
THIS BOARD DOESNOT SUPPORT WAKE UP FROM MINIPCI CARD WHILE IN SHUTDOWN
3.3aux is connected to 3.3SUS

Power requirement total 2W
+5VRUN =100mA
3.3VAUX<375mAMPMAX

VAUX Source

Circuit Configured as Always On with SUS



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DELL COMPUTER CORPORATION AUSTIN,TEXAS

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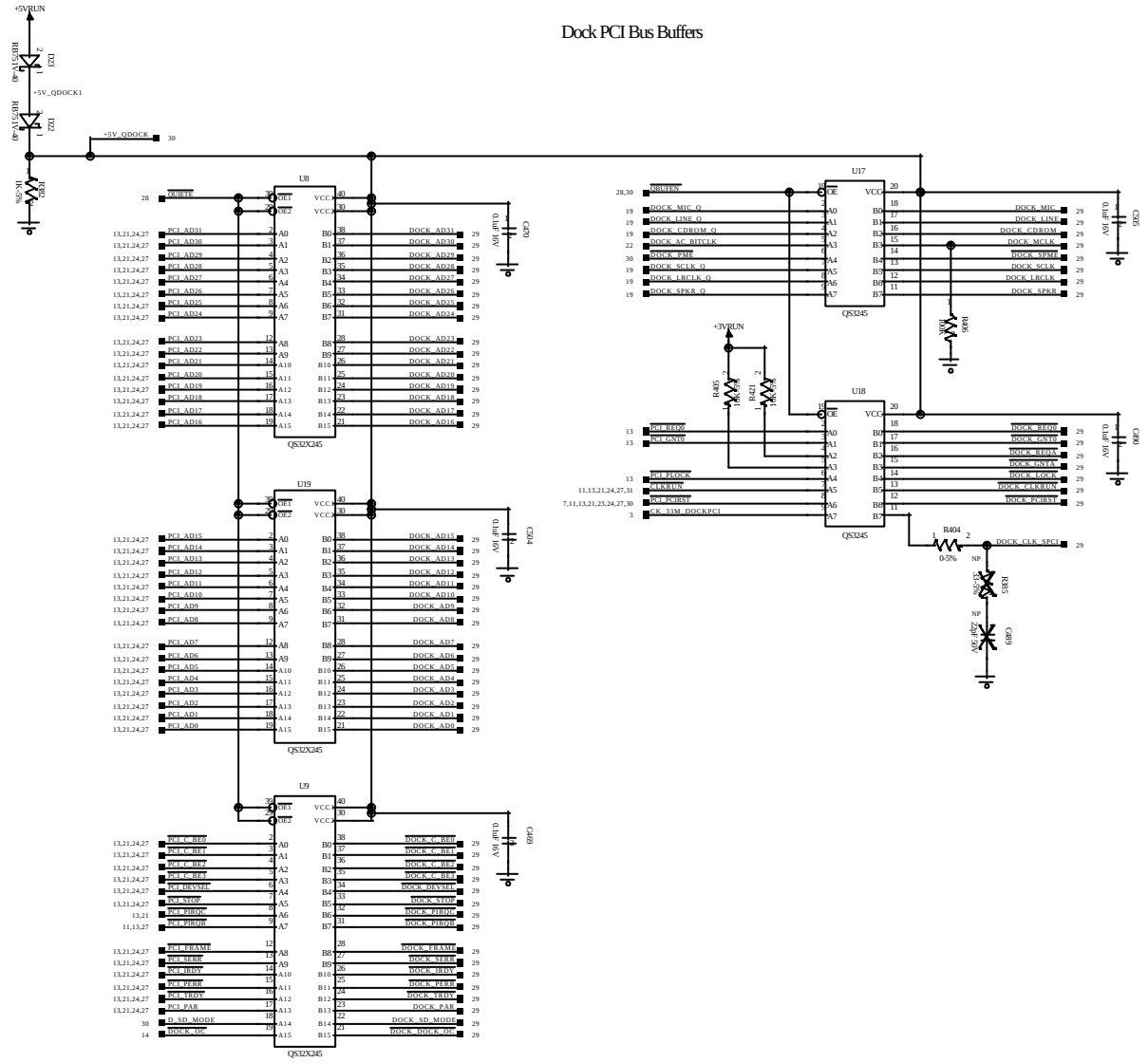
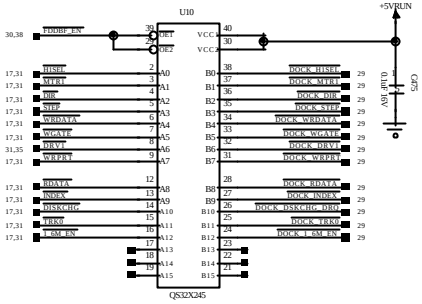
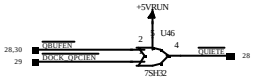
Dock PCI Bus Buffers



Dock FD Buffer



Dock Buffer Enable



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TITLE: SCHEMATIC, M/B LA-1221

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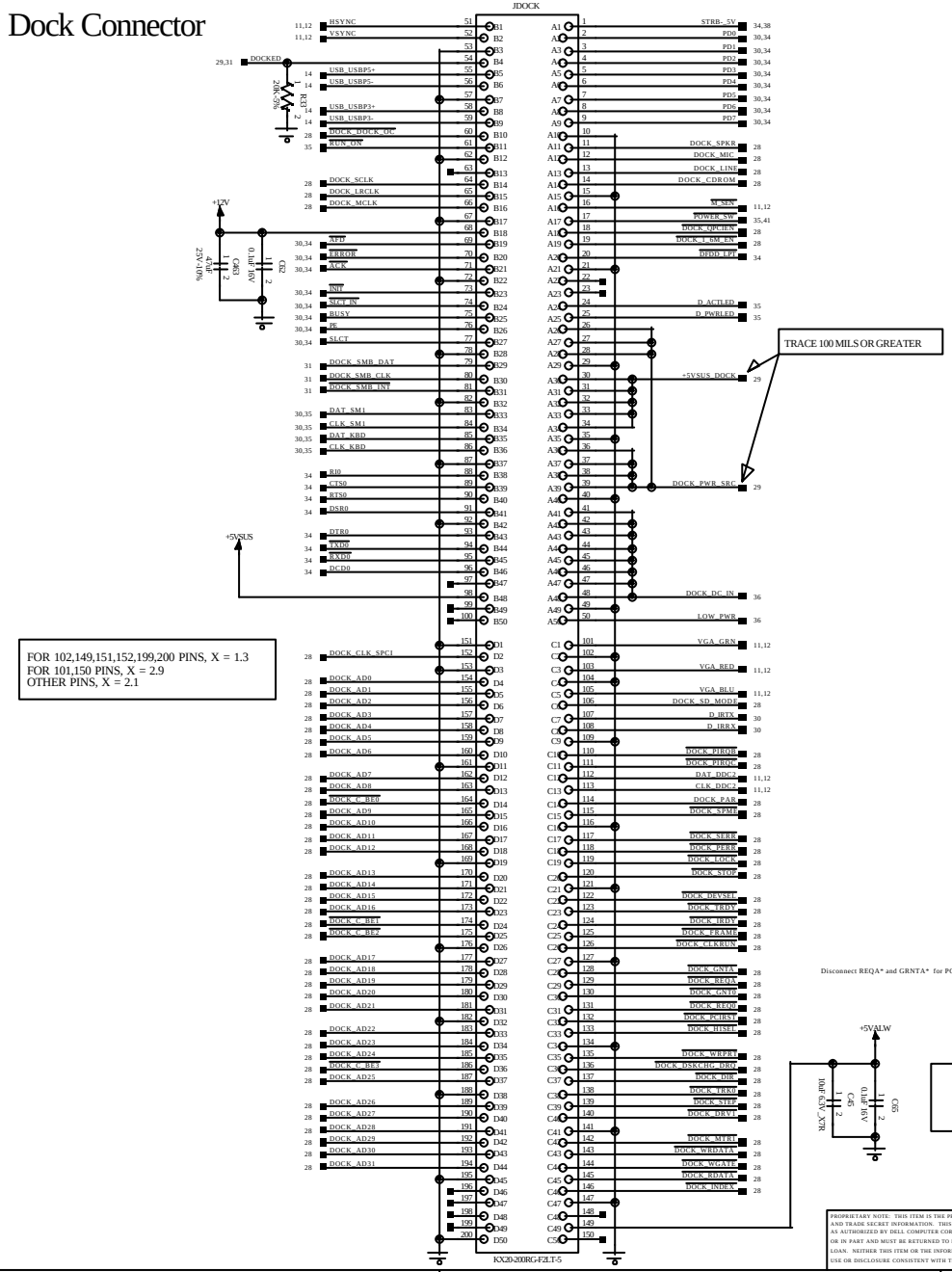
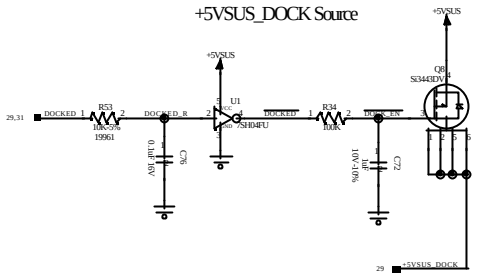
Dock Connector



+5VSUS_DOCK Source



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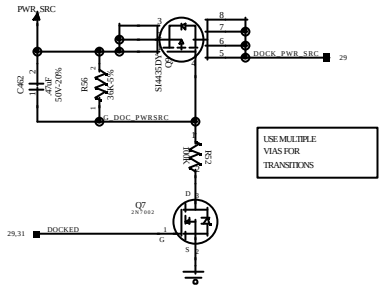

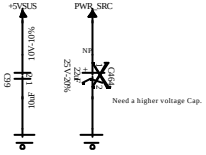

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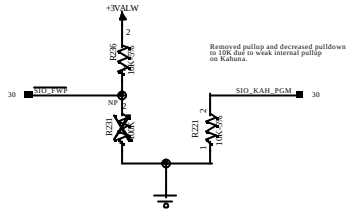
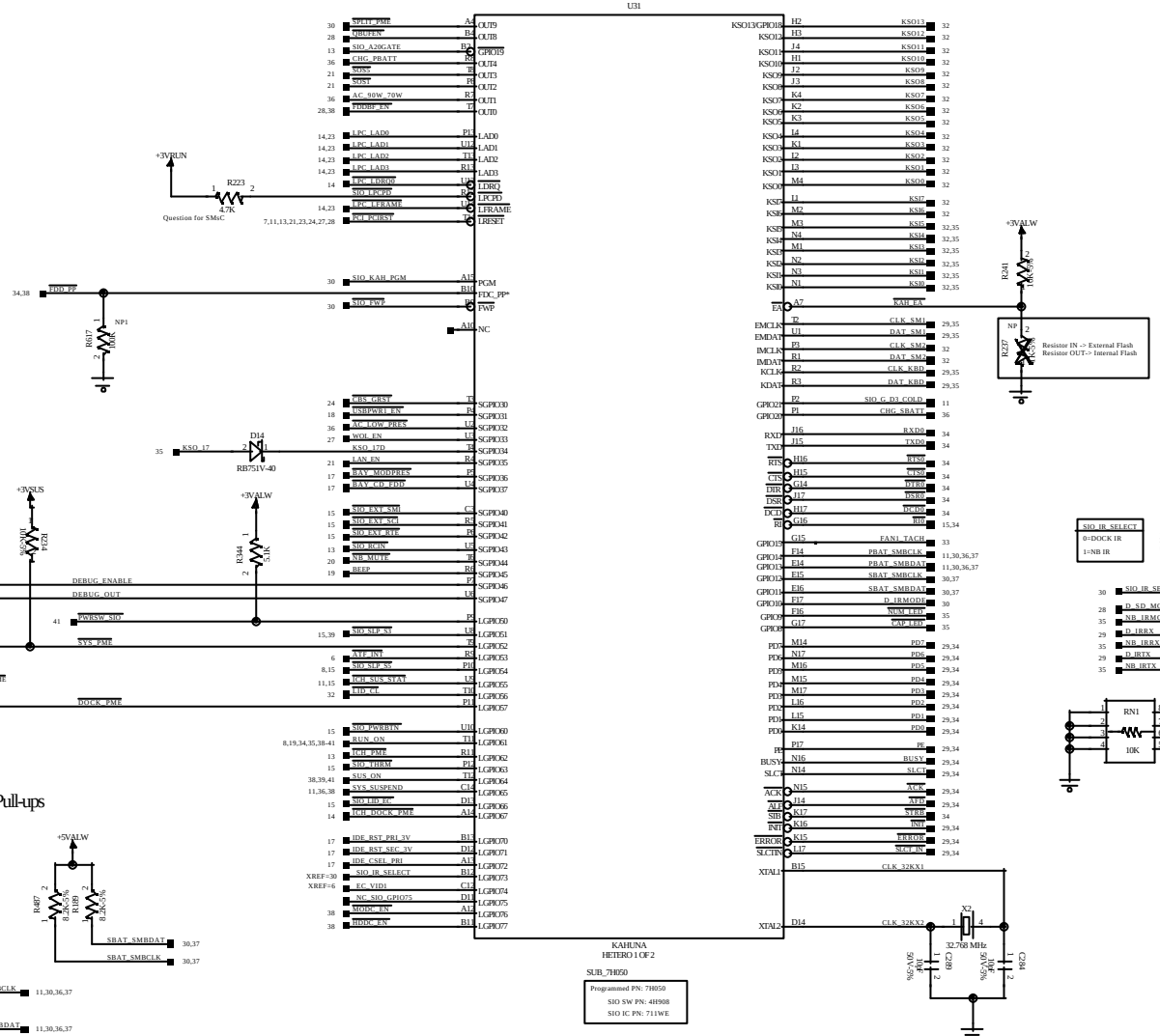
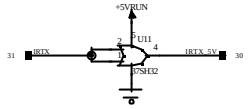
Kahuna Re-flash Setup



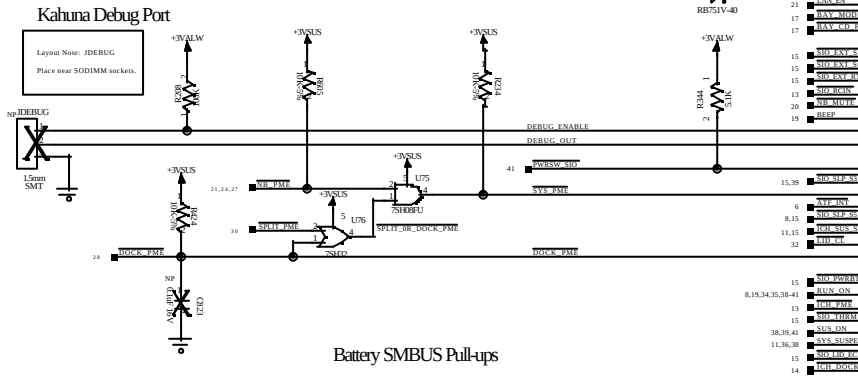
Super I/O Controller (Kahuna 1/2)



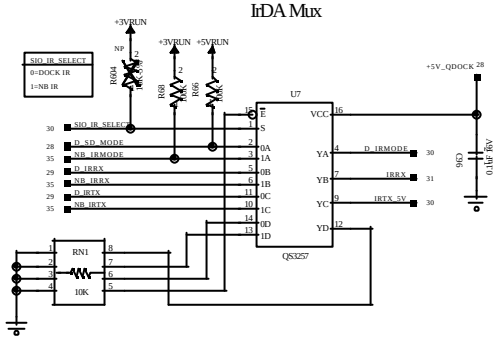
IRTX Level Shifter



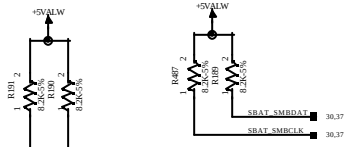
Kahuna Debug Port



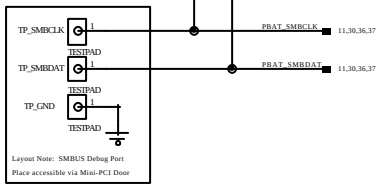
IrDA Mux



Battery SMBUS Pull-ups



SMBUS Debug Port



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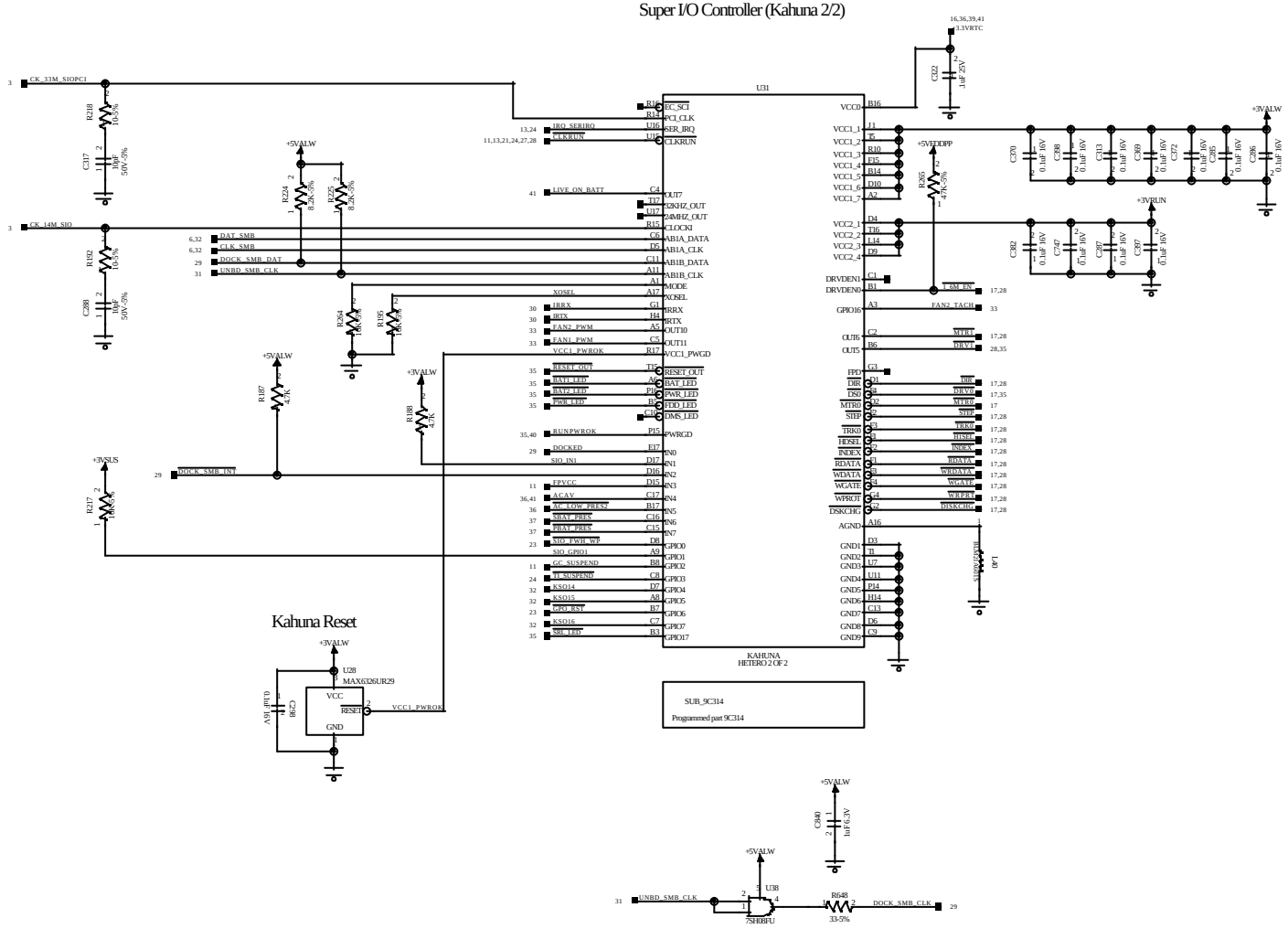
Super I/O Controller (Kahuna 2/2)



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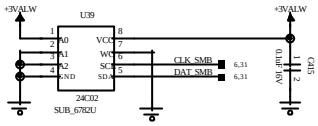
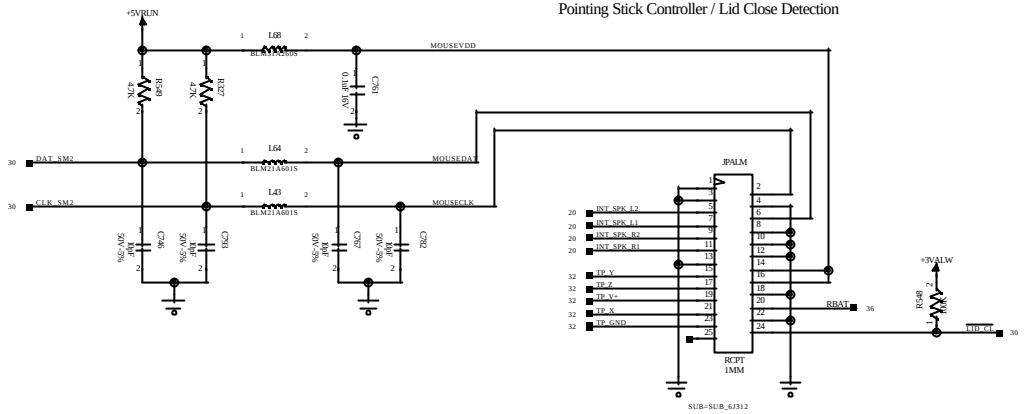
EEPROM (Service/Asset Tag, etc.)



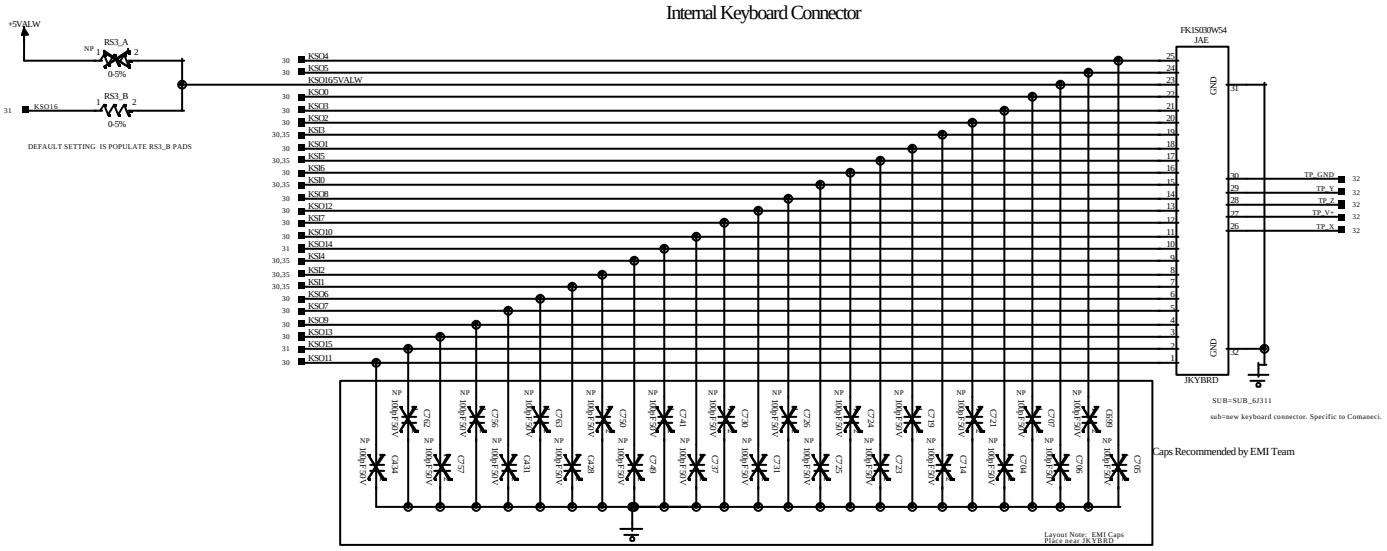
Palm Rest Connector

Rbat / Speakers / Touch Pad /
Pointing Stick Controller / Lid Close Detection



Sub= Swapped Connector between Palm rest and Connector originally on MB

Internal Keyboard Connector



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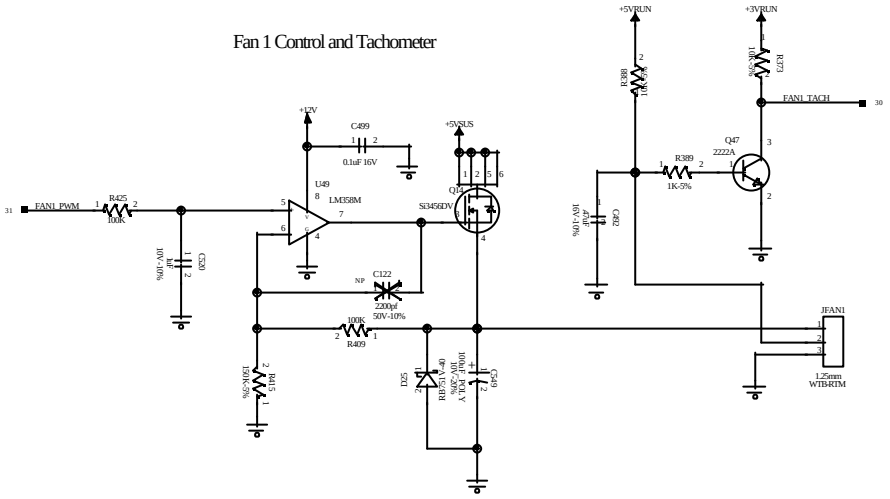
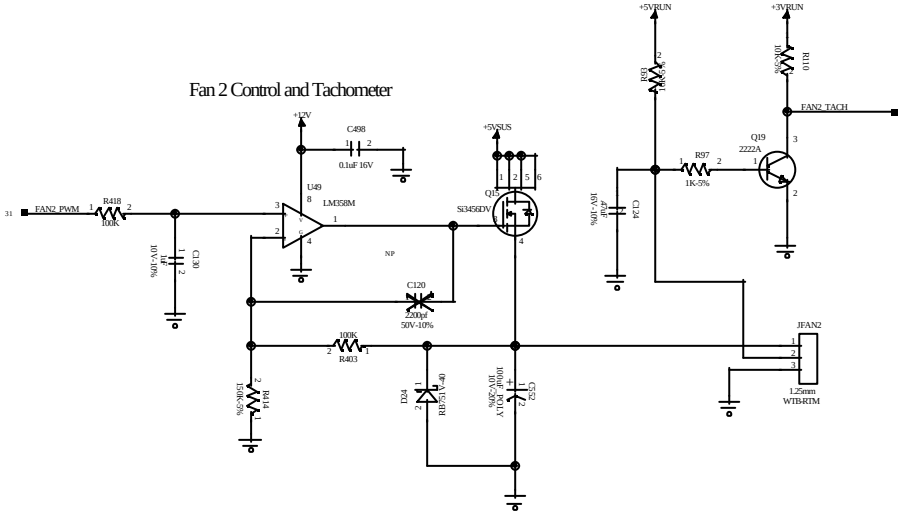
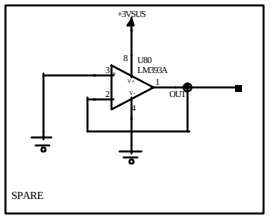
Fan Control Circuit

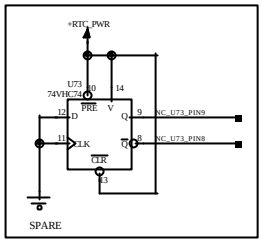
Fan 1 Control and Tachometer



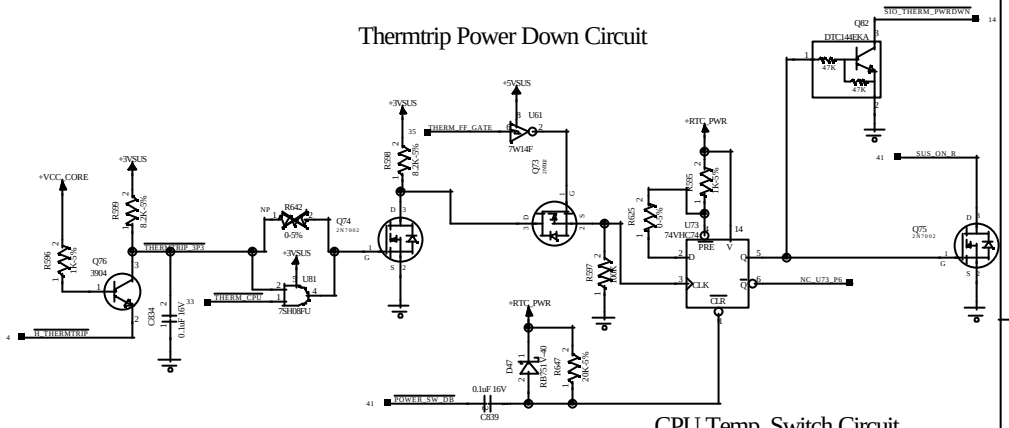
Fan 2 Control and Tachometer



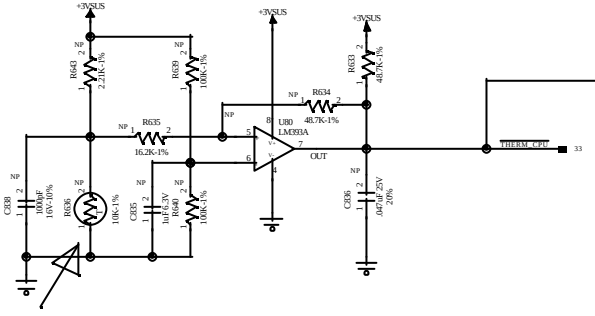




Thermtrip Power Down Circuit

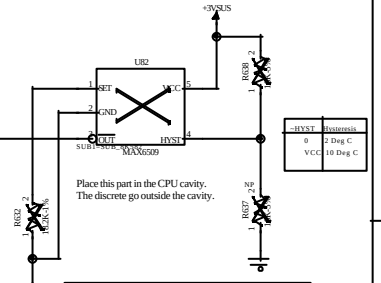


CPU Thermistor Circuit



Place this part in the CPU cavity.

CPU Temp. Switch Circuit



Place this part in the CPU cavity.
The discrete go outside the cavity.

-HYST	Hysteresis
0	2 Deg C
VCC	10 Deg C

Rset Resistor Value Calculation

Rset = [(1.3258*10^5)/(T+1.3)] - 310.1693 - [(5.7797*10^6)/(T+1.3)^2]

Rset = [(8.3793*10^4)/T] - 211.3569 + [(1.2989*10^5)/T^2]

T=Temp in Degrees Kelvin

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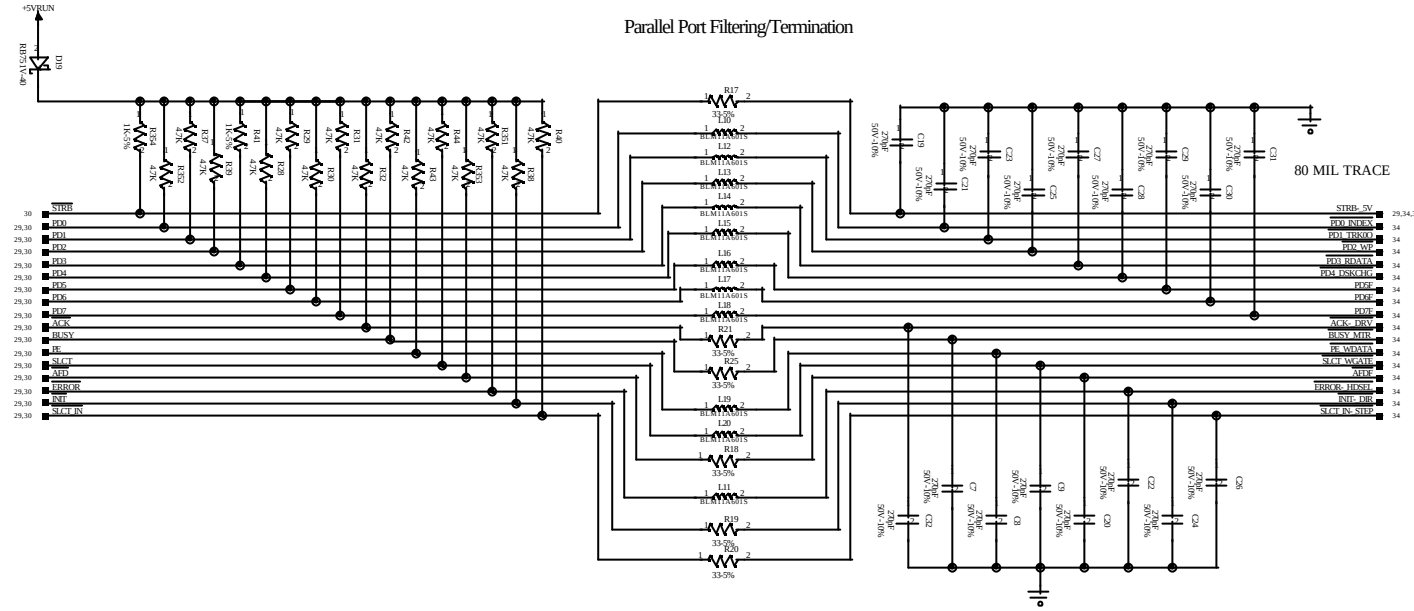
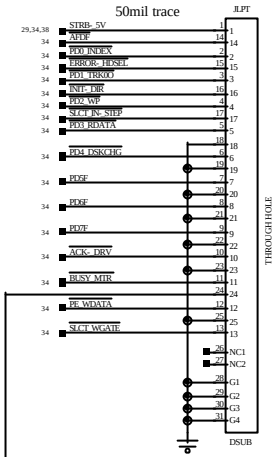
Parallel Port Filtering/Termination

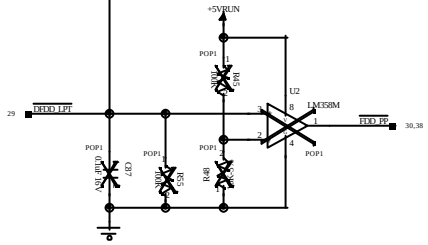


Parallel Port Connector

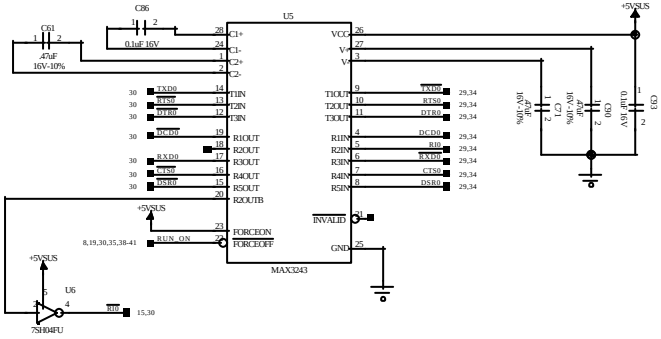


REMOVE THIS

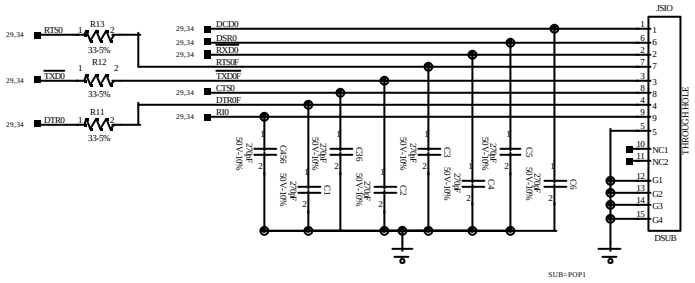
Parallel Port Floppy Detection



Serial Port Transceiver



Serial Port Connector



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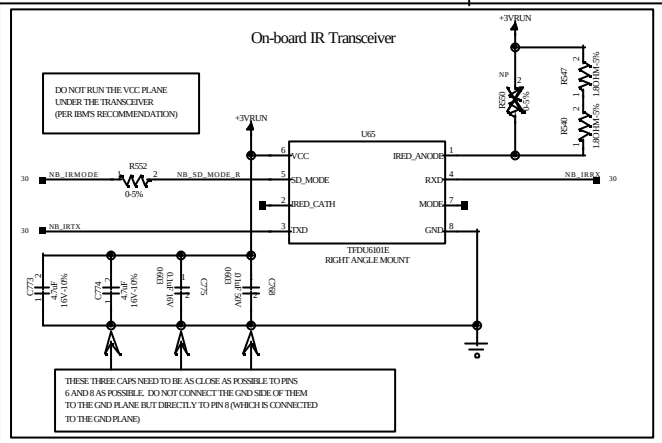
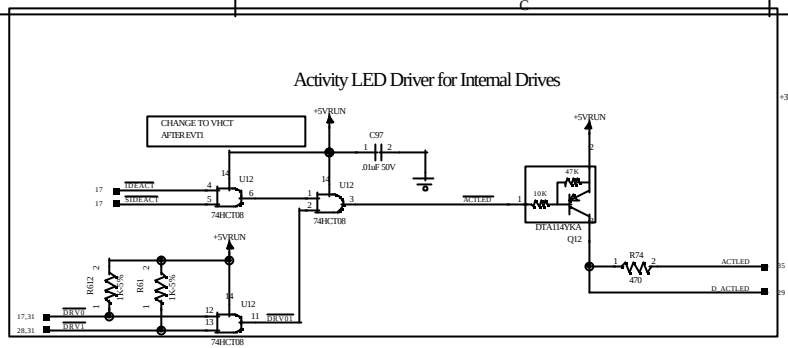
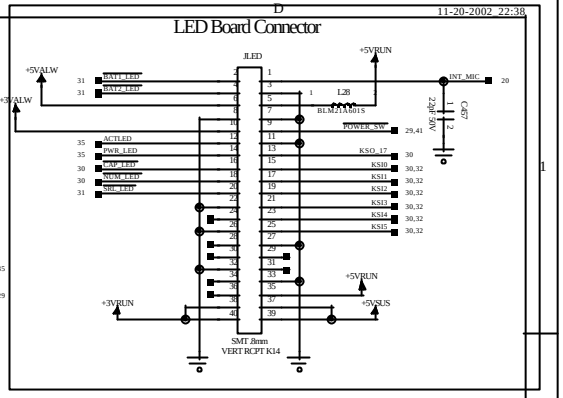
On-board IR Transceiver



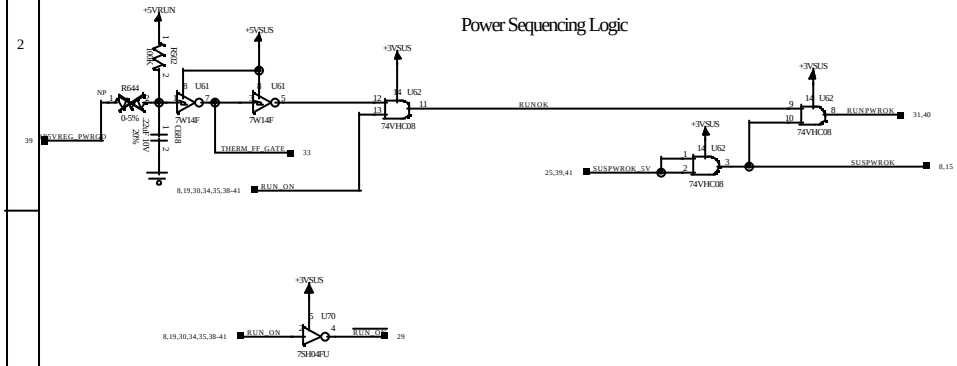
Activity LED Driver for Internal Drives



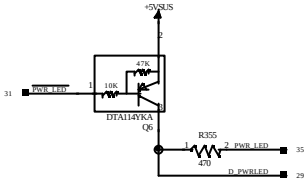
LED Board Connector



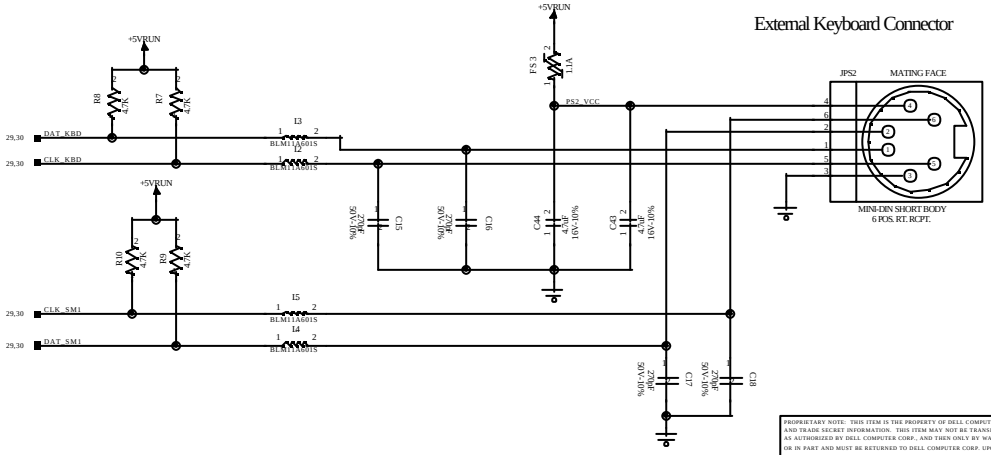
Power Sequencing Logic

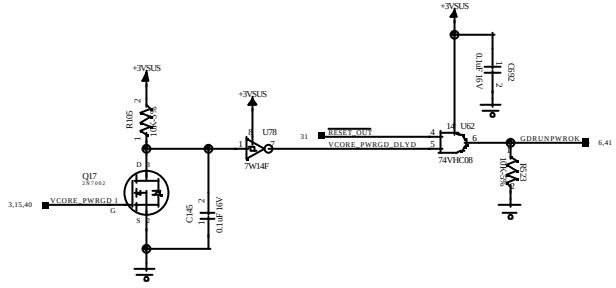


Power LED Driver



External Keyboard Connector





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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

DWG NO. 401195 REV. 4A

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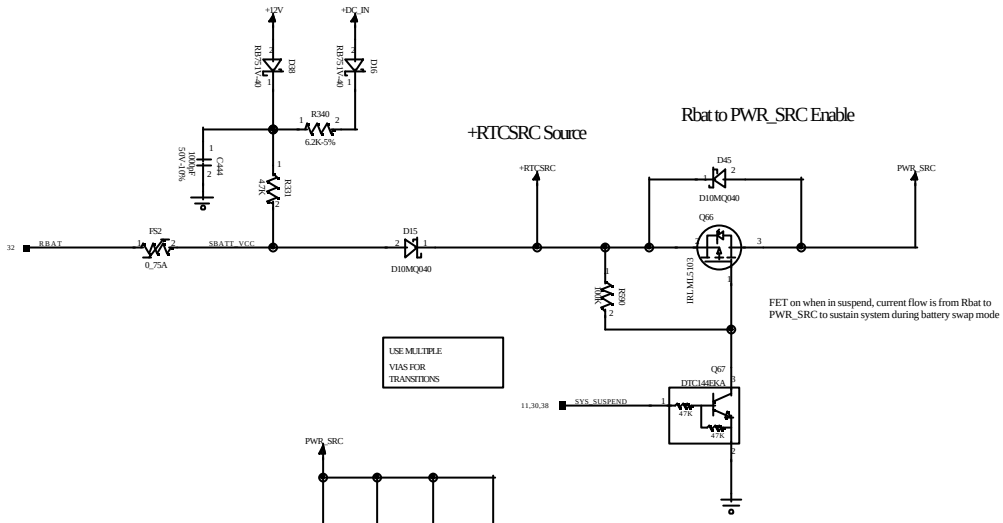
Rbat Charger

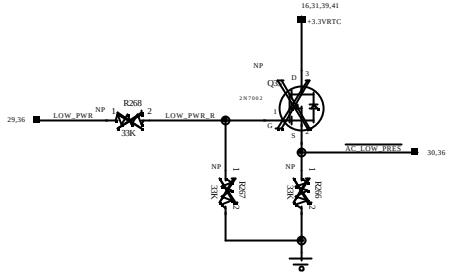


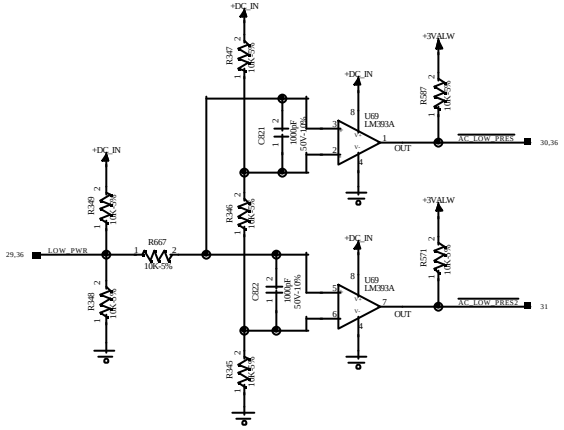
+RTCSRC Source

Rbat to PWR_SRC Enable

FET on when in suspend, current flow is from Rbat to PWR_SRC to sustain system during battery swap mode

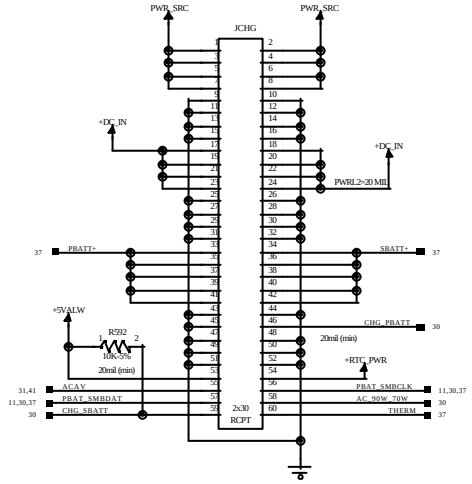
Low-Power Adapter Detection



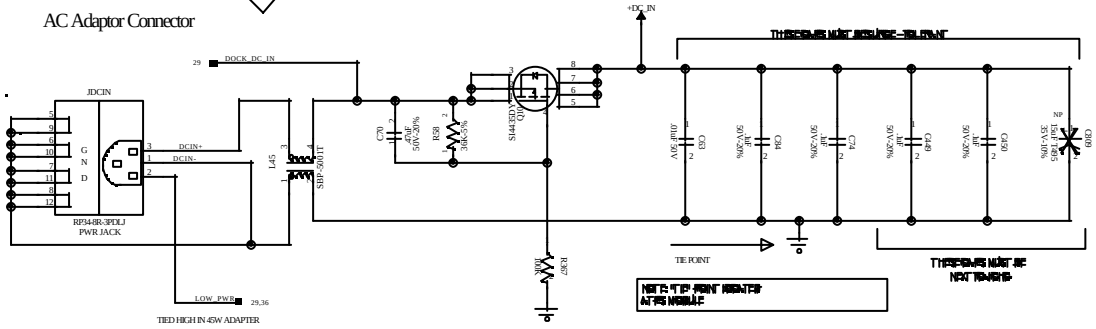


LOW_PWR	AC_LOW_PRES#	AC_LOW_PRES2#
0V	0	0
FLOAT	0	1
20V	1	1

Charger Board Connector



AC Adaptor Connector



+DC_IN Source

DELL	COMPUTER CORPORATION AUSTIN,TEXAS
TITLE	SCHEMATIC, M/B LA-1221
DWG NO. 401195	REV. 401195
DATE 11/15/02	SHEET 87 OF 102

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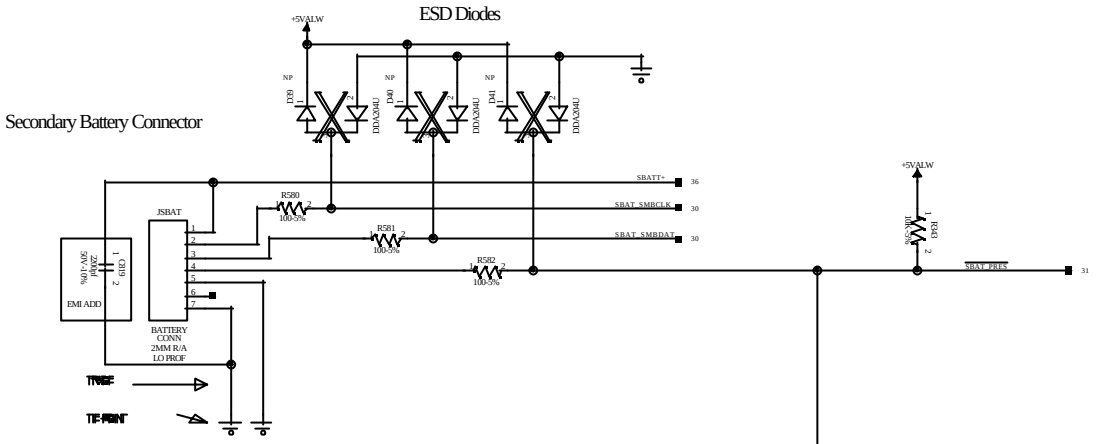

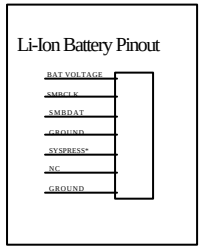

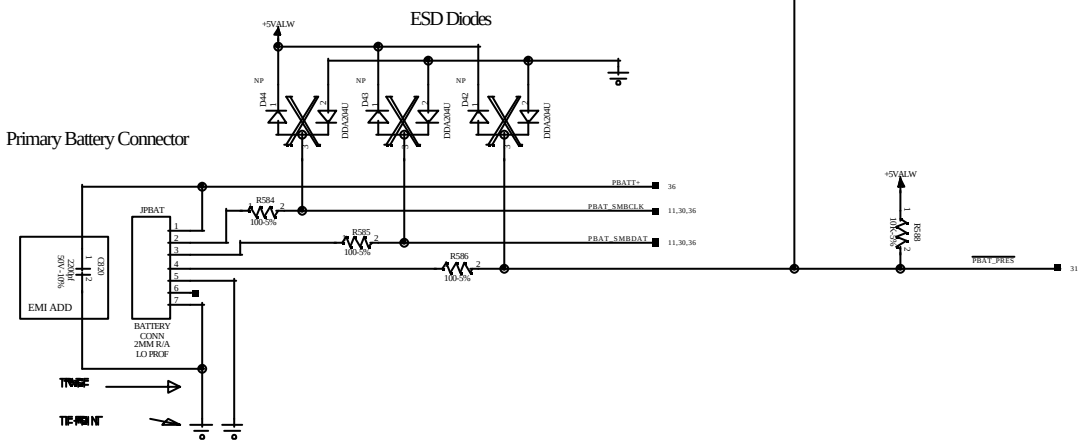

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TITLE: SCHEMATIC, M/B LA-1221

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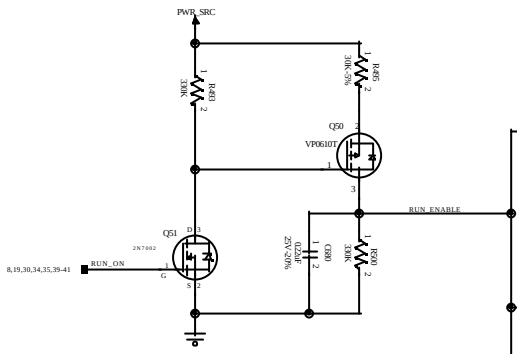
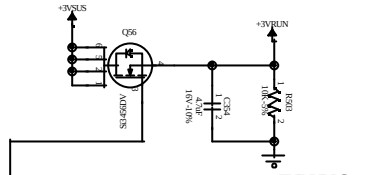
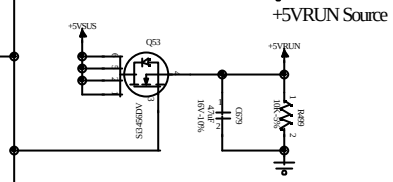
Run Planes Enable



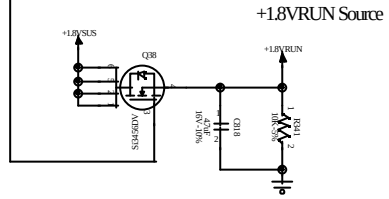
+3VRUN Source



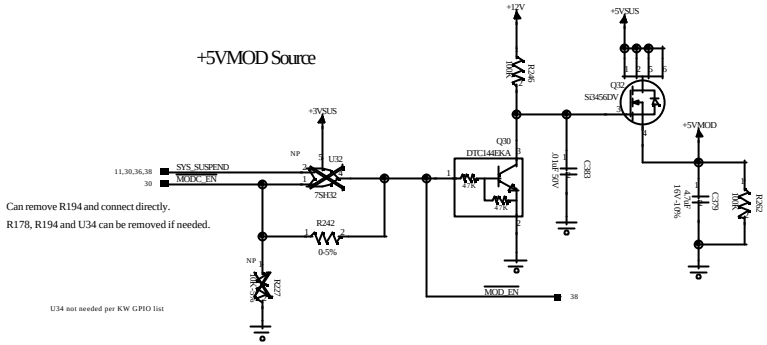
+5VRUN Source



+1.8VRUN Source



+5VMOD Source

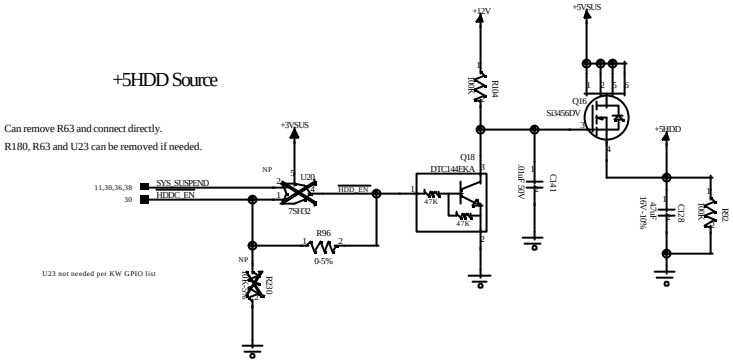


Can remove R194 and connect directly.

R178, R194 and U34 can be removed if needed.

U34 not needed per KW GPIO list

+5HDD Source

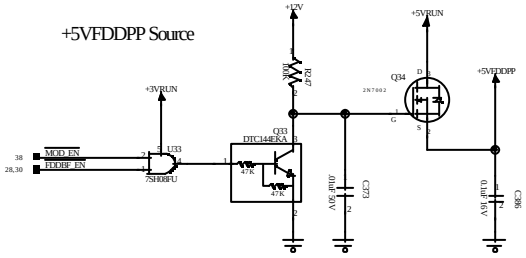


Can remove R63 and connect directly.

R180, R63 and U23 can be removed if needed.

U23 not needed per KW GPIO list

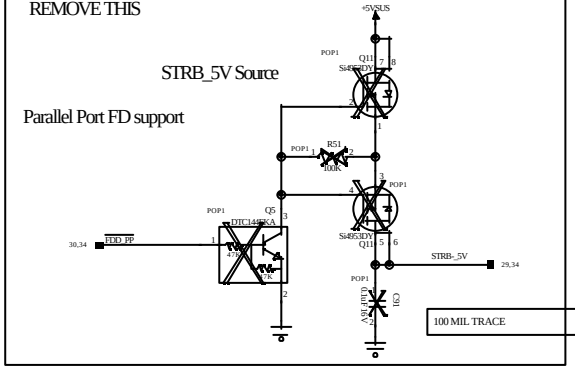
+5VFDDPP Source



REMOVE THIS

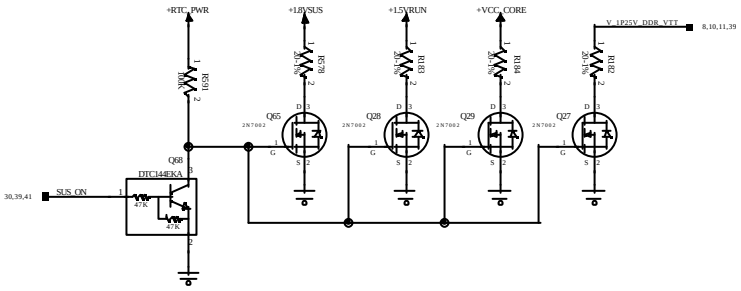
STRB_5V Source

Parallel Port FD support



100 MIL TRACE

Voltage Rail Drains



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DELL COMPUTER CORPORATION AUSTIN,TEXAS

TITLE: SCHEMATIC, M/B LA-1221

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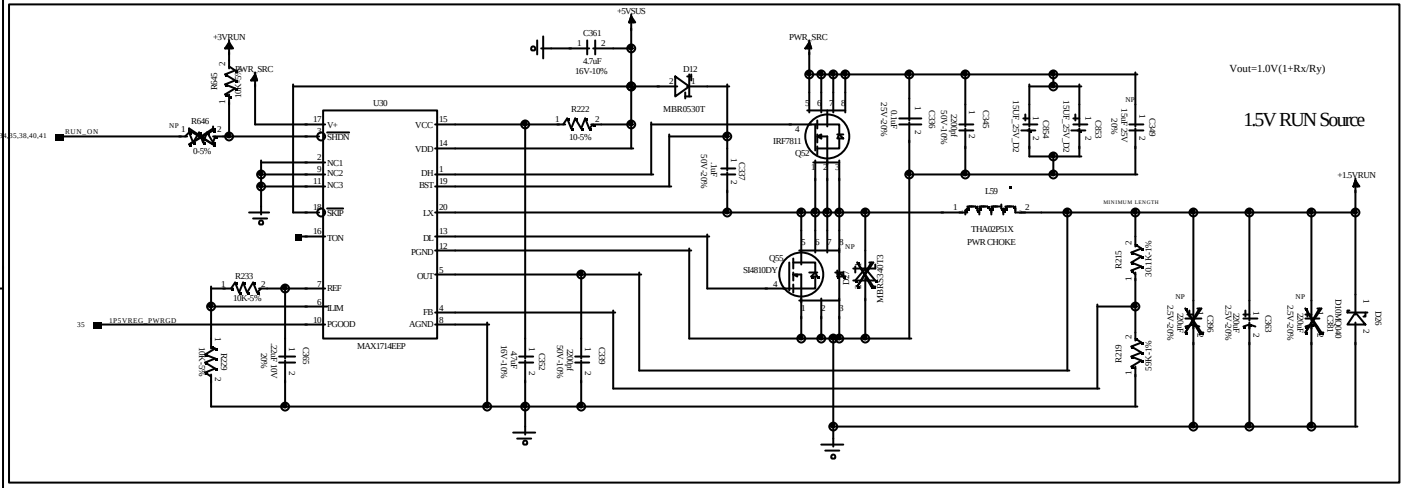

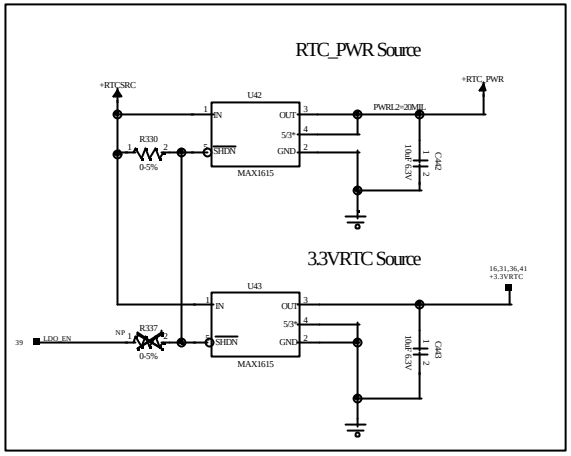

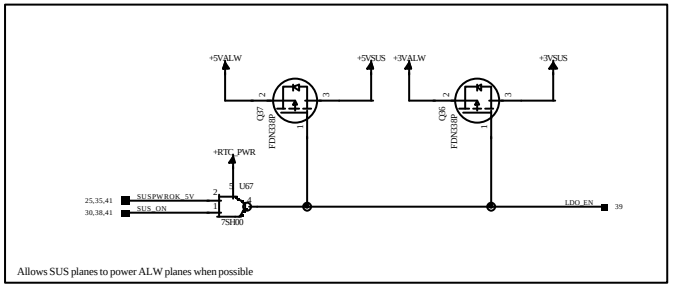

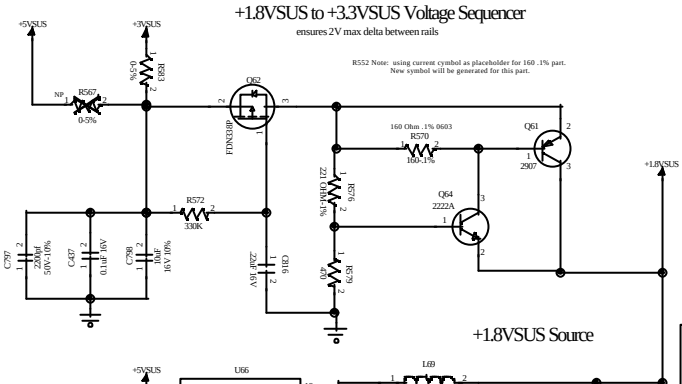

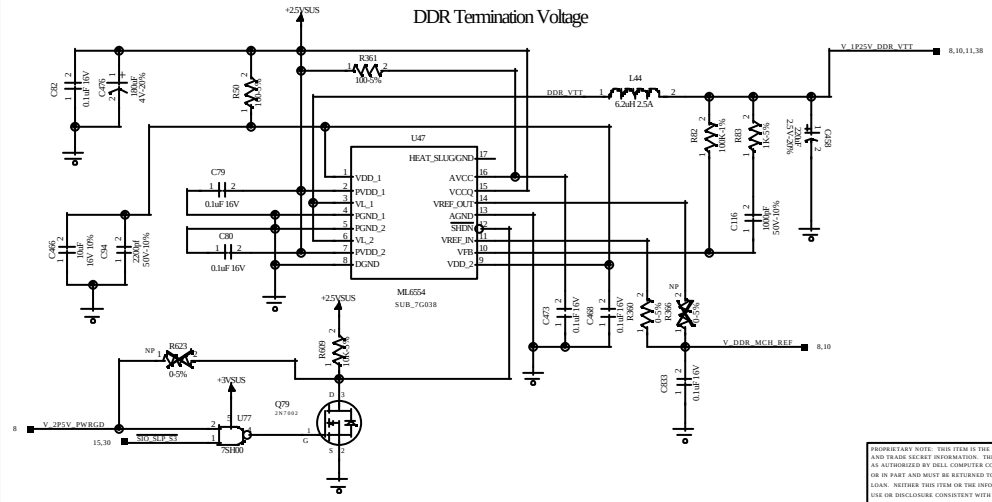

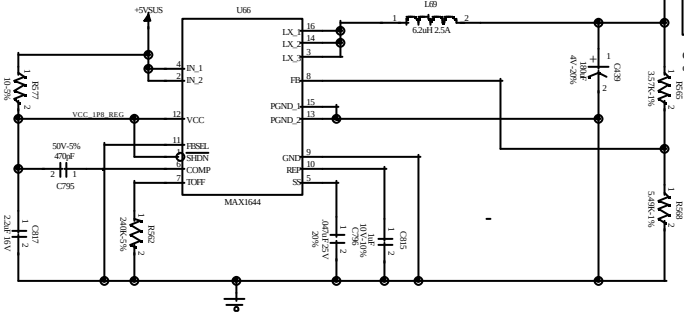

Layout Note: C694

Place a "+" character silkscreen on the board near the non-ground side of this capacitor.

C694 can be populated with either a ceramic (PN 2832P) or tantalum (PN 1033E) type capacitor

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TITLE: SCHEMATIC, M/B LA-1221

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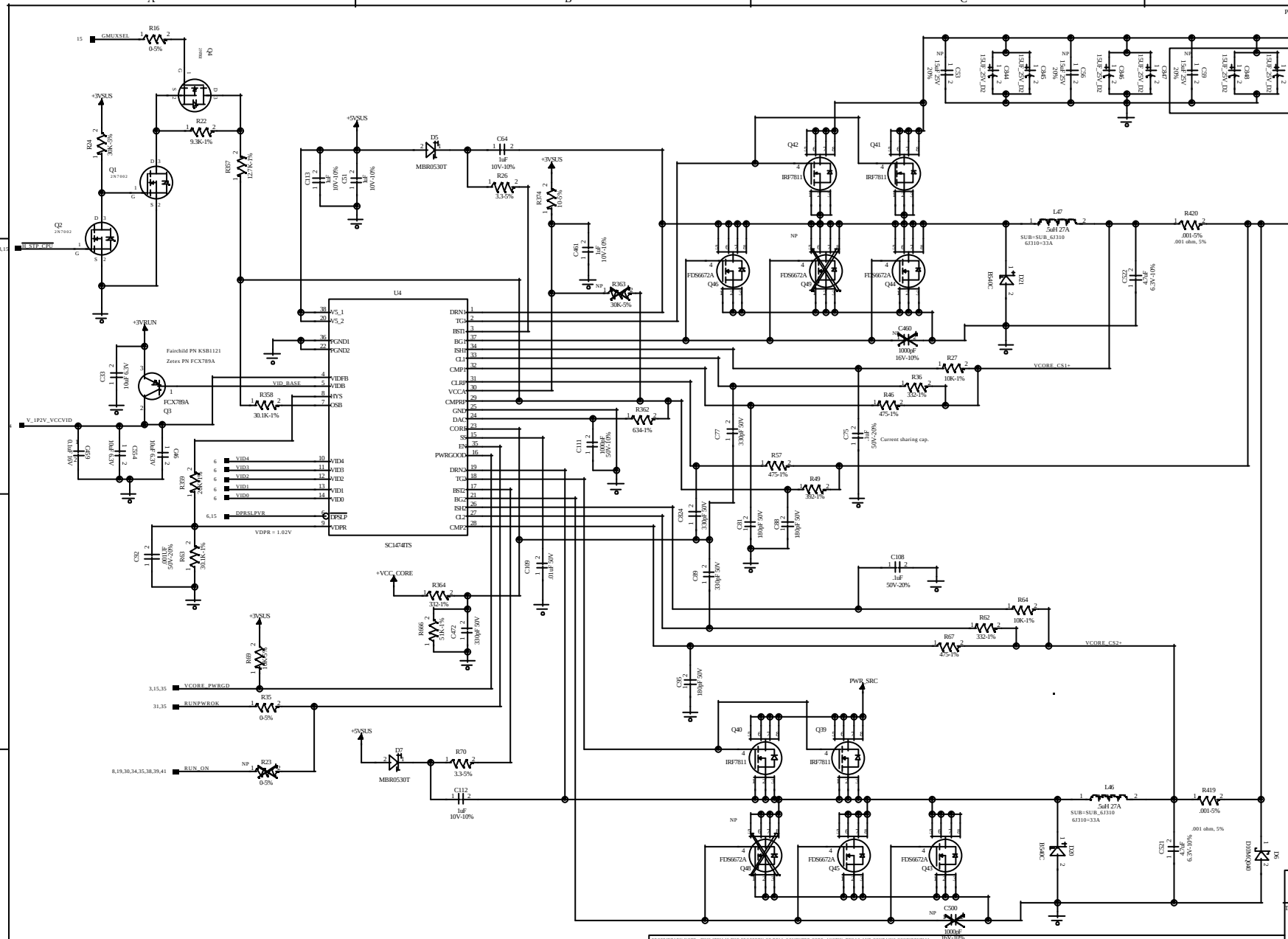
11-20-2002_22:38
+VCC_CORE Source
ALLOWS DUAL CAPACITOR POPULATION OPTION
2x10uF_X7R_1812 DP#:483CT versus
1x15uF_X7R_2220 DP#:513KHM
Fairchild PN KSB1121
Zetex PN FCX789A
SC1474ITS
Current sharing cap.
Output Caps = Panasonic EEFUE0E221R_025esr_3Arms
VDPR = 1.02V
VCORE_CS1+
VCORE_CS2+
PWR_SRC
+VCC_CORE
+3VSUS
+5VSUS
+3VRUN
GMUXSEL
H_STP_CPU
V_1P2V_VCCVID
VID_BASE
VID4
VID3
VID2
VID1
VID0
DPRSLPVR
VCORE_PWRGD
RUNPWROK
RUN_ON
U4
Q1
Q2
Q3
Q4
Q39
Q40
Q41
Q42
Q44
Q45
Q46
Q48
Q49
L47
L46
D5
D7
D8
D10
D11
D12
D13
R16
R22
R24
R26
R27
R35
R36
R46
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R62
R64
R67
R69
R70
R74
R358
R359
R362
R363
R364
R419
R420
R23
R63
R666
C64
C77
C75
C111
C112
C109
C108
C460
C461
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C522
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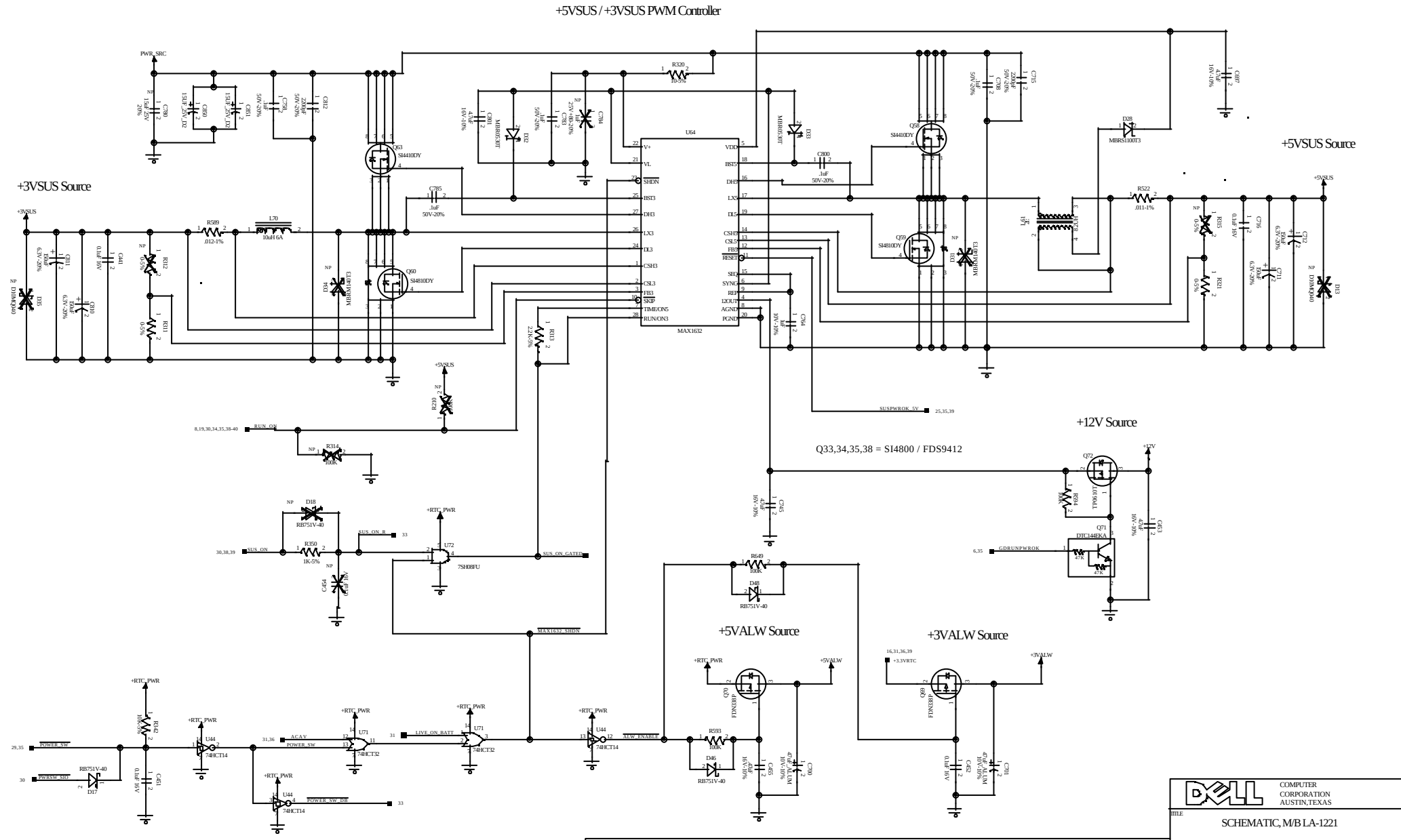

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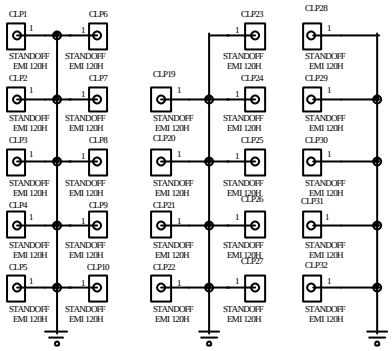
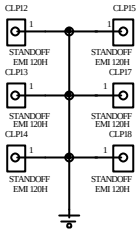
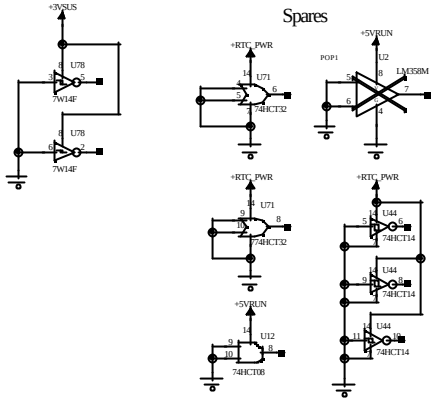
Memory Door EMI Clips



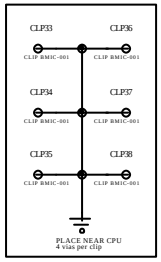
Mini-PCI Door EMI Clips



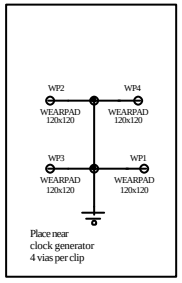
Spares



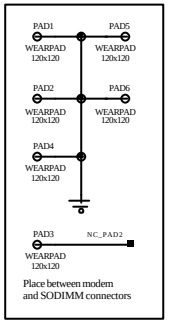
CPU EMI Clips



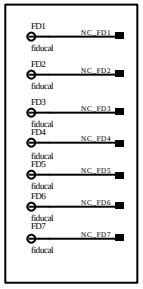
Clock Generator EMI Clips

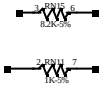


Modem Fence Pads

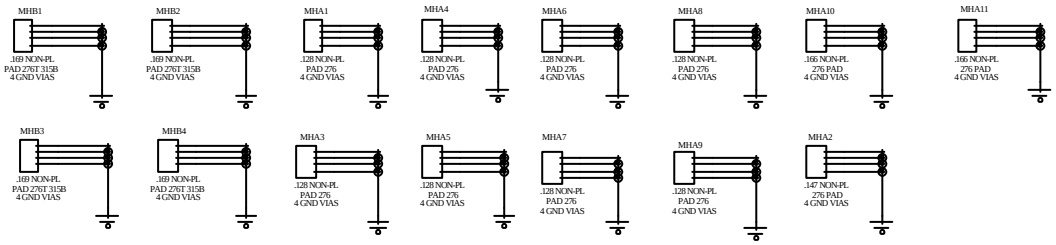


Fiducial mark





Mounting Holes



Xlbom requires ADD attributes to be attached to
an instance of a symbol (prefix I in the wire file). You may
not rename the attributes. The required order is:
ADD, ADD1, ADD2, ...

ADD=ADD*_3F195_PWB
ADD1=ADD*_1F473_SCHEM
ADD2=ADD*_0F681_ASSYDWG
ADD3=ADD*_42753_BARCODE_REVLEVEL — Label, REV LEVEL,BARCODE,PWA
ADD4=ADD*_42610_BARCODE_IDLABEL — Label, LBL,ID,BARCODE,PWA
PWA 2F219

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DELL COMPUTER CORPORATION AUSTIN,TEXAS

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Schematic Change List

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COMPUTER
CORPORATION
AUSTIN,TEXAS

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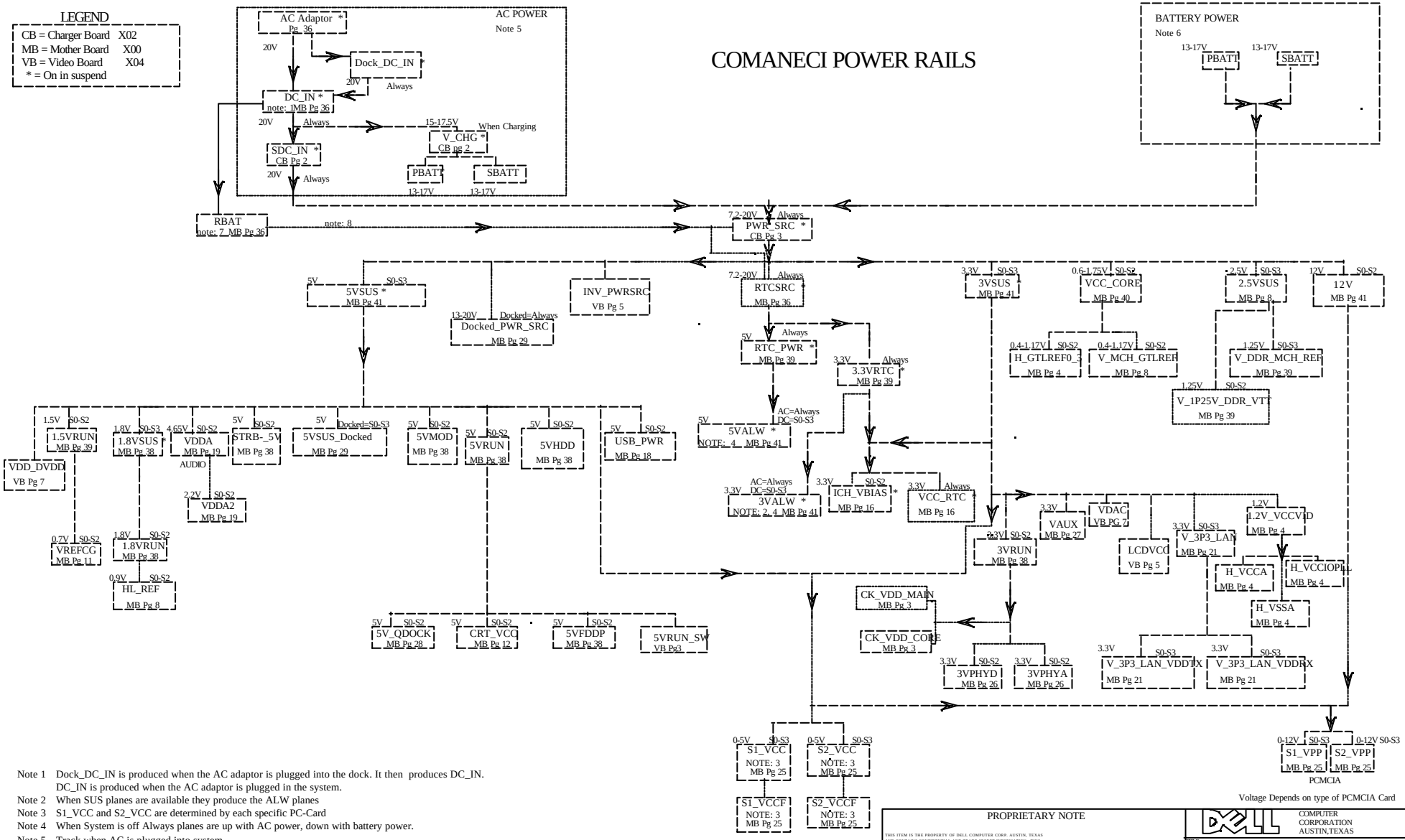
COMANECI POWER RAILS



Note 1 Dock_DC_IN is produced when the AC adaptor is plugged into the dock. It then produces DC_IN. DC_IN is produced when the AC adaptor is plugged in the system.
Note 2 When SUS planes are available they produce the ALW planes
Note 3 S1_VCC and S2_VCC are determined by each specific PC-Card
Note 4 When System is off Always planes are up with AC power, down with battery power.
Note 5 Track when AC is plugged into system
Note 6 Track when System is battery power
Note 7 Charged by 12V when on battery
Note 8 Enabes PWR_SRC during battery swap

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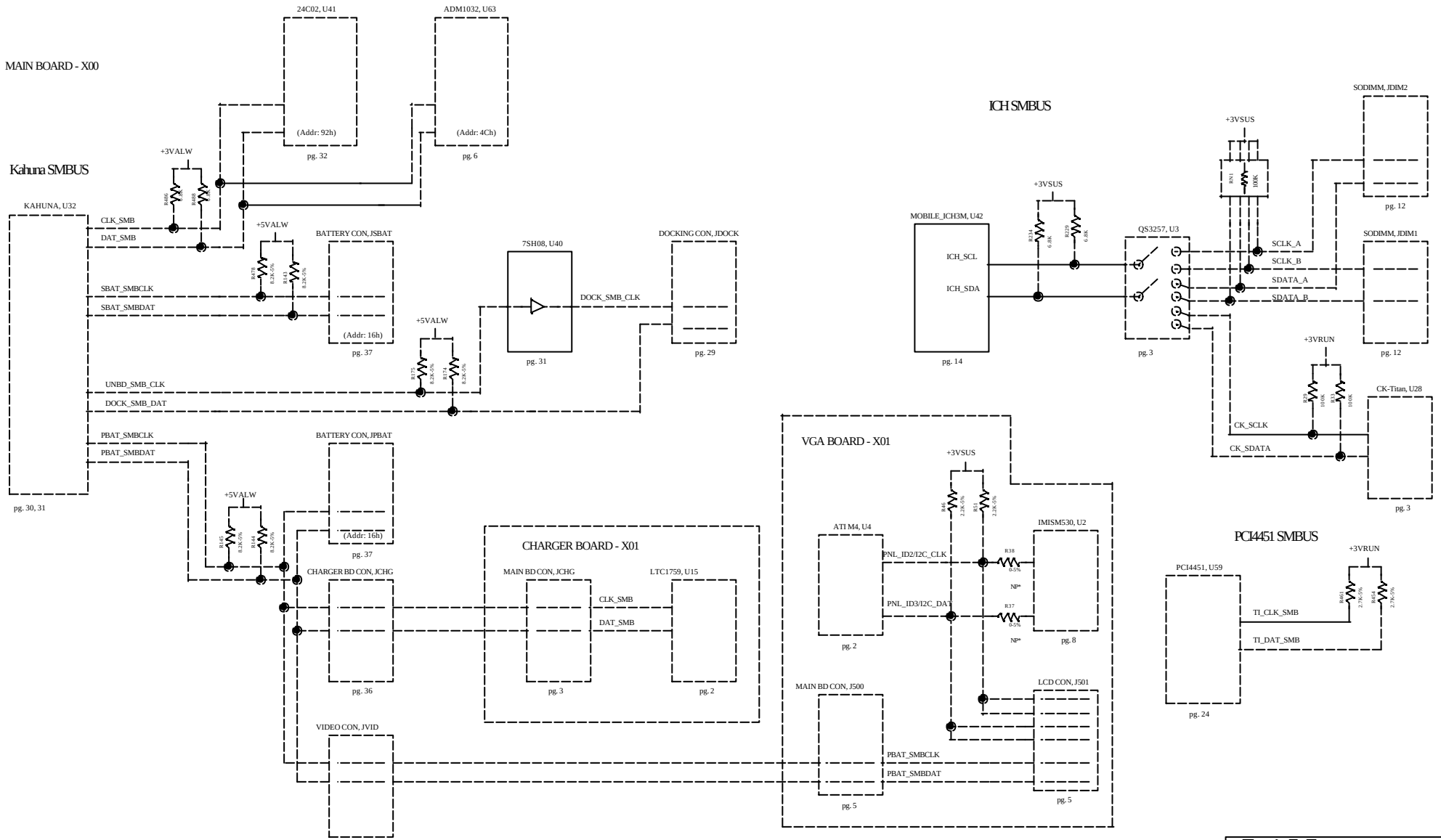

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