

Compal Confidential

LA-A141P Schematics Document

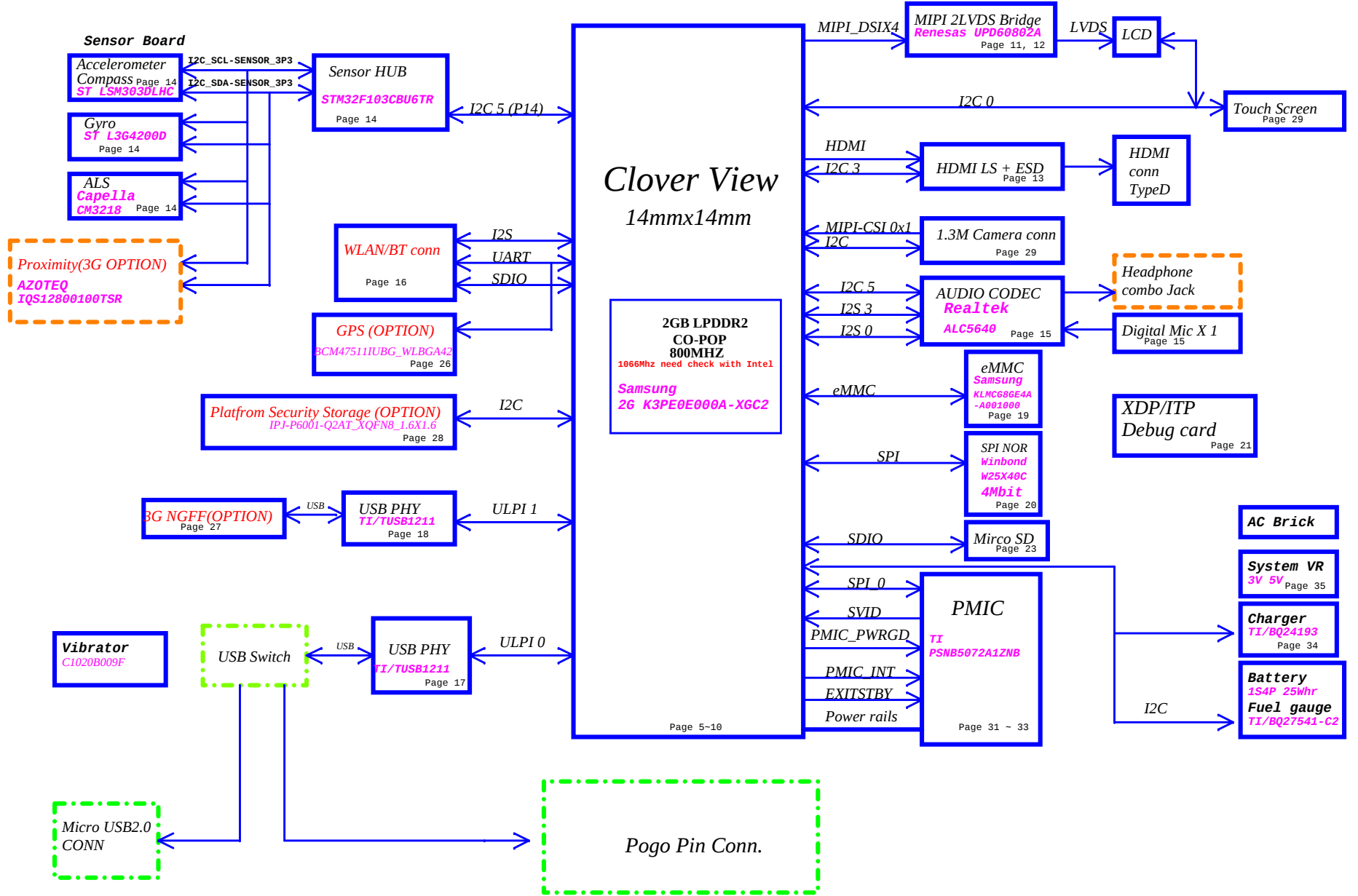
Intel Clover View

2013-03-25

REV: 0.3

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title	Cover Page	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Part Number	Document Number	Rev
				Custom	LA-A141P	0.2
				Date	Tuesday, May 14, 2013	Sheet 1 of 38

System Block Diagram



Docking daughter board

Audio daughter board

Power rail

	Net name	Voltage	Comment
	DC_IN	5V or 12V	adapter input
	+V_BATTERY	Max 4.2V	Battery input
	+VBATA	3.4V~4.2V	It's tablet's "B+"
	+V5A	5V	Should be 5VALW
	+V3.3A	3.3V	Should be 3VALW
	+V5S	5V	Should be 5VS
	+V3.3S	3.3V	Should be 3VS
	+V_VCC	0.3~1.2V	0.95V is default value
PMIC BUCK Converter	+V_VNN	0.6~1.2V	0.95V is default value
	+V_VNNAON	0.6~1.2V	0.95V is default value
	+V_1P22_VCCAON	1.250V	1.25V is default value
	+V_1P80_AON	1.836V	1.836V is default value
PMIC LDO	+V_1P08_VCCAON	1.08V	
	+V_1P08_VCCAS	1.08V	
	+V_1P08_VCC	1.08V	
	+V_1P00_VCCAS	1.025V	
	+V_1P00_VCCA	1.025V	
	+V_2P85_1P80_VCCSDIO	2.85V	2.85V is default value
	+V_2P80_VPROG1	1.2V	1.2V is default value
	+V_2P80_VPROG2	1.2V	1.2V is default value
	+V_2P85_EMMC1	2.85V	
	+V_2P85_EMMC2	2.85V	No routing
PMIC PWM output	+V_1P80_AON	1.8V	
	+V_1P80_VCCAON	1.8V	
	+V_1P80_VCC	1.8V	
	+V_3P30_VCC	3.3V	

IO MAP

Interface	Device
MIPI DSI	LVDS Bridge
MIPI CSI	X4 NC
	X1 1.3M Camera
MIPI HSI	NC
LPC	NC
EMMC	0 EMMC
	1 NC
HDMI	HDMI Conn.
SDIO	0 Micro SD
	1 WIFI/BT
	2 NC
SVID	PMIC
SPI	0 FLASH ROM/PMIC/XDP
	1 XDP
	2 Test Point
	3 NC
ULPI	0 External USB/P060
	1 USB 3G (Reserve)
UART	0 WIFI/BT
	1 GPS
	2 XDP
I2S	0 Codec
	1 BT PCM
	2 NC
	3 Codec
I2C	0 Panel / Touch screen
	1 NC
	2 Battery/PSS
	3 HDMI
	4 1.3M camera
	5 Codec / Sensor Hub

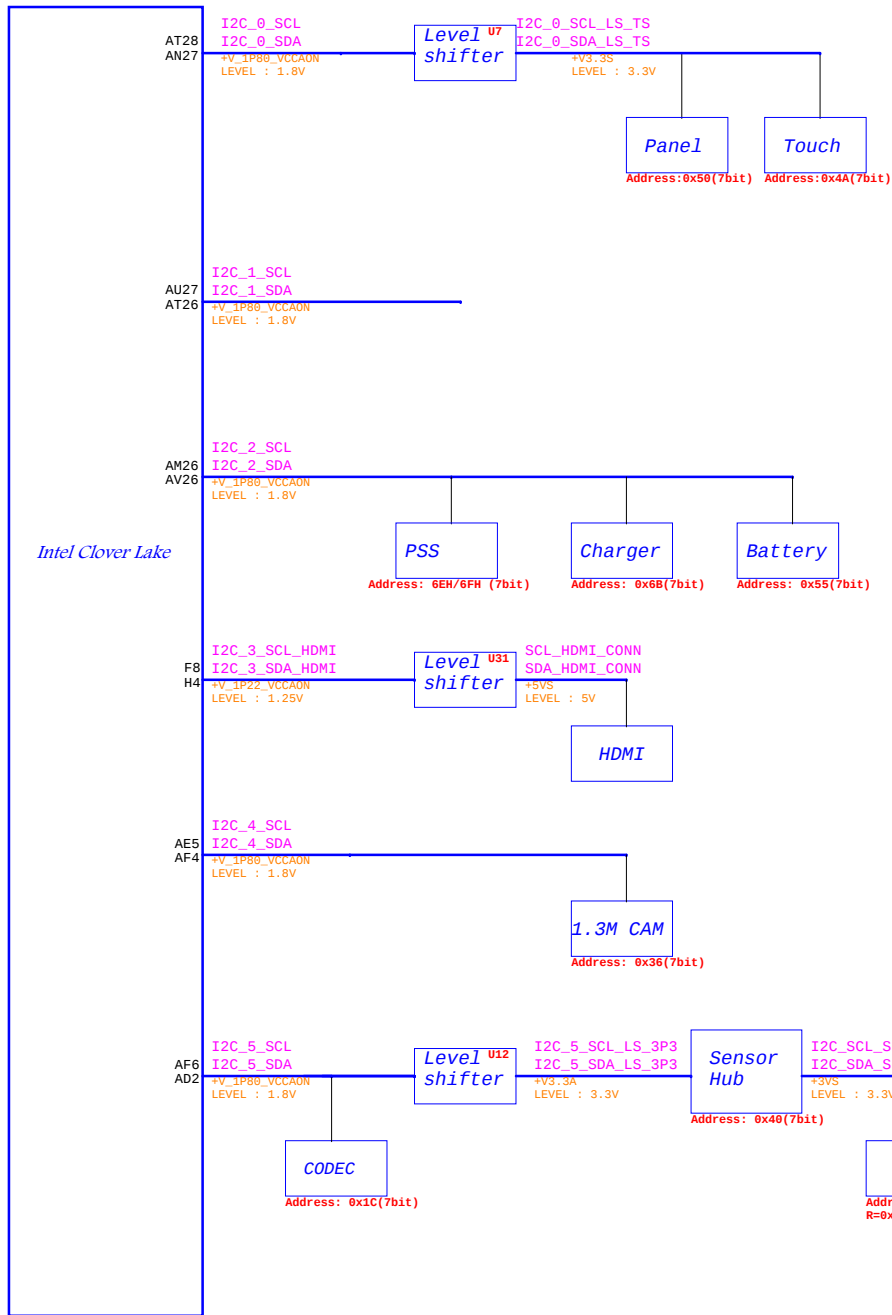
BOM Structure

ME@	ME usage
3G@	for 3G/LTE reservation
XDP@	for XDP function
NO_XDP@	for XDP function disable
SAMMEM@	for SAMSUNG DRAM
HYNMEM@	for HYNIX DRAM
M@	for EMI/ESD/RF Request
SAM64@	for SAMSUNG 64G eMMC
SAM32@	for SAMSUNG 32G eMMC
SDK64@	for Sandisk 64G eMMC
SDK32@	for Sandisk 32G eMMC
GPS@	For GPS SKU
PSS@	For PPS SKU

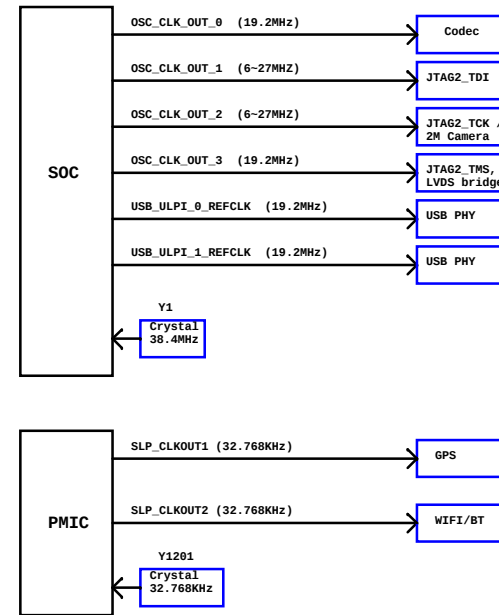
RF@ RF request to reseve

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				C	LA-A141P	0.1
				Date:	Tuesday, May 14, 2013	Sheet 3 of 38

I2C Routing

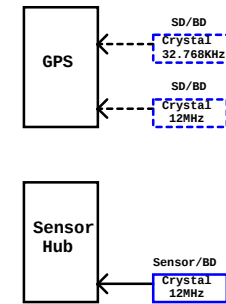


Clock Distribution

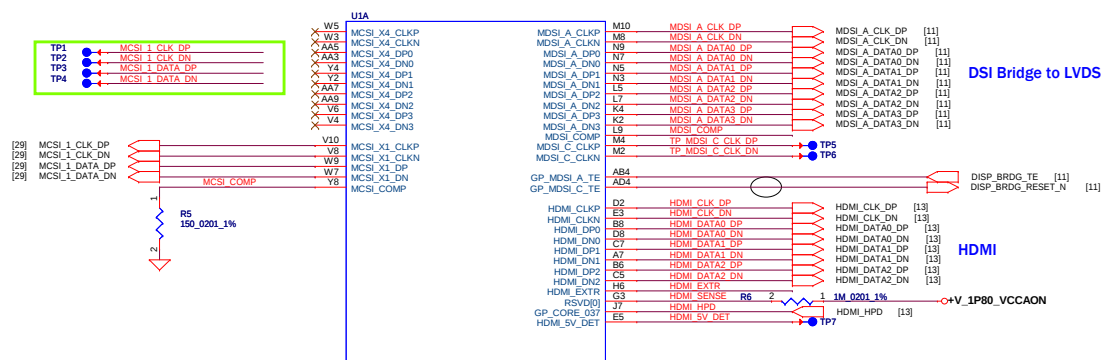


MB

SUB



Security Classification		Compal Secret Data		Compal Electronics, Inc.					
Issued Date		2012/09/20		Deciphered Date		2014/09/20		Title	
THIS SHEET OF ENGINEERING DRAWINGS IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								I2C ROUTING/Clock Distribution	
Size		Document Number		Rev		0.1		New	
Date		Tuesday, May 14, 2013		Sheet		4		of 38	



Audio Codec

BT PCM

Audio Codec

Low Intel's recommend.

OM/PMIC/XDP

XDP DEBUG

TP9

TP10

TP11

TP12

TP13

TP14

TP15

TP16

TP17

TP18

TP19

TP20

TP21

TP22

TP23

TP24

TP25

TP26

TP27

TP28

TP29

TP30

TP31

TP32

TP33

TP34

TP35

TP36

TP37

TP38

TP39

TP40

TP41

TP42

TP43

TP44

TP45

TP46

TP47

TP48

TP49

TP50

TP51

TP52

TP53

TP54

TP55

TP56

TP57

TP58

TP59

TP60

TP61

TP62

TP63

TP64

TP65

TP66

TP67

TP68

TP69

TP70

TP71

TP72

TP73

TP74

TP75

TP76

TP77

TP78

TP79

TP80

TP81

TP82

TP83

TP84

TP85

TP86

TP87

TP88

TP89

TP90

TP91

TP92

TP93

TP94

TP95

TP96

TP97

TP98

TP99

TP100

TP101

TP102

TP103

TP104

TP105

TP106

TP107

TP108

TP109

TP110

TP111

TP112

TP113

TP114

TP115

TP116

TP117

TP118

TP119

TP120

TP121

TP122

TP123

TP124

TP125

TP126

TP127

TP128

TP129

TP130

TP131

TP132

TP133

TP134

TP135

TP136

TP137

TP138

TP139

TP140

TP141

TP142

TP143

TP144

TP145

TP146

TP147

TP148

TP149

TP150

TP151

TP152

TP153

TP154

TP155

TP156

TP157

TP158

TP159

TP160

TP161

TP162

TP163

TP164

TP165

TP166

TP167

TP168

TP169

TP170

TP171

TP172

TP173

TP174

TP175

TP176

TP177

TP178

TP179

TP180

TP181

TP182

TP183

TP184

TP185

TP186

TP187

TP188

TP189

TP190

TP191

TP192

TP193

TP194

TP195

TP196

TP197

TP198

TP199

TP200

TP201

TP202

TP203

TP204

TP205

TP206

TP207

TP208

TP209

TP210

TP211

TP212

TP213

TP214

TP215

TP216

TP217

TP218

TP219

TP220

TP221

TP222

TP223

TP224

TP225

TP226

TP227

TP228

TP229

TP230

TP231

TP232

TP233

TP234

TP235

TP236

TP237

TP238

TP239

TP240

TP241

TP242

TP243

TP244

TP245

TP246

TP247

TP248

TP249

TP250

TP251

TP252

TP253

TP254

TP255

TP256

TP257

TP258

TP259

TP260

TP261

TP262

TP263

TP264

TP265

TP266

TP267

TP268

TP269

TP270

TP271

TP272

TP273

TP274

TP275

TP276

TP277

TP278

TP279

TP280

TP281

TP282

TP283

TP284

TP285

TP286

TP287

TP288

TP289

TP290

TP291

TP292

TP293

TP294

TP295

TP296

TP297

TP298

TP299

TP300

TP301

TP302

TP303

TP304

TP305

TP306

TP307

TP308

TP309

TP310

TP311

TP312

TP313

TP314

TP315

TP316

TP317

TP318

TP319

TP320

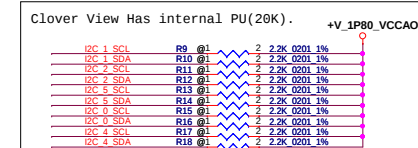
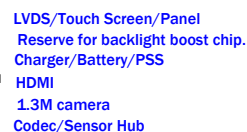
TP321

TP322

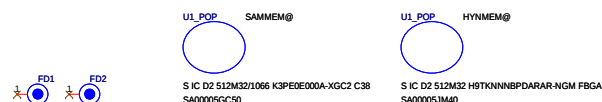
TP323

TP324

TP325



0123 According to vendor test result.
Change C1 and C2 into 18pf.

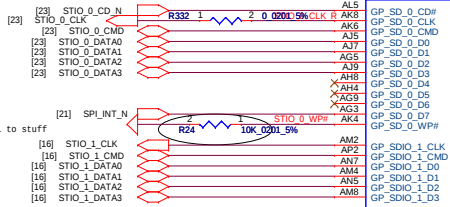


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				CLOVERVIEW (1 OF 6)		
				Size	Document Number	Rev
				C	LA-A141P	0.
Date:	Tuesday, May 14, 2013	Sheet	5	of	38	

Must Disable internal pull-high for uSD interface !!

11/28 EMI request to add R332

MicroSD

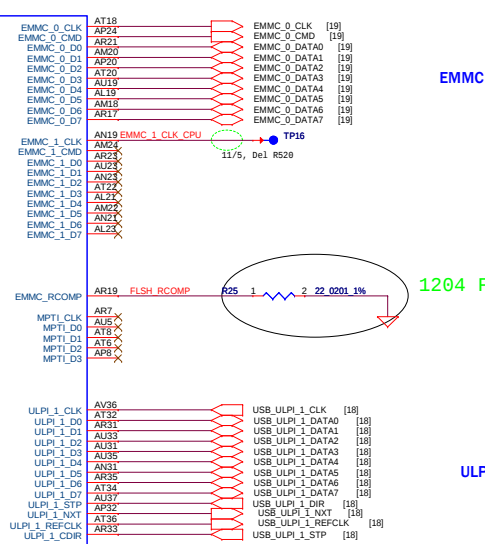


11/29 R24 follow LTK11 to stuff

WIFI/BT

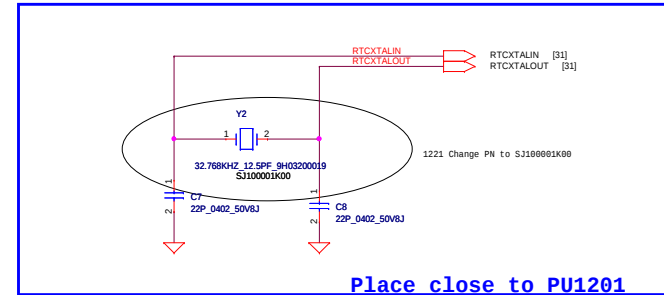
11/22 BT without RESET request

ULPI USB 0 (external USB)

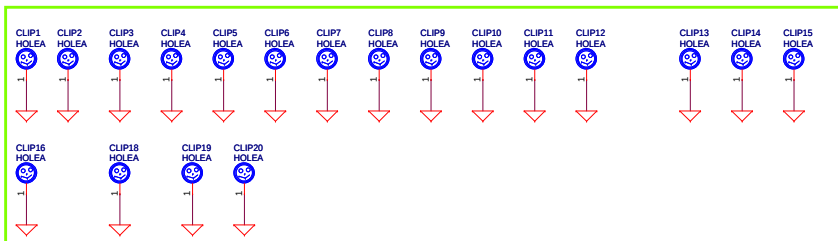


1204 Follow Intel's recommend. Change to 22 ohm

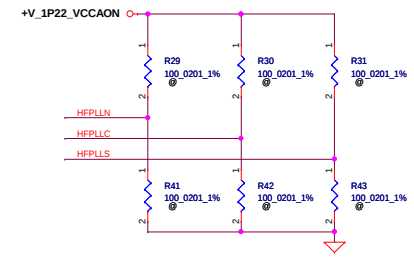
Those two pins were labeled in error on the SoC and in the EDS
The correct pinout is:
ULPI_1_DIR = AU37
ULPI_1_STP = AR33



Shielding Clip

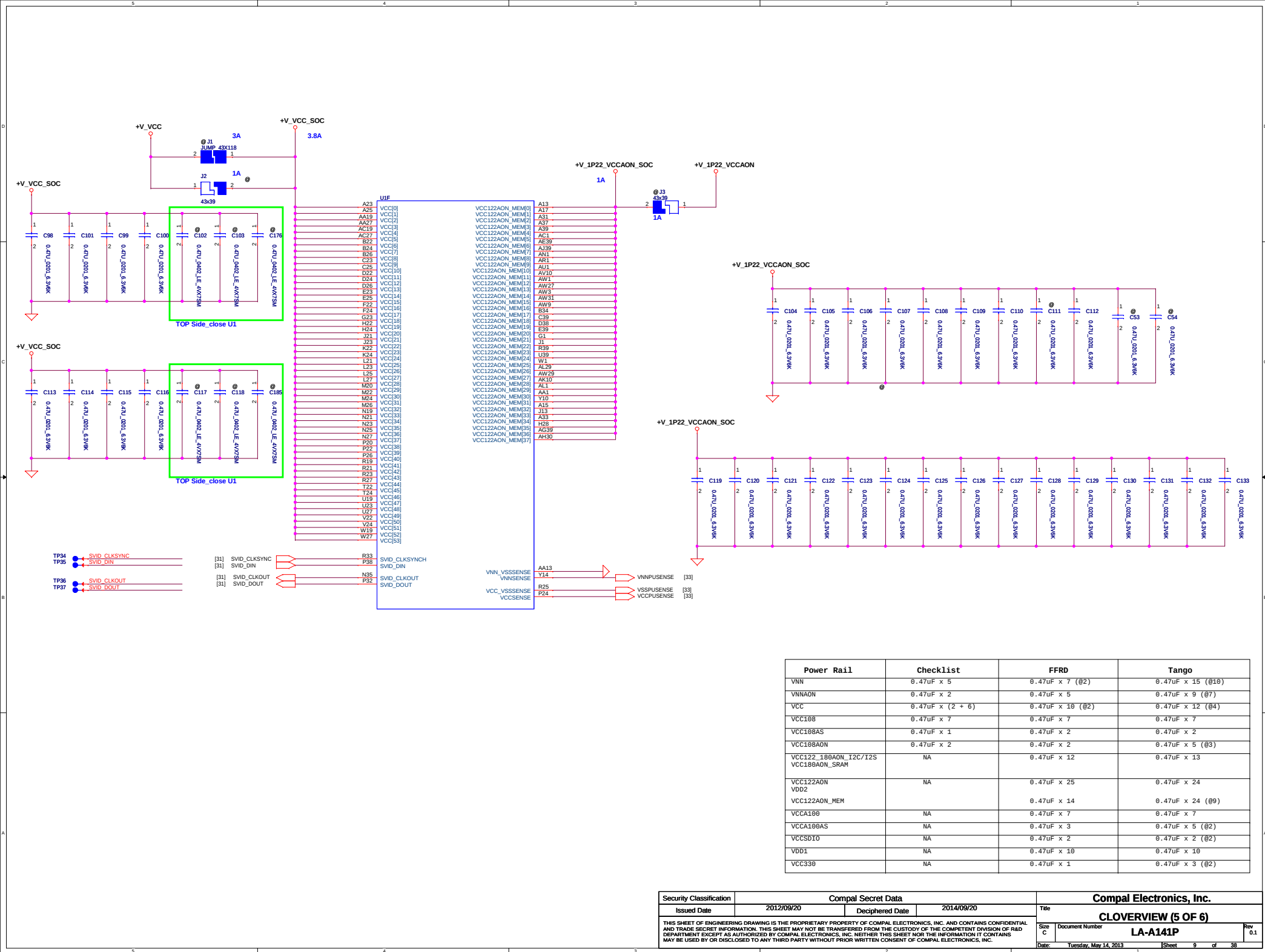


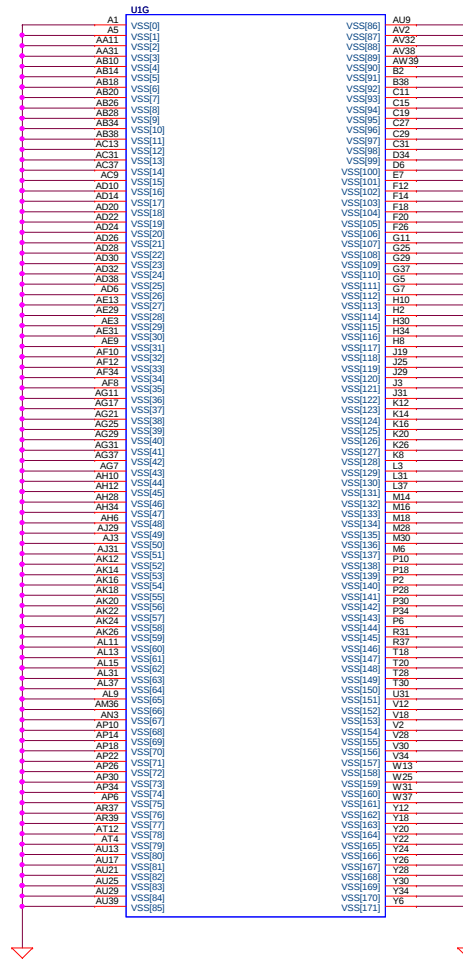
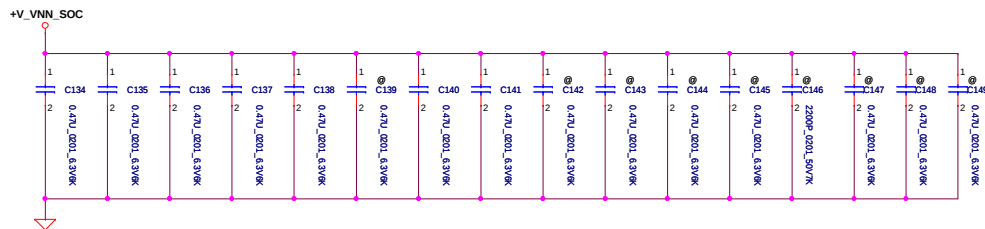
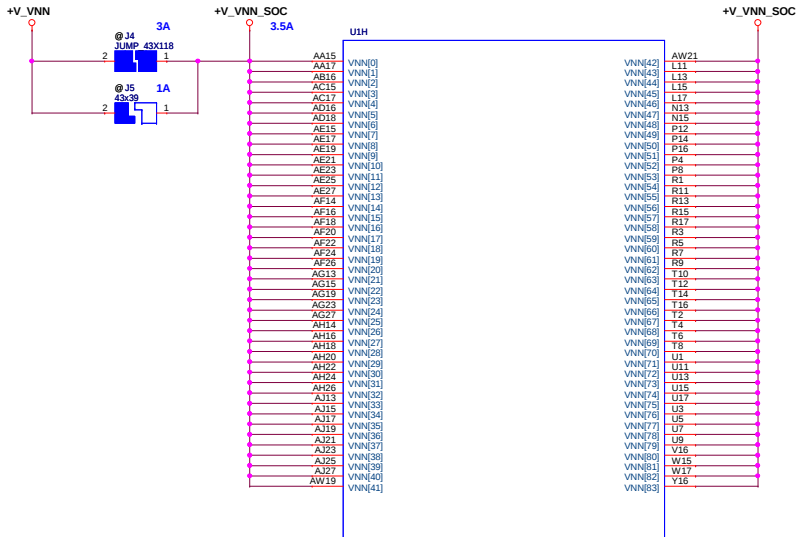
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date		2012/09/20	Deciphered Date	2014/09/20	Title	
					CLOVERVIEW (2 OF 6)	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						
Size	Document Number	LA-A141P			Rev	
C					0.1	
Date:		Tuesday, May 14, 2013		Sheet	6 of 38	

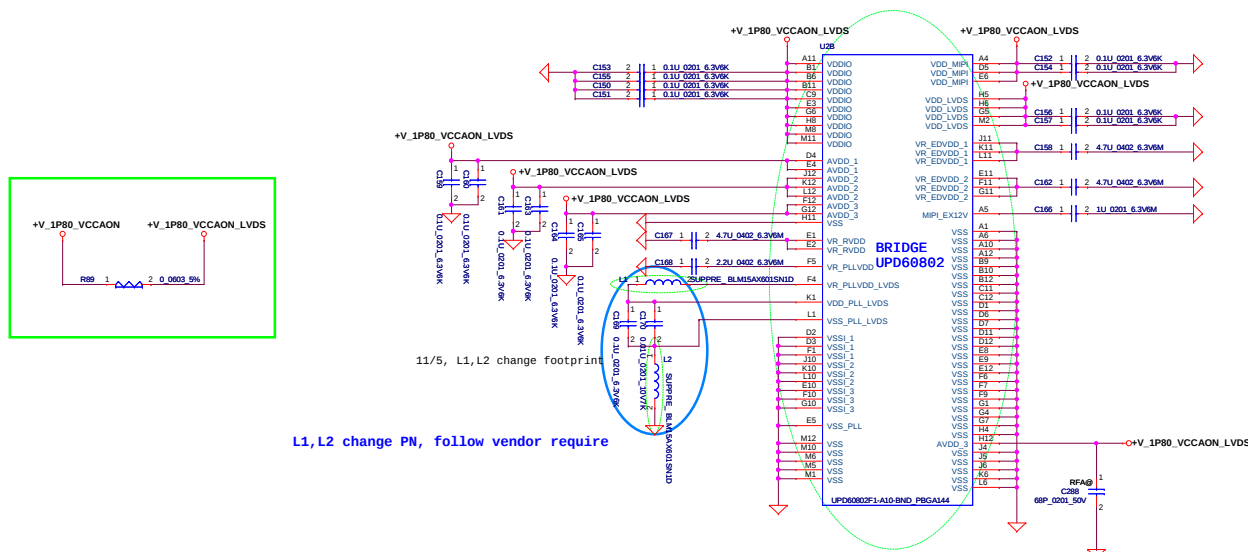
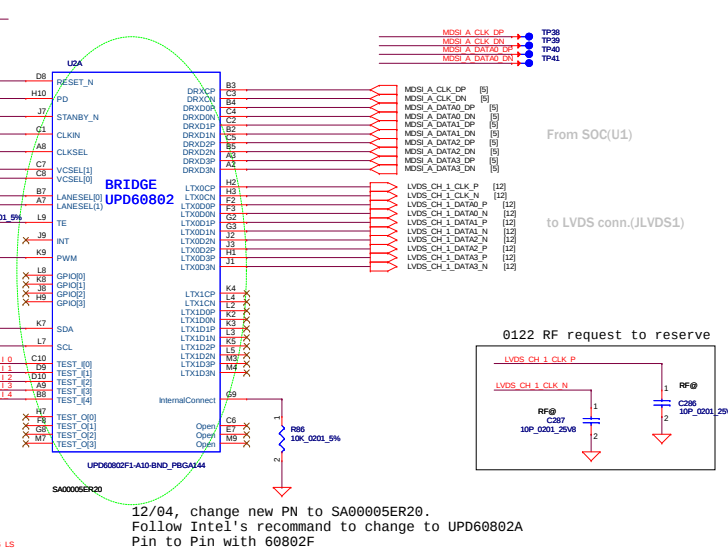


GPS

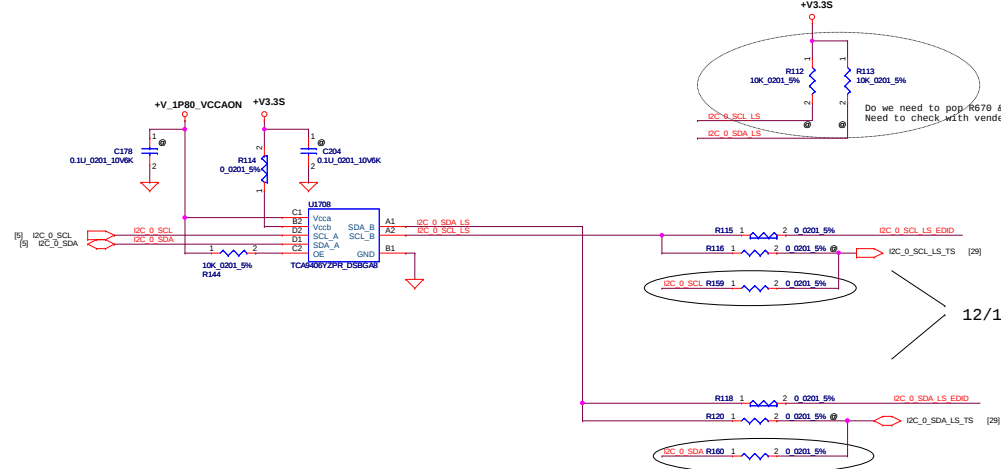
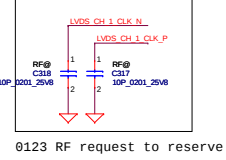
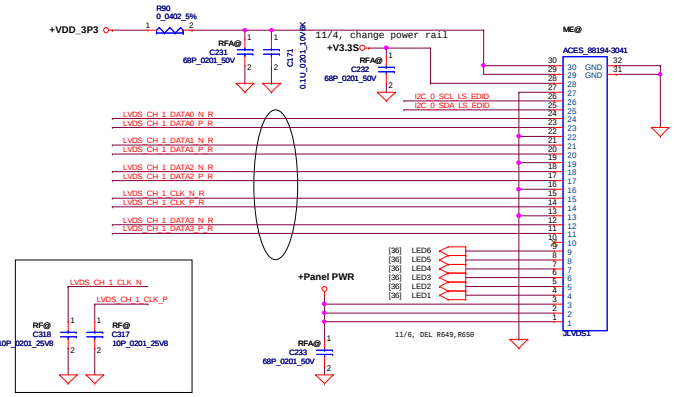
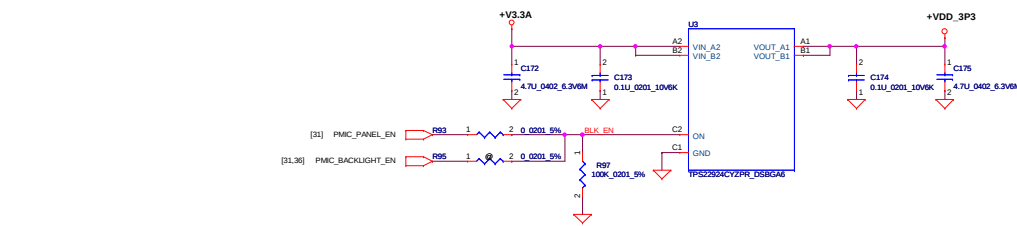
Security Classification		Compal Secret Data				Compal Electronics, Inc.								
Issued Date		2012/09/20		Deciphered Date		2014/09/20		Title CLOVERVIEW (3 OF 6)						
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Size C	Document Number		LA-A141P		Rev 0.1			
						Date:	Tuesday, May 14, 2013				Sheet	7	of	38



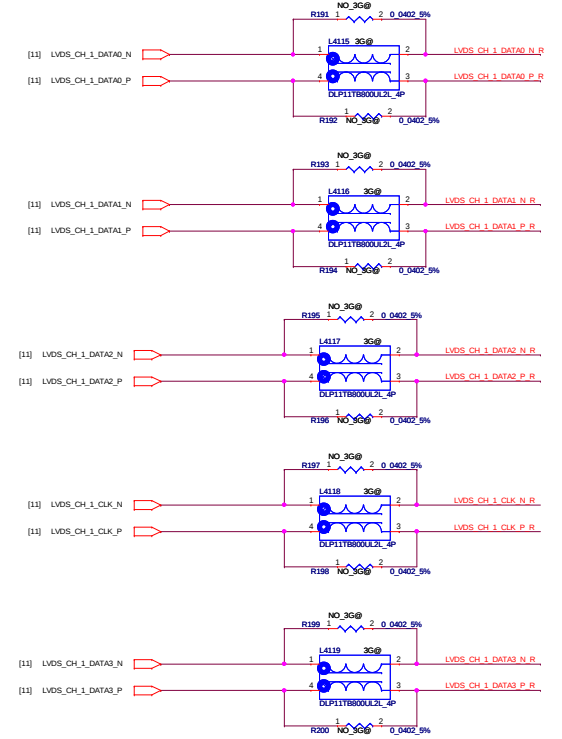




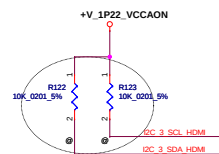
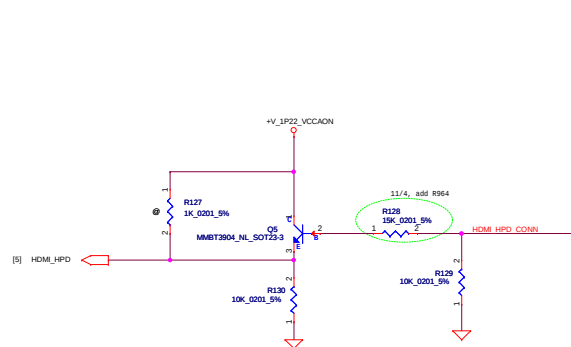
Security Classification	Compal Secret Data		Title	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Rev
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. WITHOUT WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			LVD8 Bridge LA-A141P Doc# _____ Date: Yumda, May 14, 2013 1 of 38	



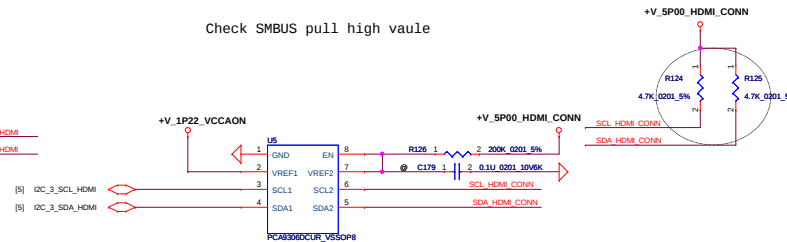
12/10 Follow Intel recommend



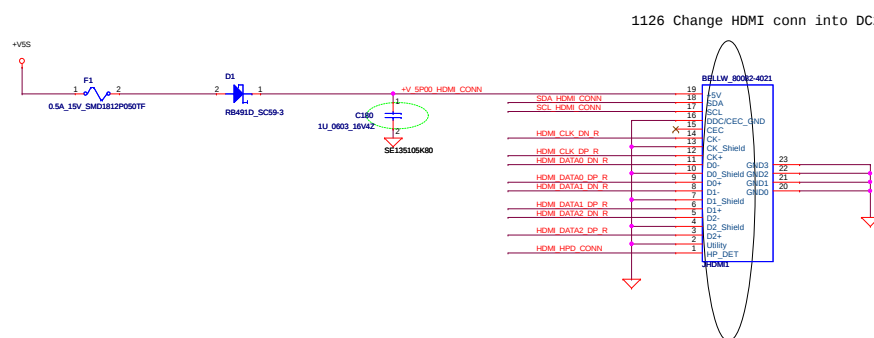
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. WITHOUT THE WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				LA-A141P
				Rev 0.1
				Date: Tuesday, May 14, 2013
				Sheet 12 of 30



Check SMBUS pull high vaule

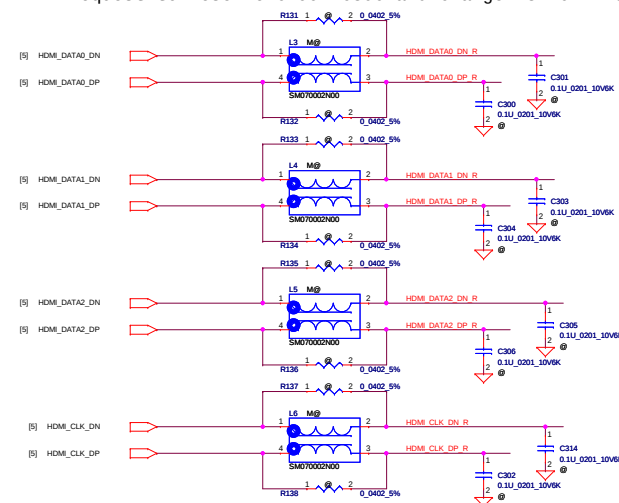


0129 Change the resistance vaule of R124 and R125.



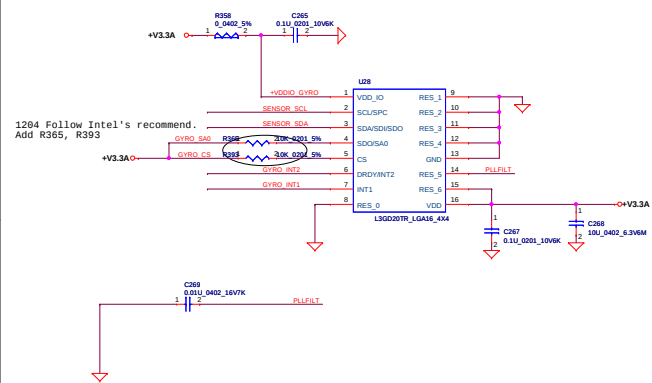
1126 Change HDMI conn into DC232001V00

0117 EMI request to reserve C299~ C306 and Change L3~L6 PN to SM070002N00

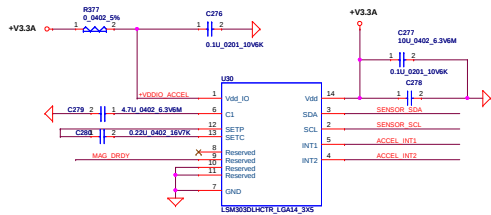


Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. WITHOUT THE WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				LA-A141P
				Date: Tuesday, May 14, 2013
				Page: 13 of 30

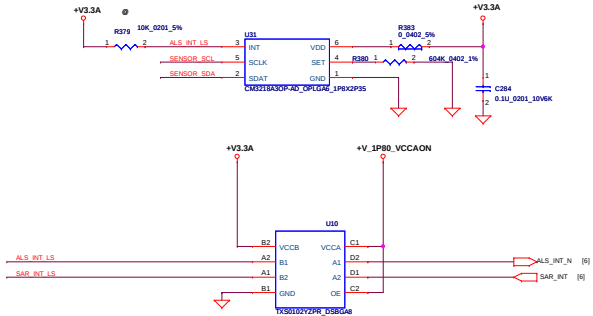
GYROSCOPE



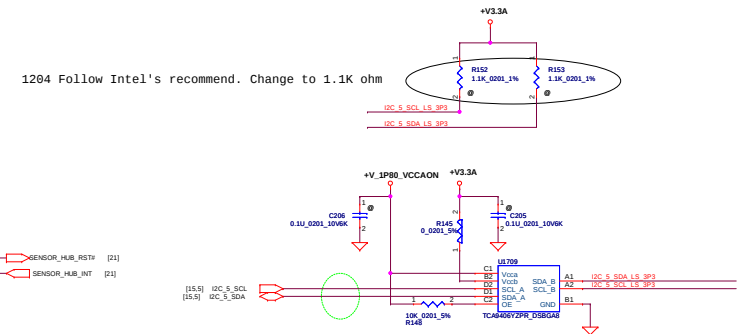
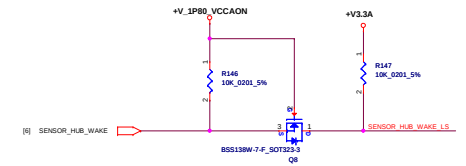
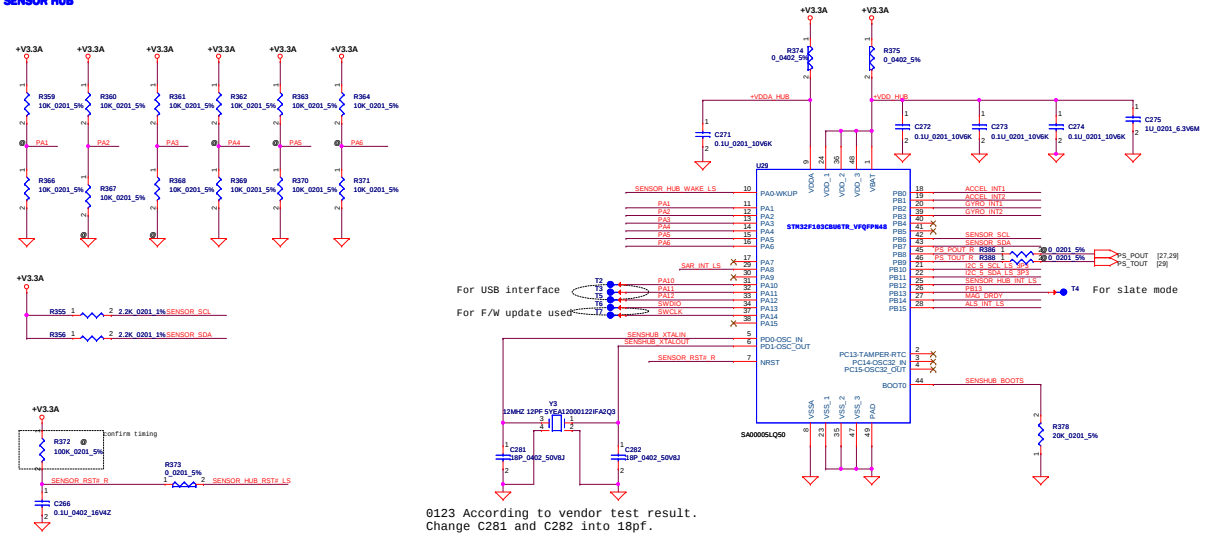
Accelerometer & E-COMPASS



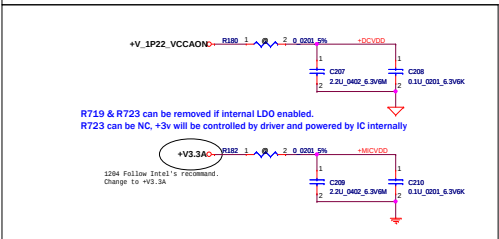
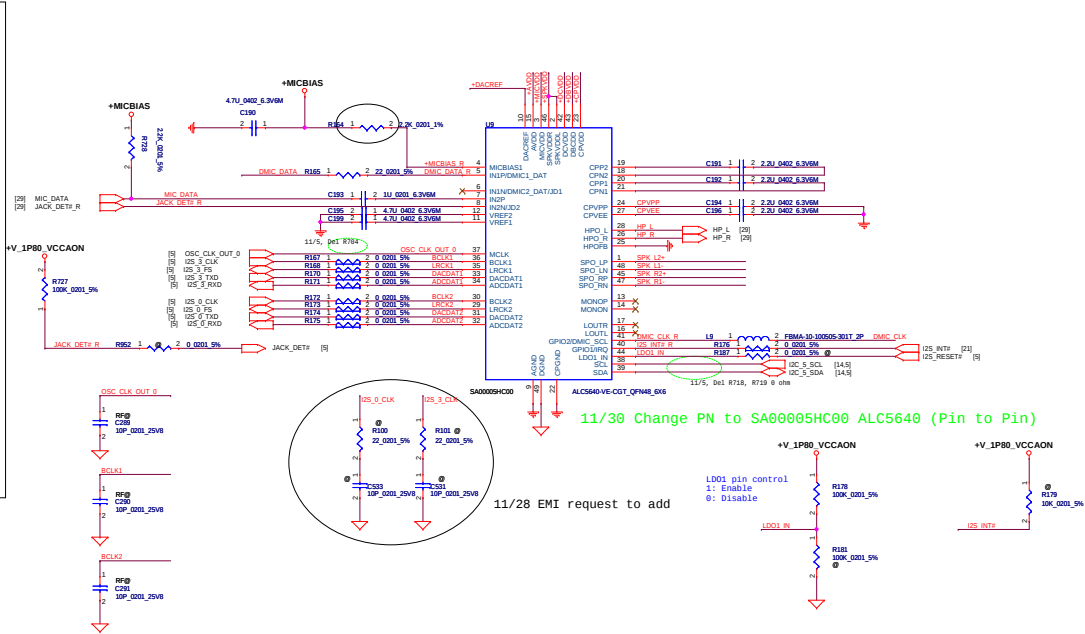
LIGHT SENSOR



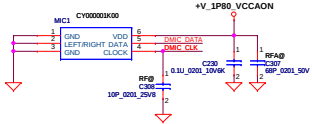
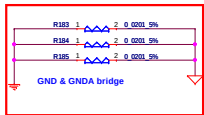
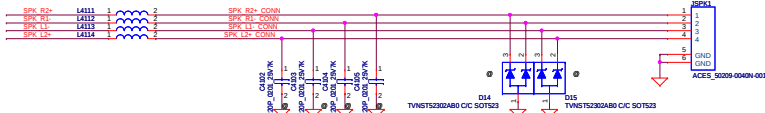
SENSOR HUB



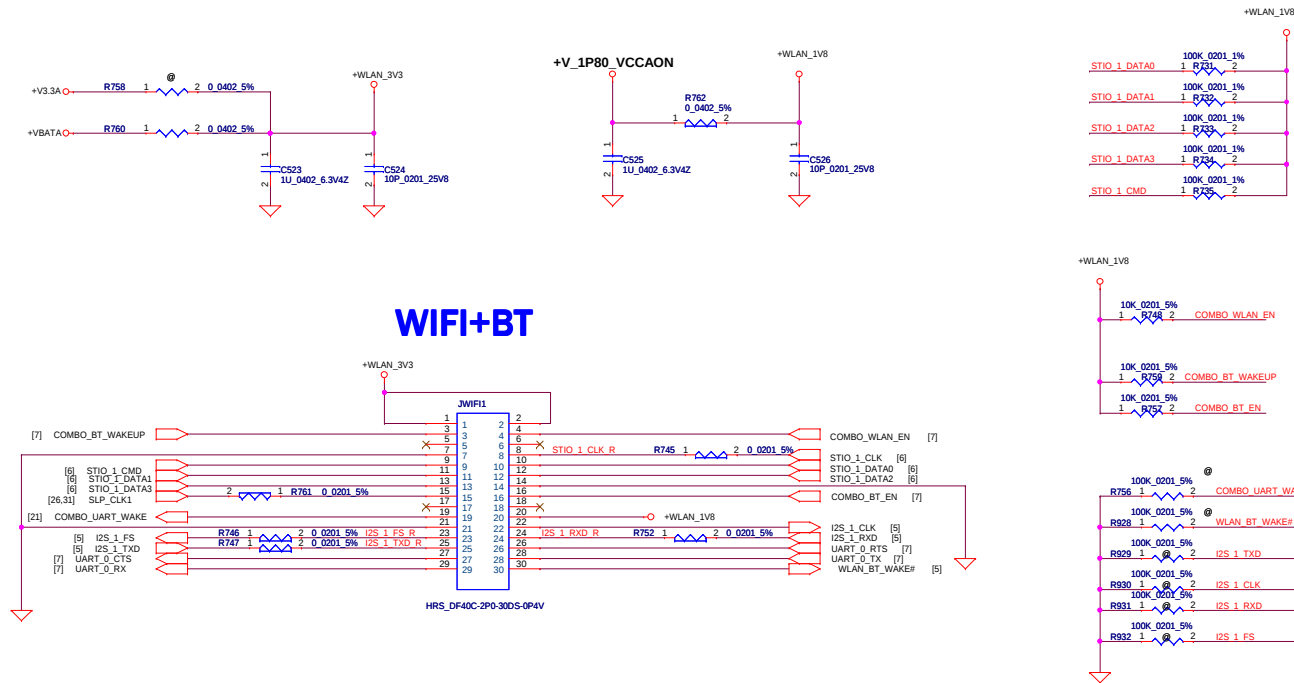
Security Classification		Compul Secret Data		Title	
Issued Date	2012/07/01	Deciphered Date	2014/04/01	Sensor Hub	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OR PAD UNIT WITHOUT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Count	Docu ment Number
				Unit	LA-A141P
				Date	Tuesday, May 14, 2013
				Page	14 of 38

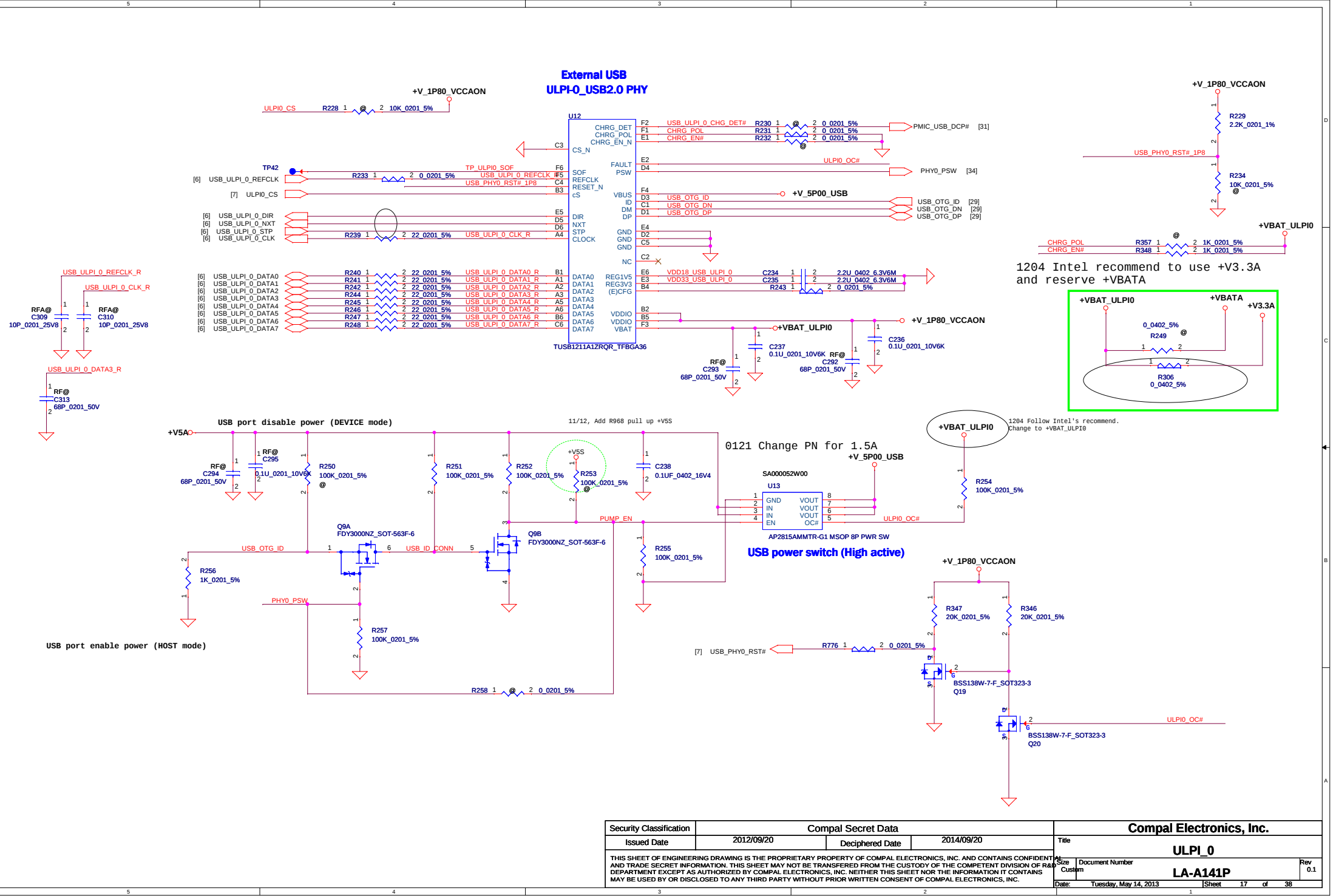


0117 EMI request to add L4111~L4114
wide 25MIL

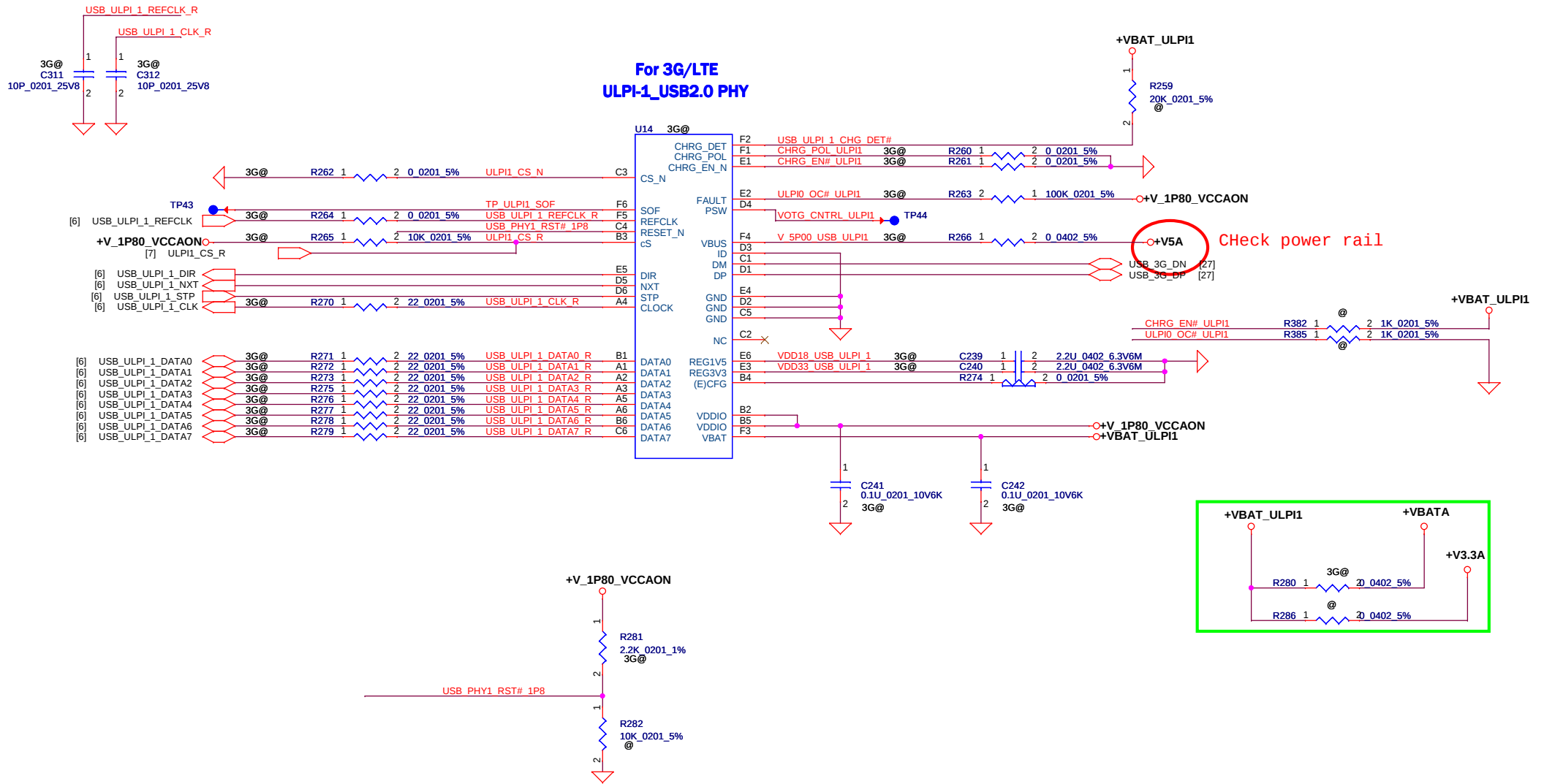


Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphred Date	2014/09/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			AUDIO CODEC	
Sheet	01	Doc Number	LA-A14-01	
Date	Tuesday, May 14, 2013		Sheet	15 of 38



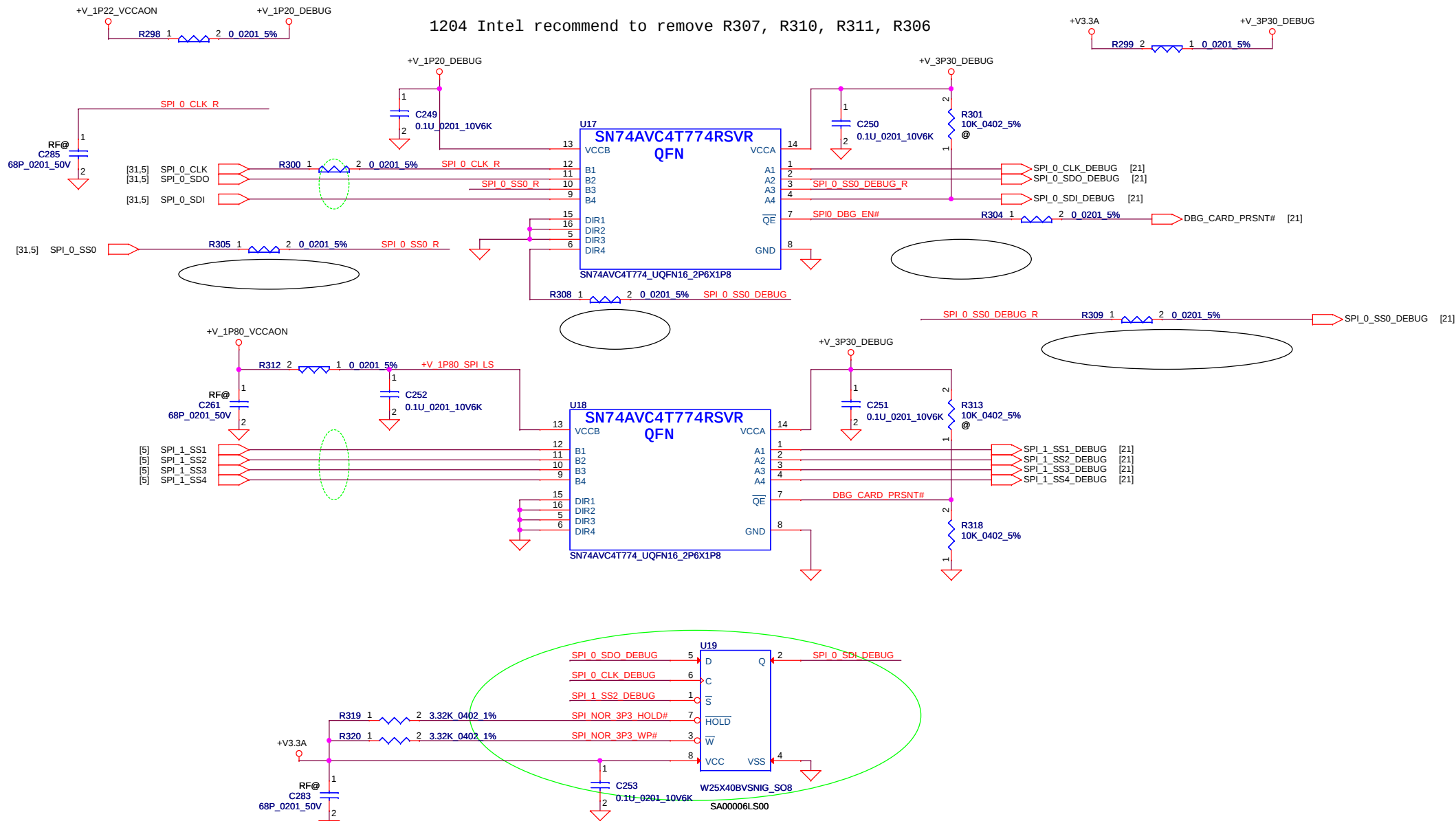


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title	ULPI_0
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAI DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				At Customer	Document Number LA-A141P
				Date: Tuesday, May 14, 2013	Rev 0.1
				Sheet 17 of 38	



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				ULPI_1	
Size		Document Number		Rev	
Custom		LA-A141P		0.1	
Date:		Tuesday, May 14, 2013		Sheet 18 of 38	

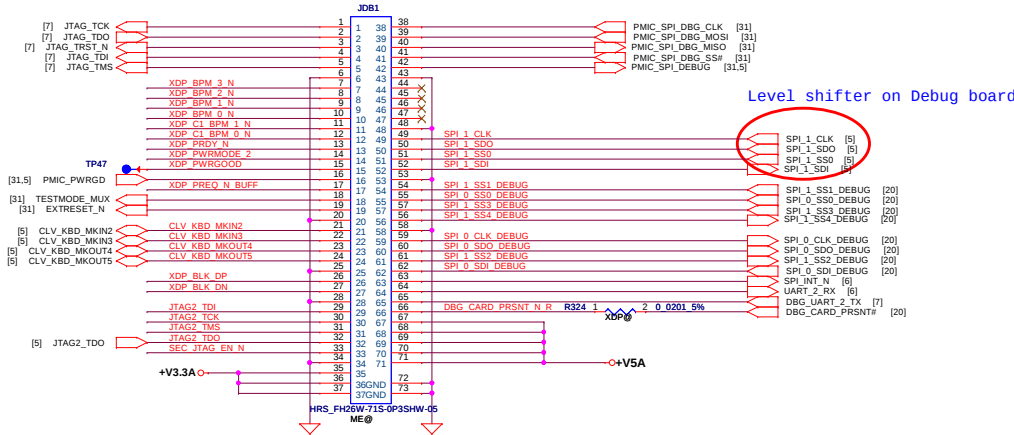
1204 Intel recommend to remove R307, R310, R311, R306



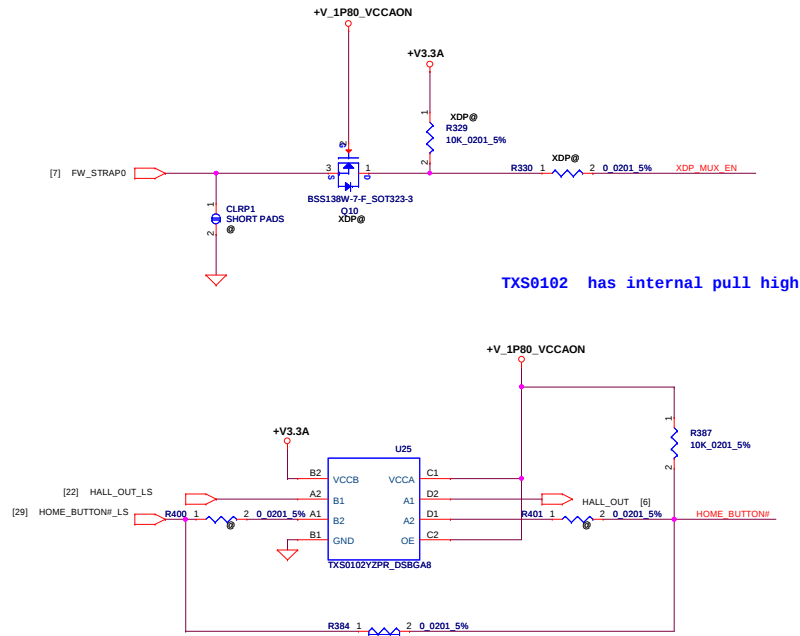
11/30 Change PN to SA00006LS00 W25XCLSNI6

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size B	Rev 0.1
				Document Number	LA-A141P
				Date: Tuesday, May 14, 2013	Sheet 20 of 38

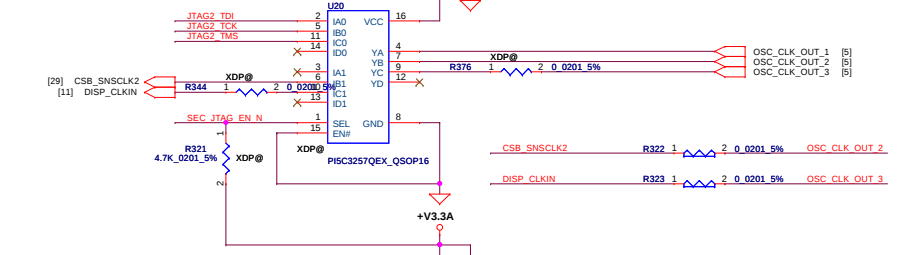
XDP



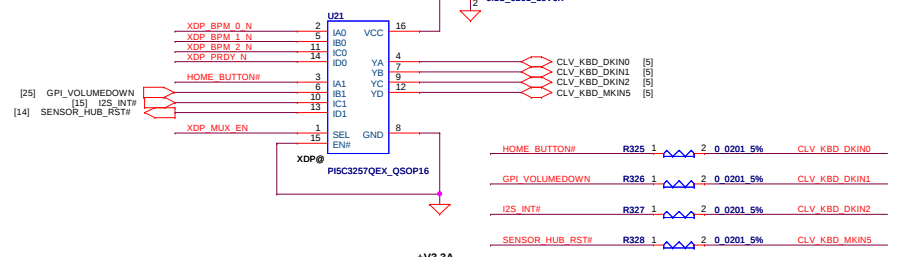
DBG_CARD_PRNT_N control circuits into debug board



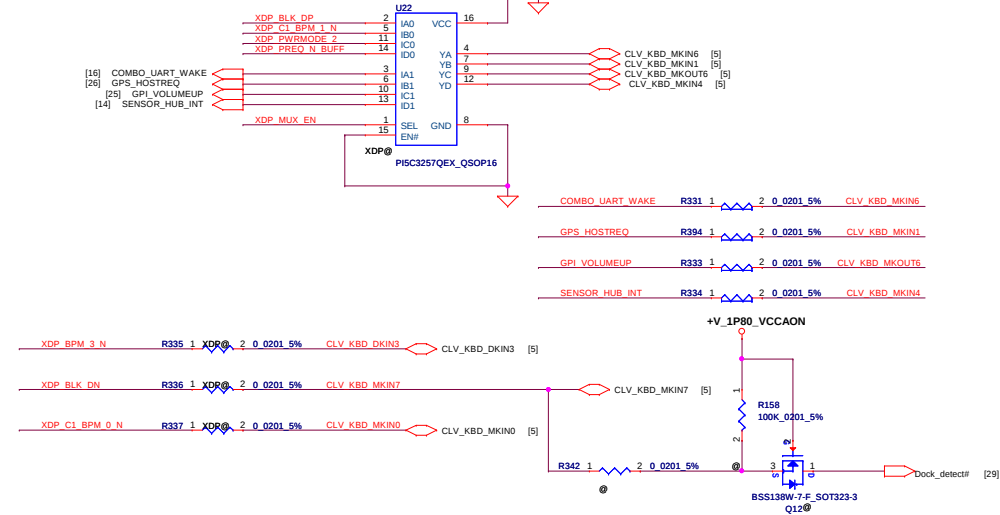
SECONDARY JTAG (TOP SIDE NO LIMITED HEIGHT)



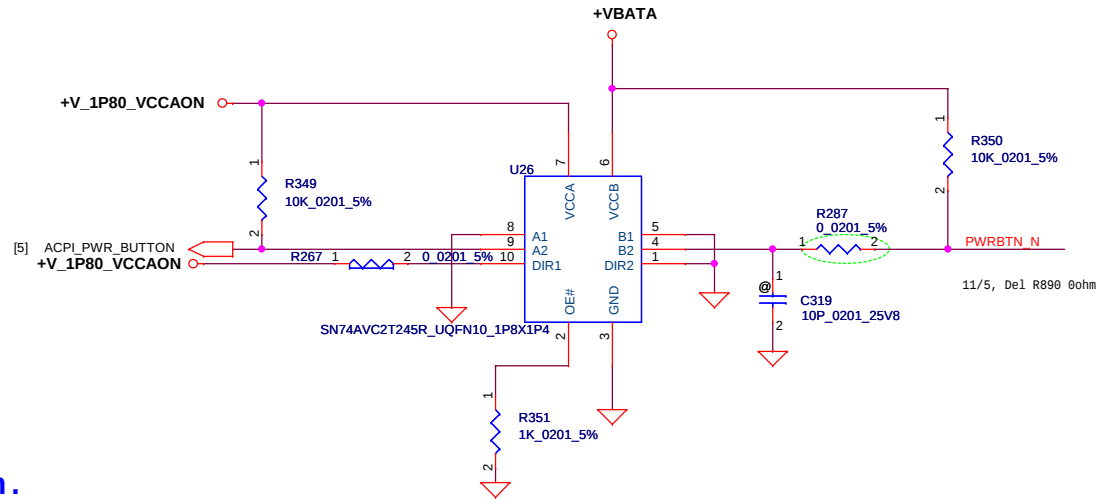
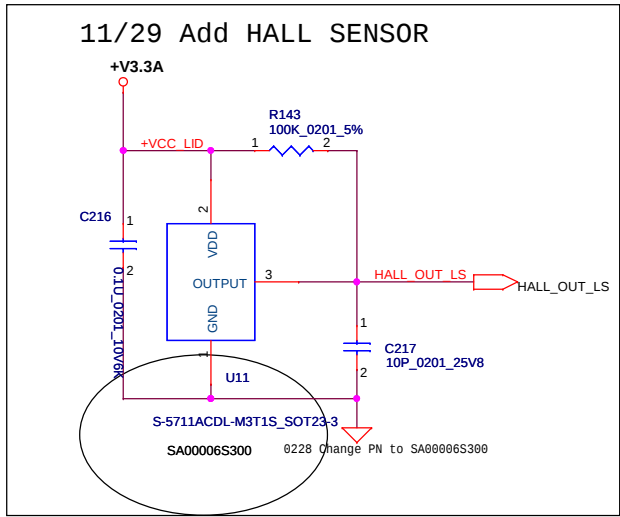
BPM SIGNALS(TOP SIDE NO LIMITED HEIGHT)



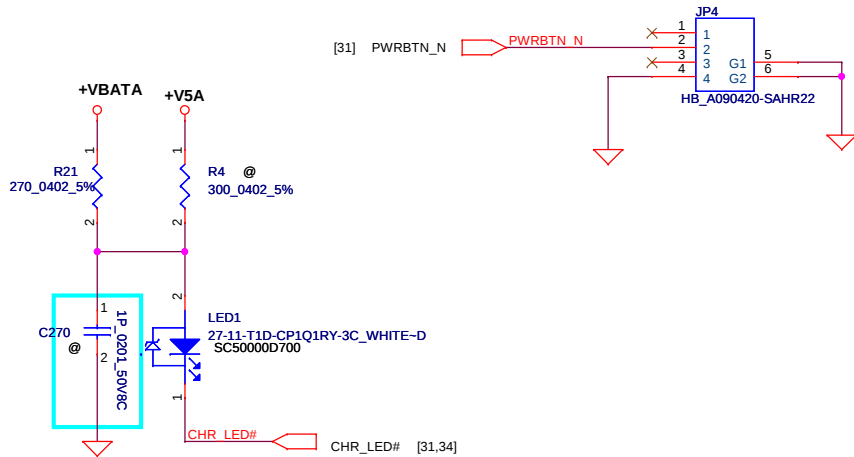
(TOP SIDE NO LIMITED HEIGHT)



Compal Secret Data				Compal Electronics, Inc.			
Security Classification	Issued Date	Deciphered Date	2014/09/20	Title	Document Number	Rev	0.1
	2012/09/20			Debug	LA-A141P		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Date:	Tuesday, May 14, 2013	Sheet	21 of 38



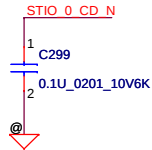
PWR Board FFC Conn.



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-A141P	0.1
Date: Tuesday, May 14, 2013				Sheet	22 of 38

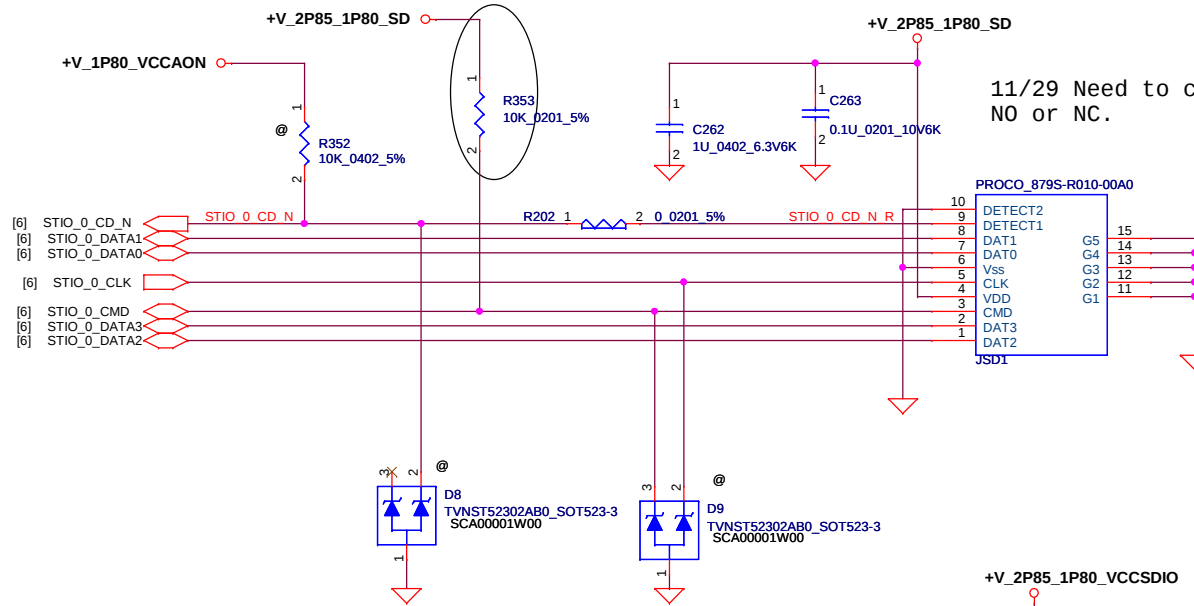
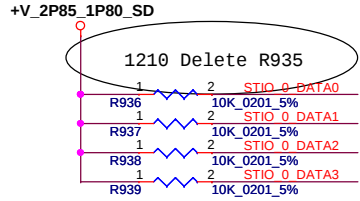
Micro SD

0321 Intel recommend to reserve

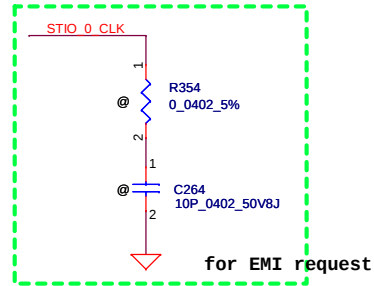


1204 Intel recommend to reserve it. 1210 Intel recommend to pop.

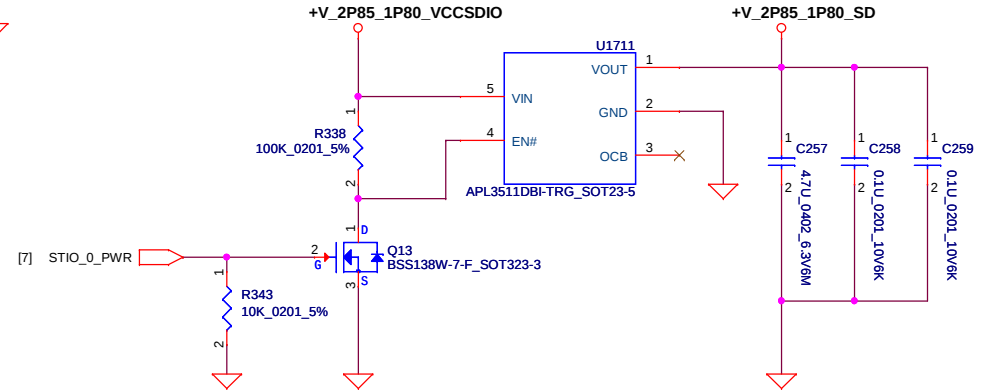
11/29 Need to check what type of this connector is. NO or NC.



for ESD Request

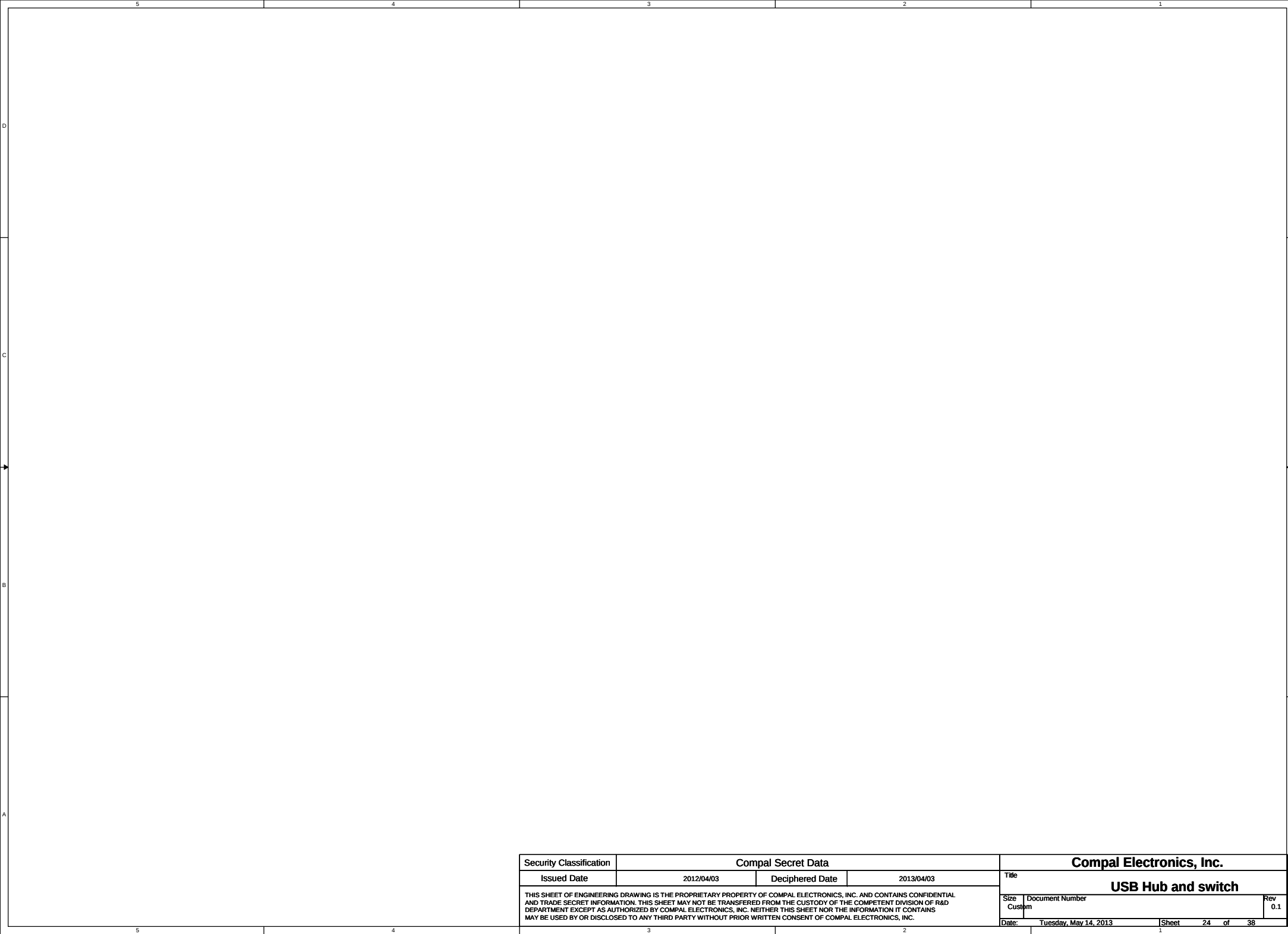


for EMI request

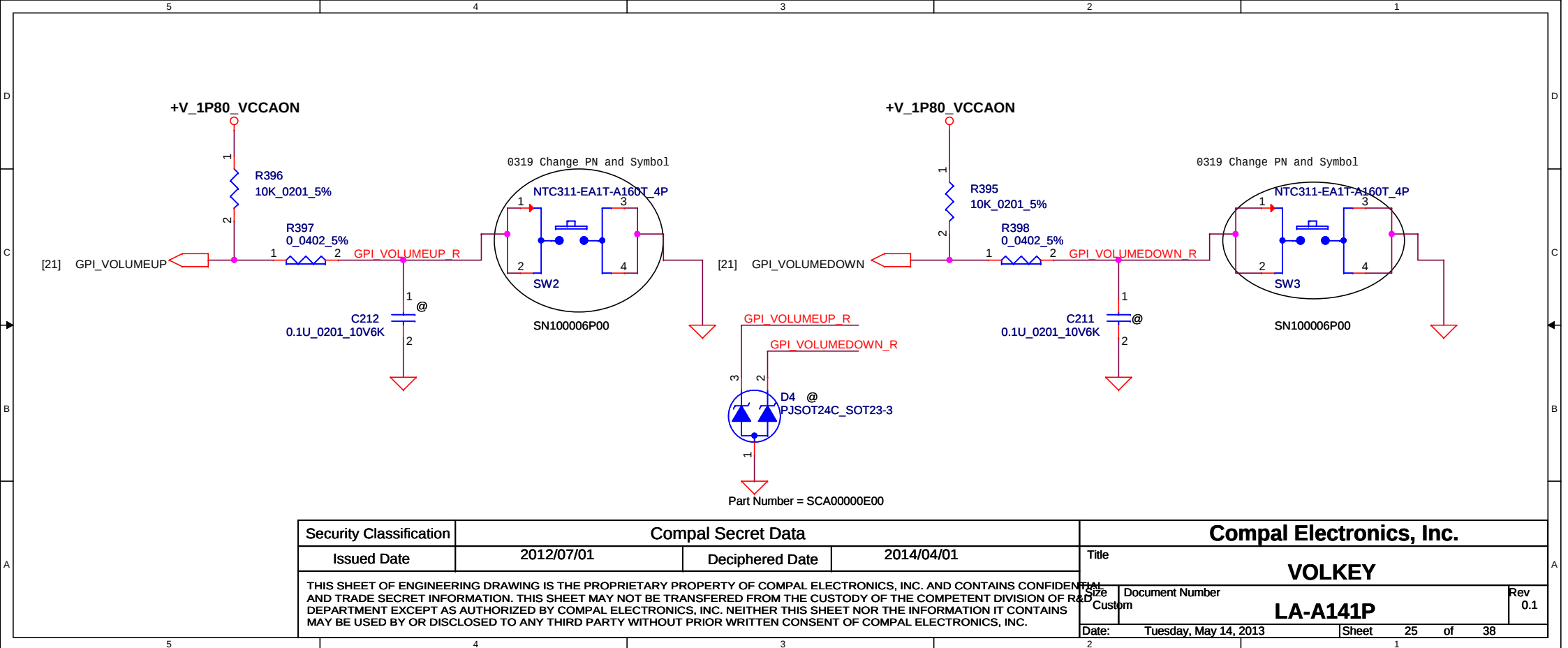


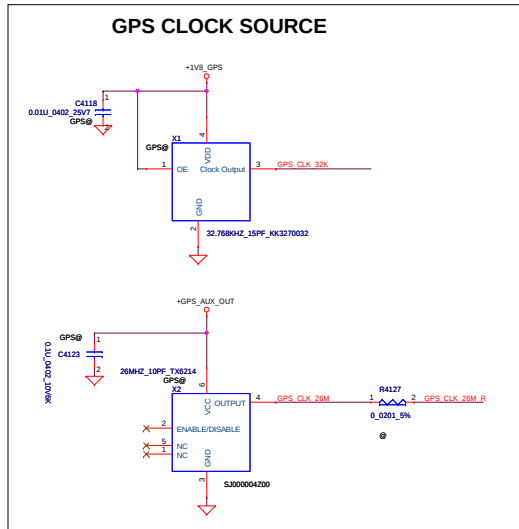
500mA IMAX

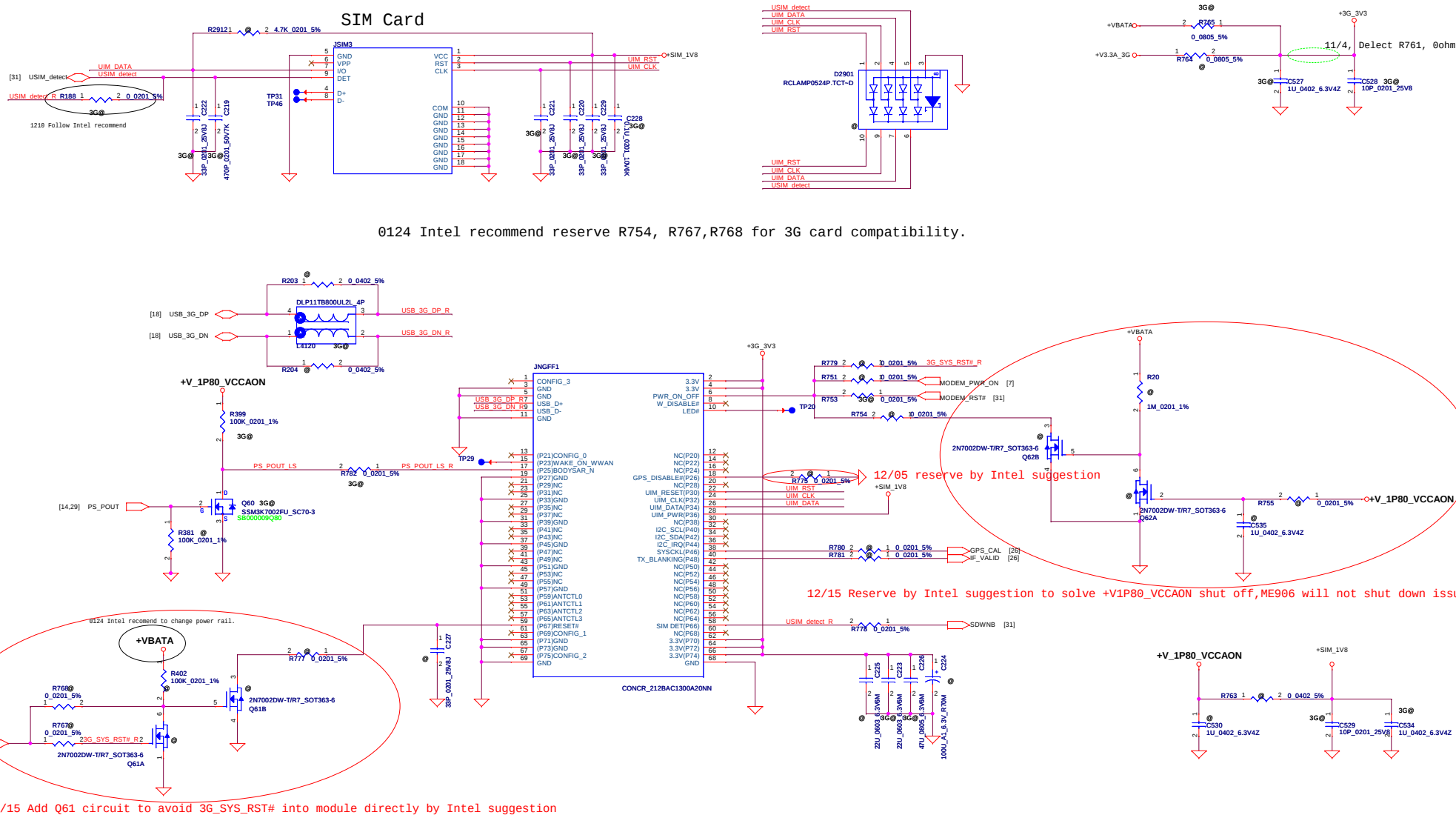
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2012/07/01	Deciphered Date	2014/04/01	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Micro SD		
				Size B	Document Number	Rev 0.1
				LA-A141P		
				Date:	Tuesday, May 14, 2013	Sheet 23 of 38



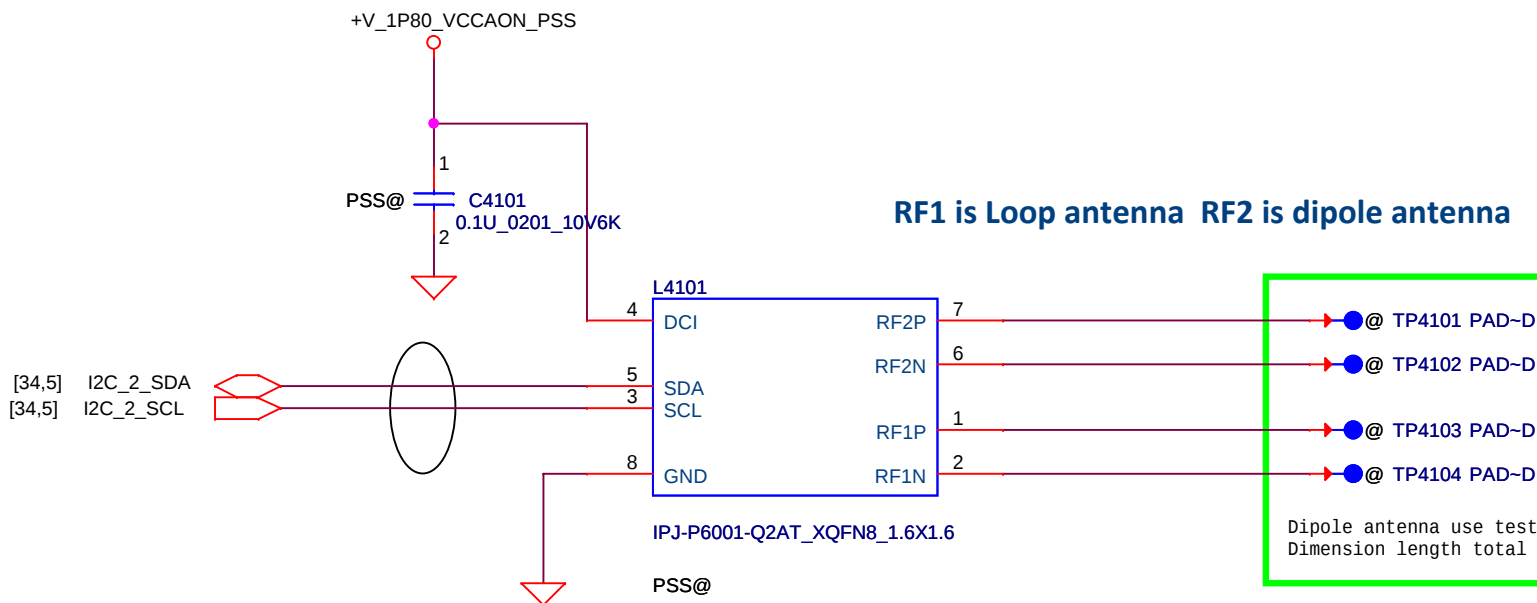
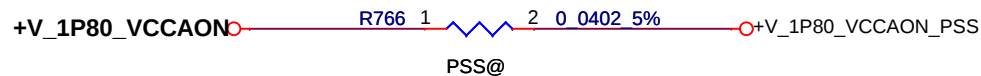
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/04/03	Deciphered Date	2013/04/03	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				USB Hub and switch	
				Size Custom	Rev 0.1
Date: Tuesday, May 14, 2013		Sheet 24 of 38			



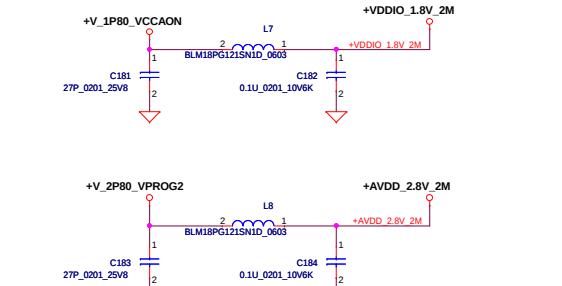
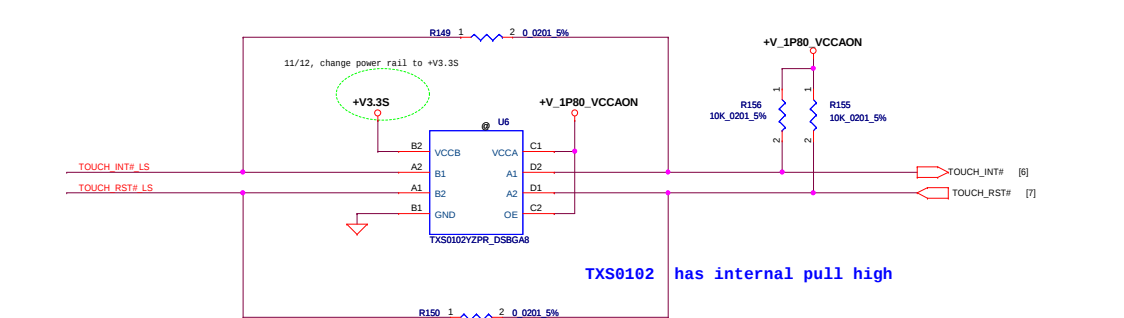
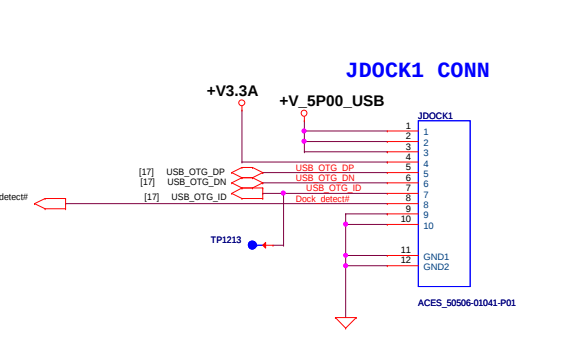
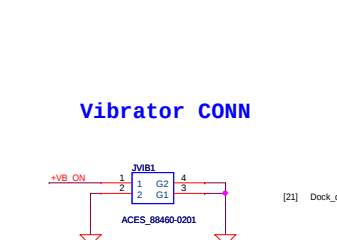
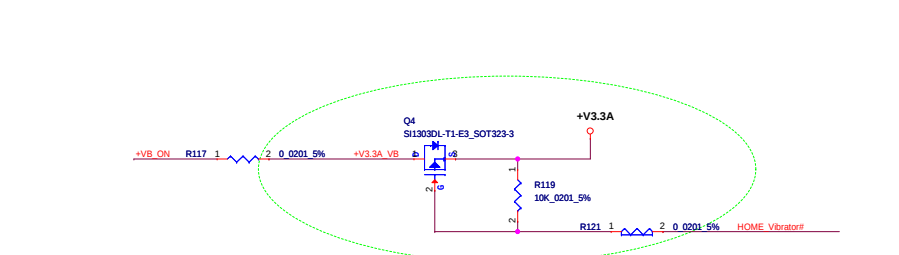
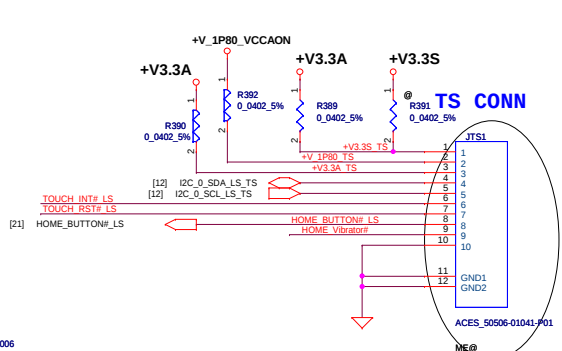
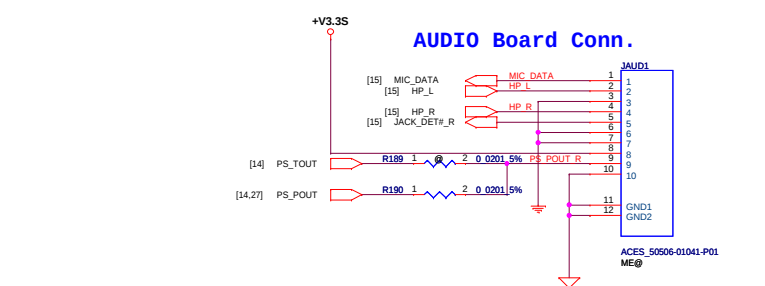
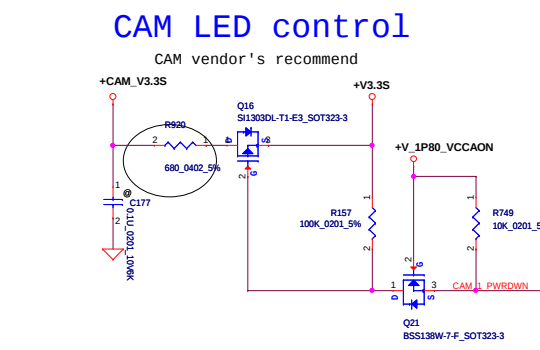
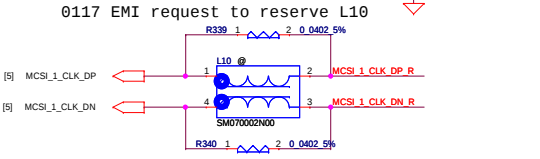
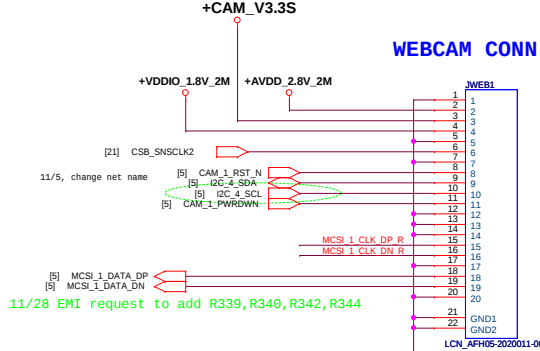




Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/03/10	Deciphered Date	2009/06/22	Title	3G/LTE
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Size	Document Number	Sheet	27	of	38
Date	Tuesday, May 14, 2013	Sheet	27	of	38

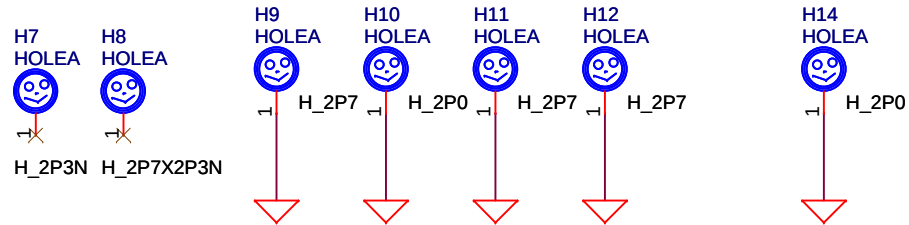


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/03/10	Deciphered Date	2009/06/22	Title PSS	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev 0.1
Date: Tuesday, May 14, 2013		Sheet 28 of 38			



Title		
<Title>		
Size	Document Number	Rev
C	LA-A141P	0.1
Date:	Tuesday, May 14, 2013	Sheet 29 of 38

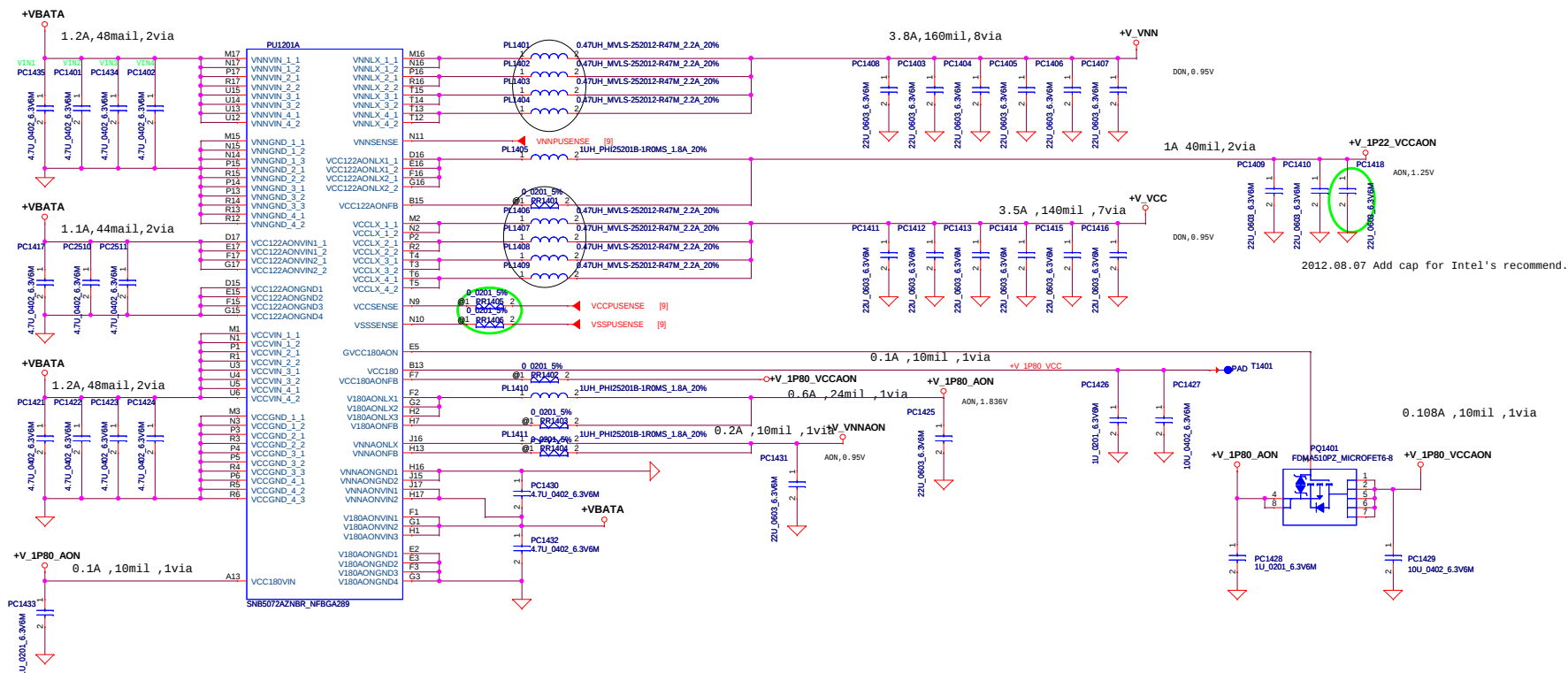
BOTTOM



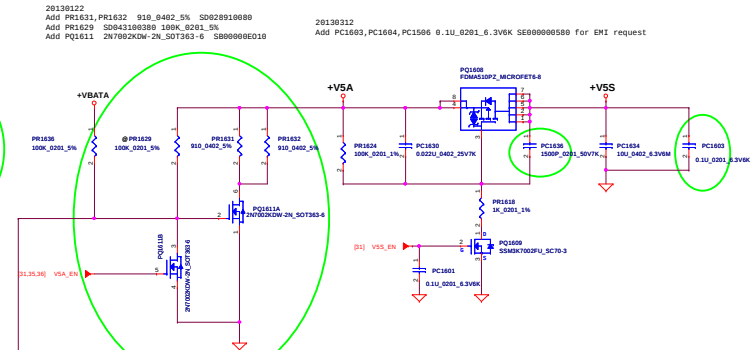
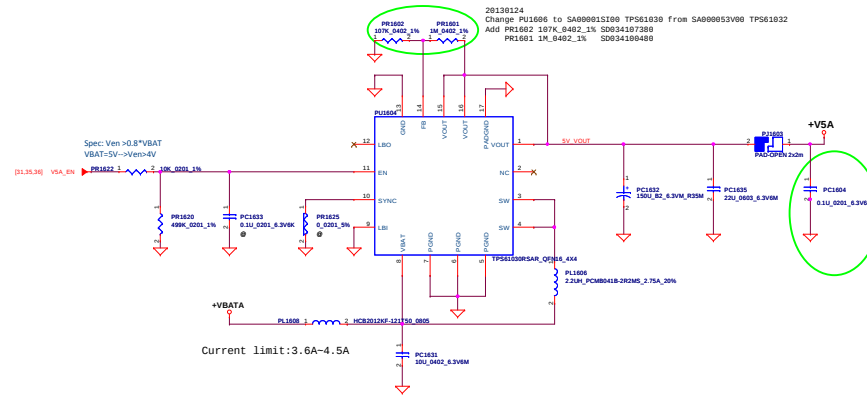
ZZZ

LA-A141P
DAA0006X000
PCB@

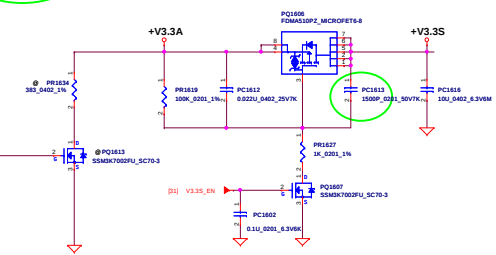
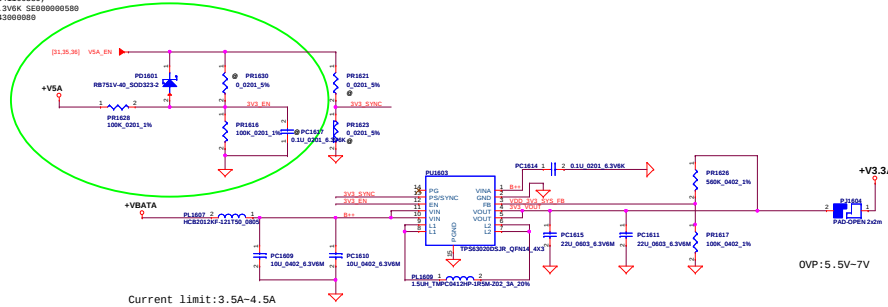
Title			
<Title>			
Size	Document Number		Rev
A	LA-A141P		0.1
Date:	Tuesday, May 14, 2013	Sheet	30 of 38







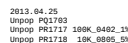
2012.12.04
Add PD1601 SC500000200,
Add PR1620 100K_0201_1% SD041100300,
Reserve PR1617 0.1u_0201_6.3V6K SE0000000500
Unpop PR1603 0.0201_5% SD043000000



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		20140401	
20120701				DCDC / Backlight	
THIS SHEET OF ENGINEERING DRAWINGS IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND IS CONTAINING CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY BE TRANSMITTED FROM THE CUSTODY OF THE COMPANIES DIVISIONS AND IS NOT TO BE REPRODUCED OR DISCLOSED BY ANY THIRD PARTY WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. ANY REUSE OR DISCLOSURE TO ANY THIRD PARTY WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. IS STRICTLY PROHIBITED.					
Rev	Document Number		Rev		
0			0.1		
Date		Issued		Page	
2012.12.04		2012.12.04		38 of 38	

20130125
Change PQ1701 to SB00000TJ00 AON7407 from AON7403L_DFN8-5 SB00000KZ00

20130228
Change PQ1701 footprint to SIS407DN-T1-GE3_PAK1212-8-5 from AON7403L_DFN8-5



```
2012.12.11
Del PR1709 and PR1710
Del net name I2C_0_SDA_BRDG and I2C_0_SCL_BRDG

20121210
1.Change PR1707 to 24K_0402_1% SD034240280 from 17.8K_0402_1% SD034178280

20121228
1.Change PR1707 to 26.1K_0402_1% SD034261280 from 24K_0402_1% SD034240280
```

Security Classification		Compal Secret Data		Title	
Issued Date	2012/07/01	Deciphered Date	2014/04/01	Compal Electronics, Inc.	
				DCDC / Backlight	
THIS SHEET OF ENGINEERING DRAWINGS IS THE PROPERTY OF PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OR FIRM TO ANY OTHER PERSON OR ENTITY WITHOUT THE WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC. ANY UNAUTHORIZED DISCLOSURE OR REPRODUCTION OF THIS SHEET OR ANY INFORMATION THEREON IS PROHIBITED. THIS SHEET MUST BE DESTROYED OR DISPOSED TO ANY 2ND PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Date	Issued, May 14, 2013
				1 Sheet	30 of 30

Version change list (P.I.R. List)

Page 1 of 1
for PWR

Item	Modify List	PG#	Date	Phase
1	Add PR1249,PR1263,PU1202,PR1264,PC1203,PR1209,PQ1213,PR1265 for TI suggestion Del PQ1203,PQ1204,PR1251,PR1247,PC1210,PR1248 for TI suggestion	31	20130117	SIV
2	Add PC1230,PC1229,PC1528,PR1239 for reserve by Intel request	31	20130124	SIV
3	Depop PQ1301 and PR1302 to reduce leakage current for Intel request	32	20130124	SIV
4	Change PQ1701 to SB00000RC00 SIS407DN-T1-GE3 from AON7403L_DFN8-5 SB00000KZ00 for panel flick in low battery	36	20130125	SIV
5	Unpop PR1704 130_0201_5% SD00000WG00 for Intel power consumption	36	20130131	SIV
6	Pop PR1239 100K_0201_5% SD043100380 for Intel request	31	20130131	SIV
7	Add PQ1611, Pr1631,PR1632 and PR1629 for +V5A discharge by HW request	35	20130201	SIV
8	Add +V3.3A discharge circuit (PR1633,PR1634,PR1635 and PQ1613) for Intel request Change PQ1613B Gate net name to V5A_EN from +V5A for Intel request	35	20130304	SIT
9	1.Pop PQ1301 and PR1302 2.Change PR1302 to 300_0201_5% ohm from 470_0201_5% 3.Add PR1303 300_0201_5% 4.Change PQ1301 Gate connect to +V_2P85_EMMC1 from +V3.3A 5.Depop PC1331 for Intel request	32	20130304	SIT
10	Change PC1313 to .47U_0402_6.3V4 from 10U_0402_6.3V6M for TI request	32	20130312	SIT
11	Change PQ1202 to SB000009610(2N7002) from SB00000RM00	31	20130312	SIT
12	Change PR1265 to 0_0402_5% from 0_0201_5% for HW request	31	20130318	SIT
13	1.Add PR1636 100K_0201_5% 2.Change PQ1613 to SSM3K7002FU 3.Del PR1633 100K_0201_5% and PR1635 910_0402_5% for Intel FFRD 4.Change PQ1613B Gate net name to V5A_EN from +V5A for Intel FFRD	35	20130321	SIT
14	Change PR1520 to 357_0402_1% from 324_0402_1%	36	20130325	SIT
15	Reserve PR1730 and add PR1731 0_0201_5% for Intel request	37	20130325	SIT
16	Unpop PU1703 Pop PR1722 S RES 1/20W 0 +-5% 0201 Pop PR1704 S RES 1/20W 100K +-1% 0201 Change PR1705 to S RES 1/16W 39K +-1% 0402 from S RES 1/16W 82 +-1% 0402 for power consumption decrease	36	20130410	SIT

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/04/18	Deciphered Date	2015/07/08	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				PIR (PWR)	
				Size	Document Number
Date: Tuesday, May 14, 2013				Sheet	37 of 38
				0.1	

	Version change list (P.I.R. List)	Page 1 of 1 for EE		
Item	Modify List	PG#	Date	Phase
1	Add LED1, R4, C270 for ID changed.	P.22	2013/1/17	SIV
2	Change JP4 from 6 pins into 4 pins for exact design.	P.22	2013/1/17	SIV
3	Change SW2, SW3, JNGFF1 into DIP type for SMT request.	P.22, 27	2013/1/18	SIV
4	Change DMIC into CY000001K00 for Audio team request.	P.15	2013/1/21	SIV
5	Change R1553, R1554, R1555, R1556 into L4111, L4112, L4113, L4114 for EMI request.	P.15	2013/1/21	SIV
6	Reserve L10 for EMI request.	P.29	2013/1/22	SIV
7	Reserve C307, C308, C309, C310, C311, C312, C313 for RF rquest.	P.15, 17, 18	2013/1/22	SIV
8	Delete D2, D3 and Reserve C300, C301, C302, C303, C304, C305, C306, C314 for EMI rquest.	P.13	2013/1/23	SIV
9	Reserve R754 for Intel recommend.	P.27	2013/1/24	SIV
10	Reserve R767 for Intel recommend.	P.27	2013/1/24	SIV
11	Reserve R768 for Intel recommend.	P.27	2013/1/24	SIV
12	Change R402 power rail from +V_1P80_VCCAON to +VBATA for Intel recommend.	P.27	2013/1/24	SIV
13	Connect R779 pin1 to Q61A pin2 for Intel recommend.	P.27	2013/1/24	SIV
14	Change R124 and R125 resistance vaule to 4.7K for HDMI I2C.	P.13	2013/1/24	SIV
15	Connect R53 pin2 to ULPI1_CS_R for power consumption control.	P.18	2013/1/24	SIV
16	Connect U26 pin8 to GND for +VBATA power consumption leakage issue.	P.25	2013/1/25	SIV
17	Reserve R391 and change R389 pin1 to +V3.3A for Touch control board power sequence control.	P.29	2013/1/29	SIV
18	Add R21 to Charger LED and connect to +VBATA.	P.25	2013/3/11	SIT
19	Shift pin define of JAUD1 to add more AGND pin.	P.29	2013/3/14	SIT
20	Add R191~R200 and L4115~L4119 for RF request.	P.12	2013/3/19	SIT
21	Change Q60 into 2N7002 single NMOS.	P.27	2013/3/19	SIT
22	Add R189, R190 for PS_TOUT and PS_POUT.	P.29	2013/3/19	SIT
23	Reserve R202 and C299 for Intel recommend.	P.23	2013/3/20	SIT
24	Add L4120, R203, R204 for RF request.	P.27	2013/5/2	MP
25	Add R287, C319 for Power button de-bounce circuit.	P.22	2013/5/2	MP
26	Change Q11 into U1711 for SD card power switch.	P.23	2013/5/3	MP
27				

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/20	Deciphered Date	2014/09/20	Title	PIR (HW)
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number Rev 0.2
				Date: Tuesday, May 14, 2013	Sheet 38 of 38