

Compal confidential

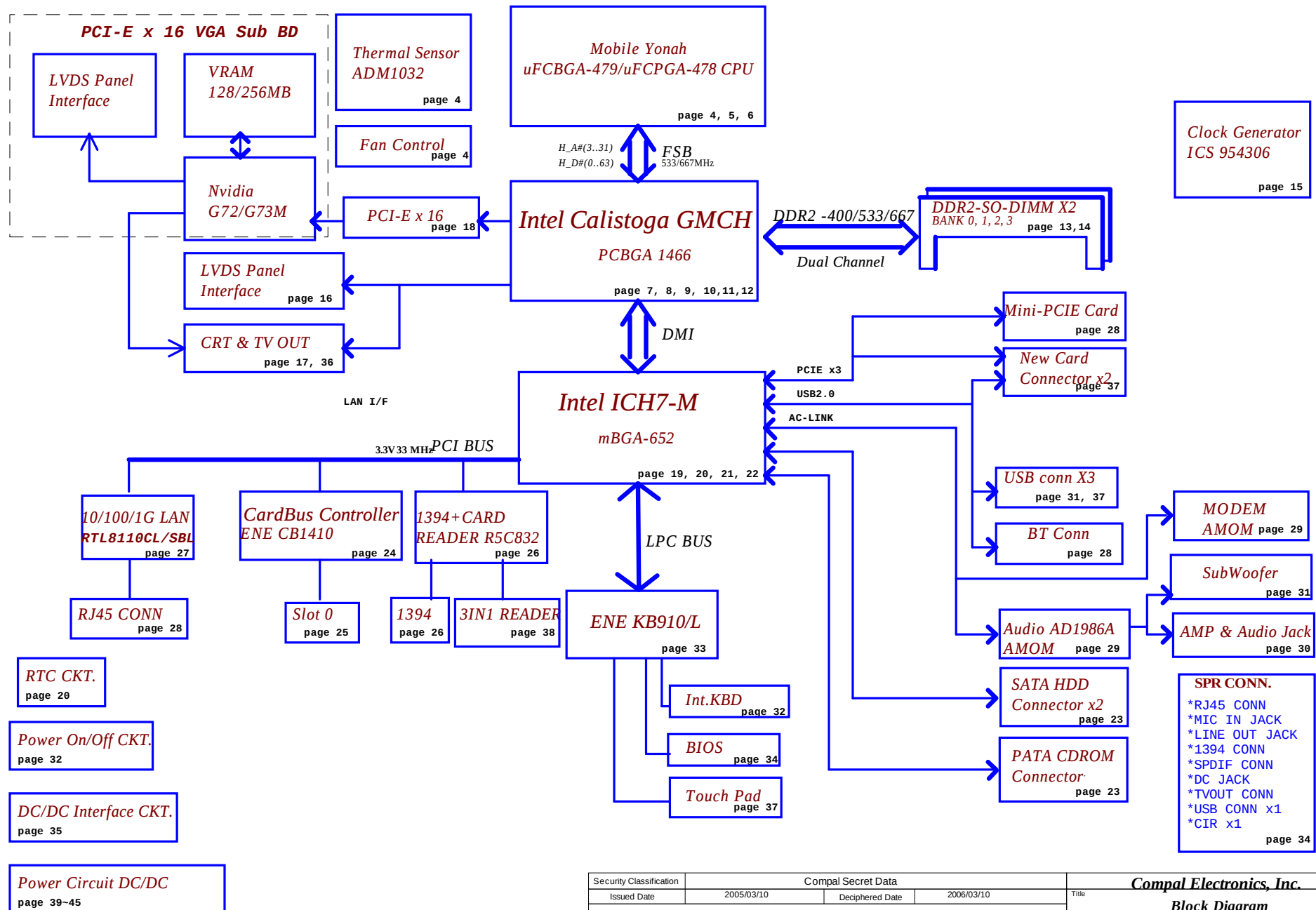
HGT30/31 Schematics Document

Mobile Yonah uFCPGA with Intel Calistoga_GM/PM+ICH7-M core logic

2006-02-15

REV:0.3

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
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				Size	Document Number
				Custom	HGT30/31 LA-3061
Date: 星期五, 二月 23, 2006				Sheet	1 of 47
				Rev	0.1



Security Classification		Compal Secret Data		Compal Electronics, Inc. Block Diagram		
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				Custn	HGT30/31 LA-3061	0.
				Date:	星期四, 二月 23, 2006	Sheet 2 of 47

[illegible]

+3VALW	KB910L SB RTL8110SBL/CL	mA 160mA
+CPU_CORE	CPU	36A
+VCCP	CPU NB	2.5A 9.8A (14.7A)
+5VS	EXPRESS CARD HDD ODD MDC APA2066 TPA0211 AD1986 USB PORT * 6	1A 1.5A 1.8A 300mA 1A mA 70mA 3A
+3VS	NB EXPRESS CARD CLK_GEN LCDVCC VGA CARD (G7XM) SB R5C832 BIOS ROM KB910L CB1410	480mA 1A 200mA 1A 655mA 680mA mA 15mA 200mA mA
+2.5VS	VGA CARD (G7XM) NB	130mA (143mA)
+1.8V	DDR2_DIMM NB (667Mhz)	8A 3.1A
+1.8VS	GDDR2 VGA CARD (G7XM)	6A 4.06A
+0.9VREF	DDR2_DIMM	10mA
+0.9VS	GDDR2 DDR2_DIMM	1A 2A
+1.5V	SB	40mA
+1.5VS	NB SB MiniCard EXPRESS CARD VGA CARD (G7XM)	8.9A(13.8A) 3.8A 1A 0.65A 2A

EXTERNAL	IDSEL#	REQ/GNT#	PIRQ	
CARD BUS CB1410	AD20	2	PCI_PIRQA#	
CARD READER & 1394 R5C832	AD22	0	PCI_PIRQG#	PCI_PIRQH#
LAN CONTROLLER RTL8110SBL/CL	AD17	3	PCI_PIRQF#	

LANE	DEVICE
1	Express Card
2	Mini Card

PORT	DEVICE
0	LEFT SIDE
1	BLUE TOOTH
2	RIGHT SIDE
3	NC
4	RIGHT SIDE
5	NC
6	NC

DEVICE	ADDRESS R/W
AT24C16AN	A3/A2 H
SMART BATTERY	17/16 H
KB910/L (SM1-Pulled-Up 5V) KB910/L (SM2-Pulled-Up 3.3V) G7xM (I2CC-Pulled-Up 3.3V)	
ADM1032AR	99/98 H
G781-1 (RESERVED)	9B/9A
ICH7M SM Bus IC9SPR325AKLFT	
DDR II DIMM0	D3/D2 H (3.3V)
DDR II DIMM1	A1/A0 H (3.3V)
DDR II DIMM1	A3/A2 H (3.3V)
Express Card	NC (2.5V)
Mini-Express	NC (2.5V)

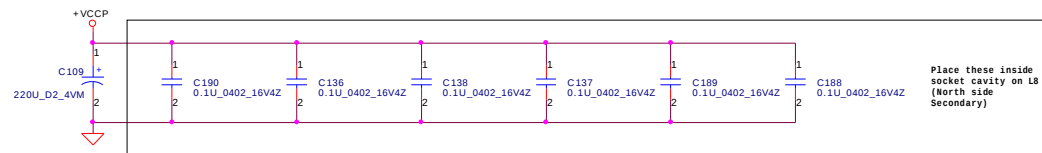
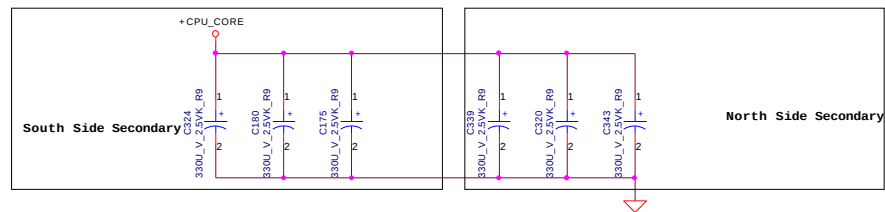
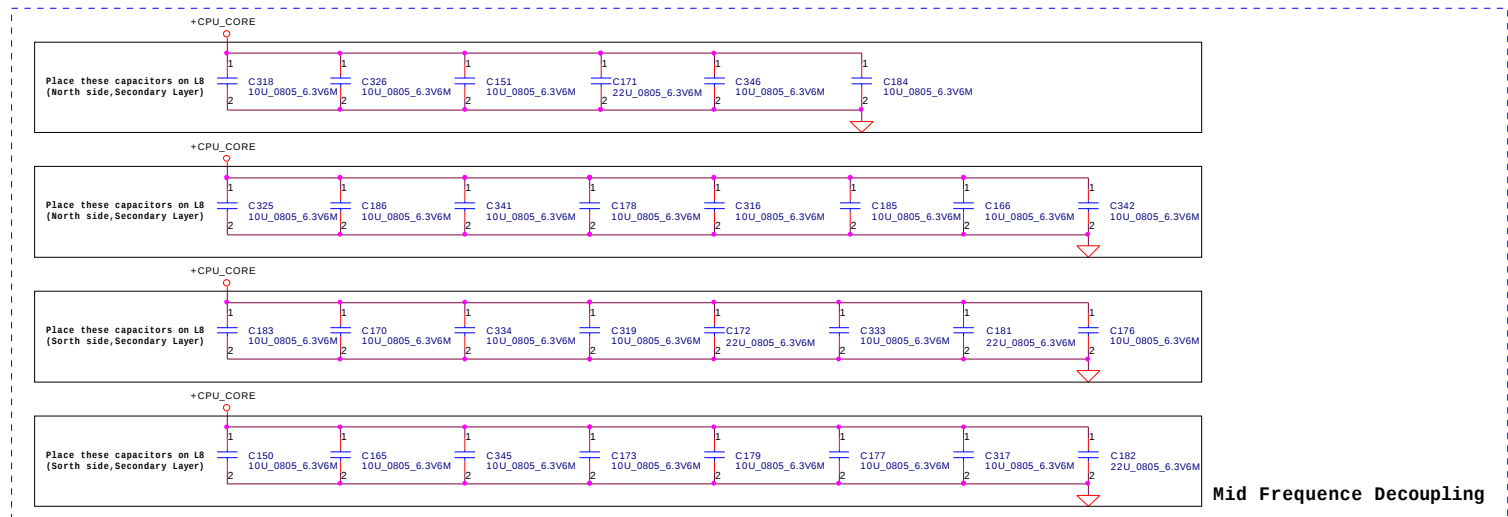
MARK	FUNCTION
@	NC FOR ALL
EXP@	PCI-E NEW CARD
BT@	BLUE TOOTH
UMA@	Internal 945GM
VGA@	External 67xm
SUBWOOFER@	SUBWOOFER
HGT30@	HGT30
CB@	PCMCTA/CARD BUS
GIGA@	8110SBL (SCL)Giga LAN
10/100@	8110CL 10/100Mb LAN

power plane State S4 : STD S5 : S0FT OFF	+B LD03 LD05	+5VALW +3VALW	+1.8V +5V	+5VS +3VS +2.5VS +1.8VS +1.5VS +VGA CORE +1.2VS +0.9VS +CPU CORE +VCCP
S0	0	0	0	0
S1	0	0	0	0
S3 : STR	0	0	0	X
S5 S4/AC	0	0	X	X
S5 S4/ Battery only	0	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

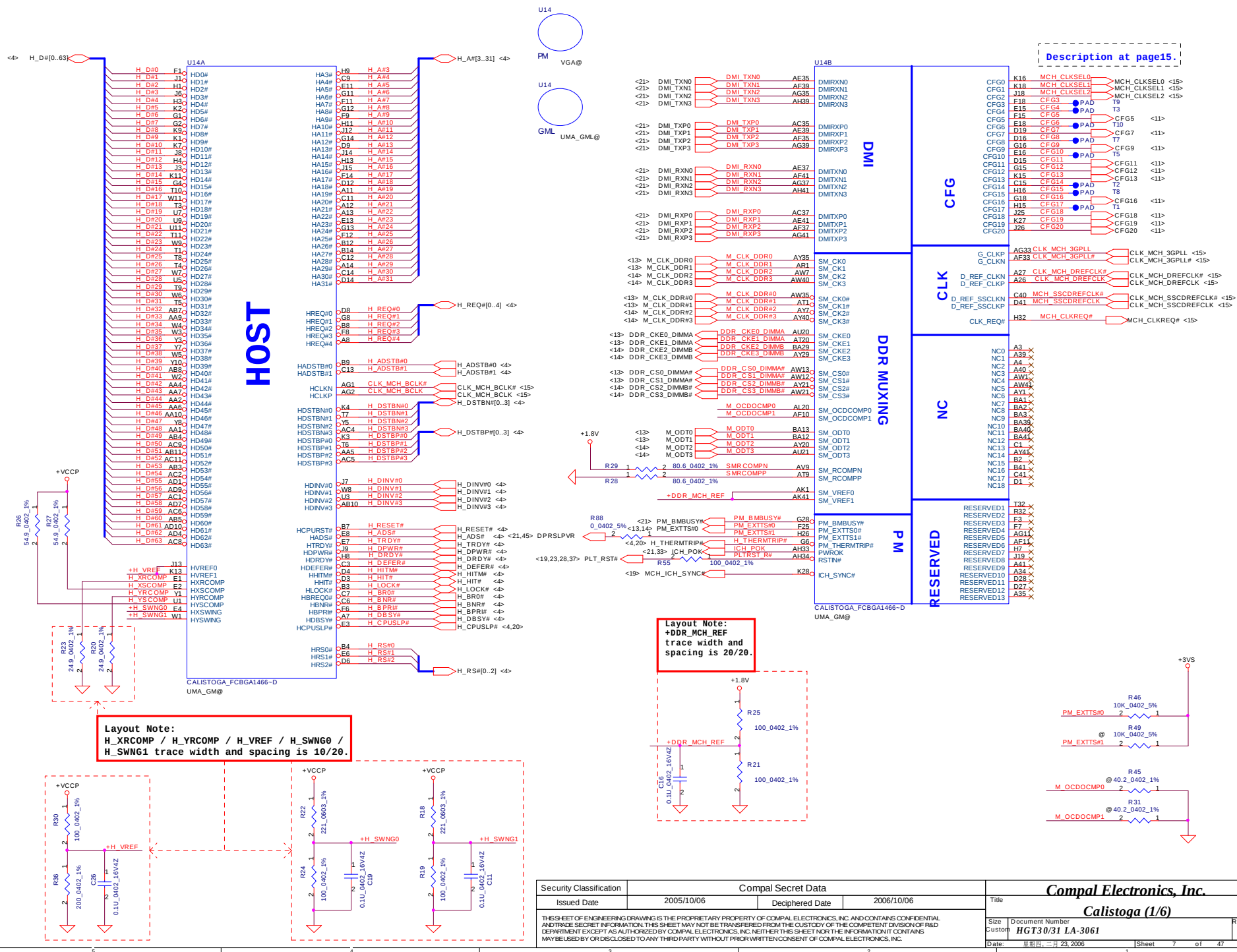
MB ID	P NAME
0	HGT-30
1	HGT-31

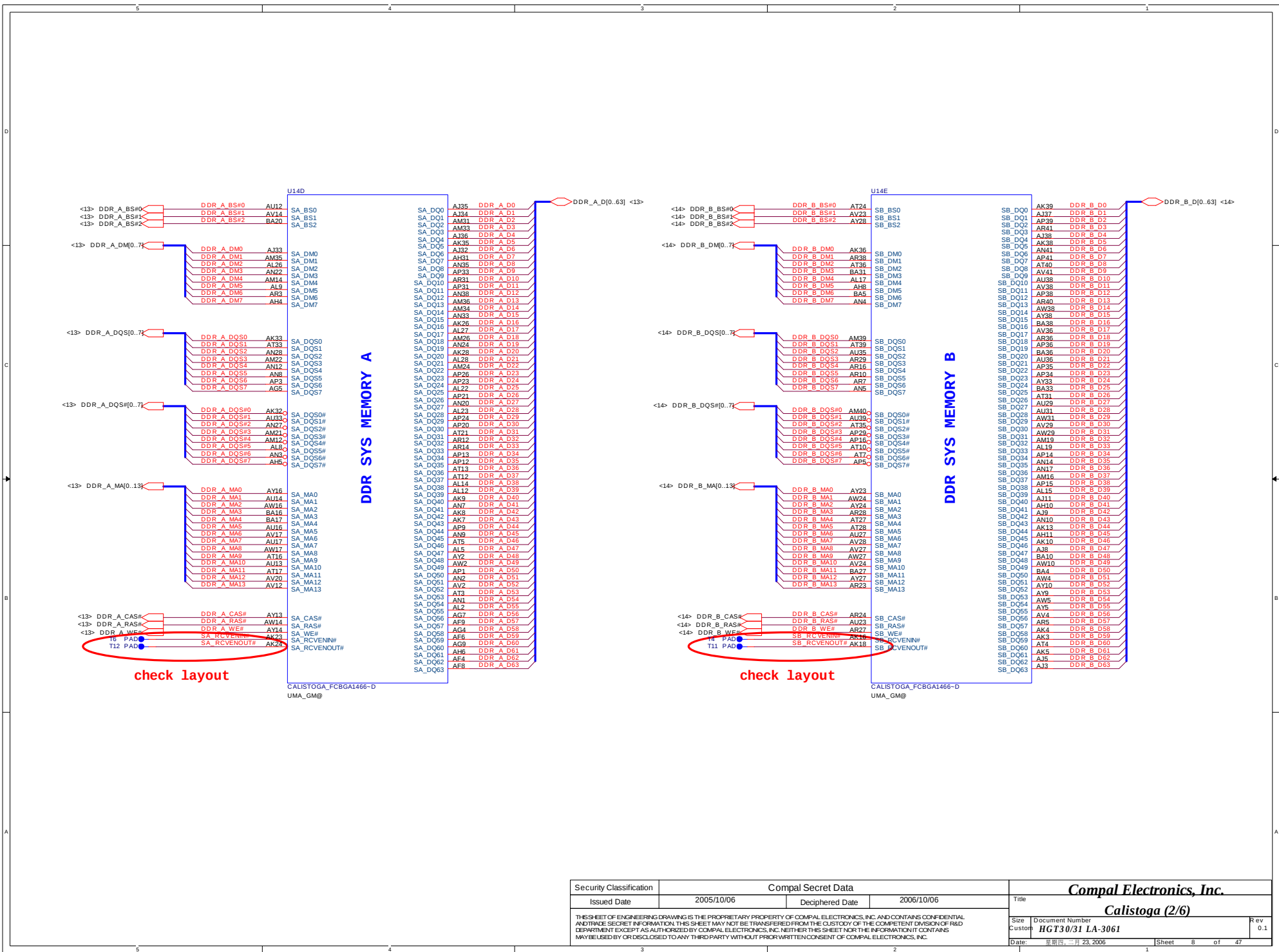
ID	MB REV#	R115(Rb)	Vab
0	R01 (EVT)	0	0'
1	R02 (DVT)	8.2K	0.25'
2	R03	18K	0.50'
3	R04	33K	0.82'
4		56K	1.19'
5		100K	1.65'
6		200K	2.20'
7		NC	3.30'

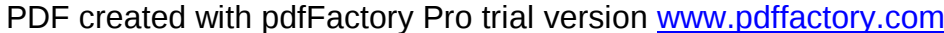
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				Date: 星期四, 二月 23, 2006	Sheet 3 of 47

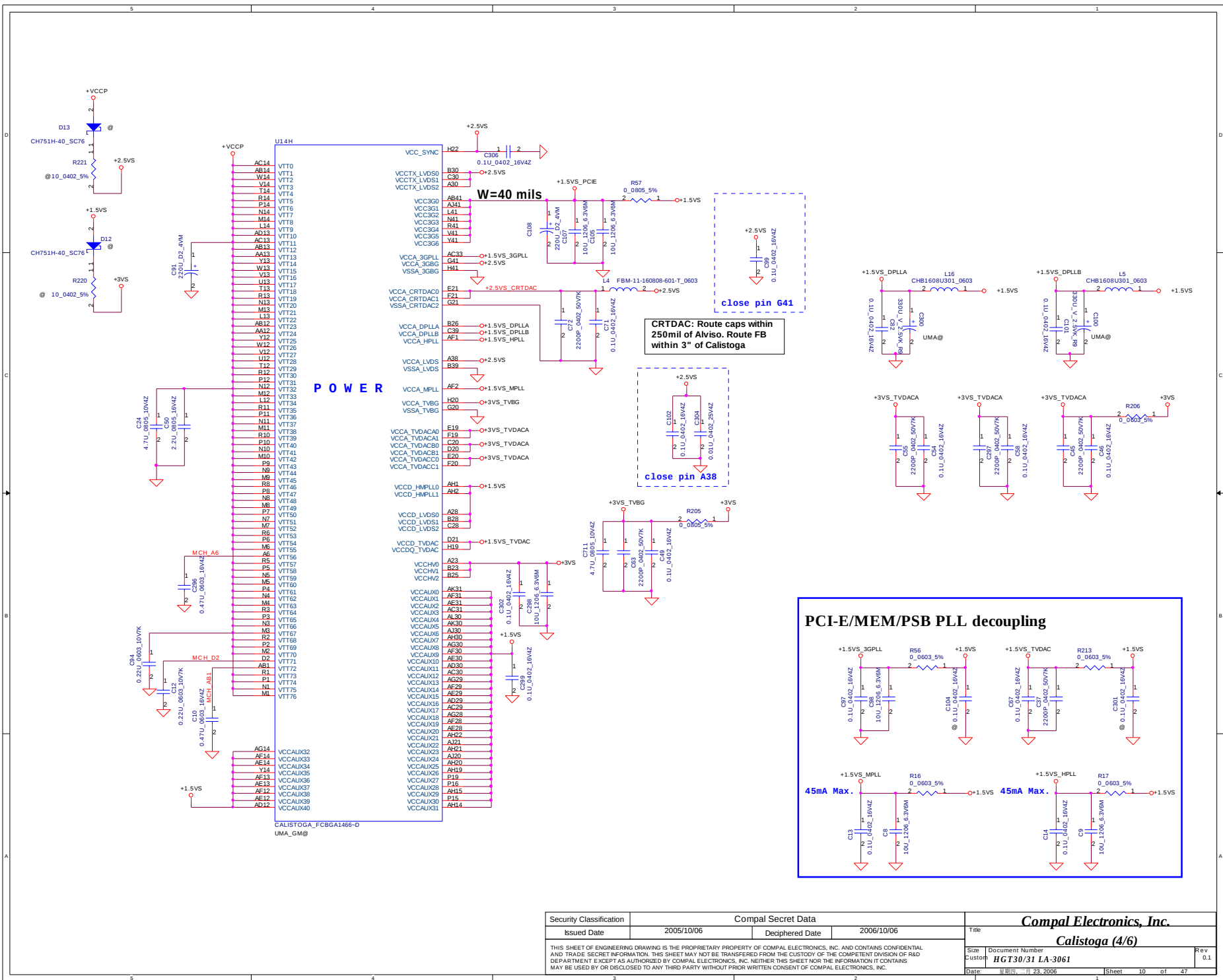


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				Size Document Number Custom HGT30/31 LA-3061
Date:	星期四, 二月 23, 2006	Sheet	6 of 47	Rev 0.1









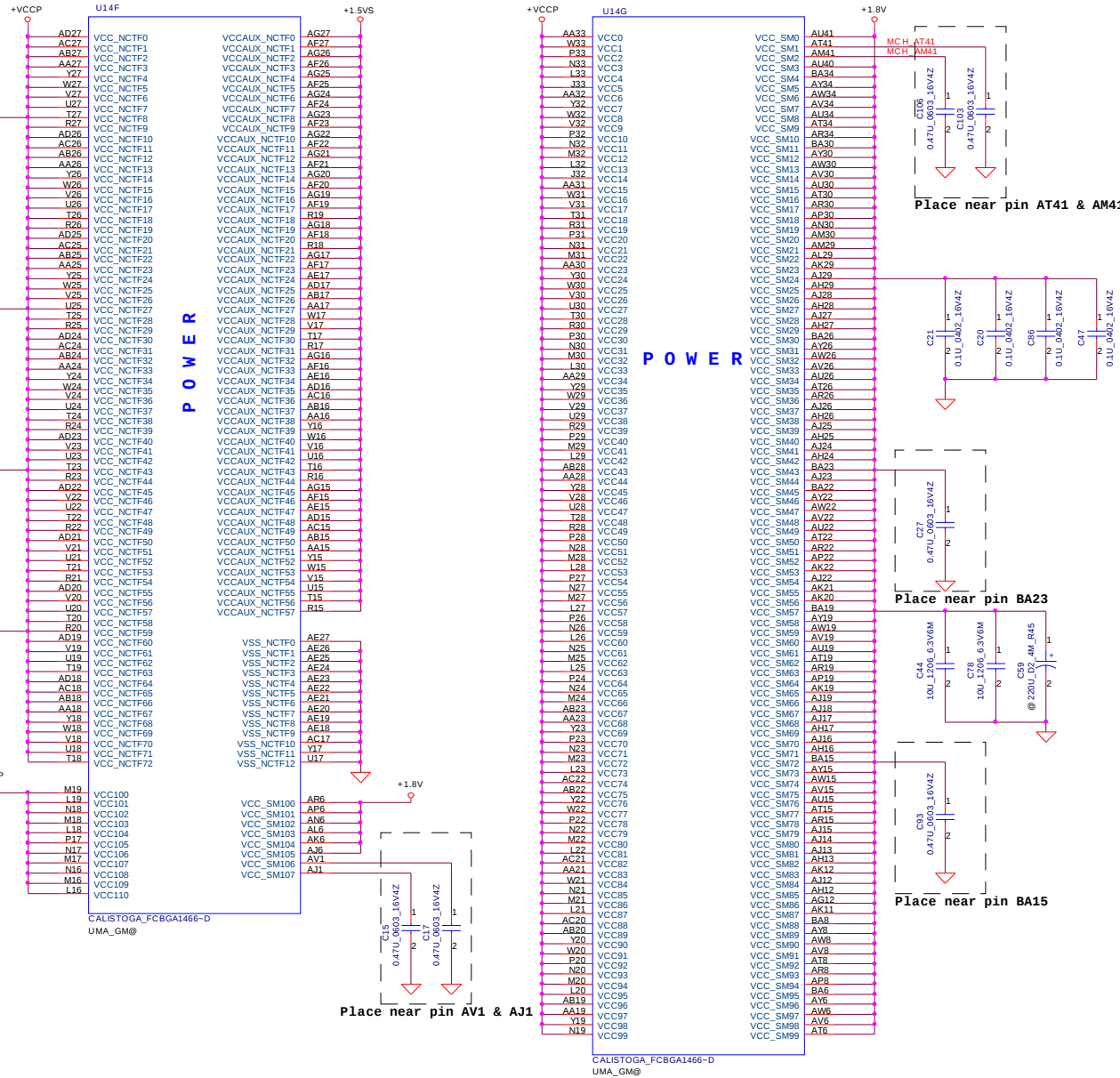
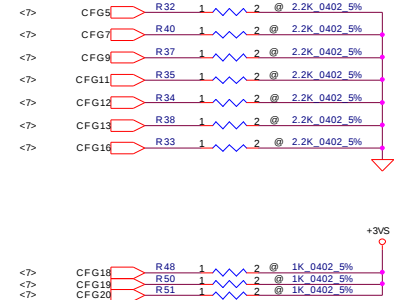
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Date: 2006/01/23				Rev: 0.1

Strap Pin Table

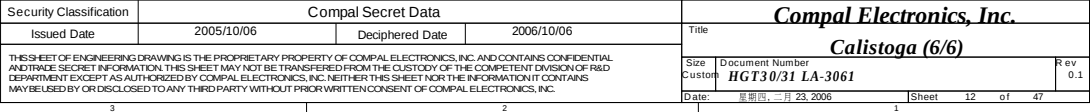
CFG[3:17] have internal pull up

CFG[19:18] have internal pull down

CFG[2:0]	011 = 667MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 * (Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU * (Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation (Default) *
CFG66	0 = Reserved
PSB 4X CLK Enable	1 = Calistoga *
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation * (Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled * (Default)
CFG10 CFG18	10 = 1.05V* (Default) 01 = 1.5V
CFG19	0 = Normal Operation * (Default) 1 = DMI Lane Reversal
SDVO_CTRLDATA	0 = No SDVO Device Present * (Default) 1 = SDVO Device Present
CFG20 (PCIe/SDVO select)	0 = Only PCIe or SDVO is operational. * (Default) 1 = PCIe/SDVO are operating simu.

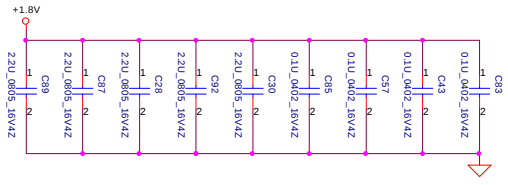


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Date	2006. 02. 23	Sheet	11 of 47

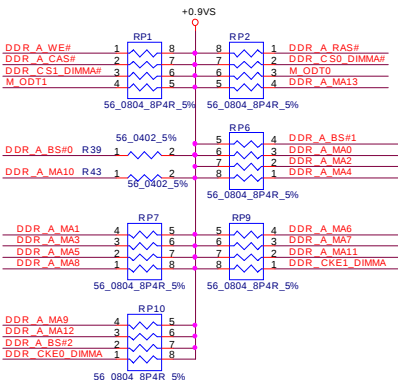
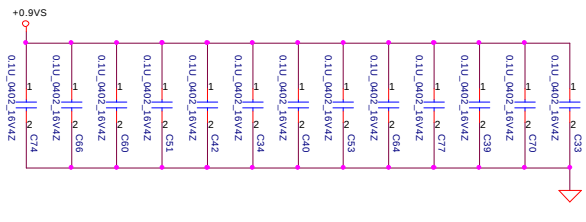


 DDR_A_DQS# [0..7]
 DDR_A_D [0..63]
 DDR_A_DM [0..7]
 DDR_A_DQS [0..7]
 DDR_A_MA [0..13]

Layout Note:
Place near JP41

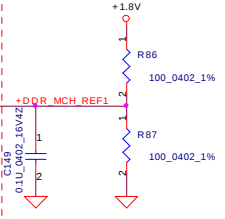


Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9VS



Layout Note:
Place these resistor closely JP41, all trace length Max<1.5"

Layout Note:
+DDR_MCH_REF trace width and spacing is 20/20.



<14.3> EC_P80_DATA

<7> DDR_CKE0_DIMMA

<14.3> EC_P80_CLK

 DDR_A_BS#2

 DDR_A_BS#0

 DDR_A_BS#1

 DDR_A_BS#2

 DDR_A_BS#3

 DDR_A_BS#4

 DDR_A_BS#5

 DDR_A_BS#6

 DDR_A_BS#7

 DDR_A_BS#8

 DDR_A_BS#9

 DDR_A_BS#10

 DDR_A_BS#11

 DDR_A_BS#12

 DDR_A_BS#13

 DDR_A_BS#14

 DDR_A_BS#15

 DDR_A_BS#16

 DDR_A_BS#17

 DDR_A_BS#18

 DDR_A_BS#19

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 DDR_A_BS#214

 DDR_A_BS#215

 DDR_A_BS#216

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 DDR_A_BS#220

 DDR_A_BS#221

 DDR_A_BS#222

 DDR_A_BS#223

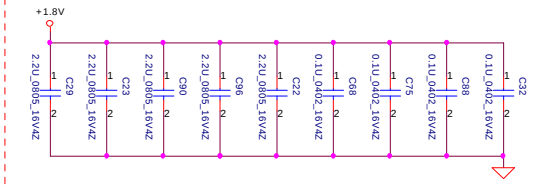
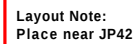
 DDR_A_BS#224

 DDR_A_BS#225

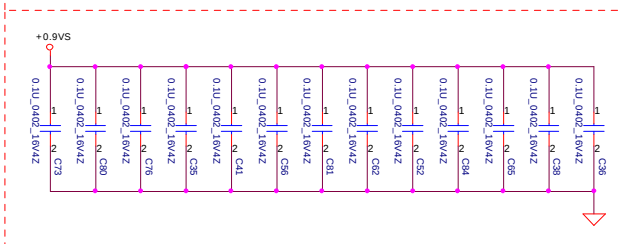
 DDR_A_BS#226

 DDR_A_BS#227

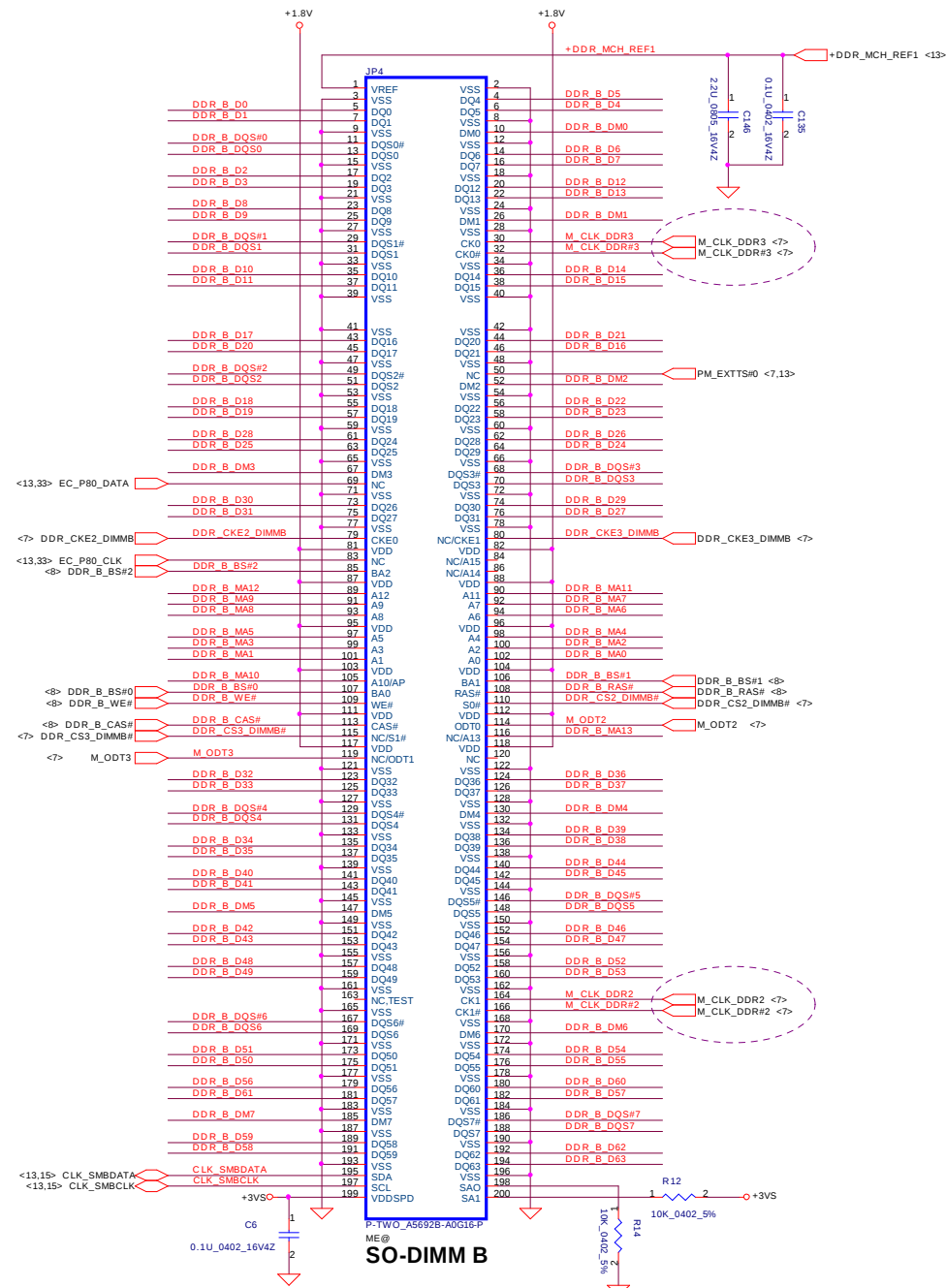
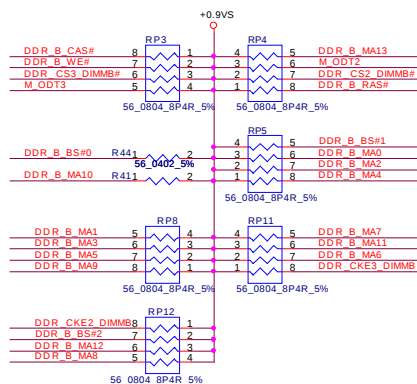
 DDR_A_BS#228



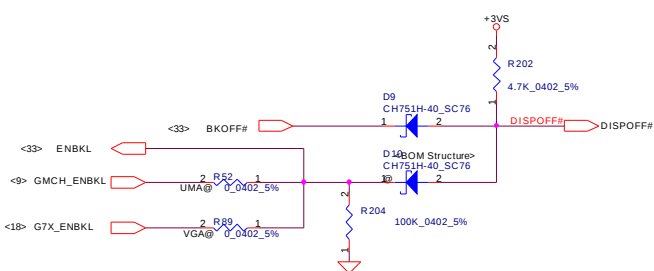
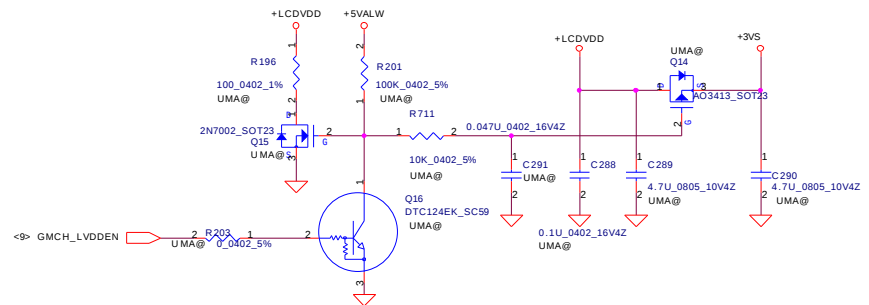
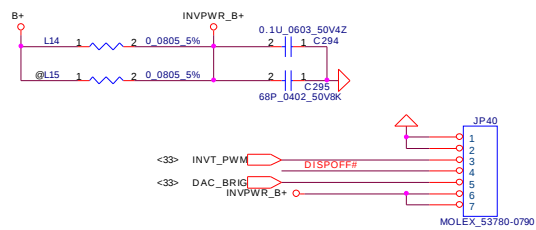
Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9VS



Layout Note:
Place these resistor
closely JP42,all
trace length Max=1.5"

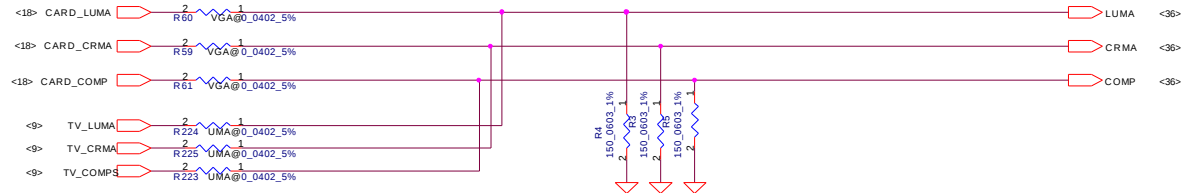


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				Date:	星期日, 二月 23, 2006
				Sheet	14 of 47

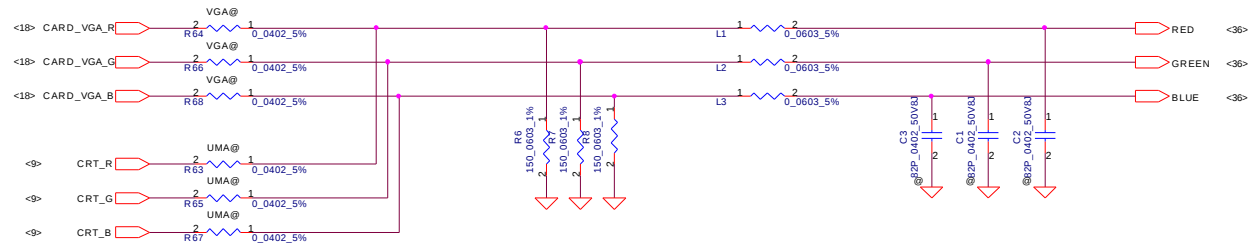


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Size	Document Number	Rev		0.1
Custom	HGT30/31 LA3061	Date		2006/03/23
Date		2006/03/23	Sheet	16 of 47

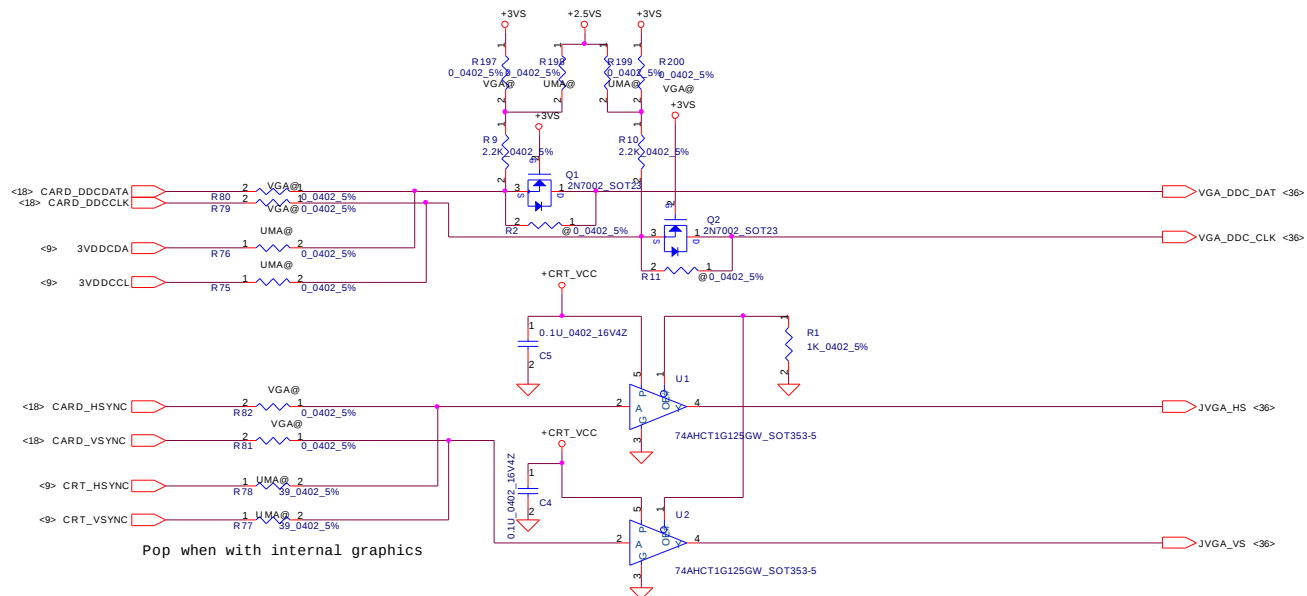
TV-OUT Conn.



Pop when with internal graphics

CRT Conn.

Pop when with internal graphics



Pop when with internal graphics

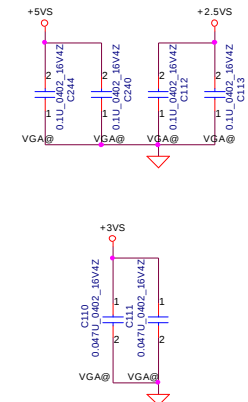
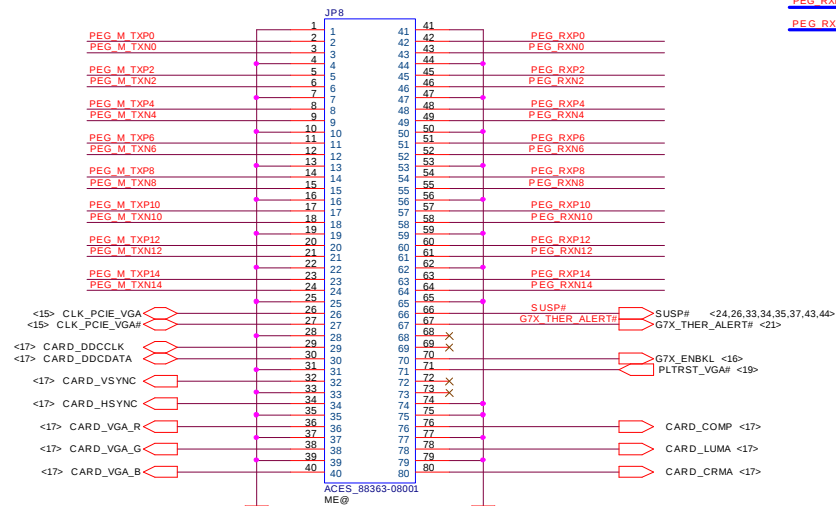
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Issued Date	2005/10/06	Deciphered Date	2006/10/06		
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				Date: 星期四, 二月 23, 2006	Sheet 17 of 47

MAX. 4.06A @ 1.8V
MAX. 130mA @ 2.5V
MAX. 655mA @ 3.3V

PEG_M_TXP[0..15] PEG_M_TXP[0..15] <9>
PEG_M_TXN[0..15] PEG_M_TXN[0..15] <9>
PEG_RXP[0..15] PEG_RXP[0..15] <9>
PEG_RXN[0..15] PEG_RXN[0..15] <9>



< New Add Pin.28 for +3VS, Pin.68 for +1.8VS @R03 >

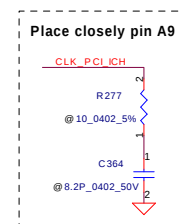
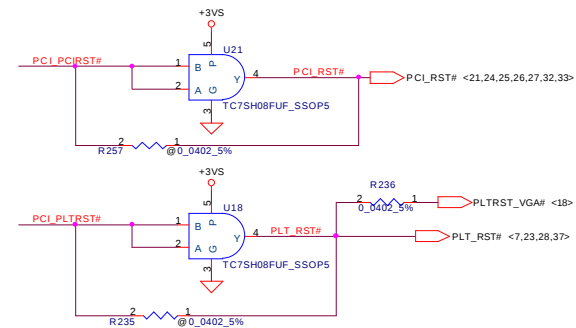
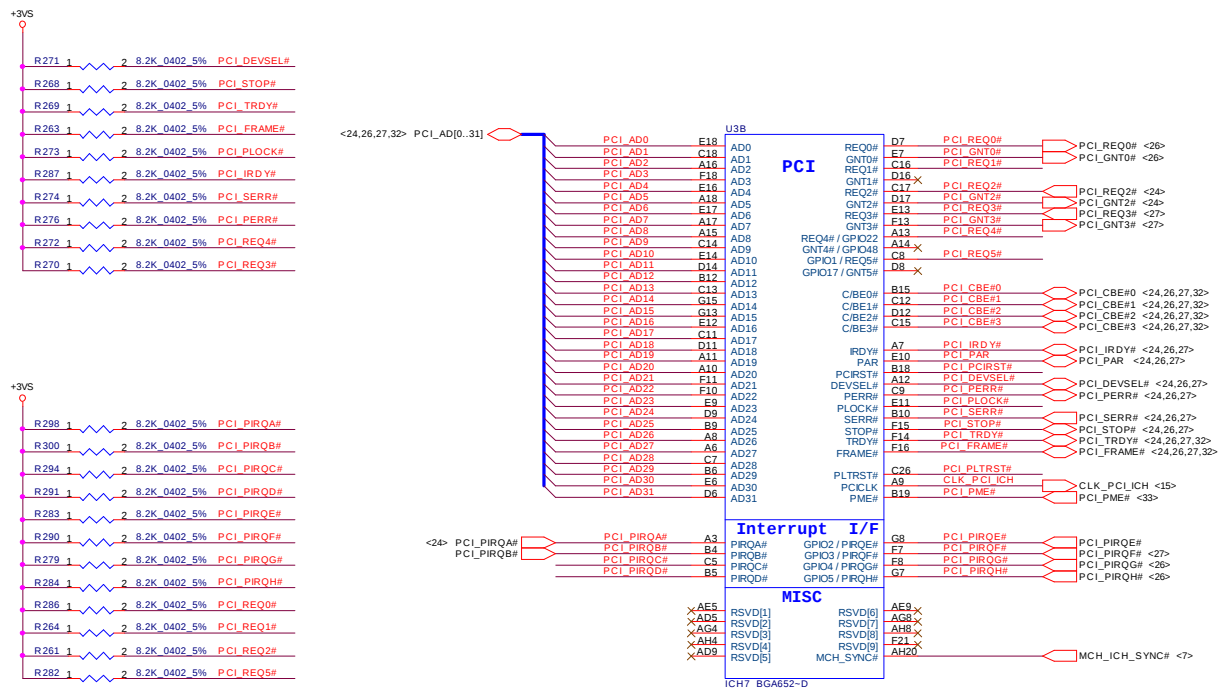


Compal Electronics, Inc.

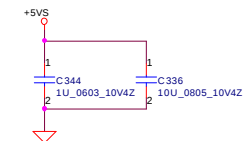
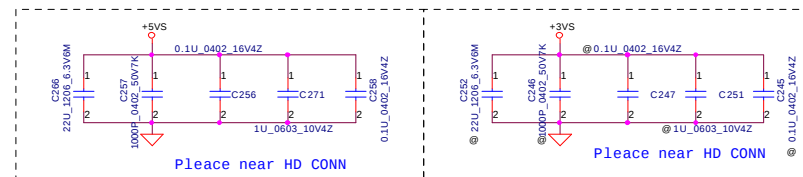
VGA/B connector

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Title	Document Number	Rev
Size	HTG30/31 LA3061	0.1
Customer		
Date	2006.03.23	Sheet 18 of 47

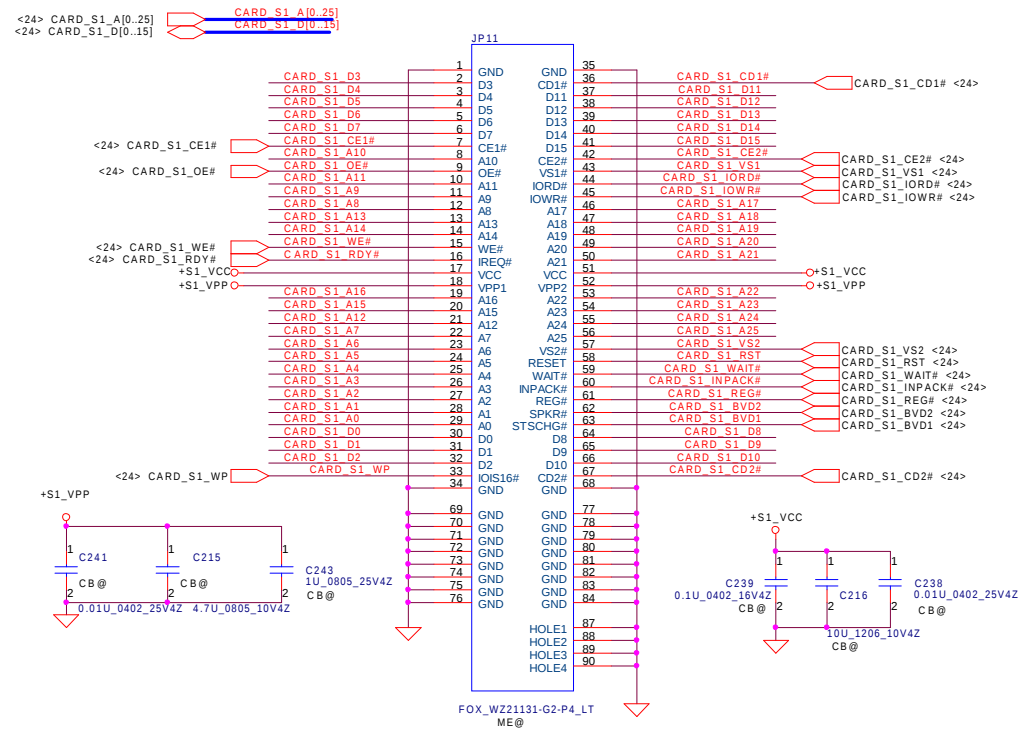
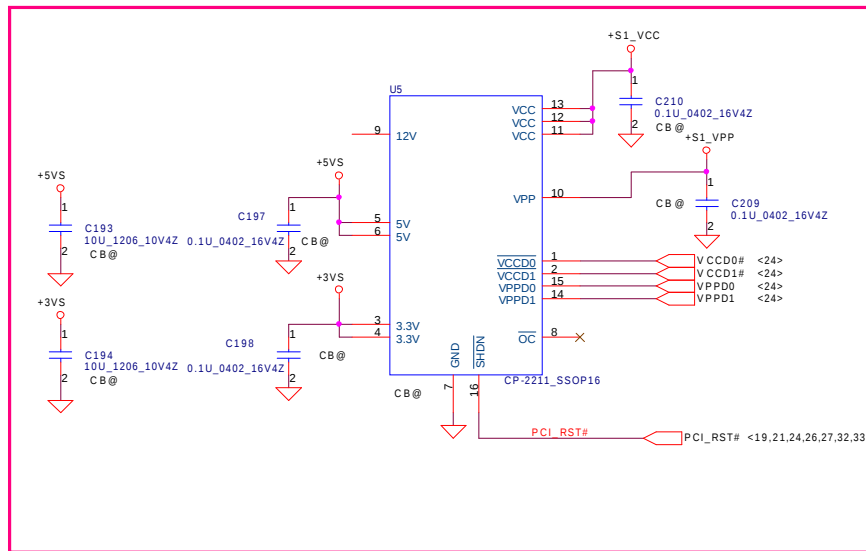


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2005/10/06	Deciphered Date	2006/10/06	Title
					ICH7-M(1/4)
					Size
					Document Number
					HGT30/31 LA3061
					Rev
					0.1
					Date
					2006.02.23
					Sheet
					19 of 47

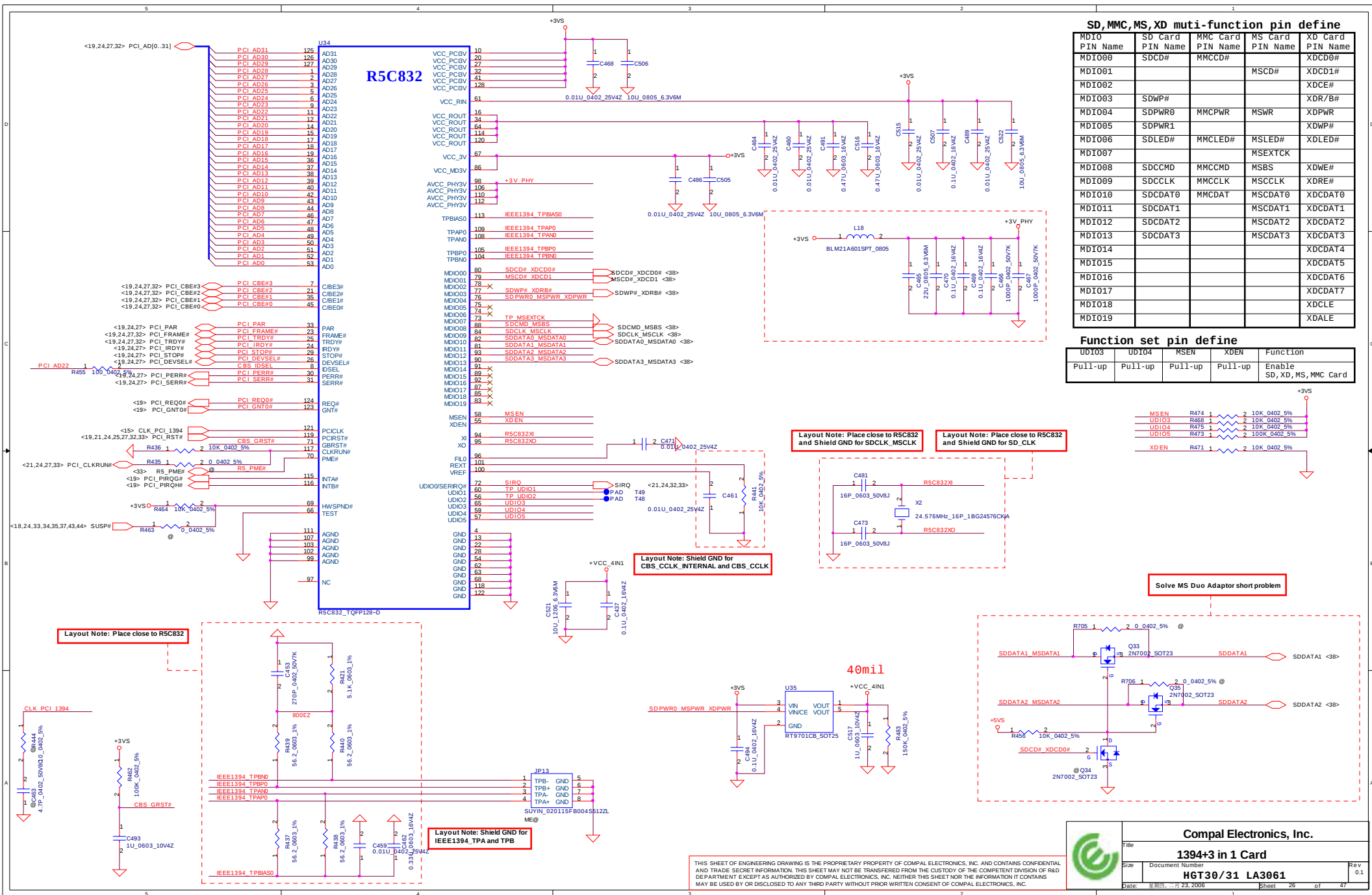


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Issued Date	2005/10/06	Deciphered Date	2006/10/06		
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				Size	Document Number
				Custor	HGT30 / 31 LA3061
Date				星期五, 二月 23, 2006	Sheet 23 of 47

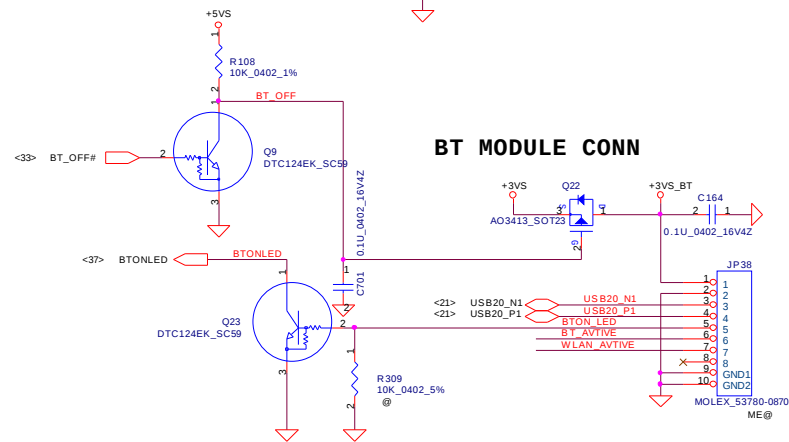
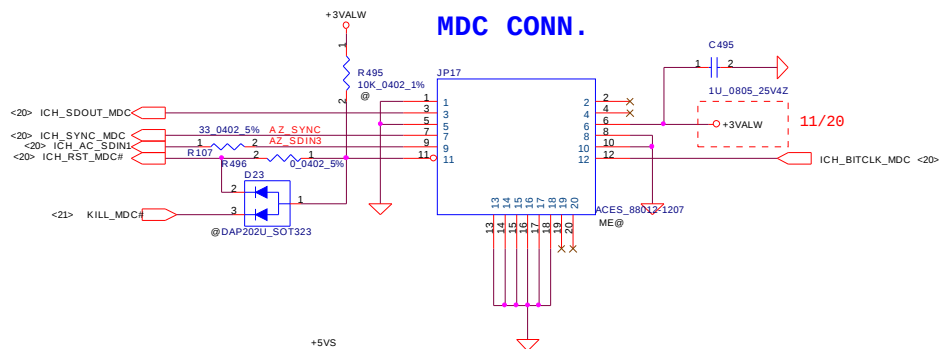
PCMCIA Power Controller



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
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Size		Document Number		Rev	
Customer		HGT30/31 LA3061		0.1	
Date		星期四, 九月 23, 2006		Sheet 25 of 47	



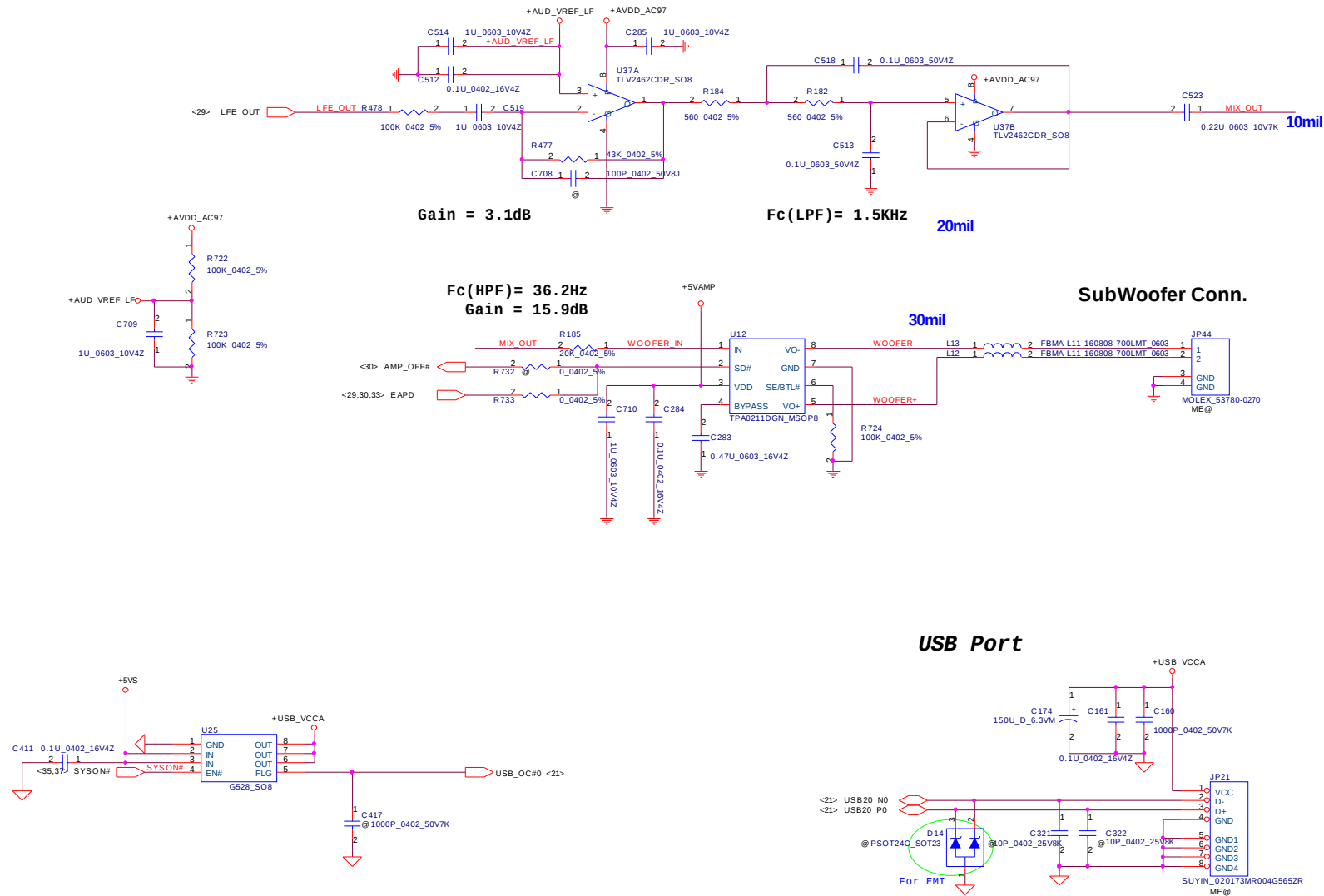
Pin-to-pin connection diagram for the FOX_A508-226-S56N-7F module. The diagram shows two columns of pins: JP16 on the left and GND1/GND2 on the right. Various signals are connected between these pins, including ICH_PCIE_WAKE#, BT_AVTTVE, WLAN_AVTTVE, CLKREQ_MCARD#, CLK_PCIE_MCARD#, CLK_PCIE_MCARDI, CLK_PCIE_MCARD, PCIE_RXN2, PCIE_RXP2, PCIE_TXN2, PCIE_TXP2, PLT_RST#, WL_OFF#, PLT_RST#, ICH_SMBCLK, ICH_SMBDATA, WIRELESS_LED#, and 3VS. The diagram also shows connections to 0.1uF capacitors and 0.1uF capacitors.



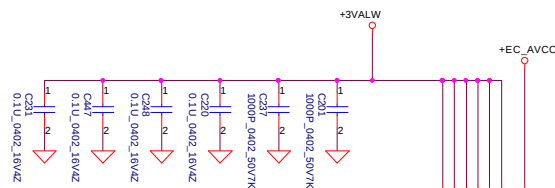
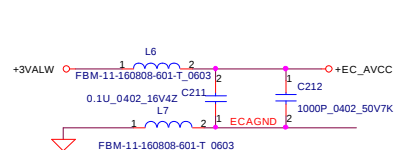
Security Classification		Compal Secret Data		Compal Electronics, Inc. Mini Card / MDC CONN	
Issued Date	2005/10/06	Deciphered Date	2006/10/06		
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				Date: 星期五, 二月 23, 2006	Sheet 28 of 47

SUBWOOFER (Reserved for C38)

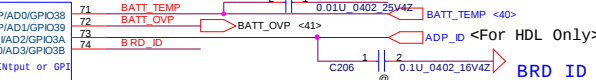
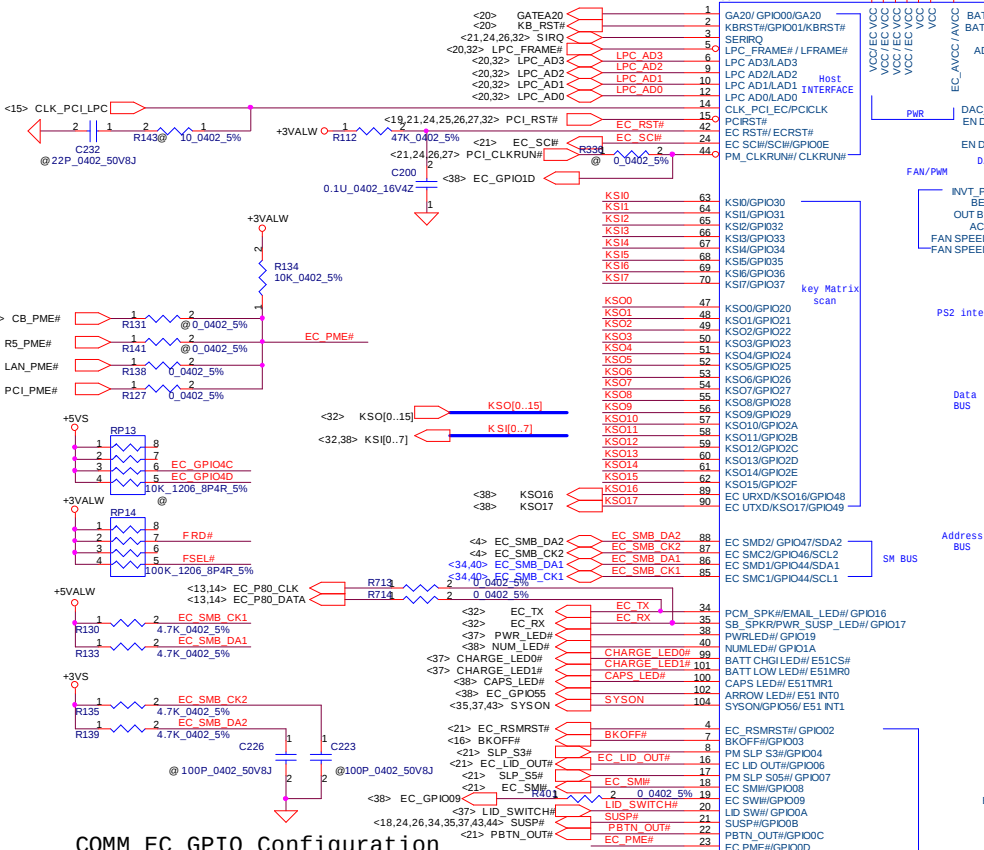
WOOFER@



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				Date	Rev
				星期日, 二月 23, 2008	0.1
				Sheet	31 of 47



Analog Board ID definition,
Please see page 3.

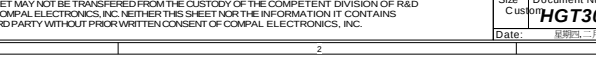
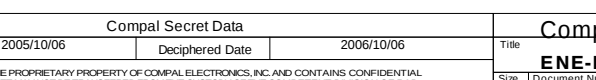
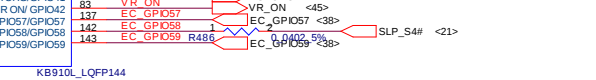
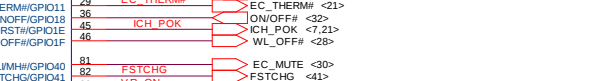
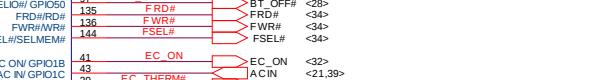
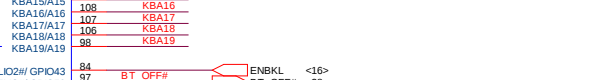
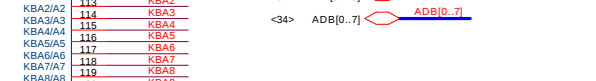
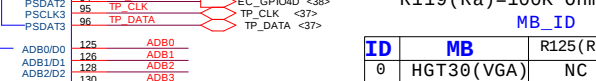
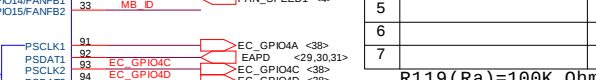
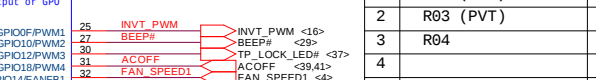
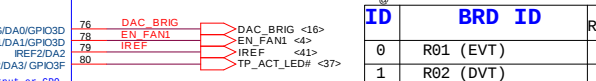
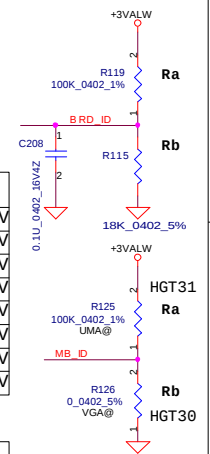


ID	BRD ID	R115(Rb)	Vab
0	R01 (EVT)	0	0V
1	R02 (DVT)	8.2K	0.25V
2	R03 (PVT)	18K	0.50V
3	R04	33K	0.82V
4		56K	1.19V
5		100K	1.65V
6		200K	2.20V
7		NC	3.30V

R119(Ra)=100K Ohm

MB_ID

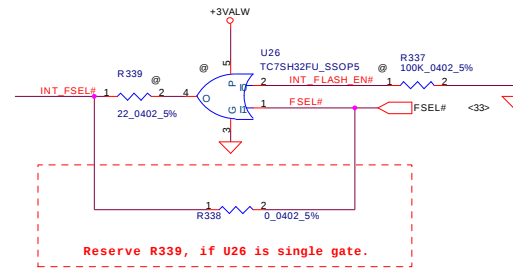
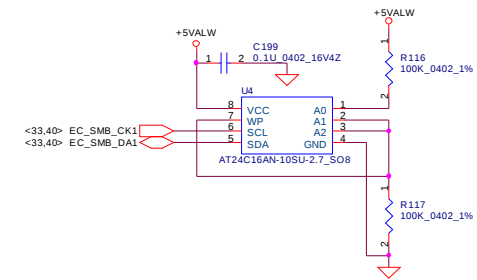
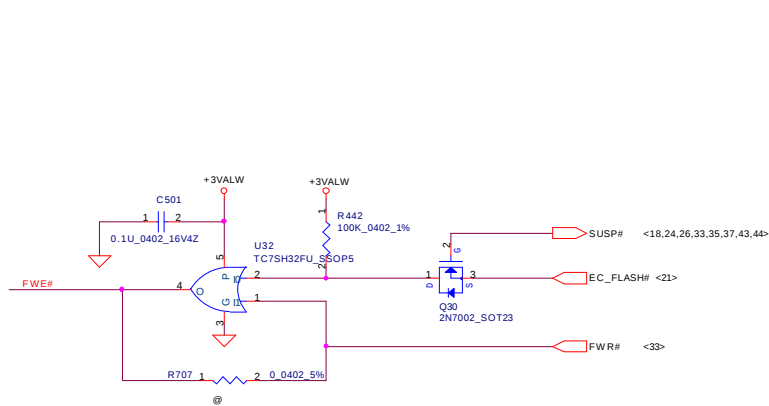
ID	MB	R125 (Ra)	R126 (Rb)
0	HGT30 (VGA)	NC	0 Ohm
1	HGT31 (UMA)	100K Ohm	NC



COMM EC_GPIO Configuration

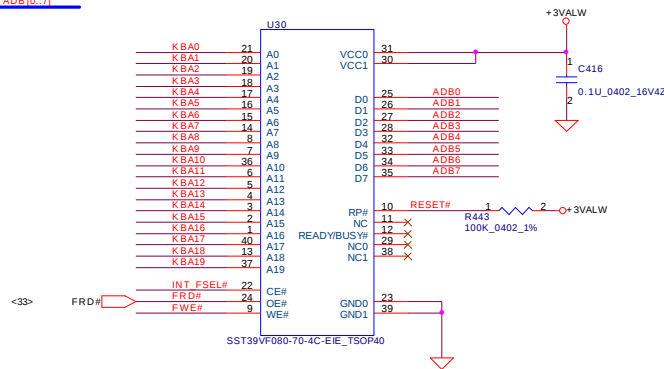
PIN	GPIO	HDL00/HDL10	HGT30/HGT31
80	EC_GPIO3F	NC	TP_ACT_LED#
19	EC_GPIO09	EASY_KEY1#	LED6#
35	EC_GPIO17	SUSP_LED#	EC_P80_DATA
44	EC_GPIO10	NC	LED3#
91	EC_GPIO4A	NC	LED4#
92	EC_GPIO4B	NC	NC
93	EC_GPIO4C	NC	LED1#
94	EC_GPIO4D	NC	LED2#
102	EC_GPIO55	NC	LED5#
97	EC_GPIO50	MEDIA#	BT_OFF#
137	EC_GPIO57	VOL_UP#	NOVO_BTN#
142	EC_GPIO58	VOL_DOWN#	SLP_S4#
143	EC_GPIO59	KILL_SW#	WIRE_LAN_BTN#
30	EC_GPIO12	AMP_MUTE	TP_LOCK_LED#

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				Customer	HGT30/31 LA3061
				Date	Rev 0.1
				Sheet	33 of 47



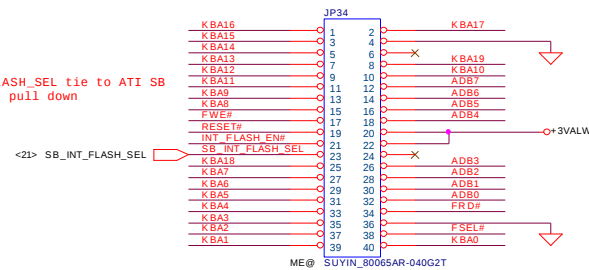
<33> KBA[0..19] KBA[0..19]
<33> ADB[0..7] ADB[0..7]

1MB Flash ROM

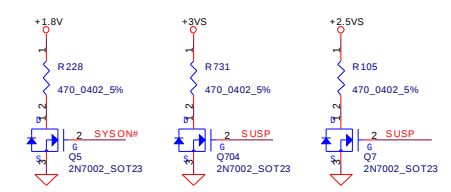
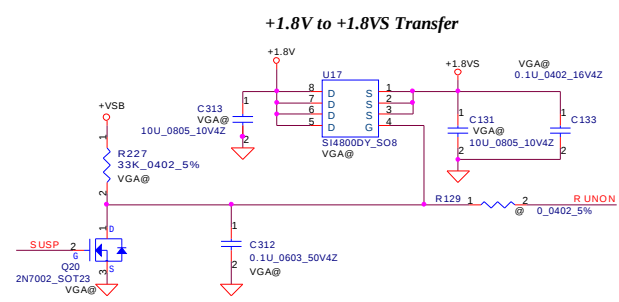
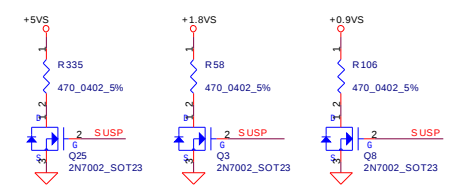
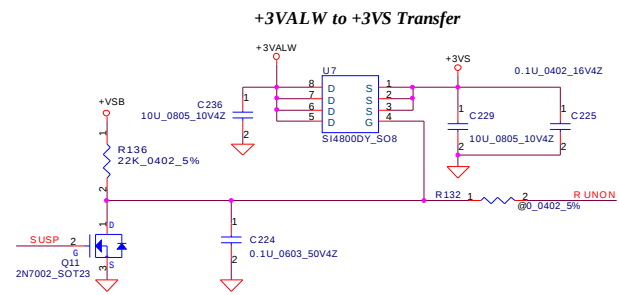
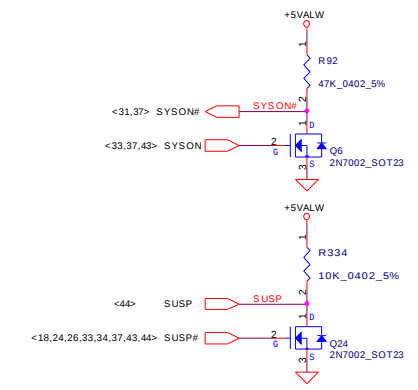
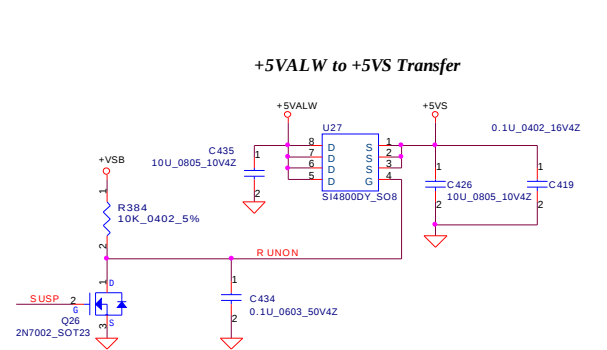


1MB ROM Socket

SB_INT_FLASH_SEL tie to ATI SB
GPIO1 and pull down

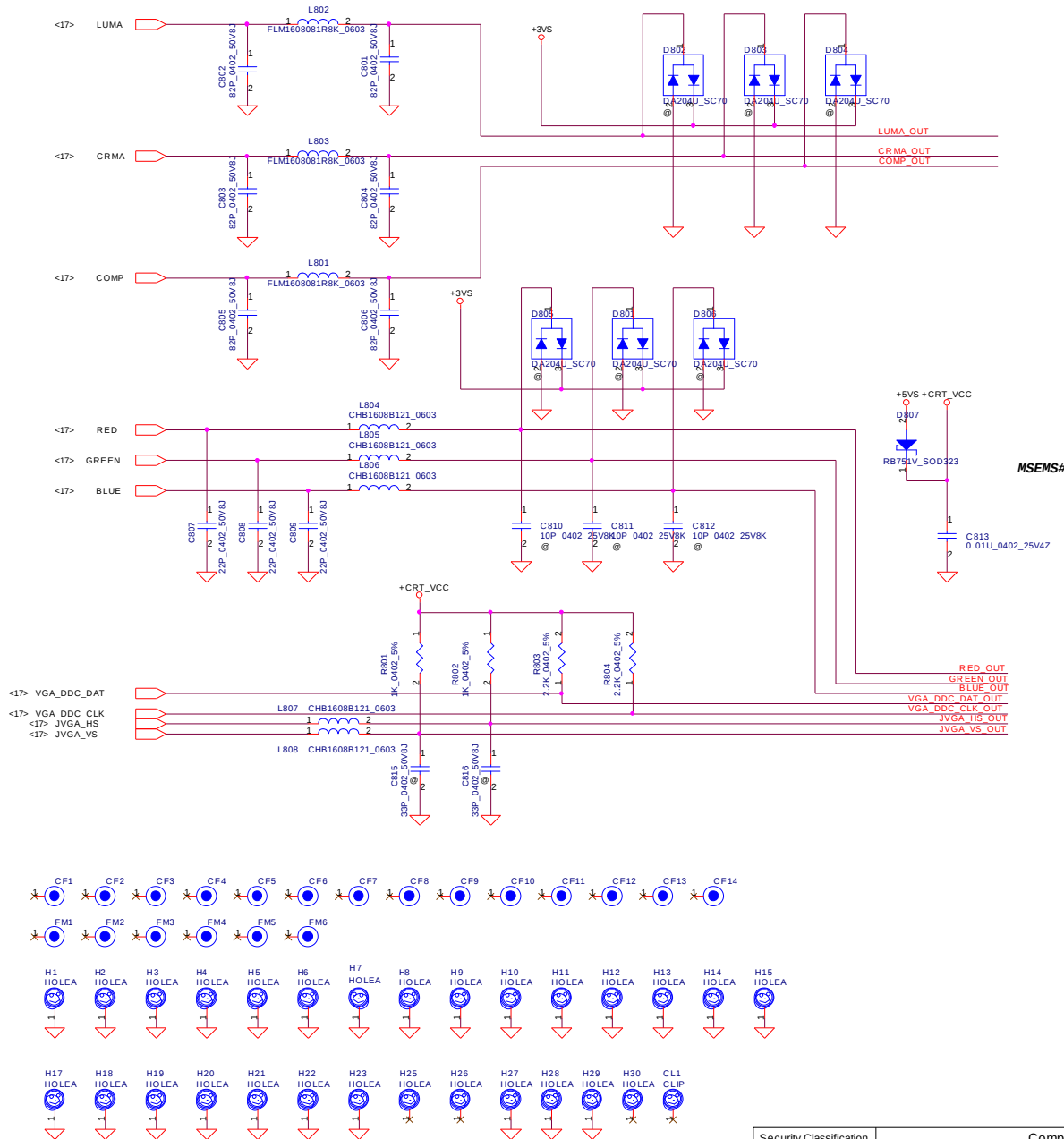


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Size of Drawing				Document Number
Date				HGT30/31 LA3061
Date				Rev 0.1
Date				Sheet 34 of 47



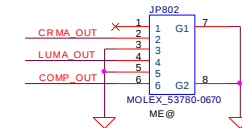
Security Classification	Compal Secret Data		Title	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	DC/DC Circuit
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				Document Number HGT30/31 LA3061
				Rev 0.1
				Date: 星期二, 二月 23, 2006
				Sheet 35 of 47

CLOSE TO JTVOUT1

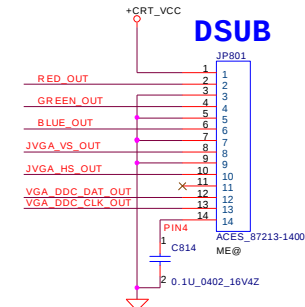


VGA I/O PORT Connector

S-VIDEO



DSUB



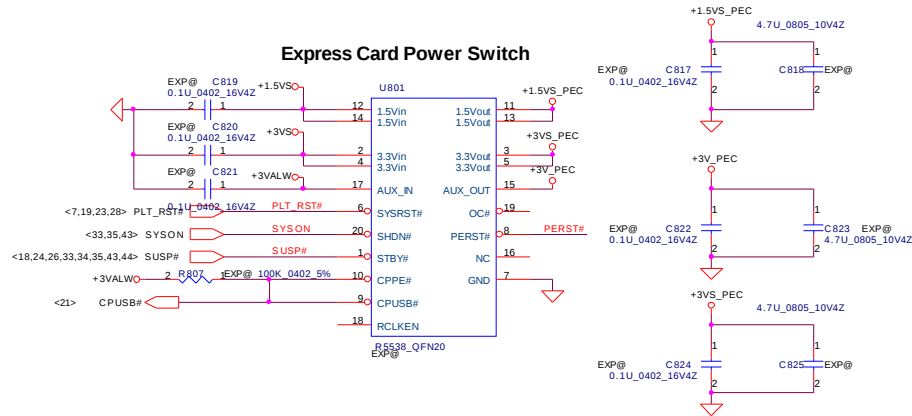
PIN ASSIGMENT

PIN	D-SUB	FUNCTION	PIN	SVIDEO	FUNCTION
1	9	+CRT_VCC	1	1	NC
2	1	RED	2	4	CRMA
3	6	GND	3	2	GND
4	2	GREEN	4	3	LUMA
5	7	GND	5	5	GND
6	3	BLUE	6	6	CVBS
7	8	GND			
8	14	VSYNC			
9	10	GND			
10	13	HSYNC			
11	11	SENSE			
12	12	SM_DAT			
13	15	SM_CLK			
14	4	PIN4			

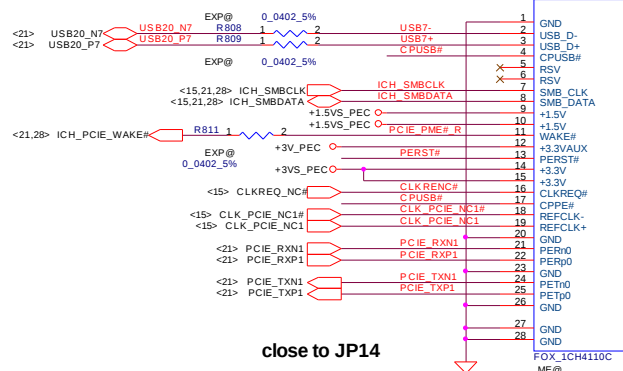
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Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title
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				Document Number
				HGT30/31 LA3061
				Rev 0.1
				Date: 2006/03/23
				Sheet 36 of 47

NEW CARD FOR C38

Express Card Power Switch

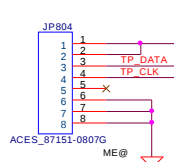


close to JP14

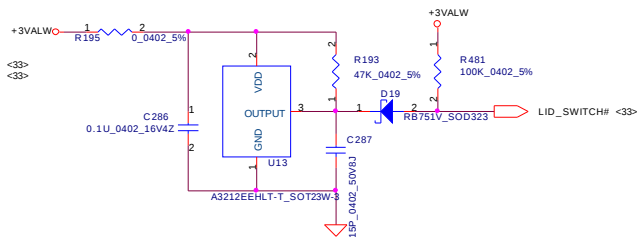


close to JP14

T/P Board



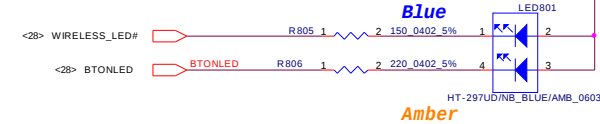
LID Switch



LED Indicator ON M/B

3 2 1
B B A B A

Wireless / Bluetooth LED

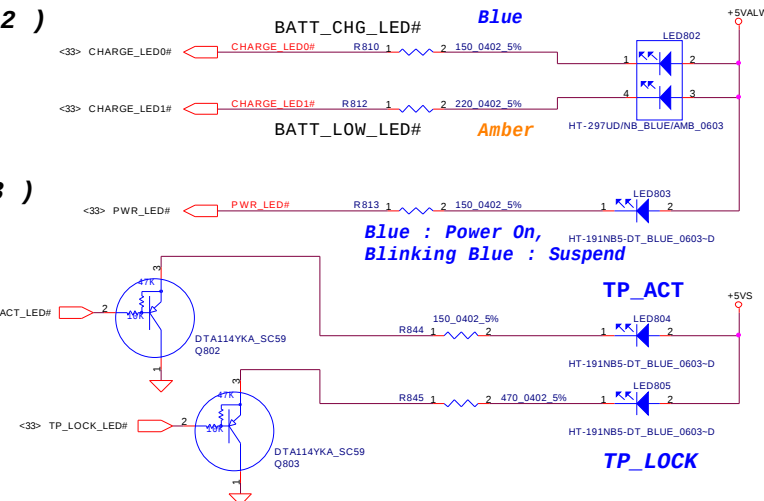


STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

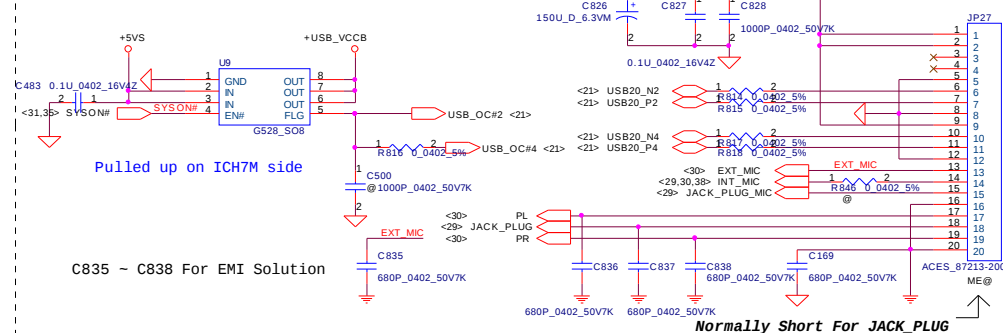
(1)

(2)

(3)



USB board

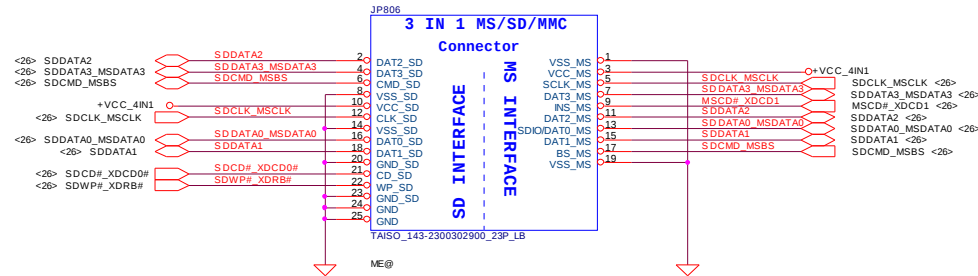


C835 ~ C838 For EMI Solution

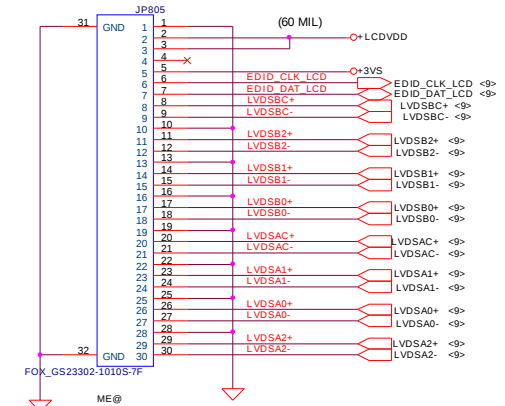
Normally Short For JACK_PLUG

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		Custom	HGT30/31 LA3061
		Date	Rev 0.1
		Sheet	37 of 47

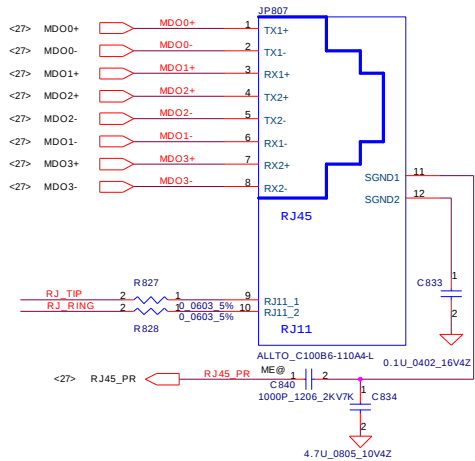
3 IN 1 Card Reader



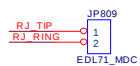
UMA LCD/PANEL Conn.



RJ11+RJ45 CONN

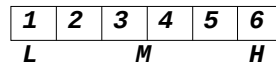


MDC CONN



SWITCH BD

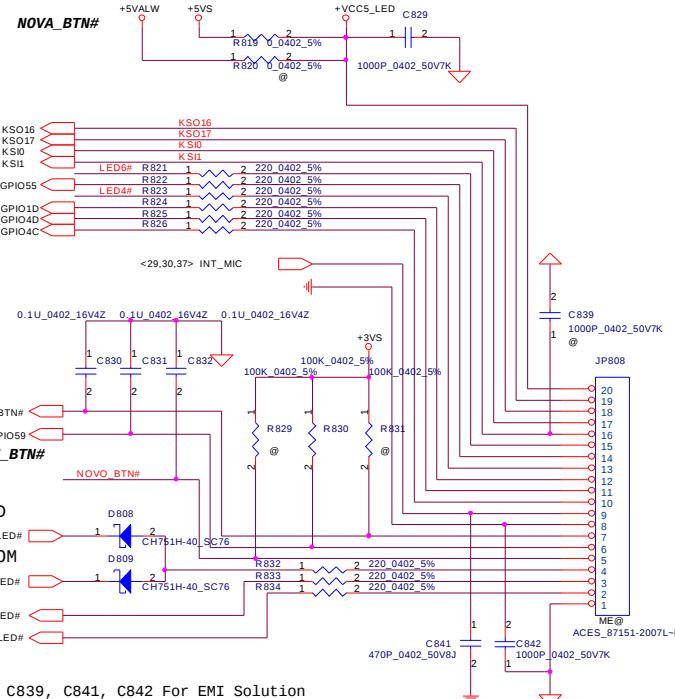
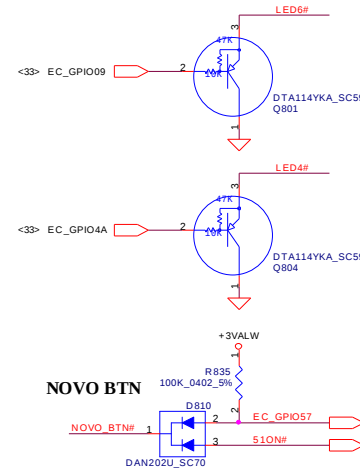
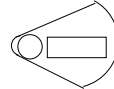
LED Indicator BD



Function

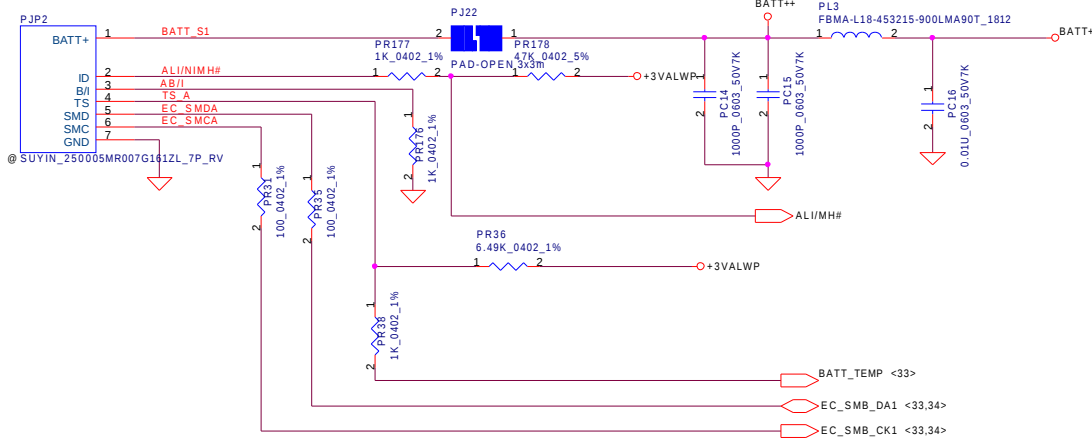
KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel

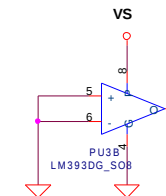
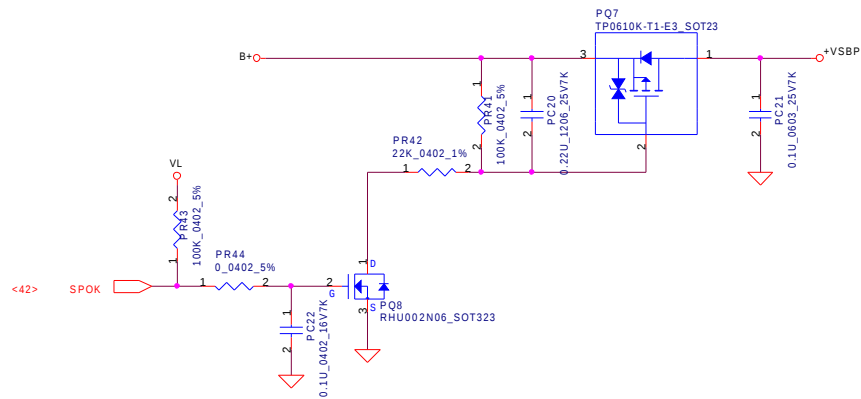
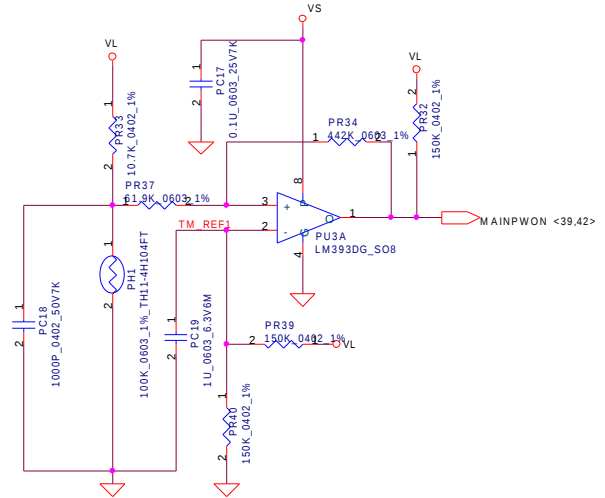


C839, C841, C842 For EMI Solution

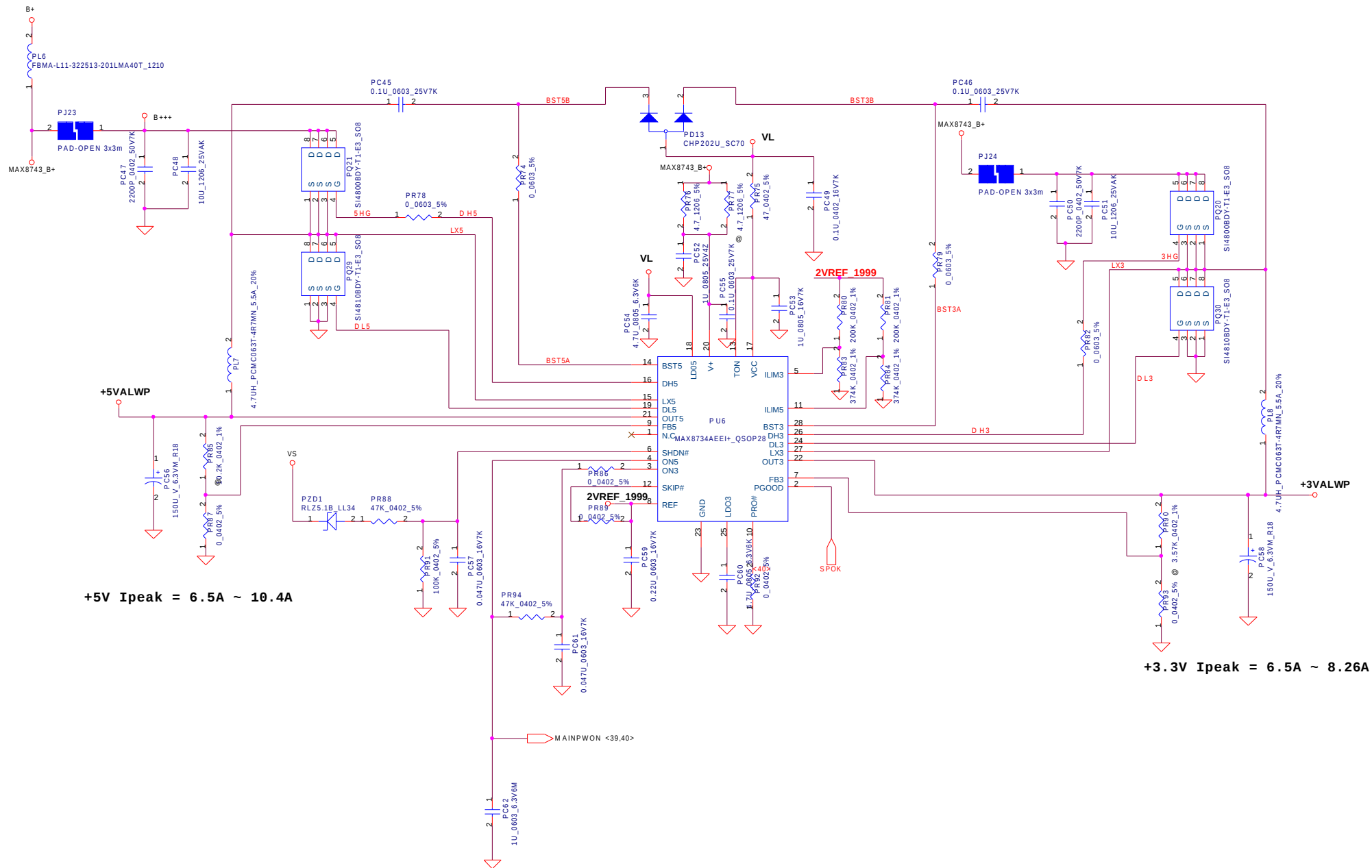
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		Custom	HGT30/31 LA3061
		Date	Rev 0.1
		Sheet	38 of 47



PH1 under CPU bottom side :
CPU thermal protection at 85 degree C
Recovery at 70 degree C

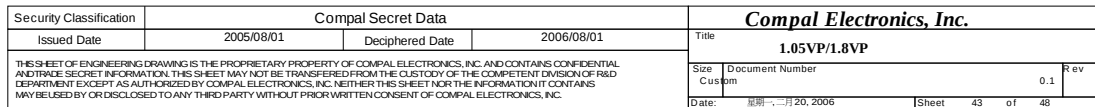


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Date:	星期一, 二月 20, 2006	Sheet	40	of	48

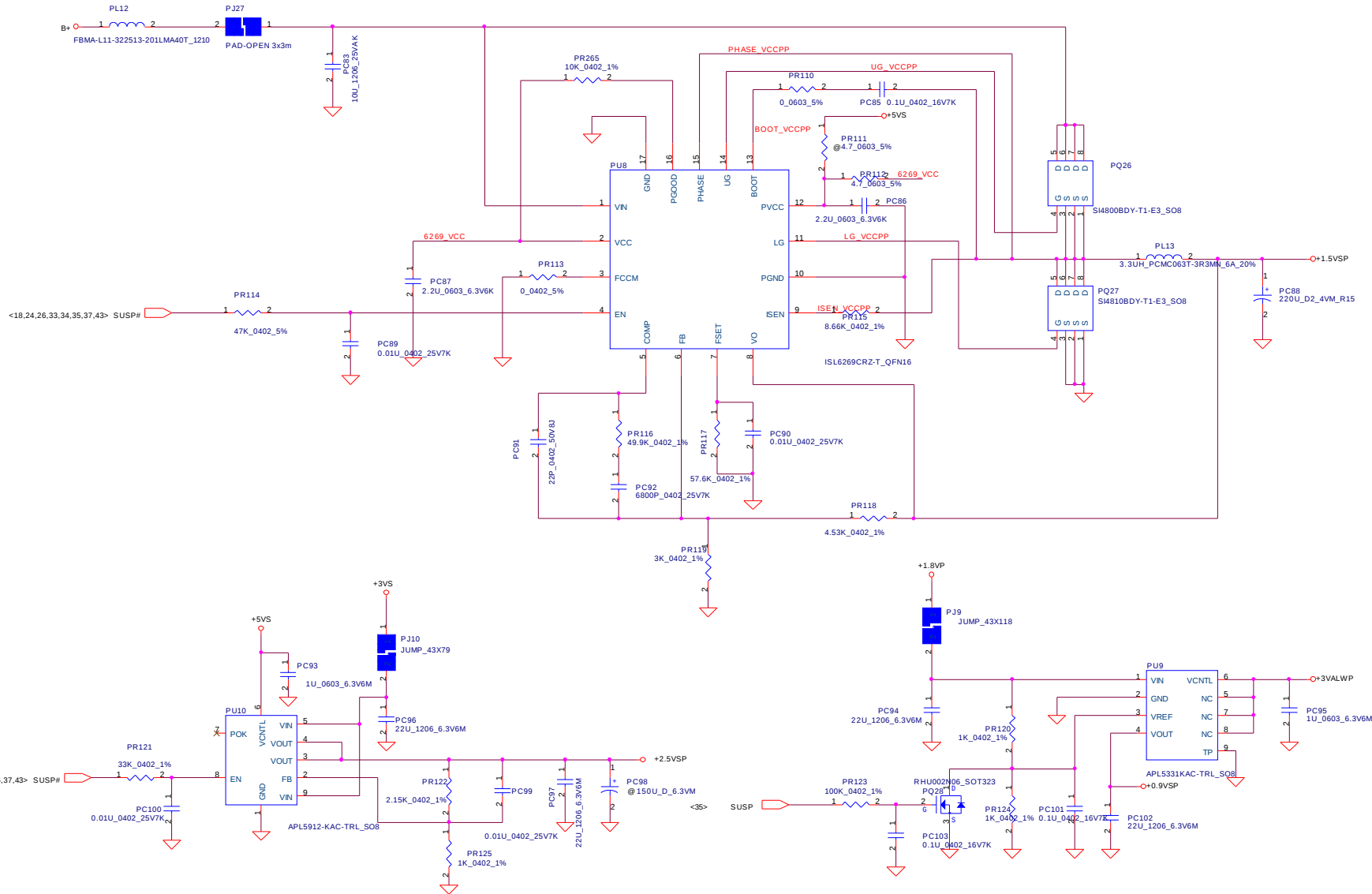


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Size	Document Number	Custom		Rev	0.1
Date:	星期一, 二月 20, 2006	Sheet	42	of	48

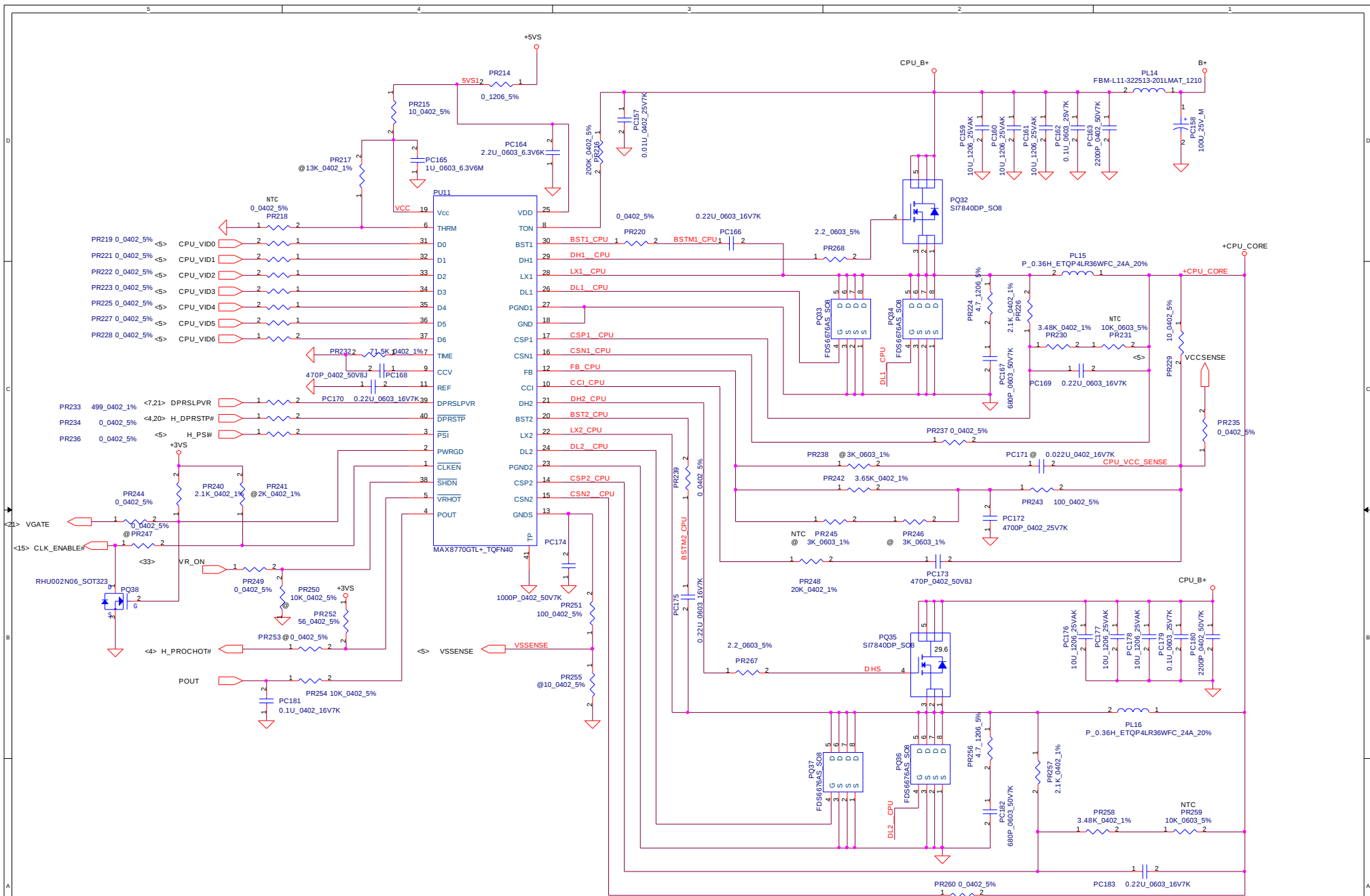
+1.05VSP Ilimit=6.33A~10.03A



+1.5VSP Ilimit = 9.38A-12.5A



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				Document Number	0.1 Rev
Date: 星期 二 月 20, 2006				Sheet	44 of 48



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				Custom	Rev
Date: 2006-06-20				Sheet	45 of 48

Version change list (P.I.R. List)

Page 1 of 1
for PWR

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	VER	Phase
1		MODIFY 3V/5V current limit to 6.5A~8.1A/6.5A to 10.2A		42	MODIFY PR83/PR84 FROM 499K TO 374K		DVT
2		ADD or decrease CPU CORE ring with EMI solution : snubber		45	Reserve PR224//PR256: 4.7 1206 ,add PC167/PC182:680P		DVT
3		Reserve PR267,PR268 seperate in CPU CORE high side gate for EMI require		45	Reserve PR267,PR268:0 0603		DVT
4		change PJP1 from 5 pin to 4 pin		39	change PJP1 from 5 pin to 4 pin		DVT
5		modify sequecce		43	change PR179 to 100k, PC132 =0.1U		DVT
6		modify Vgate		45	add PQ38:RHU002N06,PR240:2K,delete PR247		
7							
8							
9							
10							
11							
8							
9							

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Title PIR (PWR)		
Size	Document Number	Rev 0.2
Date: 星期二, 二月 20, 2006	Sheet 46 of 48	

Version change list (P.I.R. List)

Page 1 of 1

Item	Fixed Issue	Rev.	PG #	Modify List	B.Ver #	Phase
1	Add EC_Port80 Signals to DDR2 DIMM1 & DIMM2	0.2	P.13 P.14	1.Connect U6.91 (EC_P80_CLK) through a 0 Ohm R to JP3.83 & JP4.83 2.Connect U6.92 (EC_P80_DATA) through a 0 Ohm R to JP3.69 & JP4.69	0.2	DVT
2	Impedance not match for both CRT & TV Out	0.2	P.17	R3, R4, R5, R6, R7, R8 Change from NC to 150 Ohm	0.2	DVT
3	LCD Panel will flash white frame when power on	0.2	P.16	no stuff D10 (CH751H), stuff R204 (100K)	0.2	DVT
4	ICH7's GPIO configuration modification	0.2	P.21	GPIO10 connect to ACIN GPIO7 connect to G7X_THER_ALERT# GPIO39 connect to KILL_MDC#	0.2	DVT
5	<New Add> MDC supports S4/S5 resuming	0.2	P.28	MDC power connection change to +3VALW from +3VS	0.2	DVT
6	Audio Circuit modification for : 1. Line-Out connection change from Pin.35/Pin.36 to Pin.43/Pin.45 2. Audio-OUT Auto-Switch by HP Plugging In 3. MicPhone Noise Reduction 4. Cleared off B0 Sound from both entry of Windows XP & Power Off	0.2	P.29-31	Line-Out connection change from Pin.35/Pin.36 to Pin.43/Pin.45 Connect Pin.32 (LFE_OUT) through 2 1uF Cap to Pin.43/Pin.45 +Audio_VREF_LF connection change to 1/2 +AVDD_AC97 Int.MIC connection changes to Pin31/Pin32 through 1uF for each New Add a JACK_PLUG_MIC signal from MIC JACK EAPD signal connect to EC's GPIO4B APA2068's I3PIN(SE/BTL#) connect to GND	0.2	DVT
7	EC GPIO configuration modification	0.2	P.33	New add Port80 information OUT from Pin34(CLK), pin35(DATA) LED4 connection changes from GPIO17(35 PIN) to GPIO4A(91 PIN) New Add DAC's EAPD connect to EC's GPIO4B (92PIN). SKU_ID (GPIO3B) Changes to BRD_ID New Add WL_OFF# (GPIO1F, 46PIN) New Add BT_OFF# (GPIO50, 84PIN) New Add TP_ACT_LED# (GPIO3F, 80PIN) New Add TP_LOCK_LED# (GPIO12, 30PN)	0.2	DVT
8	Lid Switch changes from USB BD to M/B	0.2	P.37	Circuit of Lid SW changes from USB BD to M/B (DEL Lid SW on USB BD)	0.2	DVT
9	Blue LED too dark when active due to VF too High on blue LED	0.2	P.37-38	Changes LED power from +3VS(+3VALW) to +5VS(+5VALWS)	0.2	DVT
10	New Add LED Buffer for LED4, TP_LOCK_LED#, TP_ACT_LED#	0.2	P.37 P.38	TP_ACT_LED# connect to Q802.2, and Q802.1 to GND, then connect to LED804 through a 220 Ohm R TP_LOCK_LED# connect to Q803.2, and Q802.1 to GND, then connect to LED805 through a 220 Ohm R EC_GPIO4A connect to Q804.2, and Q804.1 to GND, then connect to R283 (220 Ohm)	0.2	DVT
11	leakage of electricit when System is running S3 mode	0.2		NC For R458, R402, R411, R128, R831, R829	0.2	DVT

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Size	Document Number		Rev
	HGT30/31 LA-3061		0.0
Date:	星期二, 二月 20, 2006	Sheet	47 of 48

Version change list (P.I.R. List)

Page 1 of 1

Item	Fixed Issue	Rev.	PG #	Modify List	B.Ver #	Phase
12	GIGA LAN function failed for both Loopback or PXE on RTL8110SCL solution	0.3	P.27	1. Add a R734 (0 Ohm) resistor between +2.5V_LAN & VTCT for MAC 2. Removed all of the bypass Cap.(C398, C396, C418, C408) for MAC's TCT pins 3. Removed all of pulled down resistors (49.9 Ohm) & Cap.(0.01u)	0.3	PVT
13	Subwoofer still make POP Sound	0.3	P.31	1. Add 0 Ohm resistors, R732 (0 Ohm) connection between AMP_OFF# & U12.2 (SD#) for reserved 2. a 0 Ohm connection between EAPD & U12.2(SD#)	0.3	PVT
14	Mic Switch between Int. & Ext. be Failed	0.3	P.29	1. R717.1 Disconnect from U11.17 2. R717.1 connect to U11.16	0.3	PVT
15	Wrong parts	0.3	P.35	Change package of C434, C224 & C312 from 0402 to 0603	0.3	PVT
16	Add a discharging path of +3.3VS	0.3	P.35	1. Connect +3.3VS to R731.1 2. Connect R731.2 to Q704.1 3. Connect Q704.2 to NET : SUSP signal 4. Connect Q704.3 to GND	0.3	PVT

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Size	Document Number	Rev
	HGT30/31 LA-3061	0.0
Date:	星期二, 二月 20, 2006	Sheet 48 of 48