Compal Confidential

DCL513A Schematics Document

Banias ICP uFCBGA/uFCPGA Package with Montara-GM Core Logic

2004-05-05

REV: 1.0

Compal Electronics, Inc.		
Title: Cover Sheet		
Size	Document Number: DCL55 LA-2201	Rev: 1.0
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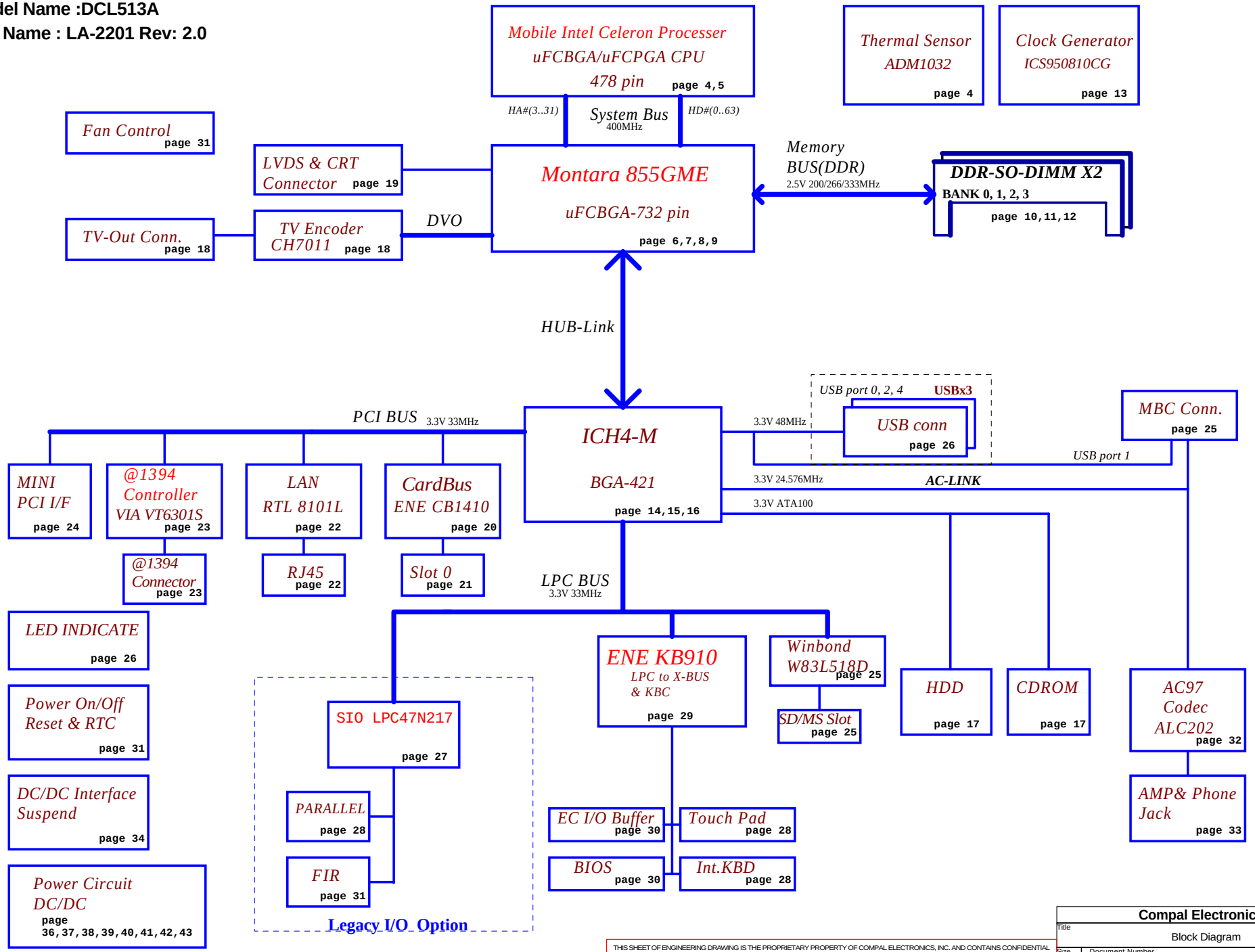
Block Diagram

Model Name :DCL513A
File Name : LA-2201 Rev: 2.0



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Title: Block Diagram		
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Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+VCCP	1.05V rail for Processor I/O	ON	OFF	OFF
+1.2VS	1.2VS switched power rail for GMCH	ON	OFF	OFF
+1.25VS	1.25V switched power rail	ON	OFF	OFF
+1.5VALW	1.5V power rail	ON	ON	ON
+1.5V	1.5V power rail	ON	ON	OFF
+1.5VS	1.5V power rail for GMCH	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5V	2.5V power rail	ON	ON	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5V	5V power rail	ON	ON	OFF
+5VS	5V switched power rail	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
RTCVCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

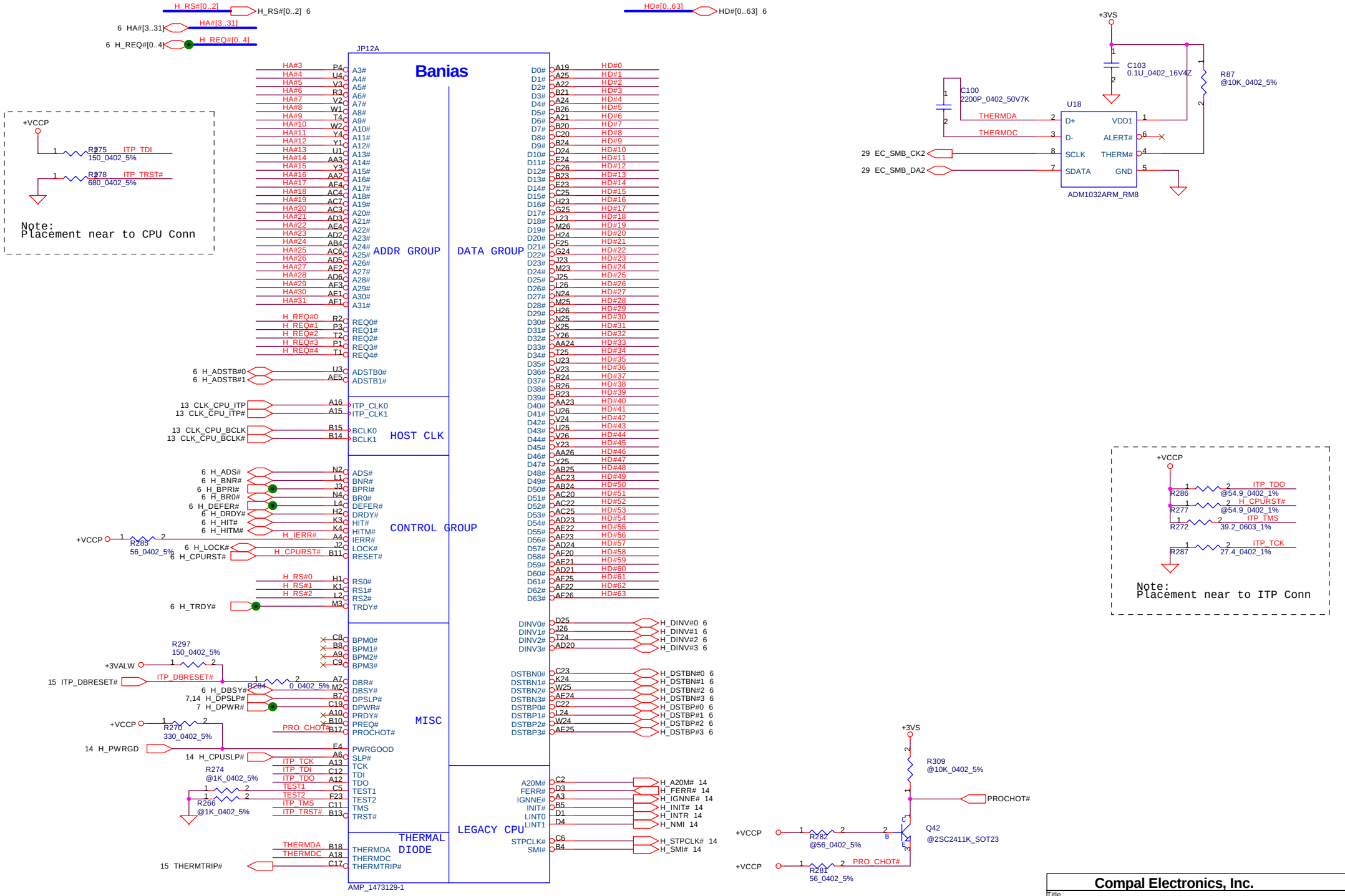
Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD20	2	PIRQA
LAN	AD17	3	PIRQF
Mini-PCI	AD18/AD20	1/4	PIRQG/PIRQH
1394	AD16	0	PIRQE

Board ID Table for AD channel

Vcc	3.3V +/- 5%
R368	100K +/- 5%

Board ID	R361	V_{AD_BID} min	V_{AD_BID} typ	V_{AD_BID} max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

Board ID	PCB Revision
0	
1	0.1
2	0.2
3	1.0
4	
5	
6	
7	



Compal Electronics, Inc.	
Title	Banias Processor in mFCPGA479 with ITP
Size	A3
Document Number	DCL55 LA-2201
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Date:	星期四, 五月 06, 2004
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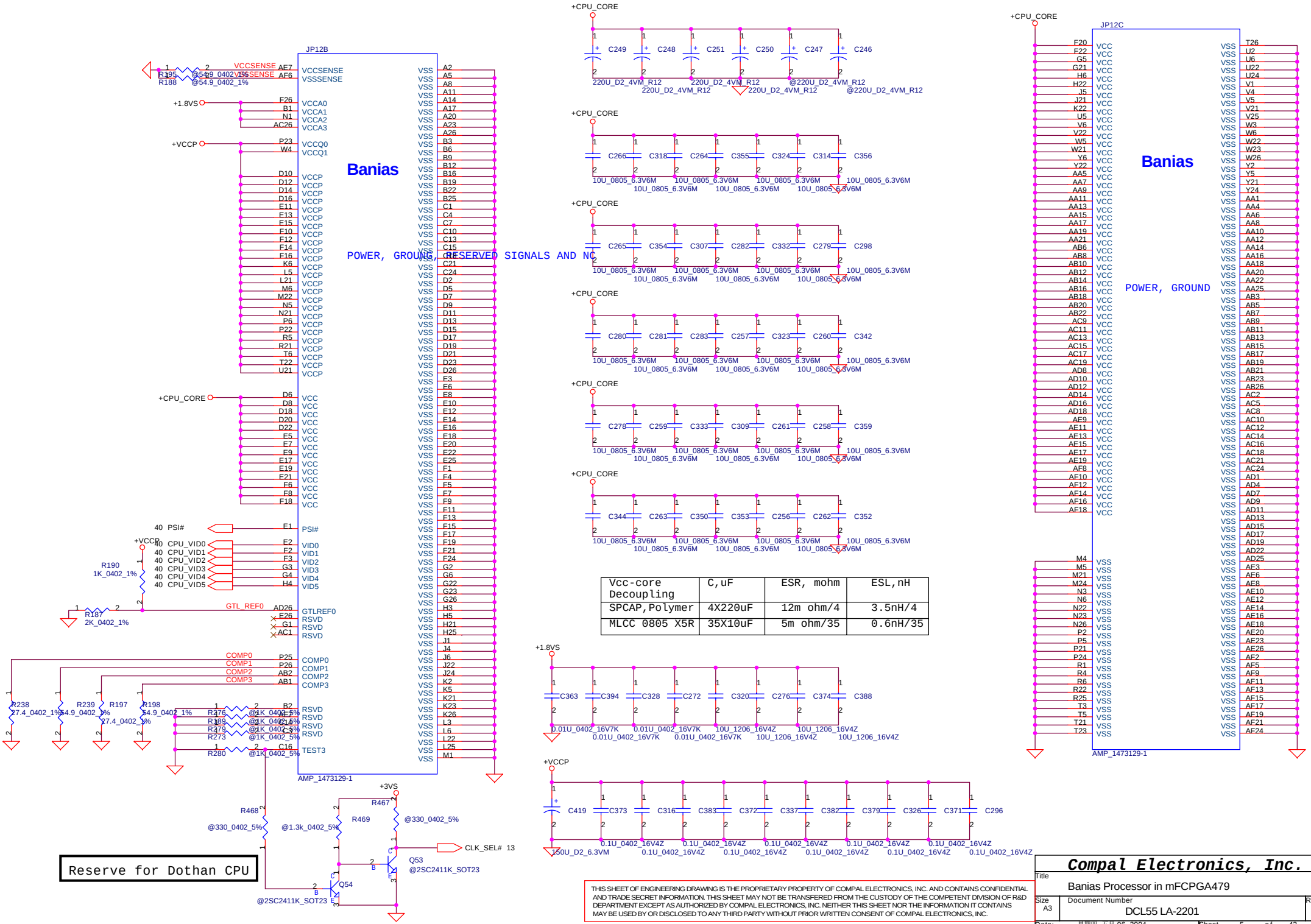

Vcc-core Decoupling	C,uF	ESR, mohm	ESL,nH
SPCAP,Polymer	4X220uF	12m ohm/4	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35

Compal Electronics, Inc.

Title: Banias Processor in mFCPGA479

Size: A3 | Document Number: DCL55 LA-2201 | Rev: 1.0

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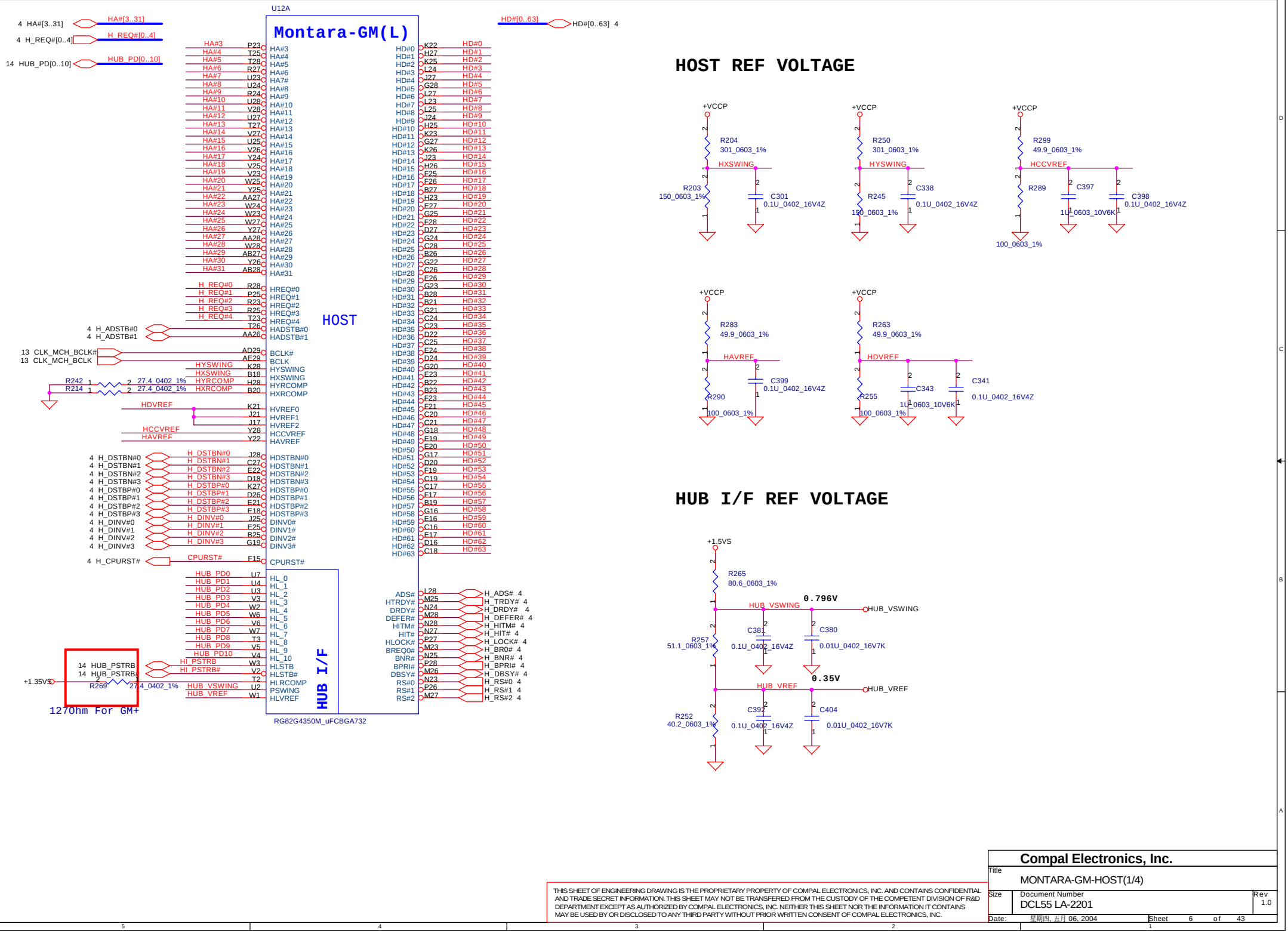

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Title	MONTARA-GM-HOST(1/4)	
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U12B

Montara-GM(L)

DVOC_D[0..11]

DVOBD0/(NC)
DVOBD1/(NC)
DVOBD2/(NC)
DVOBD3/(NC)
DVOBD4/(NC)
DVOBD5/(NC)
DVOBD6/(NC)
DVOBD7/(NC)
DVOBD8/(NC)
DVOBD9/(NC)
DVOBD10/(NC)
DVOBD11/(NC)

DVOBCLK/(NC)
DVOBCLK#/(NC)
DVOBHSYNC/(NC)
DVOBVSYNC/(NC)
DVOBBLANK#/(NC)
DVOBFLDSTL/(NC)
DVOBCINTR#
DVOBCCLKINT

DVOCCLK
DVOCCLK#
DVOCHSYNC
DVOCVSYNC
DVOCBLANK#
DVOCFLDSTL

MI2CCLK
MI2CDATA
MDVICLK
MDVIDATA
MDDCCLK
MDDCDATA

DVOCD0
DVOCD1
DVOCD2
DVOCD3
DVOCD4
DVOCD5
DVOCD6
DVOCD7
DVOCD8
DVOCD9
DVOCD10
DVOCD11

ADDID0
ADDID1
ADDID2
ADDID3
ADDID4
ADDID5
ADDID6
ADDID7

DVODETECT
DPMS
GVREF
AGPBUSY#
DVORCOMP
GCLKIN

RVSD0
RVSD1
RVSD2
RVSD3
RVSD4
RVSD5
GST[1]
GST[0]
RVSD8
RVSD9
RVSD10
RVSD11

RG82G4350M_uFCBGA732

DAC

BLUE
BLUE#
GREEN
GREEN#
RED
RED#
HSYNC
VSYNC
REFSET
DDCACLK
DDCADATA

B 19
G 19
R 19
HSYNC 19
VSYNC 19
DDC_CLK 19
DDC_DATA 19

1270hm For GM+

R247
127_0603_1%

LVDS

IYAM0
IYAM1
IYAM2
IYAM3
IYAP0
IYAP1
IYAP2
IYAP3
IYBM0
IYBM1
IYBM2
IYBM3
IYBP0
IYBP1
IYBP2
IYBP3
ICLKAM
ICLKAP
ICLKBM
ICLKBP

TXOUT0- 19
TXOUT1- 19
TXOUT2- 19
TXOUT0+ 19
TXOUT1+ 19
TXOUT2+ 19
TZOUT0- 19
TZOUT1- 19
TZOUT2- 19
TZOUT0+ 19
TZOUT1+ 19
TZOUT2+ 19
TXCLK- 19
TXCLK+ 19
TZCLK- 19
TZCLK+ 19

DDCPCLK
DDCPDATA

LCD_CLK 19
LCD_DATA 19

PANELBKLTCTL
PANELBKLTEN
PANELVDDEN

INVT_PWM 19,29
ENBKL 29
ENVDD 19

LVREFH
LVREFL

LVBG
LIBG

1.5K_0603_1%

CLKS

DREFCLK
DREFSSCLK
LCLKCTLA
LCLKCTLB

CLK_MCH_48M 13
CLK_SSC_66M 13

MISC

DPWR#/(NC)
DPSLP#
RSTIN#
PWROK
EXTTS0
MCHDETECTVSS

H_DPWR# 4
H_DPSLP# 4,14
PCIRST# 14,18,20,21,22,23,24,25,27
VGATE 13,15,40

NC

NC0
NC1
NC2
NC3
NC4
NC5
NC6
NC7
NC8
NC9
NC10
NC11

DVO

no TV OUT mount R253

no TV out mount R466 , Del R251,R235

R240 1K_0402_5%
R256 100K_0402_5%
R253 100K_0402_5%
R241 100K_0402_5%
R466 1K_0402_5%
R251 @1K_0603_1%
R219 10K_0603_1%
Q38 BSN20_SOT23
R235 @1K_0402_5%
R234 40.2_0603_1%
R243 @0_0402_5%
C321 22P_0402_50V8J
R191 1.5K_0603_1%
R222 @510_0402_5%
R200 10K_0603_1%

CLK_MCH_48M
CLK_SSC_66M
CLK_MCH_66M
PCIRST#
EXTTS
GVREF
DVORCOMP
DVODETECT
GST0
GST1

R223 @33_0402_5%
C302 @22P_0402_50V8J
R211 @33_0402_5%
C284 @22P_0402_50V8J
R291 @33_0402_5%
C405 @22P_0402_50V8J
R227 @1K_0402_5%
R226 @1K_0402_5%

LCD_DC2 R217 2.2K_0402_5%
LCD_DD2 R216 2.2K_0402_5%
+3VS

I2C BUS PULL UP

RP50 2.2K_0804_8P4R_5%
MDVICLK
MDDCCLK
MDVIDATA
MDDCDATA
MI2CCLK R249 2.2K_0402_5%
MI2CDATA R264 2.2K_0402_5%
+1.5VS

DVO REF VOLTAGE

R236 1K_0603_1%
R237 1K_0603_1%
GVREF
C325 0.1U_0402_16V4Z
+1.5VS

Starp pin list

LCLKCTLB: High for P4
N.C. for Banias(default low)
DVODETECT: Low to enable DVO
High to disable DVO

GST0	GST1	PSB/Sys Mem/ GFX Core
0	0	400/266/200 (default)
0	1	400/200/200
1	0	400/200/133

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Title: MONTARA-GM-LVDS(2/4)

Size: | Document Number: DCL55 LA-2201 | Rev: 1.0

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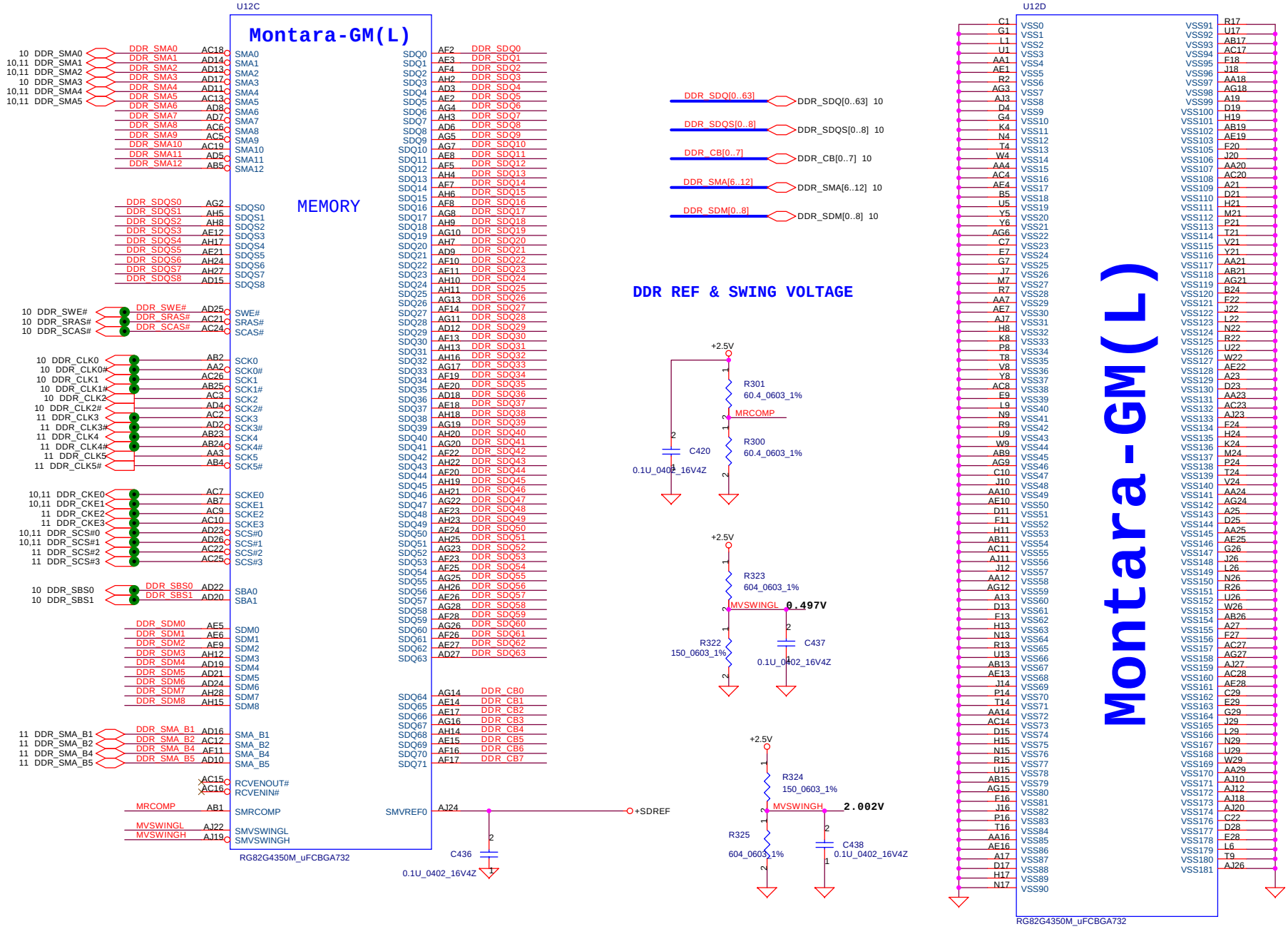
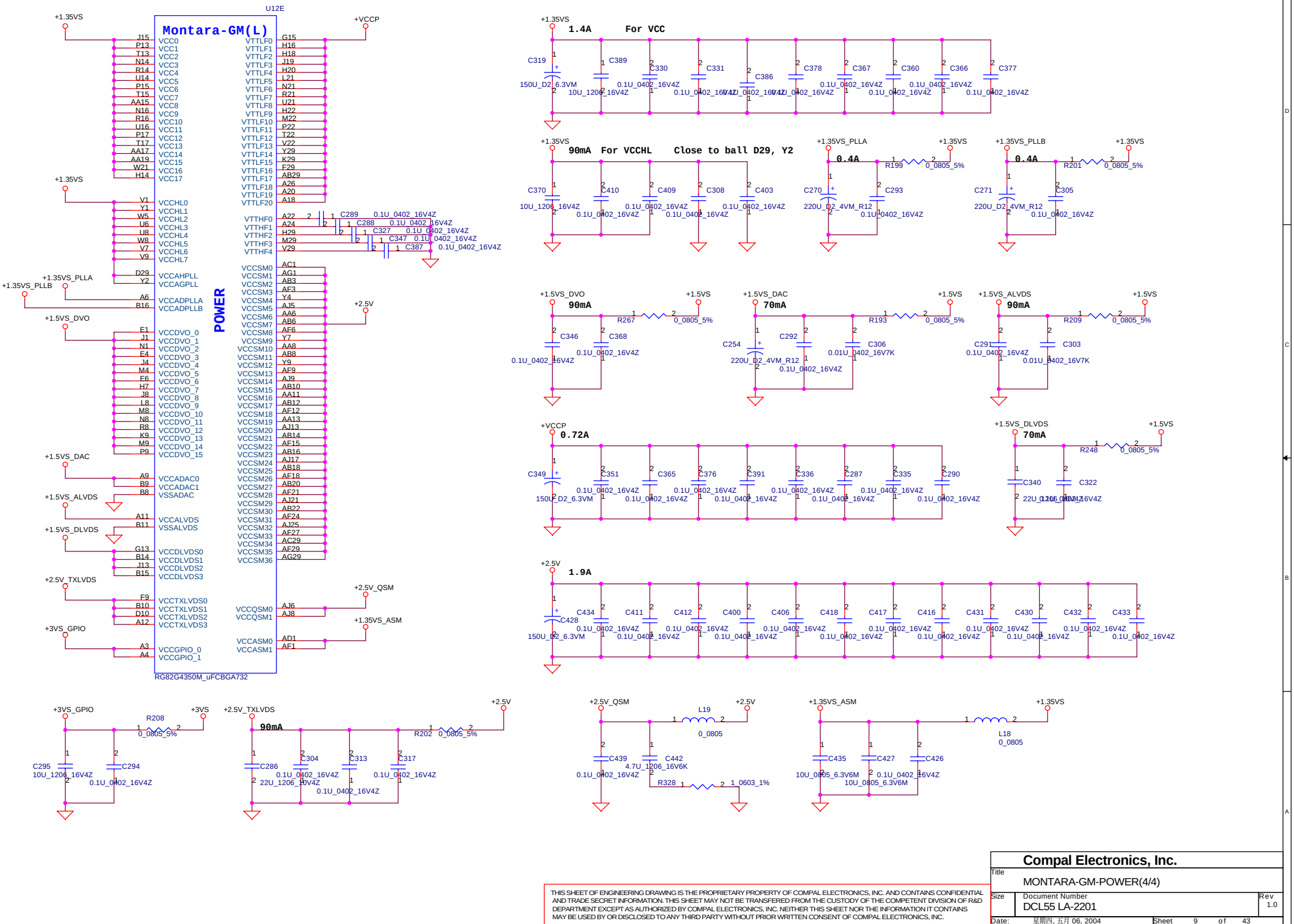


Compal Electronics, Inc.		
Title: MONTARA-GM-DDR(3/4)		
Size	Document Number: DCL55 LA-2201	Rev: 1.0
Date: 星期四, 五月 06, 2004	Sheet 8 of 43	



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Title	MONTARA-GM-POWER(4/4)
Size	Document Number: DCL55 LA-2201
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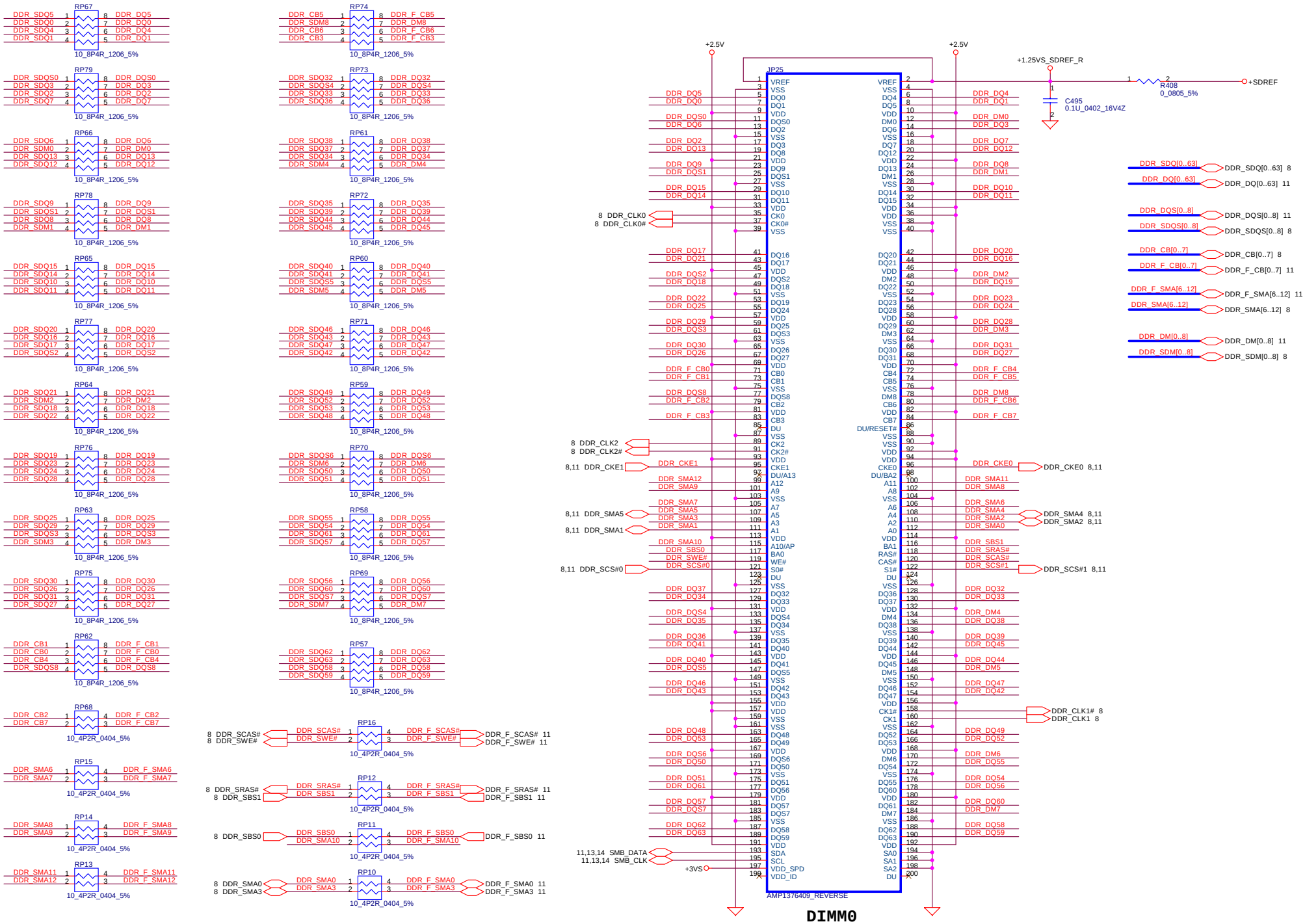

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Title	DDR-SODIMM SLOT0	
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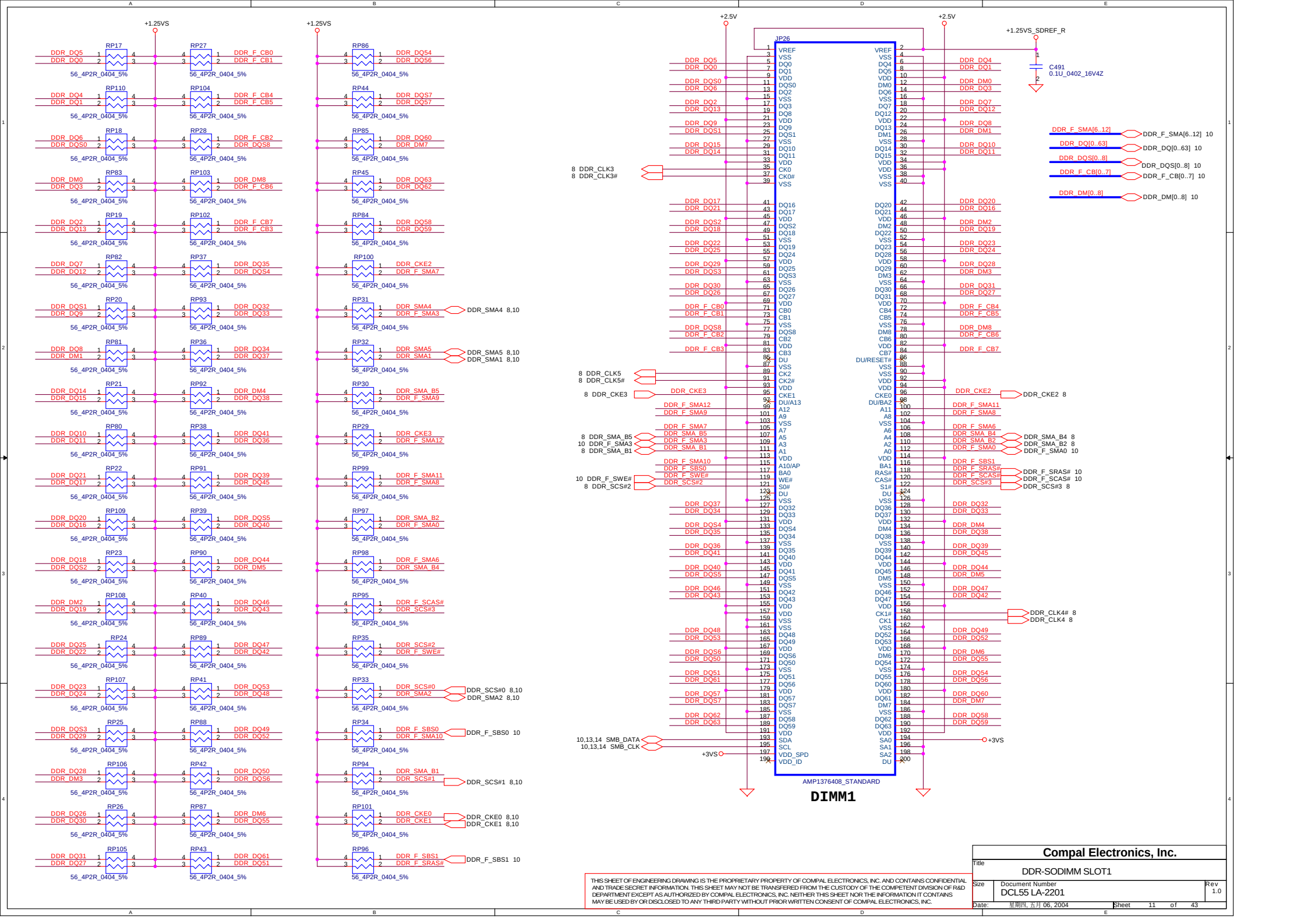

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Title: DDR-SODIMM SLOT1

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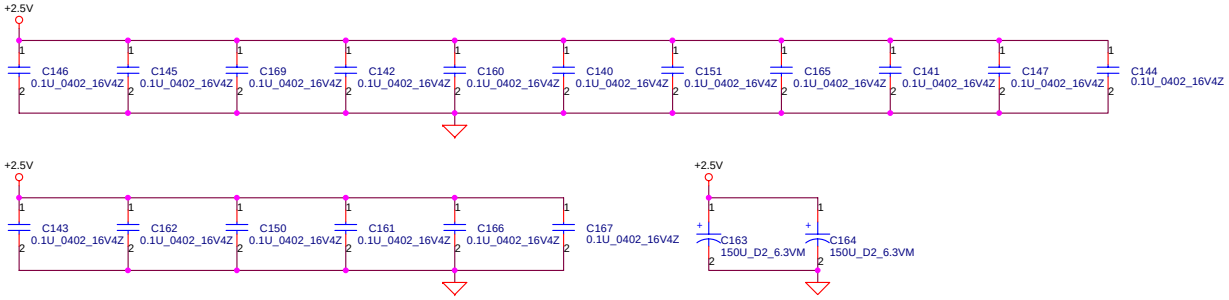
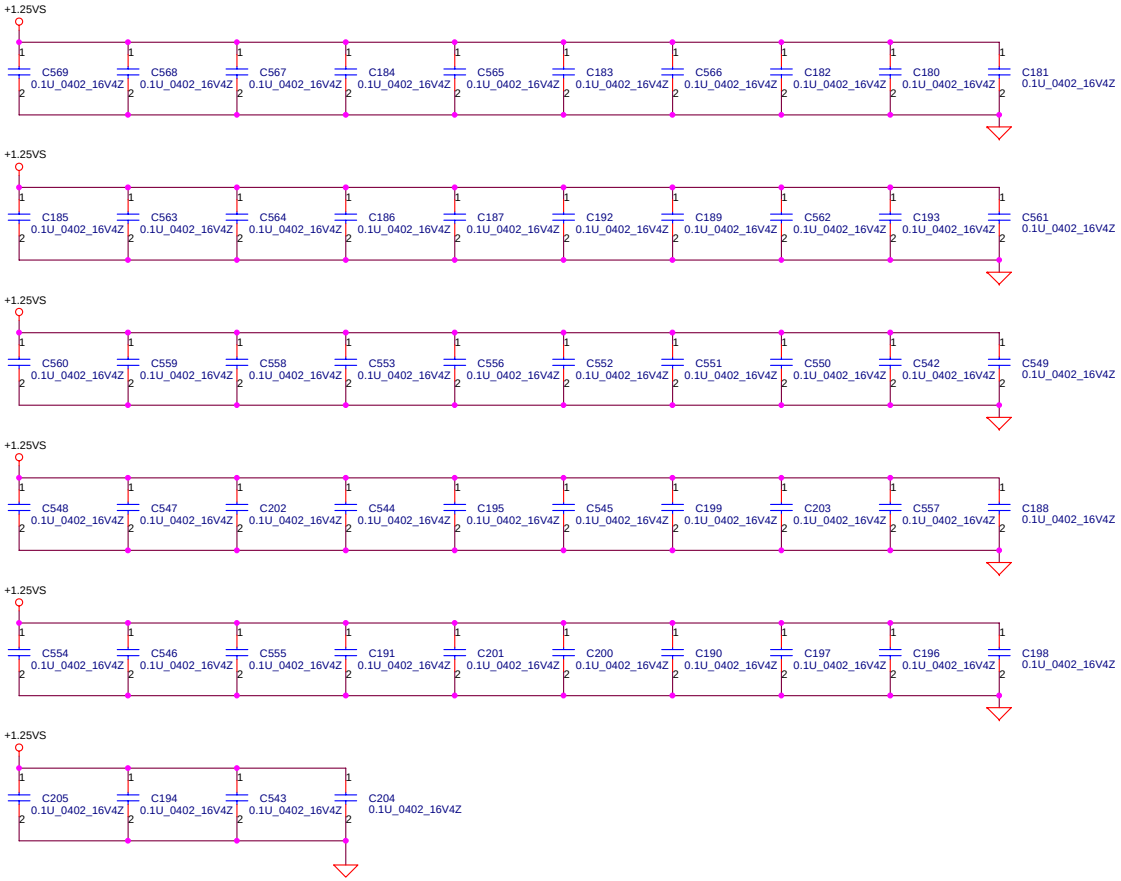
Layout note :

Distribute as close as possible to DDR-SODIMM.



Layout note :

Place one cap close to every 2 pull up resistors termination to +1.25V



Compal Electronics, Inc.		
Title: DDR SODIMM Decoupling		
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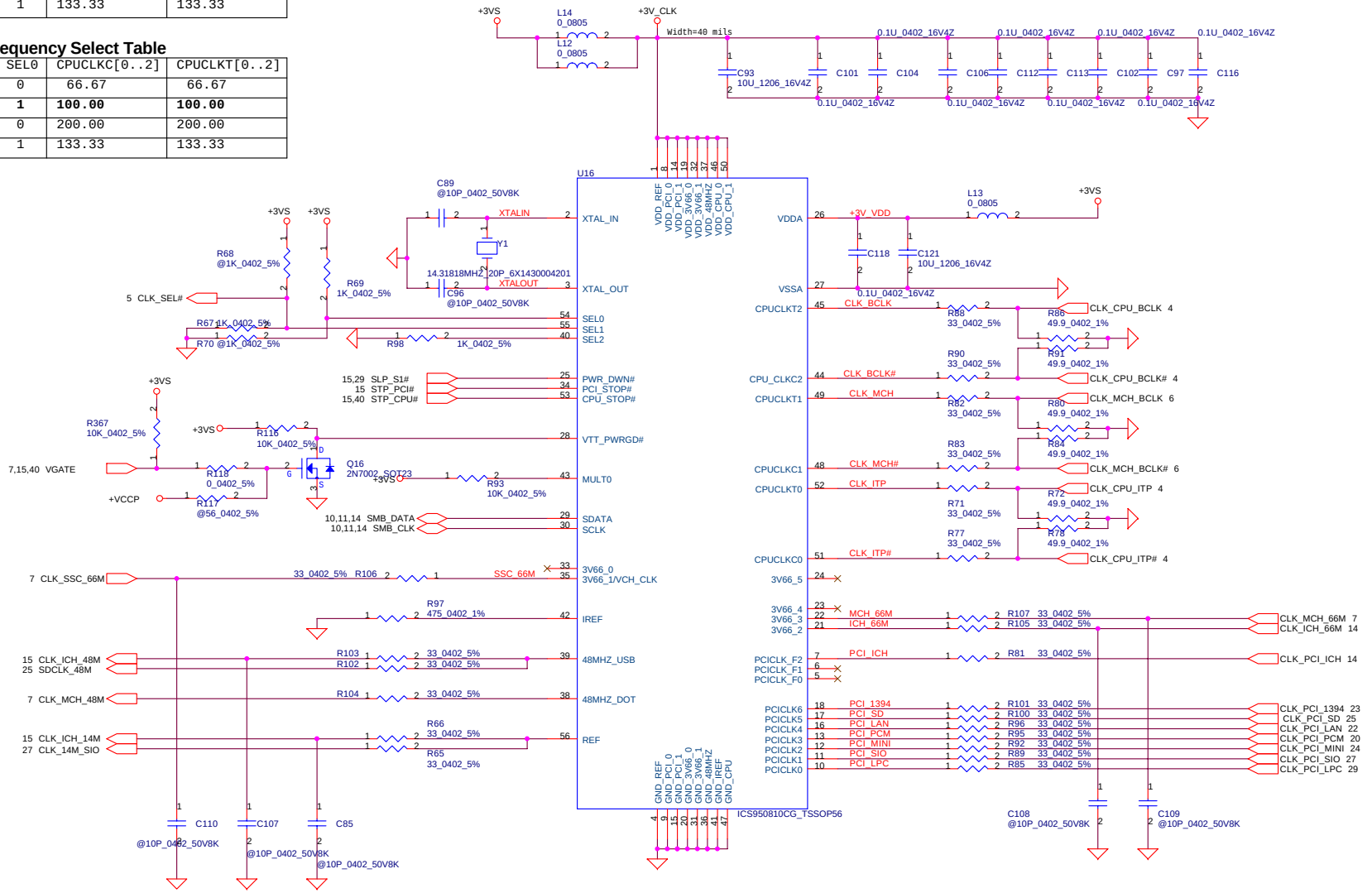
Clock Generator

ICS950810 Frequency Select Table

SEL2	SEL1	SEL0	CPUCLKC[0..2]	CPUCLKT[0..2]
0	0	0	166.67	166.67
0	**0**	**1**	**100.00**	**100.00**
0	1	0	200.00	200.00
0	1	1	133.33	133.33

CY28346-2 Frequency Select Table

SEL2	SEL1	SEL0	CPUCLKC[0..2]	CPUCLKT[0..2]
0	0	0	66.67	66.67
0	**0**	**1**	**100.00**	**100.00**
0	1	0	200.00	200.00
0	1	1	133.33	133.33



Compal Electronics, Inc.		
Title: Clock Generator		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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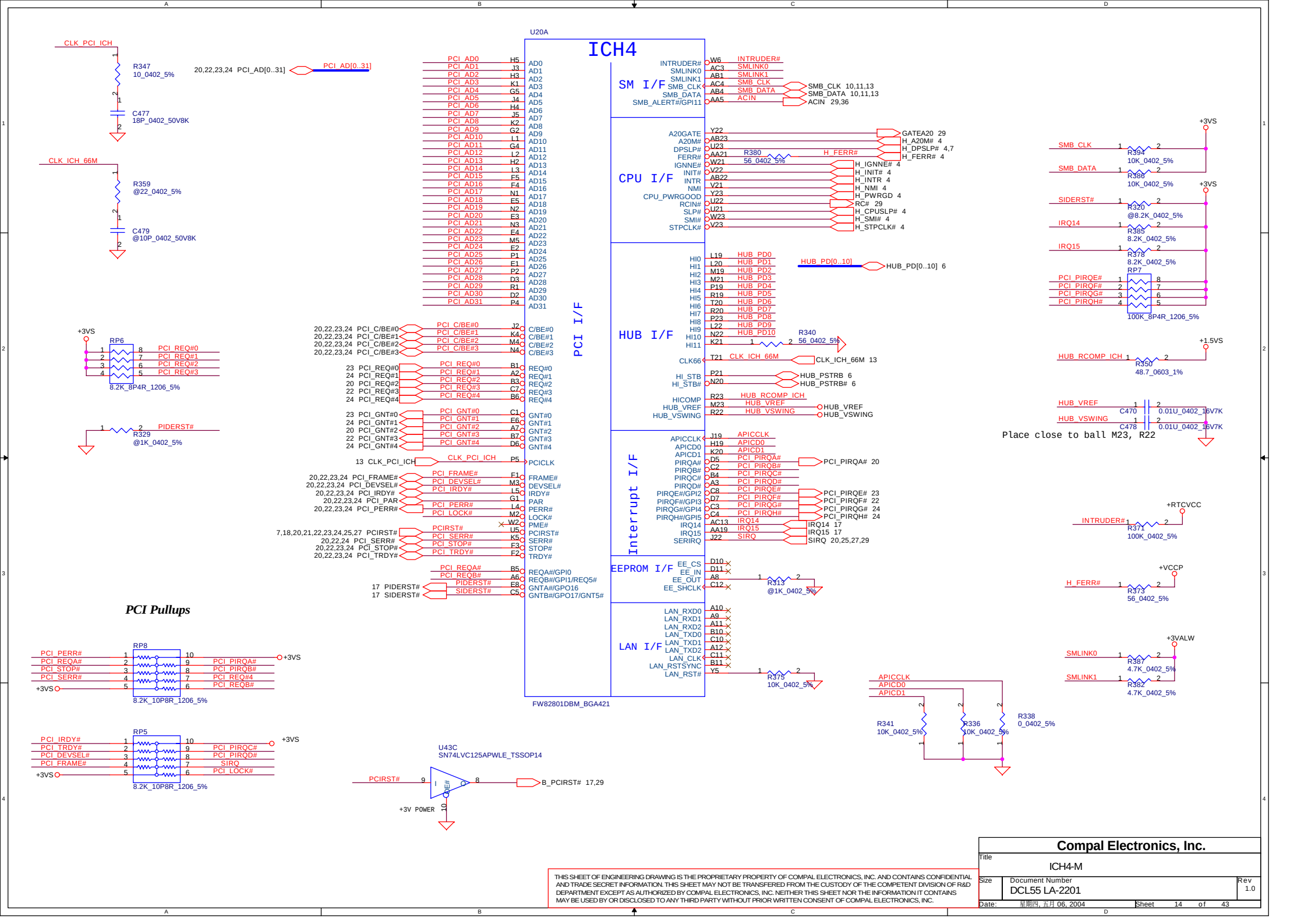

Compal Electronics, Inc.	
Title	ICH4-M
Size	Document Number: DCL55 LA-2201
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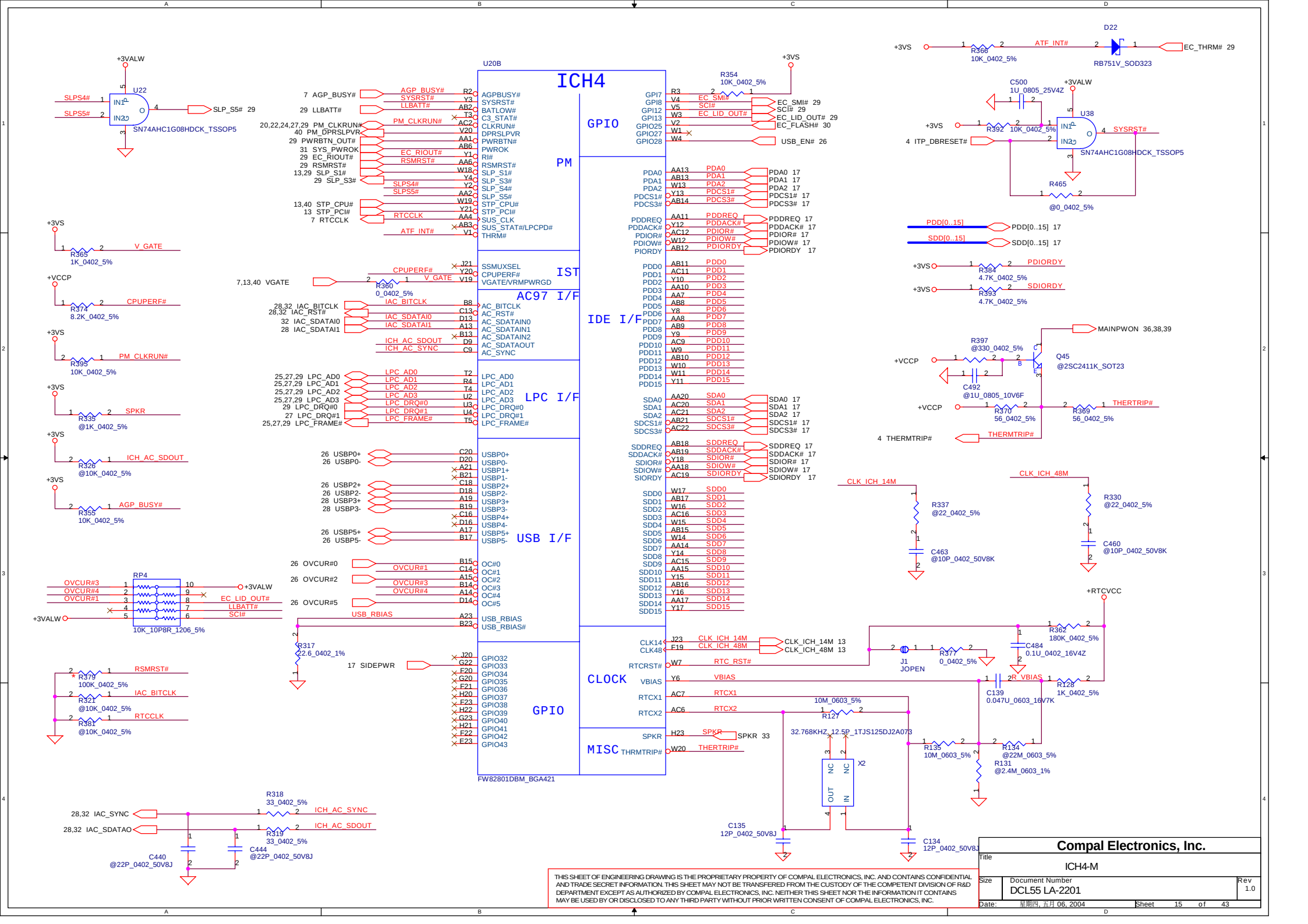
ICH4
U20B
GPIO
PM
IST
AC97 I/F
IDE I/F
LPC I/F
USB I/F
GPIO
CLOCK
MISC
FW82801DBM_BGA421
SN74AHC1G08HDCK_TSSOP5
RB751V_SOD323
RP4
10K_10P8R_1206_5%
32.768KHZ_12.5P_1TJS125DJ2A073
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ICH4-M
DCL55 LA-2201
Rev 1.0
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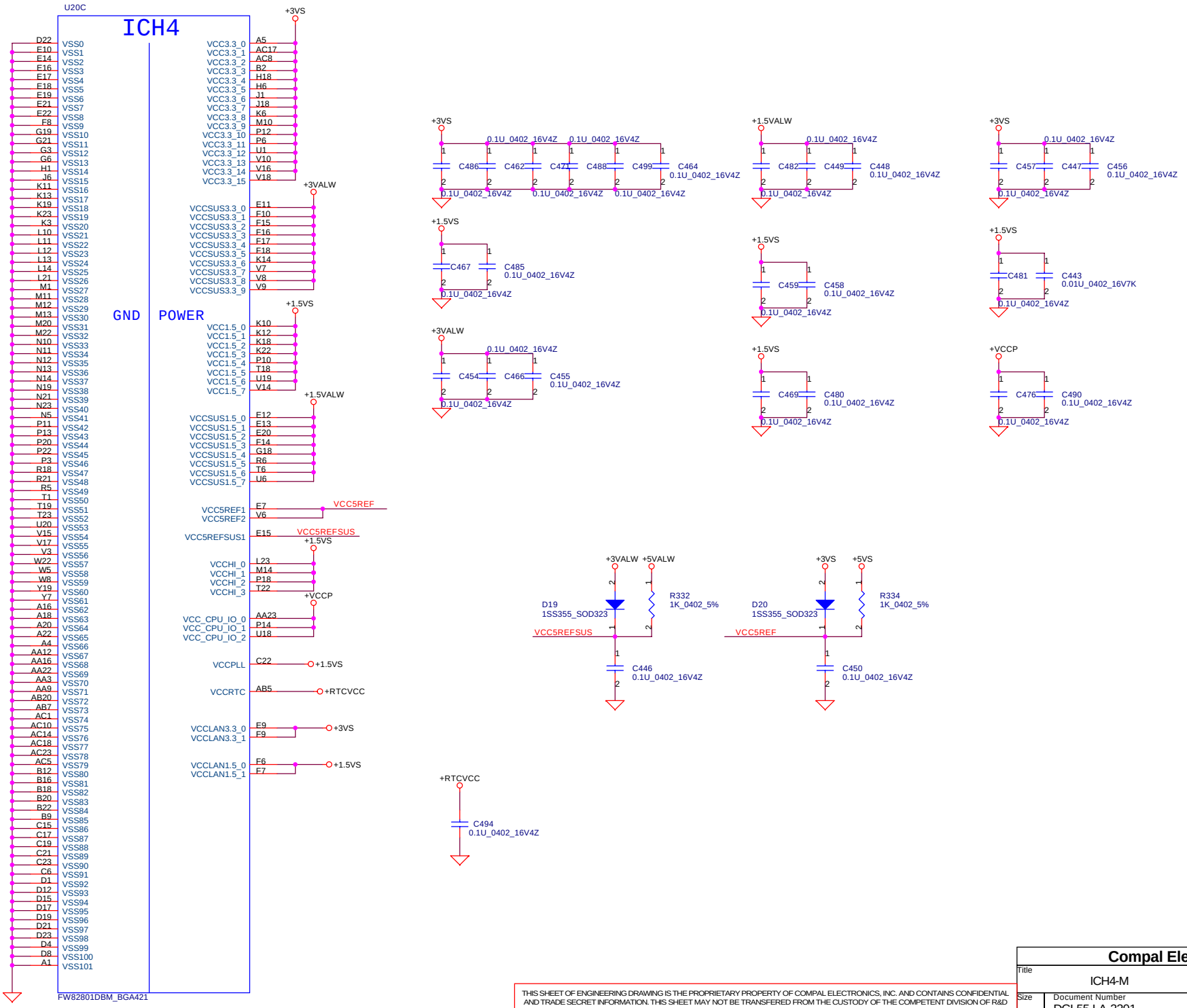

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Title	ICH4-M
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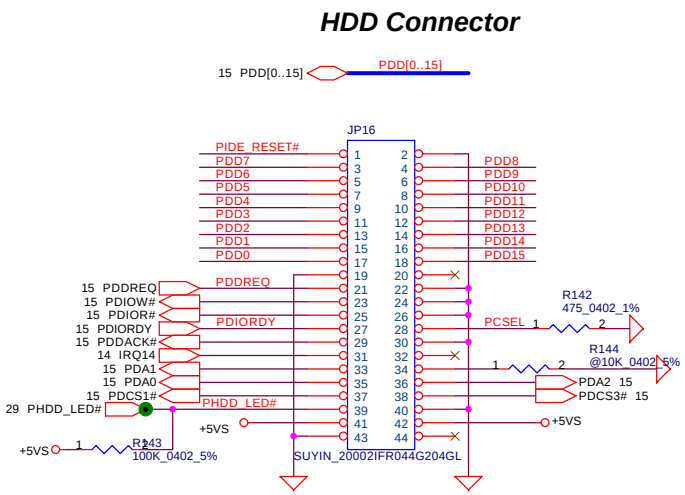
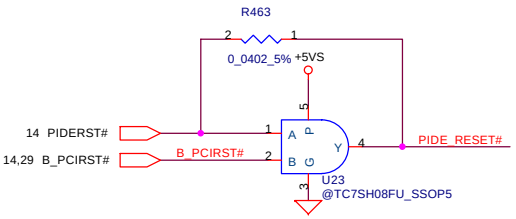
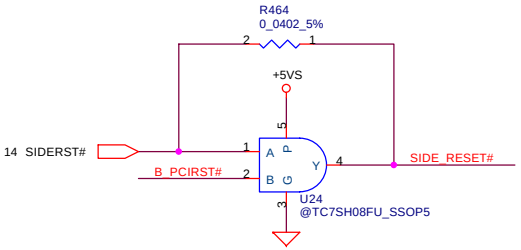
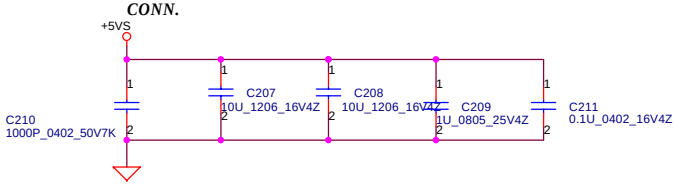
HDD Connector



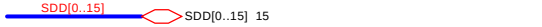


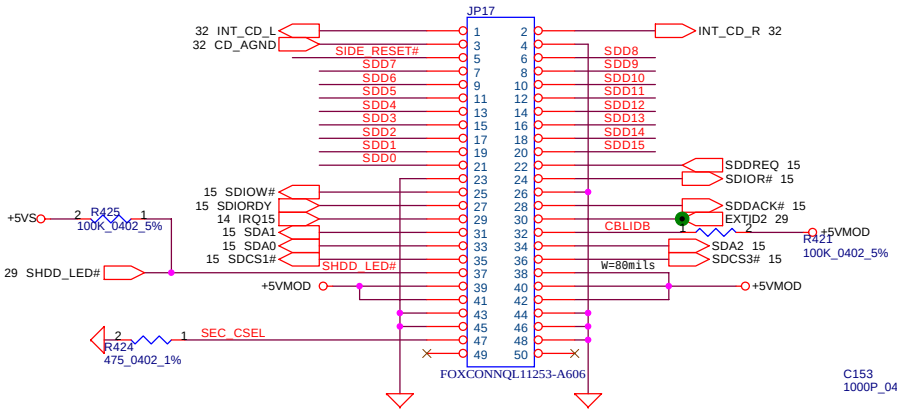


Placea caps. near HDD CONN.

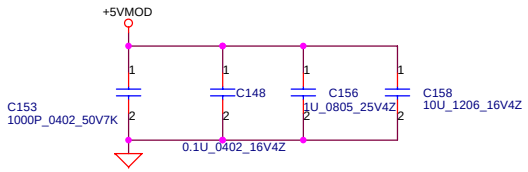


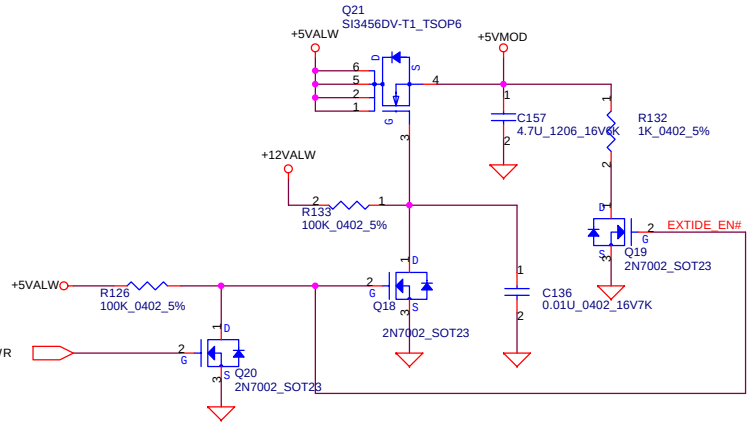
CD-ROM Connector





Placea caps. near CDROM CONN.





SI3456DV: N CHANNEL
VGS: 4.5V, RDS: 65 mOHM
Id(MAX): 5.1A
VGS,+-20V

Compal Electronics, Inc.		
Title: IDE/CD-ROM Module		
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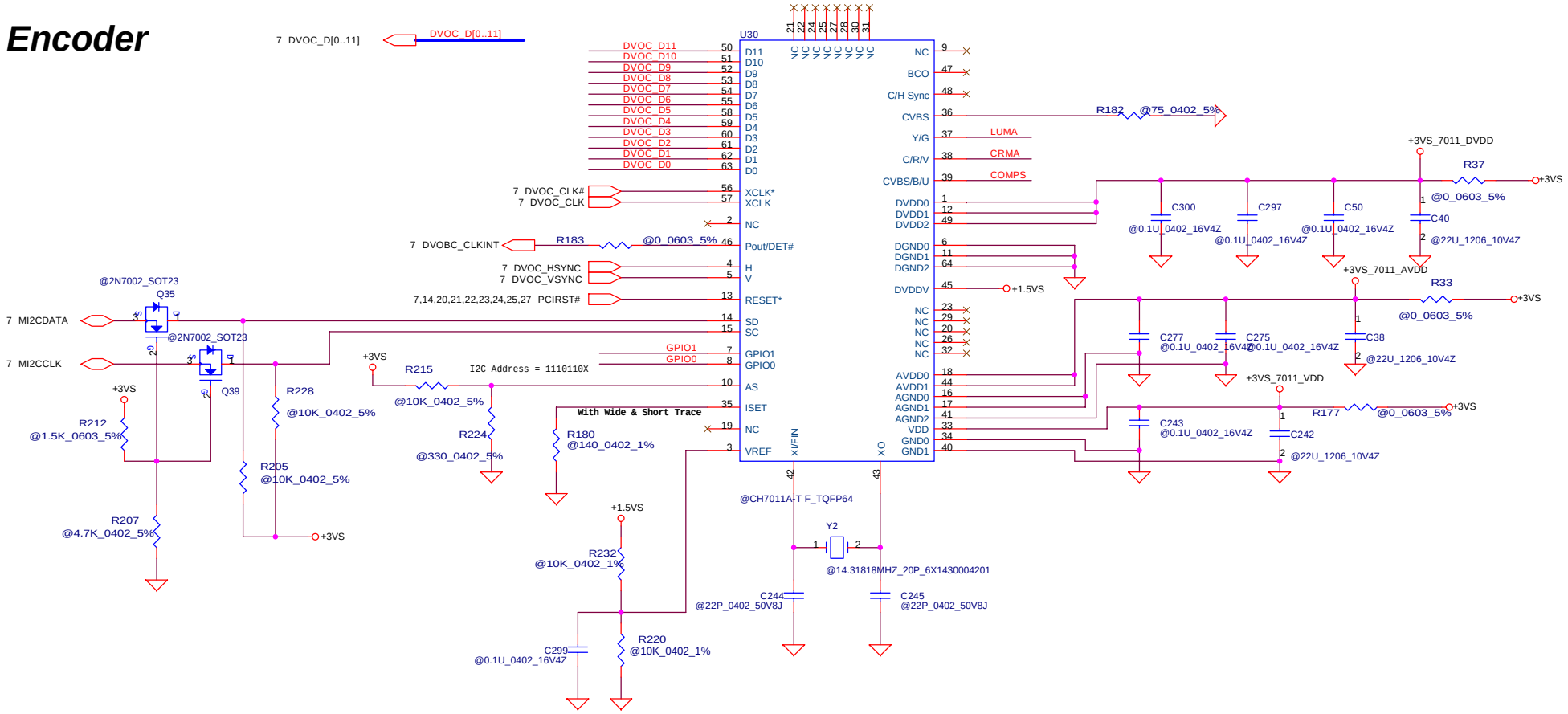
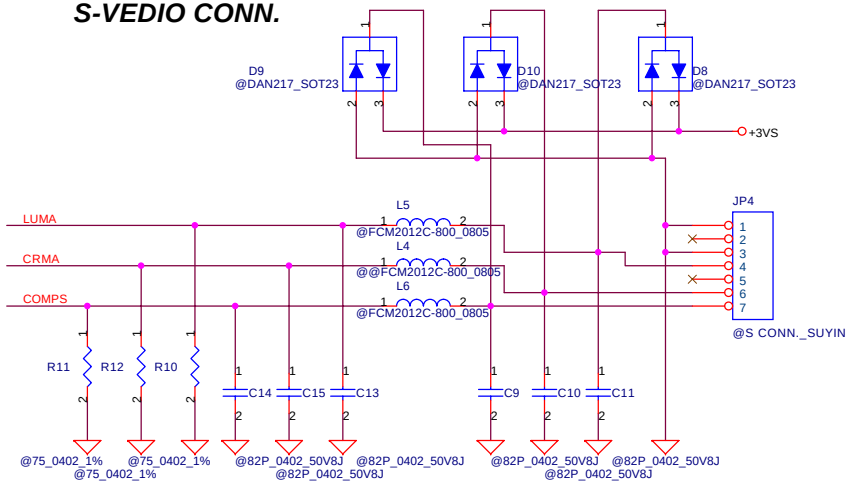
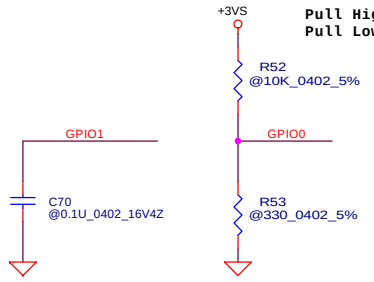
for ABO without TV_out:all this page component unmount

TV Encoder



S-VEDIO CONN.





Compal Electronics, Inc.		
Title: TV Encoder		
Size	Document Number: DCL55 LA-2201	Rev: 1.0
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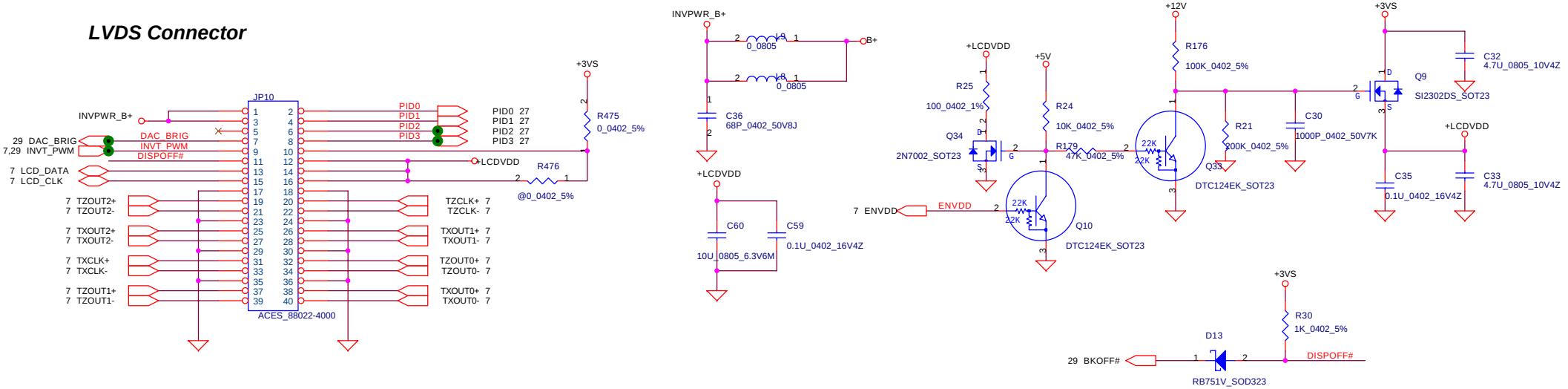
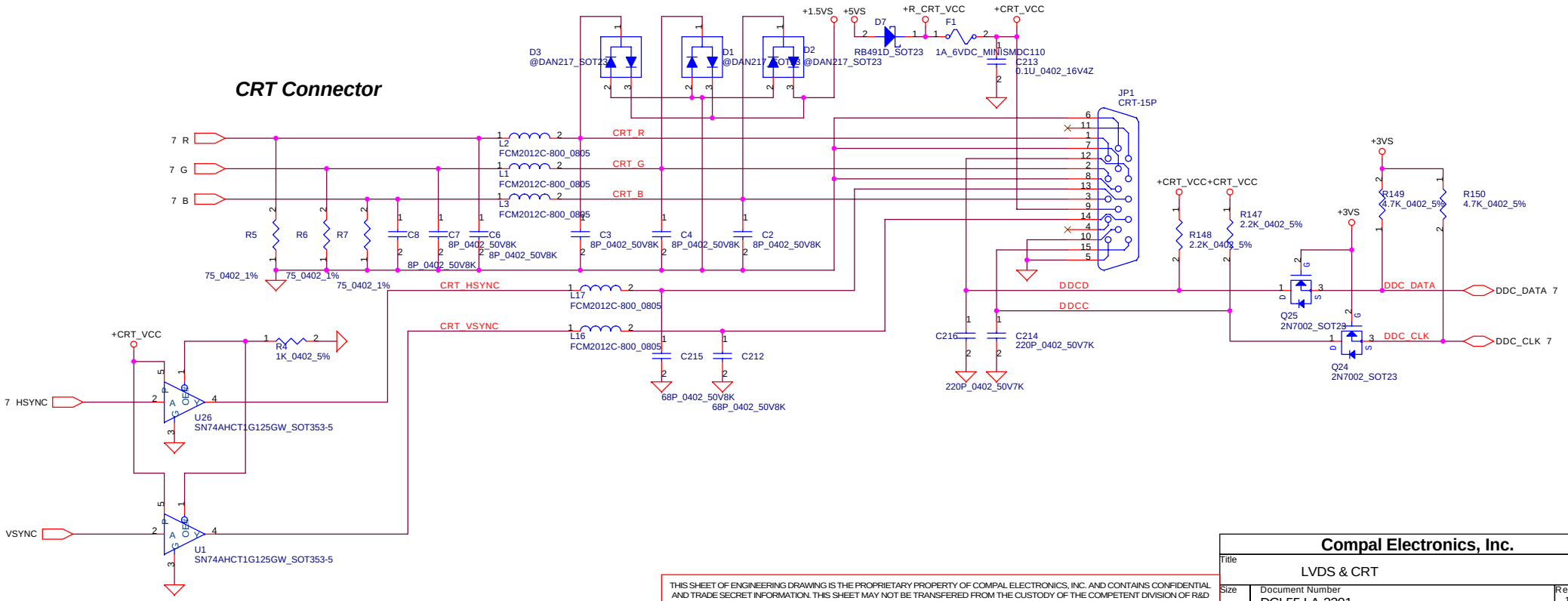
LVDS Connector



CRT Connector



Compal Electronics, Inc.	
Title	LVDS & CRT
Size	Document Number: DCL55 LA-2201
Rev	1.0
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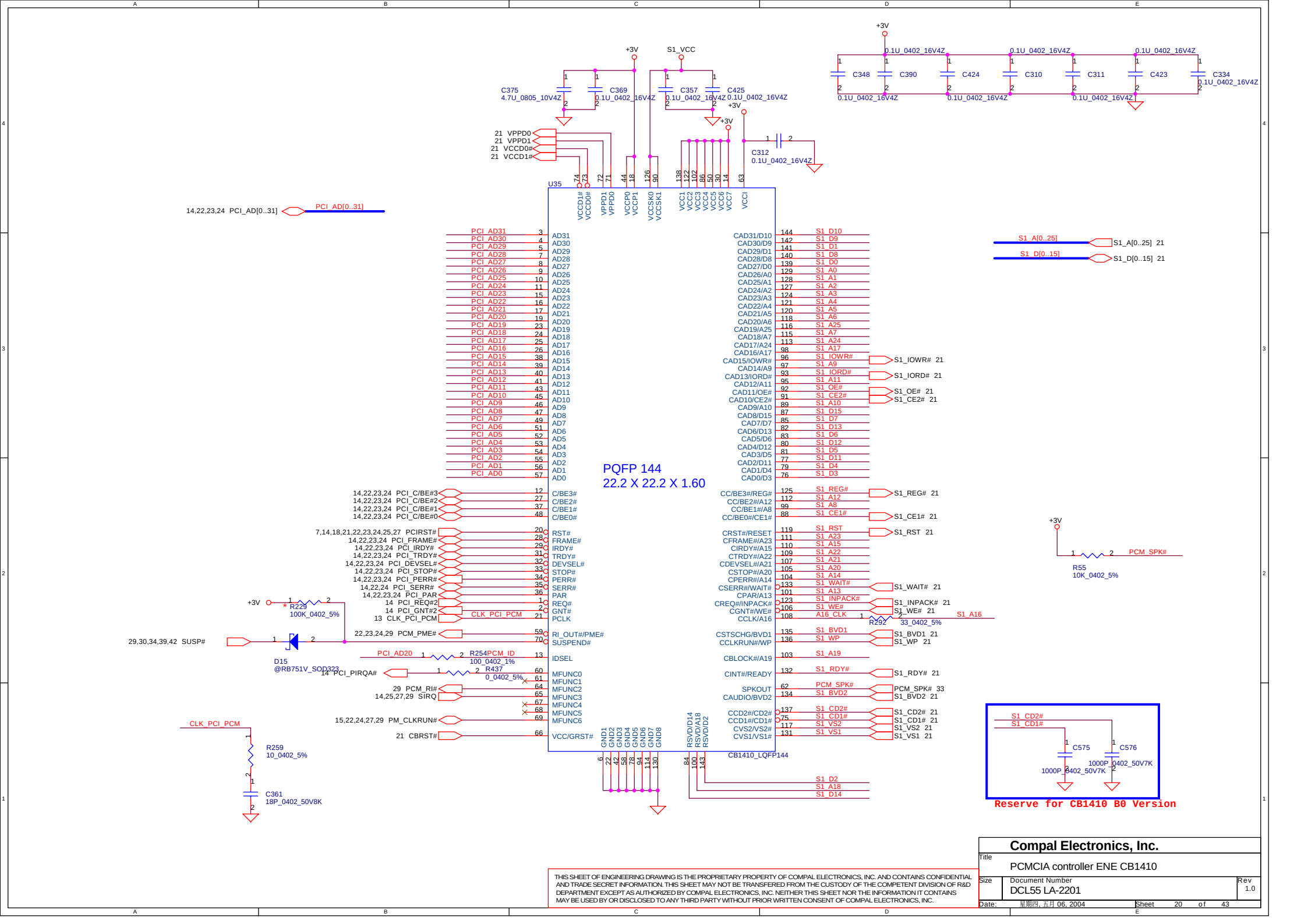
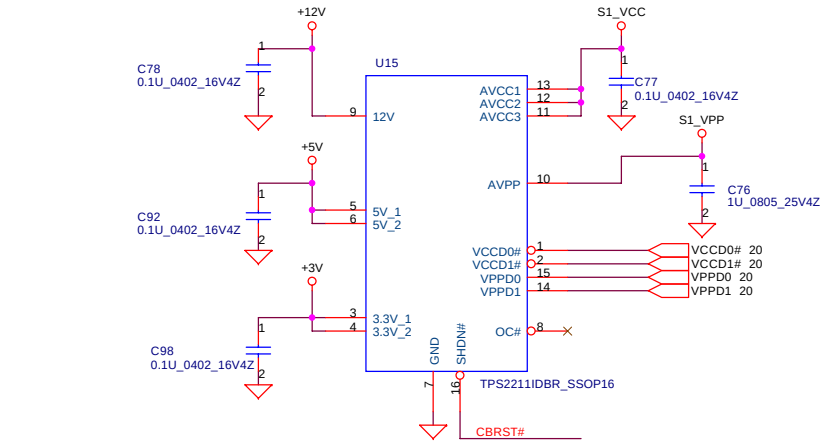
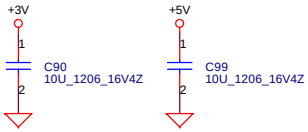


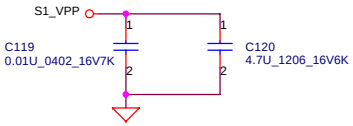
Compal Electronics, Inc.

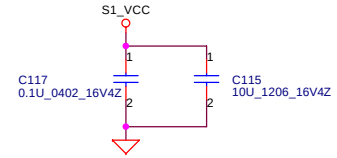
Title	PCMCIA controller ENE CB1410	
Size	Document Number DCL55 LA-2201	Rev 1.0
Date: 星期四, 五月 06, 2004	Sheet 20 of 43	

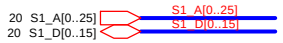
PCMCIA Power Controller



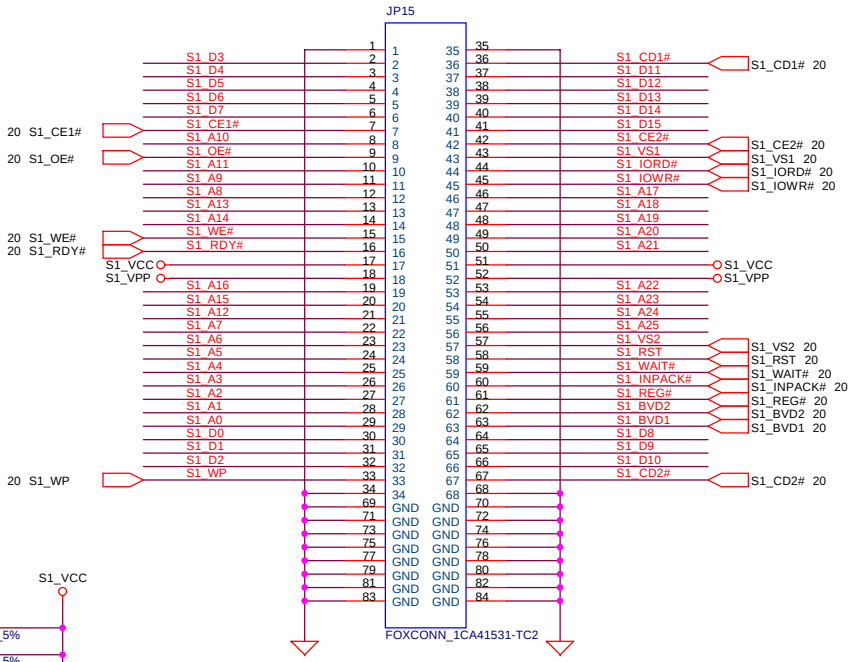


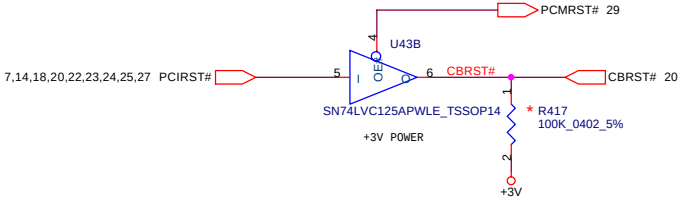


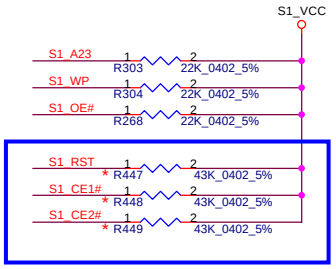




CardBus Socket







Reserve for CB1410 B0 Version

Compal Electronics, Inc.		
Title: CardBus Socket		
Size	Document Number: DCL55 LA-2201	Rev: 1.0
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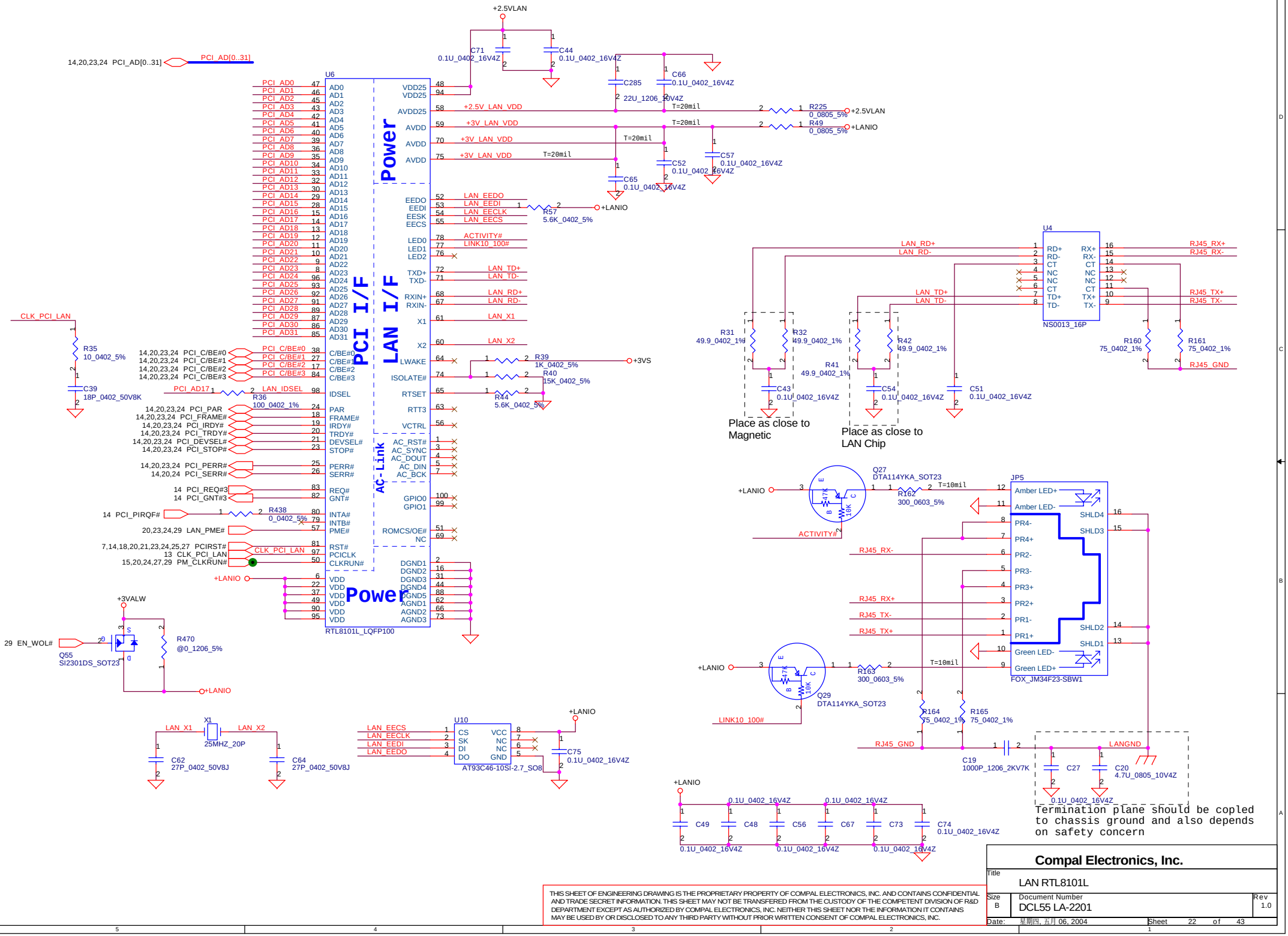
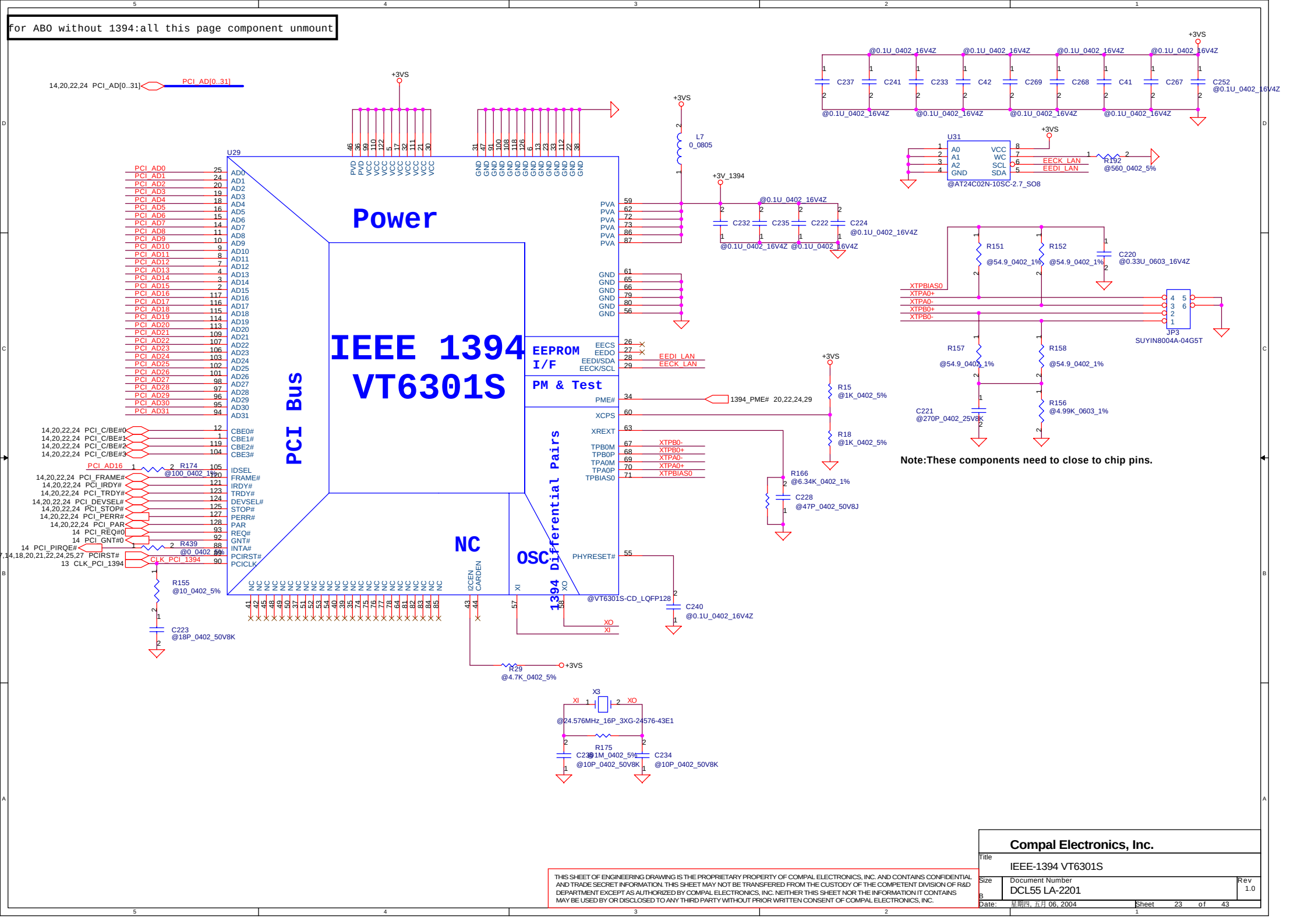


Compal Electronics, Inc.

Title	LAN RTL8101L	
Size B	Document Number DCL55 LA-2201	Rev 1.0
Date: 星期四, 五月 06, 2004	Sheet 22 of 43	

for ABO without 1394:all this page component unmount



Compal Electronics, Inc.		
Title: IEEE-1394 VT6301S		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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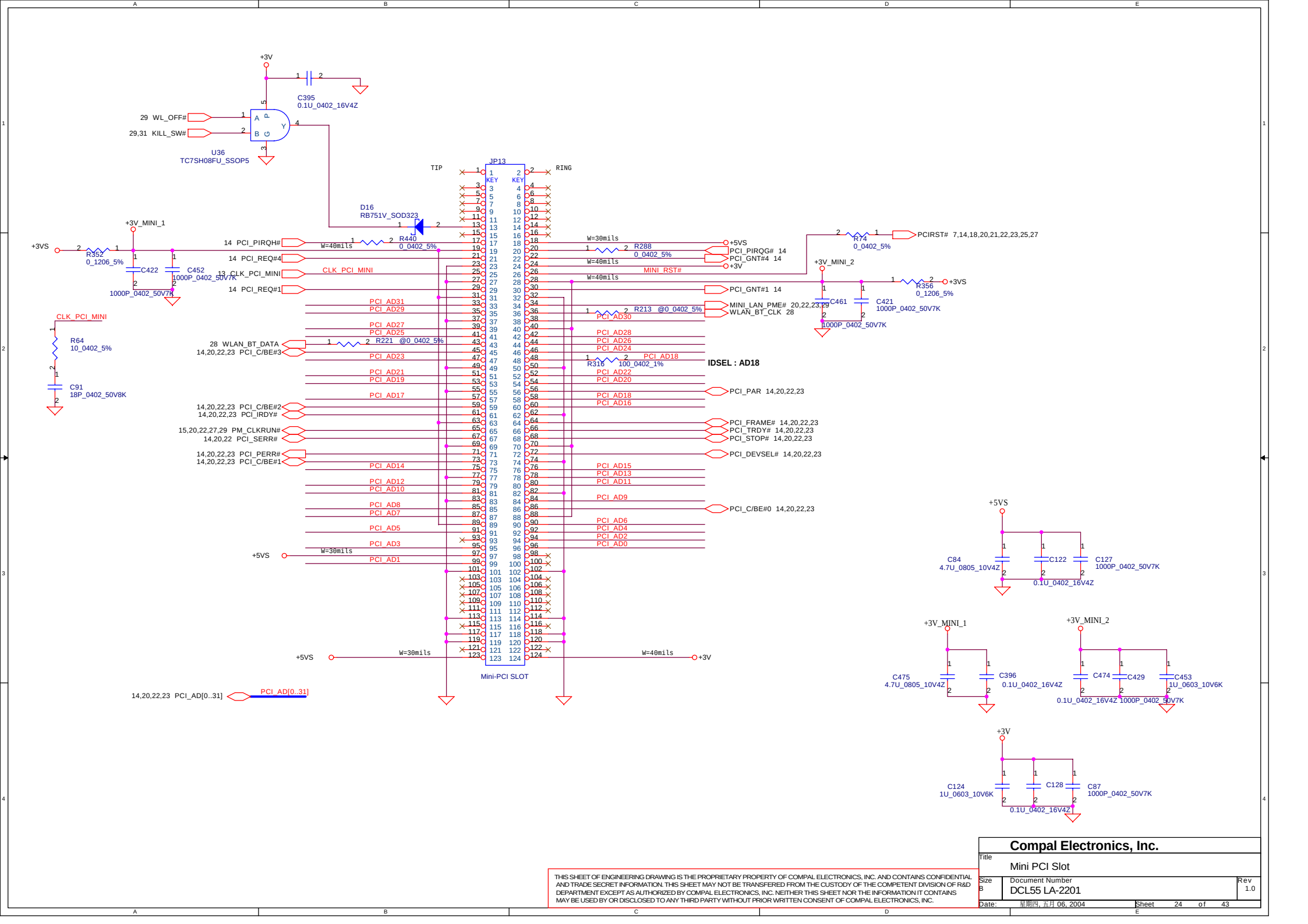

Compal Electronics, Inc.

Title	Mini PCI Slot	
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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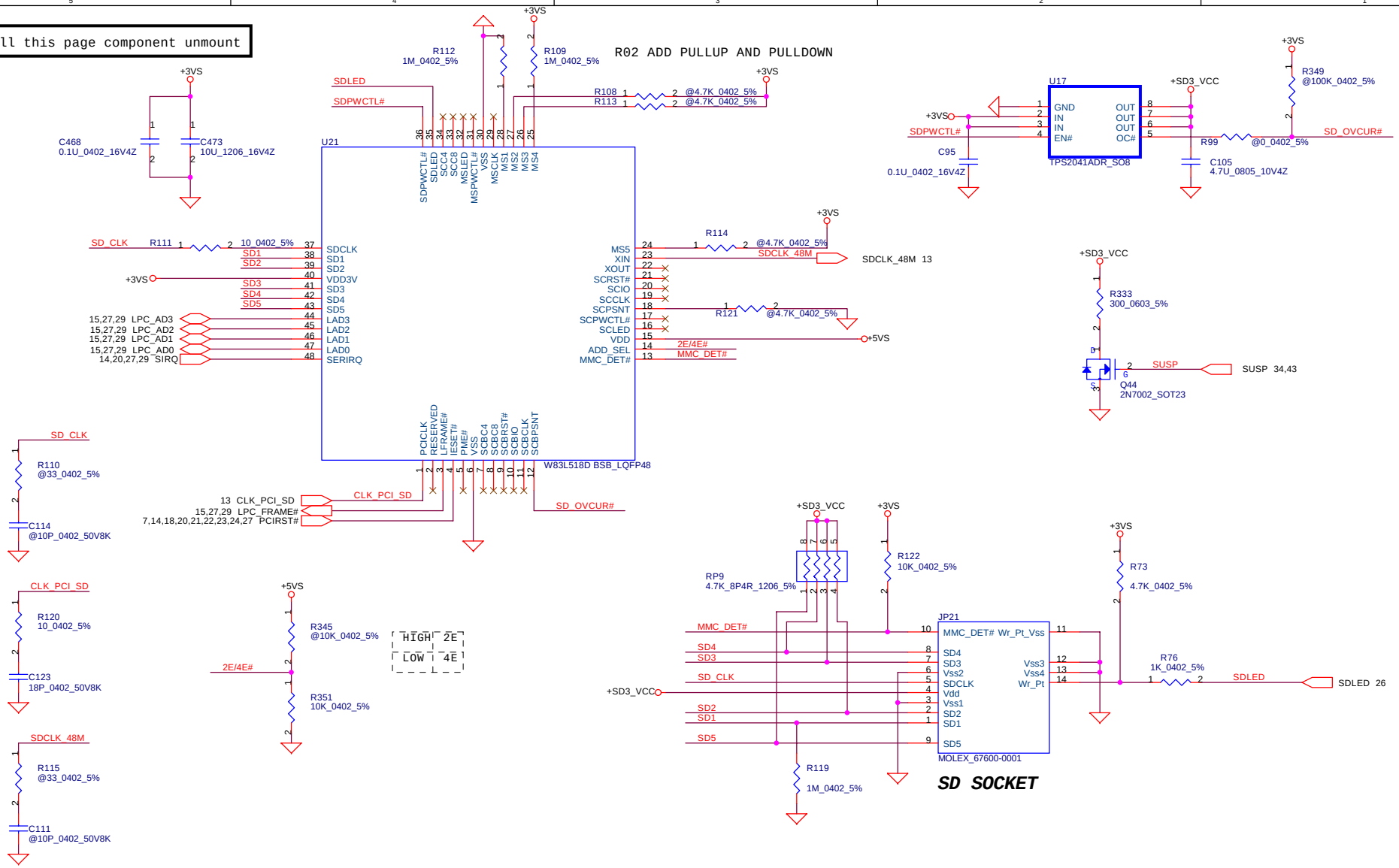
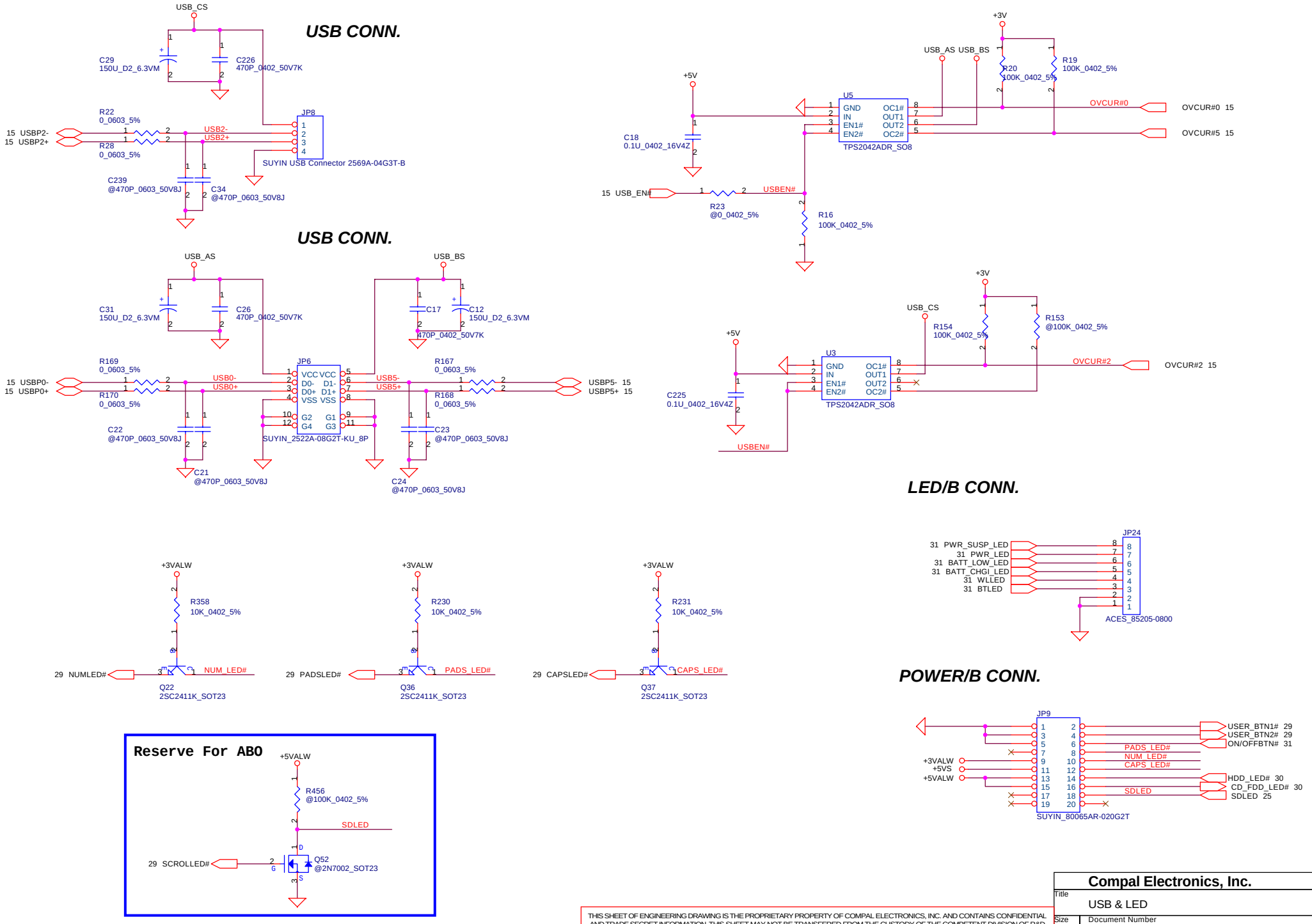
FOR ABO: all this page component unmount

R02 ADD PULLUP AND PULLDOWN



Compal Electronics, Inc.

Title	SD CARD Controller/Socket	
Size B	Document Number DCL55 LA-2201	Rev 1.0
Date: 星期四, 五月 06, 2004	Sheet 25 of 43	



Compal Electronics, Inc.		
Title: USB & LED		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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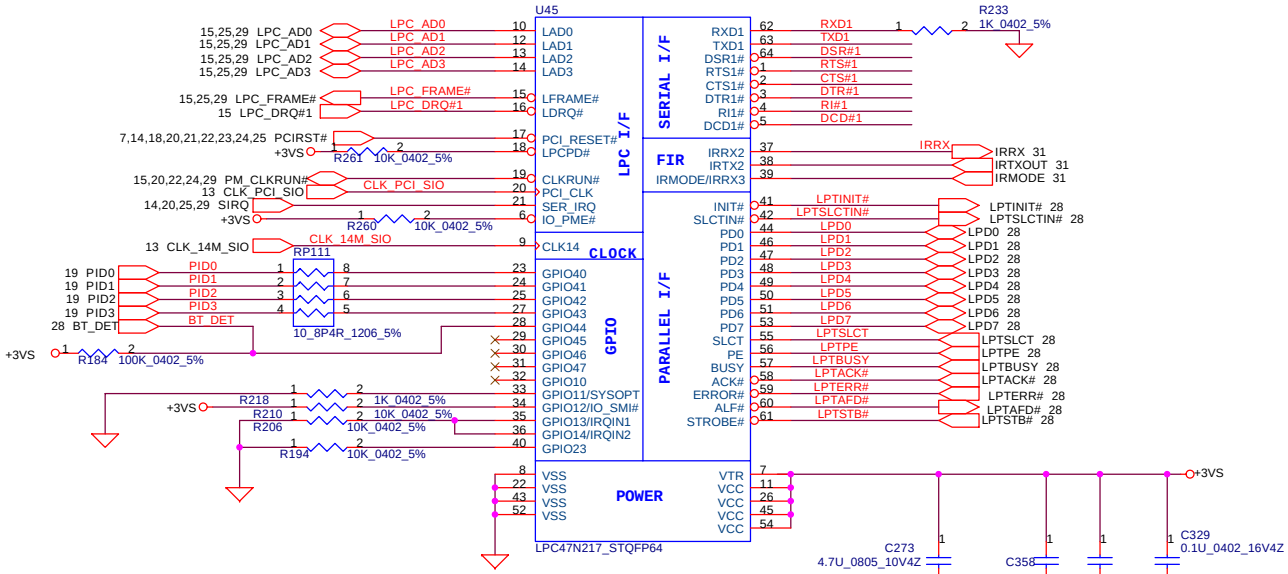
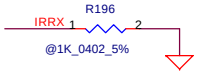
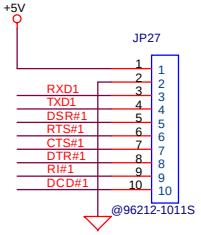
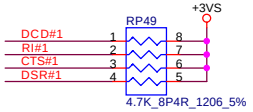
SUPER I/O SMsC FDC47N227

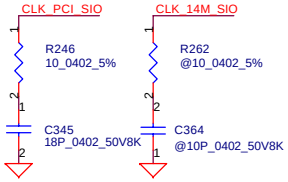


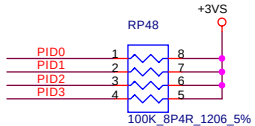
Base I/O Address
*** 0 = 02Eh**
1 = 04Eh











Compal Electronics, Inc.

Title	SUPER I/O	
Size B	Document Number DCL55 LA-2201	Rev 1.0
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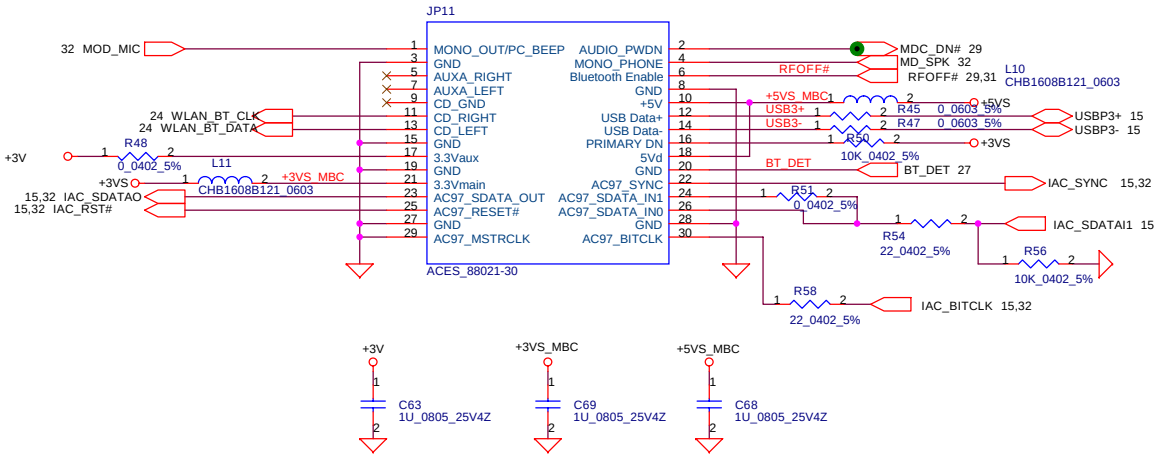
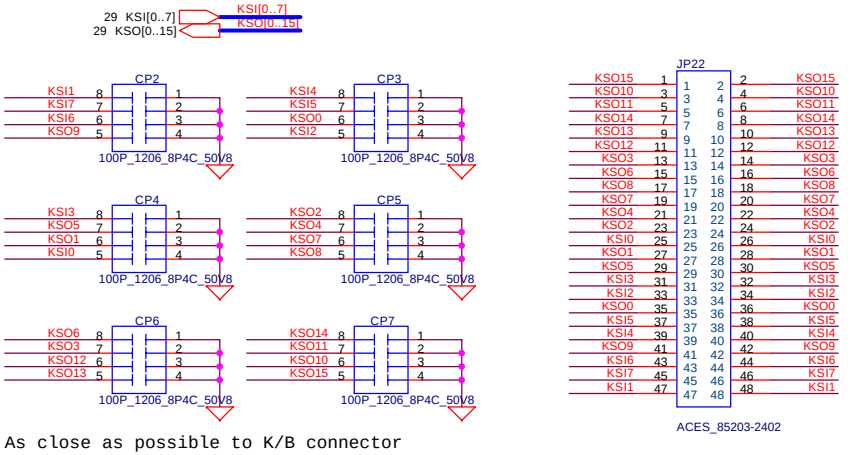
MBC CONN.

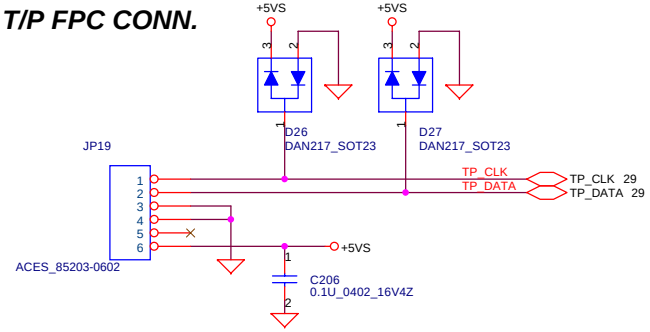


INT_KBD CONN.

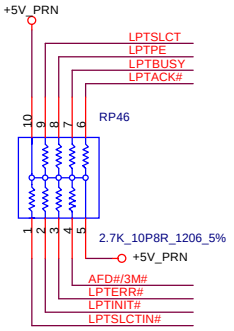


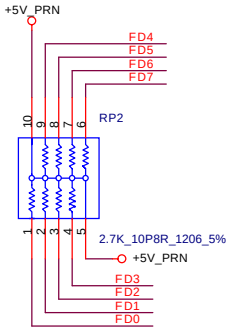
As close as possible to K/B connector

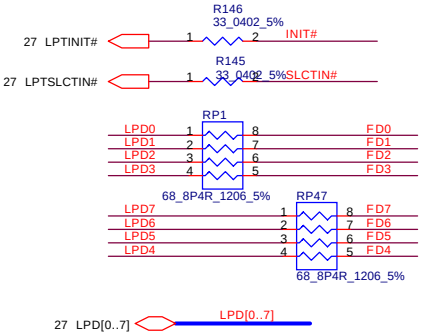
T/P FPC CONN.

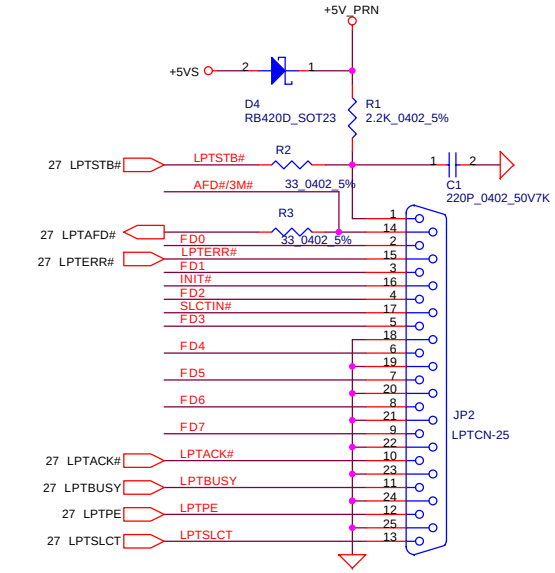


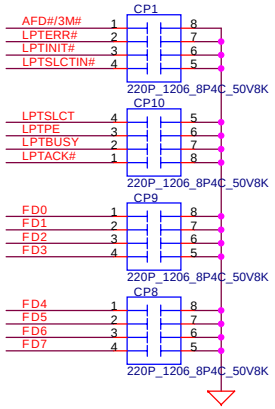
PARALLEL PORT











Compal Electronics, Inc.

Title	PARALLEL/MDC PORT	
Size	Document Number DCL55 LA-2201	Rev 1.0
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KB910 B1 mount R458, B2 mount R457

option for KB910 and LPC87591L
default is KB910

For EC Tools

RESERVE FOR KB3910

	I/O Address	
BADDR1(KBA3) BADDR0(KBA2)	Index	Data
0 0	2E	2F
0 1	4E	4F
* 1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1 1	Reserved	

	ENV0 (KBA0)	ENV1 (KBA1)	TRIS (KBA4)
IRE	0	0	0
OBD	0	1	0
DEV	1	0	0
PROG	1	1	0

SHBM(KBA5)=1: Enable shared memory with host BIOS
TRIS(KBA4)=1: While in IRE and OBD, float all the signals for clip-on ISE use

	KB910	87591
R294	0	120K
R295	no stuff	20M

Compal Electronics, Inc.

Title: EC KB910

Size: Custom | Document Number: DCL55 LA-2201 | Rev: 1.0

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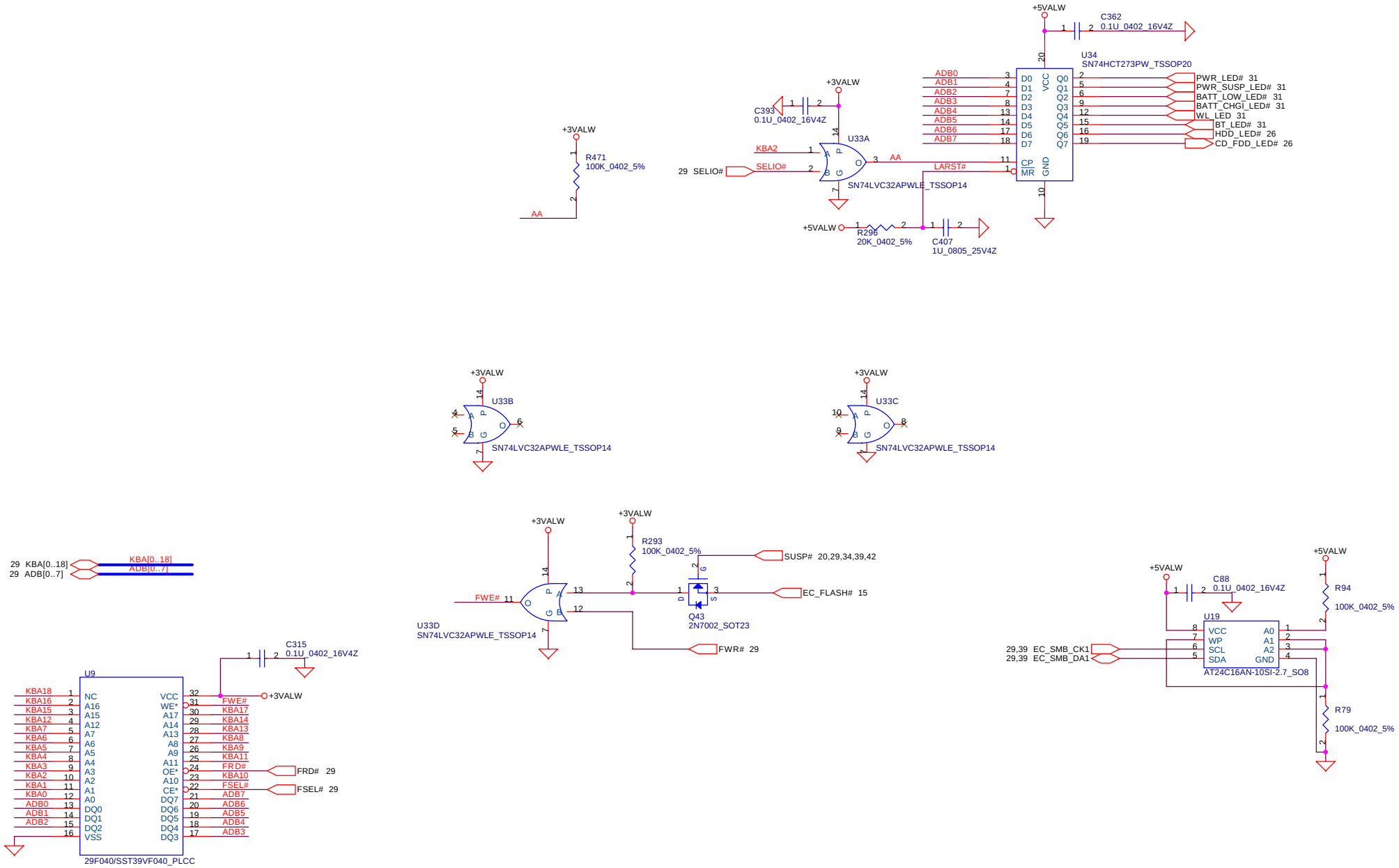

Compal Electronics, Inc.

Title: BIOS & EXT. I/O PORT

Size	Document Number	Rev
B	DCL55 LA-2201	1.0

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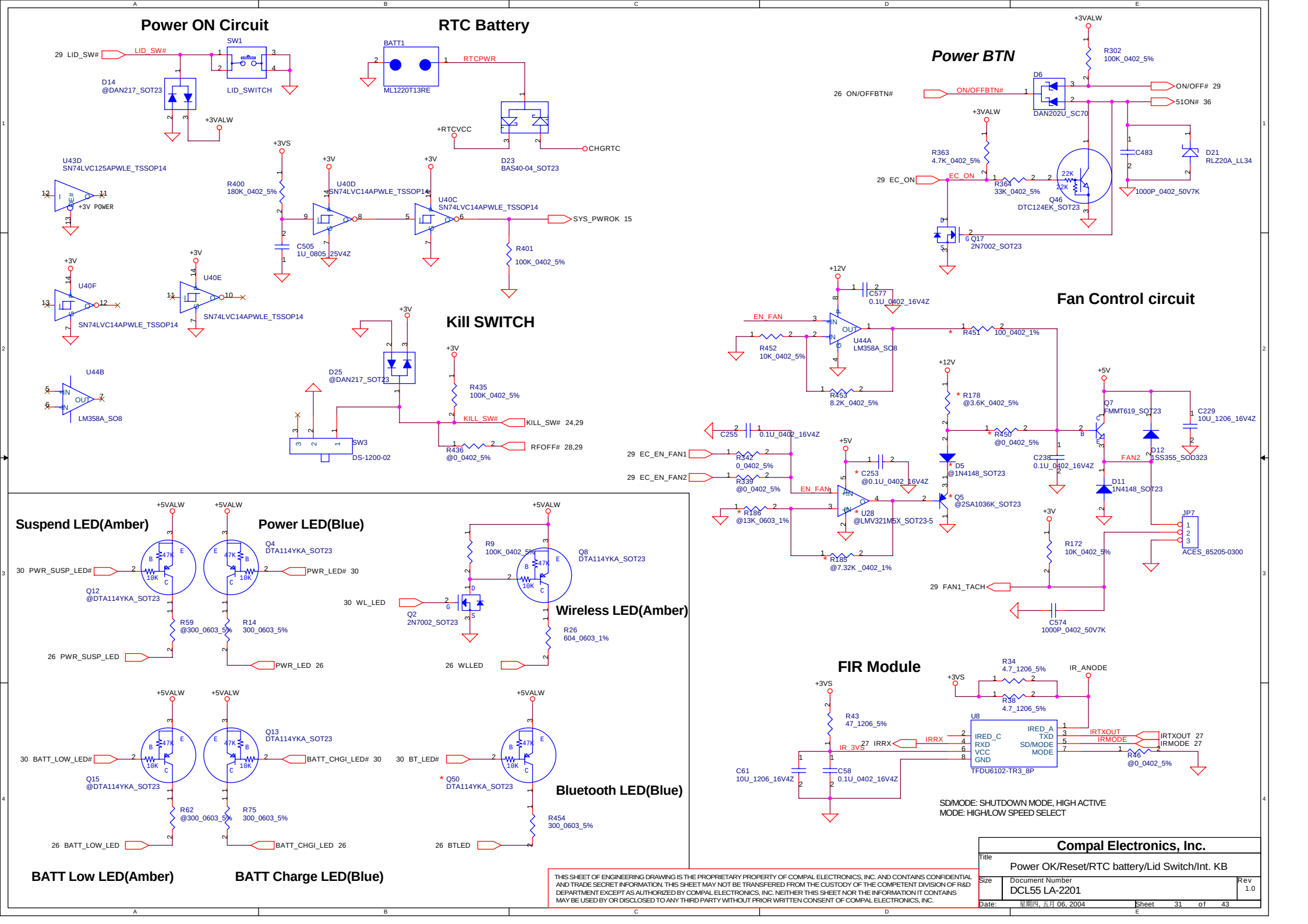
Power ON Circuit
RTC Battery
Power BTN
Kill SWITCH
Fan Control circuit
Suspend LED(Amber)
Power LED(Blue)
Wireless LED(Amber)
Bluetooth LED(Blue)
BATT Low LED(Amber)
BATT Charge LED(Blue)
FIR Module
SW1
LID_SW#
LID_SWITCH
D14
@DAN217_SOT23
BATT1
ML1220T13RE
RTCPWR
+RTCVCC
CHGRTC
D23
BAS40-04_SOT23
U43D
SN74LVC125APWLE_TSSOP14
+3V POWER
R400
180K_0402_5%
U40D
SN74LVC14APWLE_TSSOP14
U40C
SN74LVC14APWLE_TSSOP14
SYS_PWROK 15
C505
1U_0805_25V4Z
R401
100K_0402_5%
U40F
U40E
SN74LVC14APWLE_TSSOP14
U44B
LM358A_SO8
D25
@DAN217_SOT23
SW3
DS-1200-02
R435
100K_0402_5%
KILL_SW# 24,29
R436
@0_0402_5%
RFOFF# 28,29
R302
100K_0402_5%
D6
DAN202U_SC70
ON/OFFBTN#
ON/OFF# 29
51ON# 36
R363
4.7K_0402_5%
EC_ON
R364
33K_0402_5%
Q46
DTC124EK_SOT23
C483
1000P_0402_50V7K
D21
RLZ20A_LL34
Q17
2N7002_SOT23
C577
0.1U_0402_16V4Z
EN_FAN
U44A
LM358A_SO8
R451
100_0402_1%
R452
10K_0402_5%
R453
8.2K_0402_5%
R178
@3.6K_0402_5%
R450
@0_0402_5%
Q7
FMMT619_SOT23
C229
10U_1206_16V4Z
C255
0.1U_0402_16V4Z
R342
0_0402_5%
R339
@0_0402_5%
C253
@0.1U_0402_16V4Z
D5
@1N4148_SOT23
C238
0.1U_0402_16V4Z
FAN2
D12
1SS355_SOD323
D11
1N4148_SOT23
Q5
@2SA1036K_SOT23
R188
@13K_0603_1%
U28
@LMV321M5X_SOT23-5
R185
@7.32K _0402_1%
R172
10K_0402_5%
JP7
ACES_85205-0300
C574
1000P_0402_50V7K
29 EC_EN_FAN1
29 EC_EN_FAN2
29 FAN1_TACH
26 ON/OFFBTN#
29 EC_ON
30 PWR_SUSP_LED#
Q12
@DTA114YKA_SOT23
Q4
DTA114YKA_SOT23
R59
@300_0603_5%
R14
300_0603_5%
26 PWR_SUSP_LED
PWR_LED# 30
PWR_LED 26
30 WL_LED
Q2
2N7002_SOT23
R9
100K_0402_5%
Q8
DTA114YKA_SOT23
R26
604_0603_1%
26 WLLED
30 BATT_LOW_LED#
Q15
@DTA114YKA_SOT23
Q13
DTA114YKA_SOT23
R62
@300_0603_5%
R75
300_0603_5%
26 BATT_LOW_LED
BATT_CHGI_LED# 30
BATT_CHGI_LED 26
30 BT_LED#
Q50
DTA114YKA_SOT23
R454
300_0603_5%
26 BTLED
R34
4.7_1206_5%
R38
4.7_1206_5%
IR_ANODE
R43
47_1206_5%
U8
TFDU6102-TR3_8P
IRRX
IR_3VS
IRTXOUT
IRMODE
IRTXOUT 27
IRMODE 27
R46
@0_0402_5%
C61
10U_1206_16V4Z
C58
0.1U_0402_16V4Z
SD/MODE: SHUTDOWN MODE, HIGH ACTIVE
MODE: HIGH/LOW SPEED SELECT
Compal Electronics, Inc.
Power OK/Reset/RTC battery/Lid Switch/Int. KB
DCL55 LA-2201
Rev 1.0
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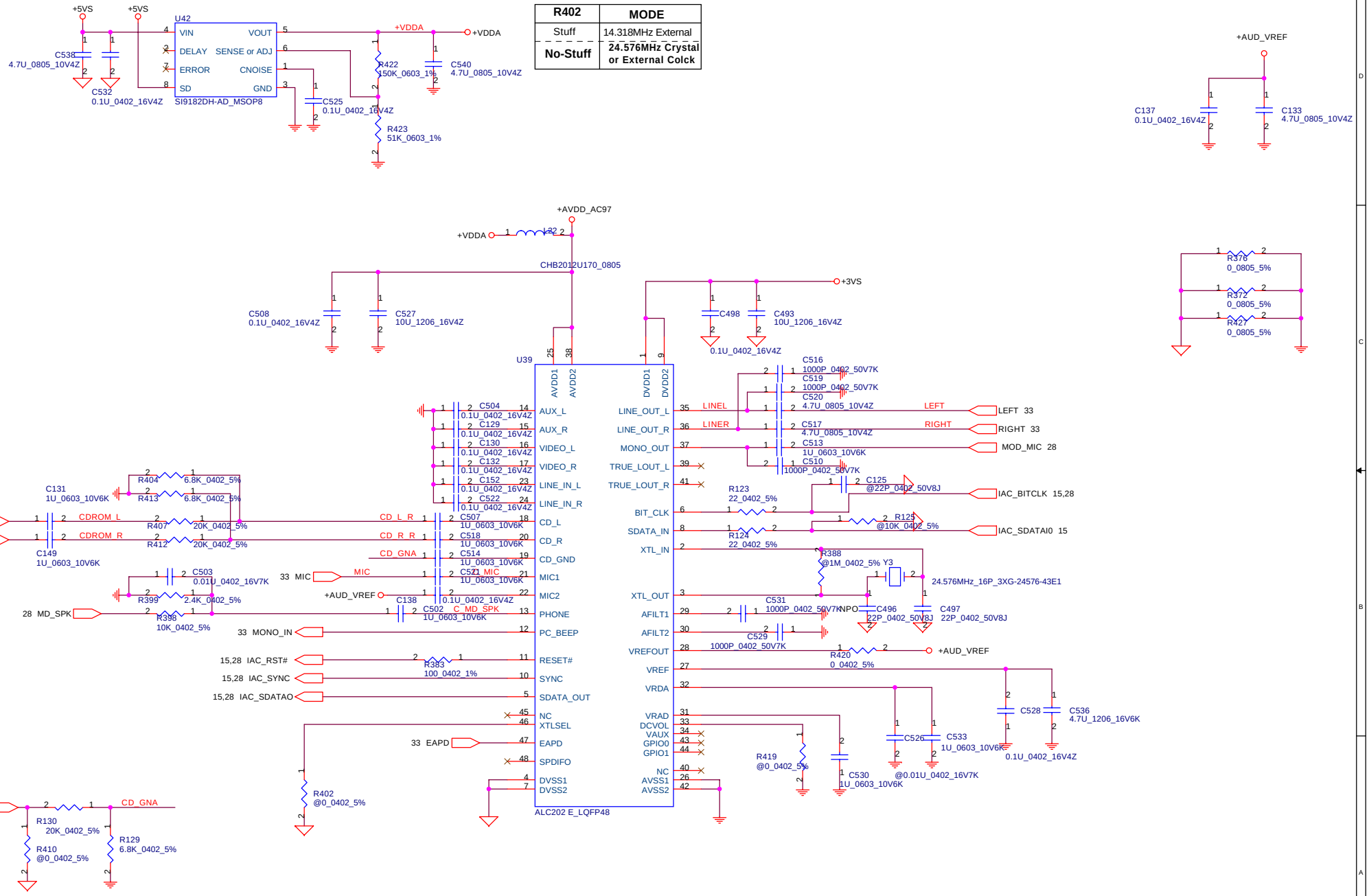
AC97 Codec



R402	MODE
Stuff	14.318MHz External
No-Stuff	24.576MHz Crystal or External Colck

Compal Electronics, Inc.

Title	AC97 Codec	
Size B	Document Number DCL55 LA-2201	Rev 1.0
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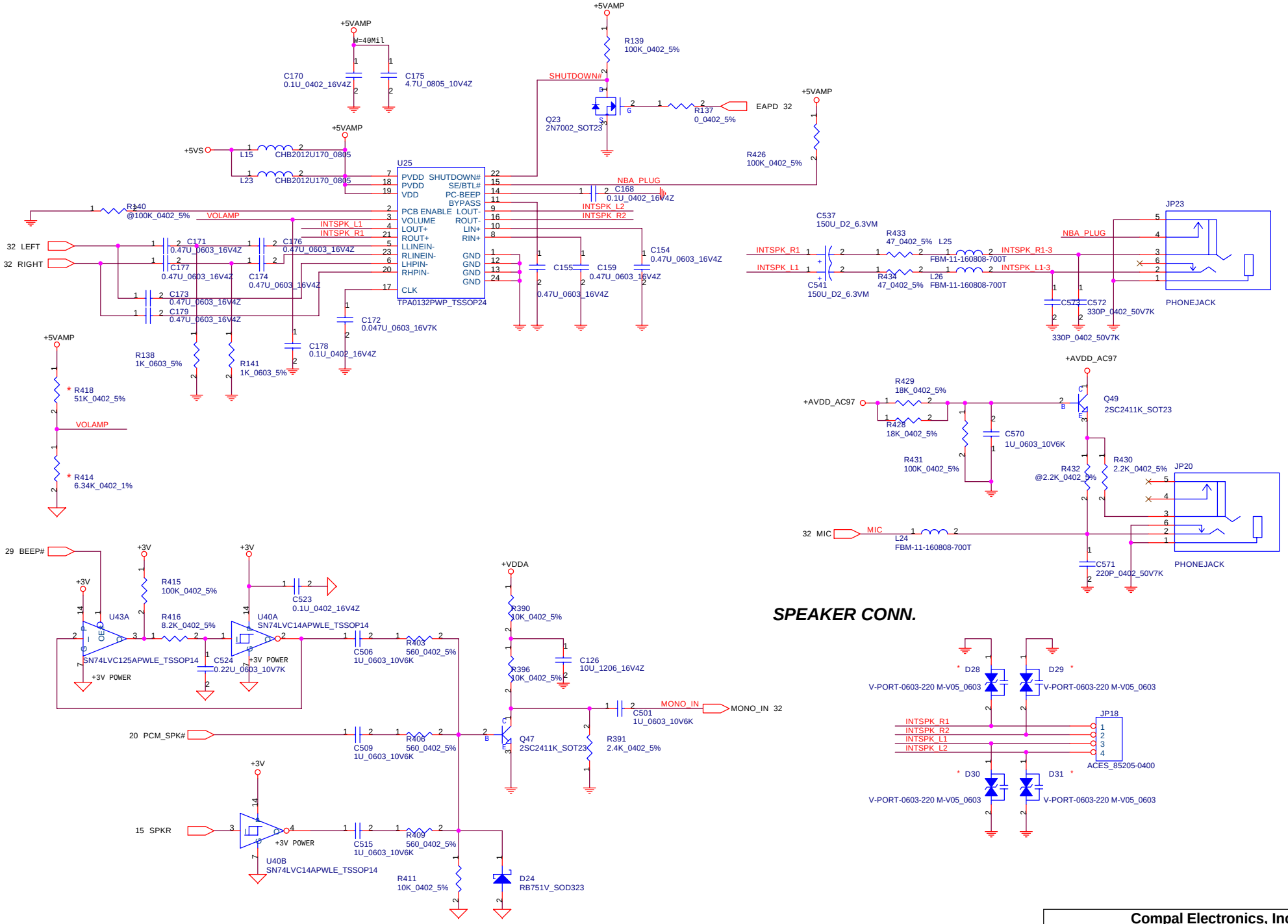

Compal Electronics, Inc.

Title	AMP & Audio Jack	
Size	Document Number DCL55 LA-2201	Rev 1.0
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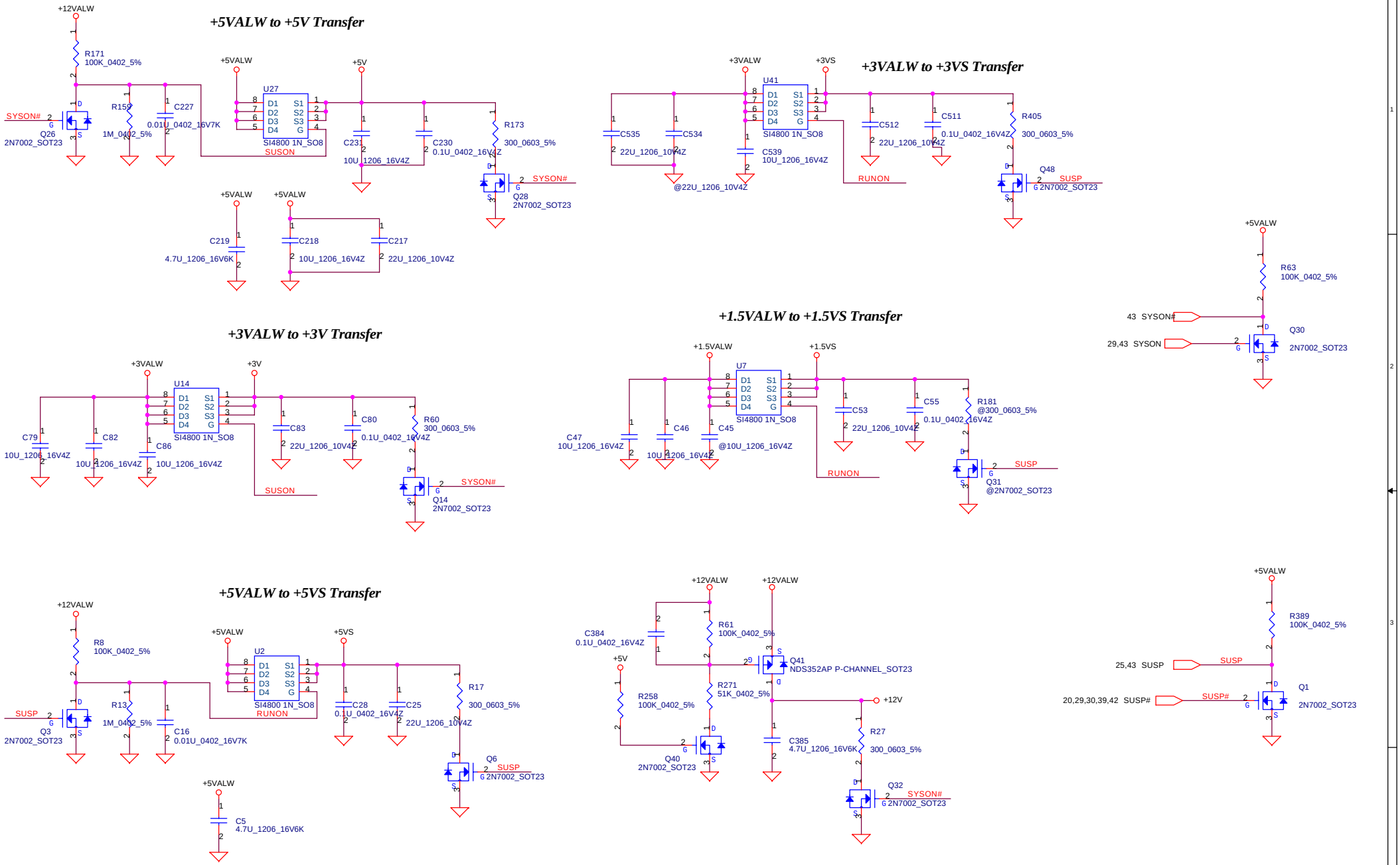

Compal Electronics, Inc.	
Title	DC/DC Circuit Interface
Size	Document Number: DCL55 LA-2201
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CF4 SMDC40M80
CF1 SMDC40M80
CF6 SMDC40M80
CF8 SMDC40M80
CF3 SMDC40M80
CF7 SMDC40M80

CF5 SMDC40M80
CF2 SMDC40M80
CF9 SMDC40M80
CF11 SMDC40M80
CF12 SMDC40M80
CF15 SMDC40M80

CF16 SMDC40M80
CF14 SMDC40M80
CF18 SMDC40M80
CF17 SMDC40M80
CF19 SMDC40M80
CF20 SMDC40M80
CF10 SMDC40M80
CF13 SMDC40M80
CF21 SMDC40M80
CF22 SMDC40M80

FD5 FIDUCAL
FD1 FIDUCAL
FD6 FIDUCAL
FD3 FIDUCAL
FD4 FIDUCAL
FD2 FIDUCAL

H5 H_T315B354D165
H6 H_T315B354D165
H13 H_T315B354D165
H12 H_T315B354D165

H2 H_S354D118
H3 H_S354D118
H4 H_S354D118
H1 H_S354D118
H9 H_S354D118
H16 H_S354D118
H20 H_S354D118
H23 H_S354D118

H22 H_S354D118
H21 H_S354D118
H14 H_S354D118
H7 H_S354D173
H11 H_S354D173
H17 H_S354D173
H19 H_S354D162

H15 H_S354D173
H10 H_S276D165
H8 H_S276D165
H18 H_O146X122D146X122N

Compal Electronics, Inc.

Title: Skew Hole

Size | Document Number: DCL51 LA-1881 | Rev: 1.0

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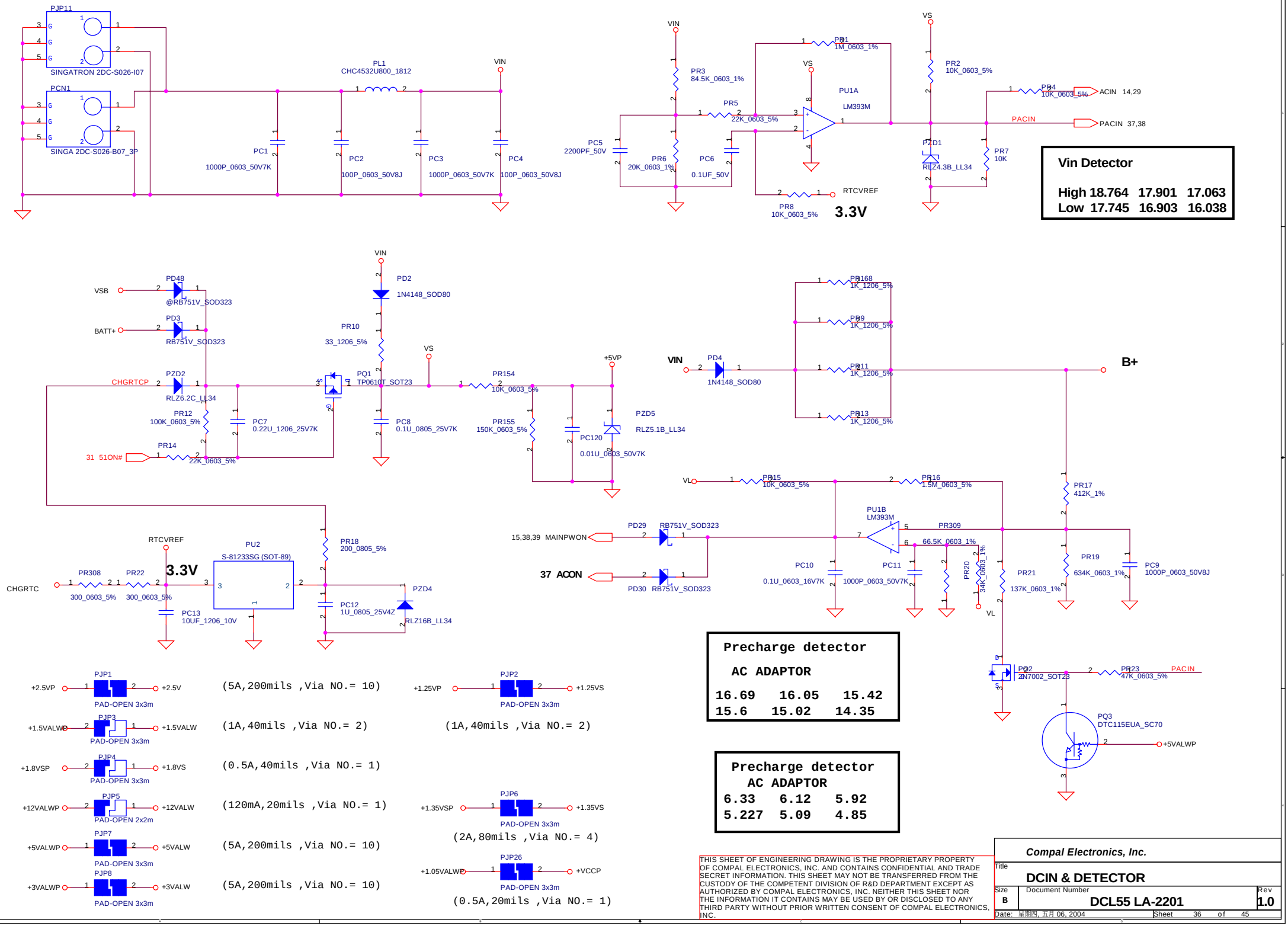

Compal Electronics, Inc.

Title: DCIN & DETECTOR

Size: B | Document Number: DCL55 LA-2201 | Rev: 1.0

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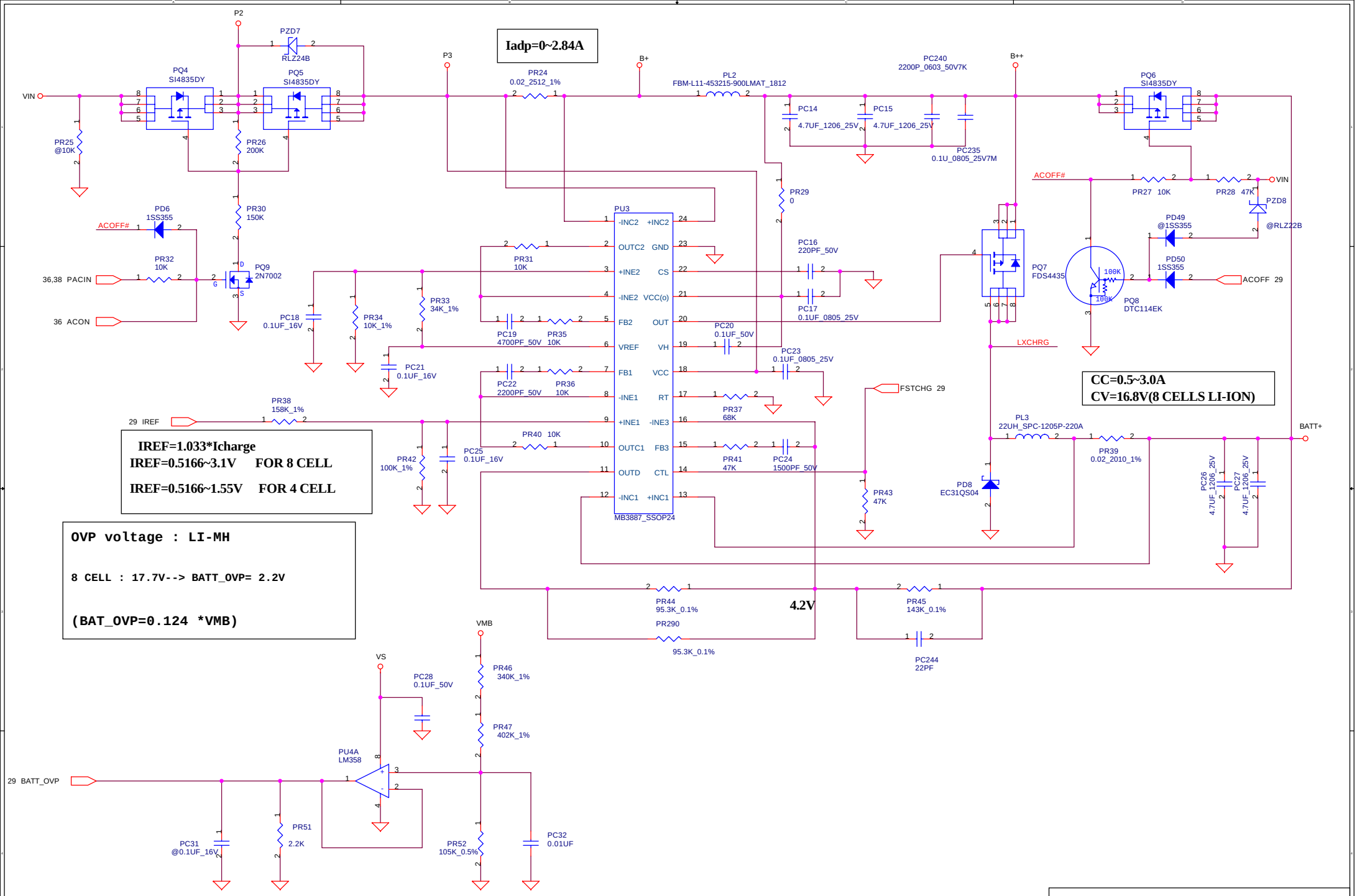
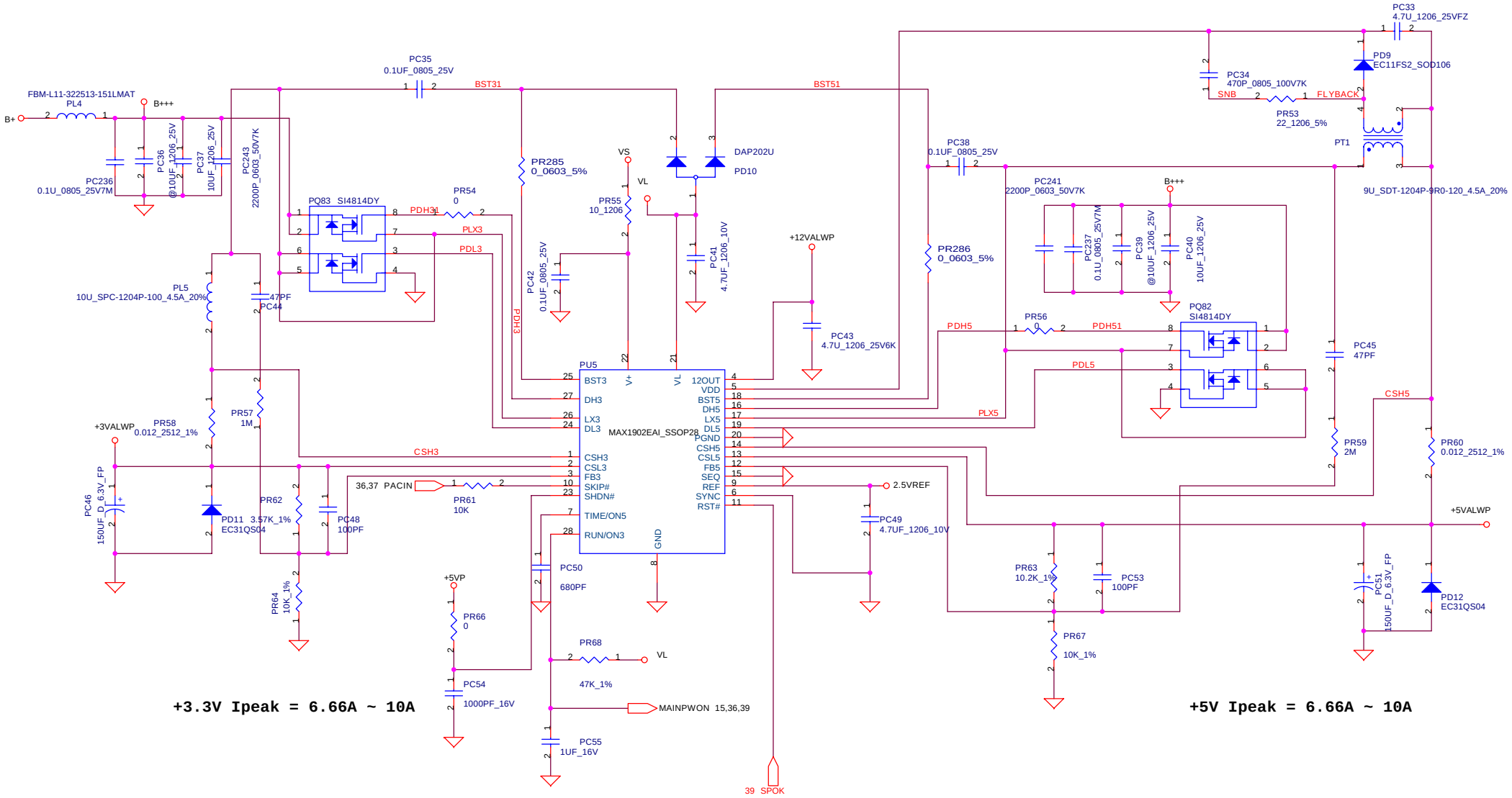


Compal Electronics, Inc.

Title: CHARGER

Size: B | Document Number: DCL55 LA-2201 | Rev: 1.0

Date: 星期四, 五月 06, 2004 | Sheet 37 of 45



Compal Electronics, Inc.		
Title: 5V/3.3V/12V		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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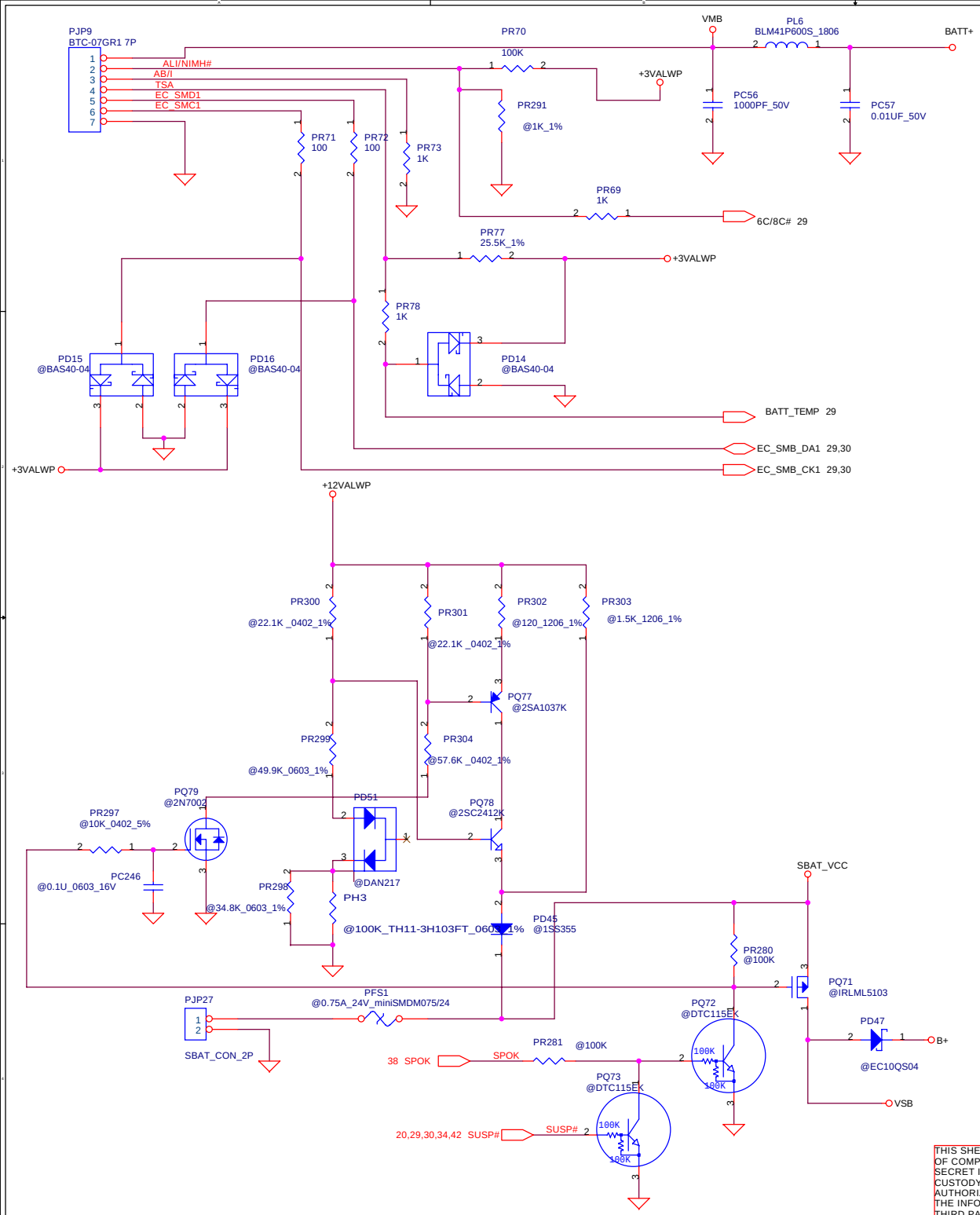
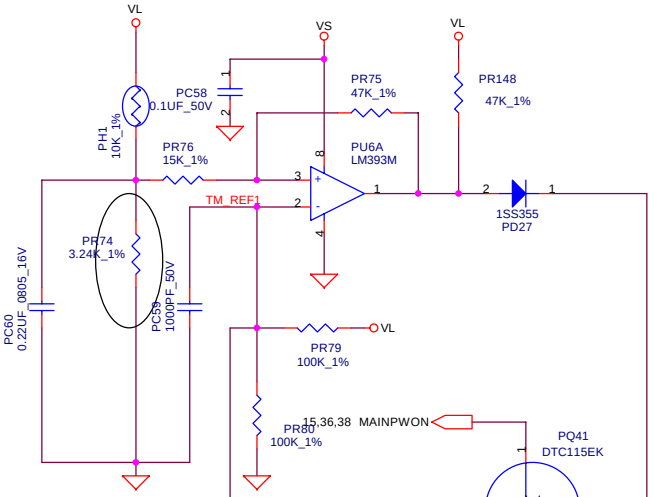
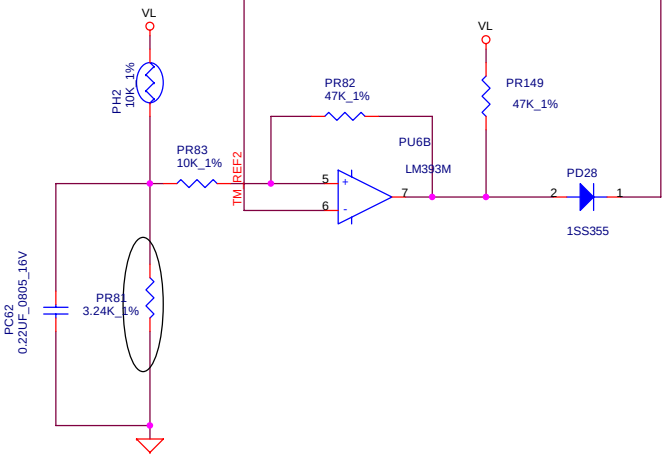


PH1 under CPU botten side :
CPU thermal protection at 82 degree C
Recovery at 48 degree C



PH2 near main Battery CONN :
BAT. thermal protection at 73 degree C
Recovery at 50(51) degree C



Compal Electronics, Inc.

Title: BATTERY CONN / OTP/1.2V

Size: B | Document Number: DCL55 LA-2201 | Rev: 1.0

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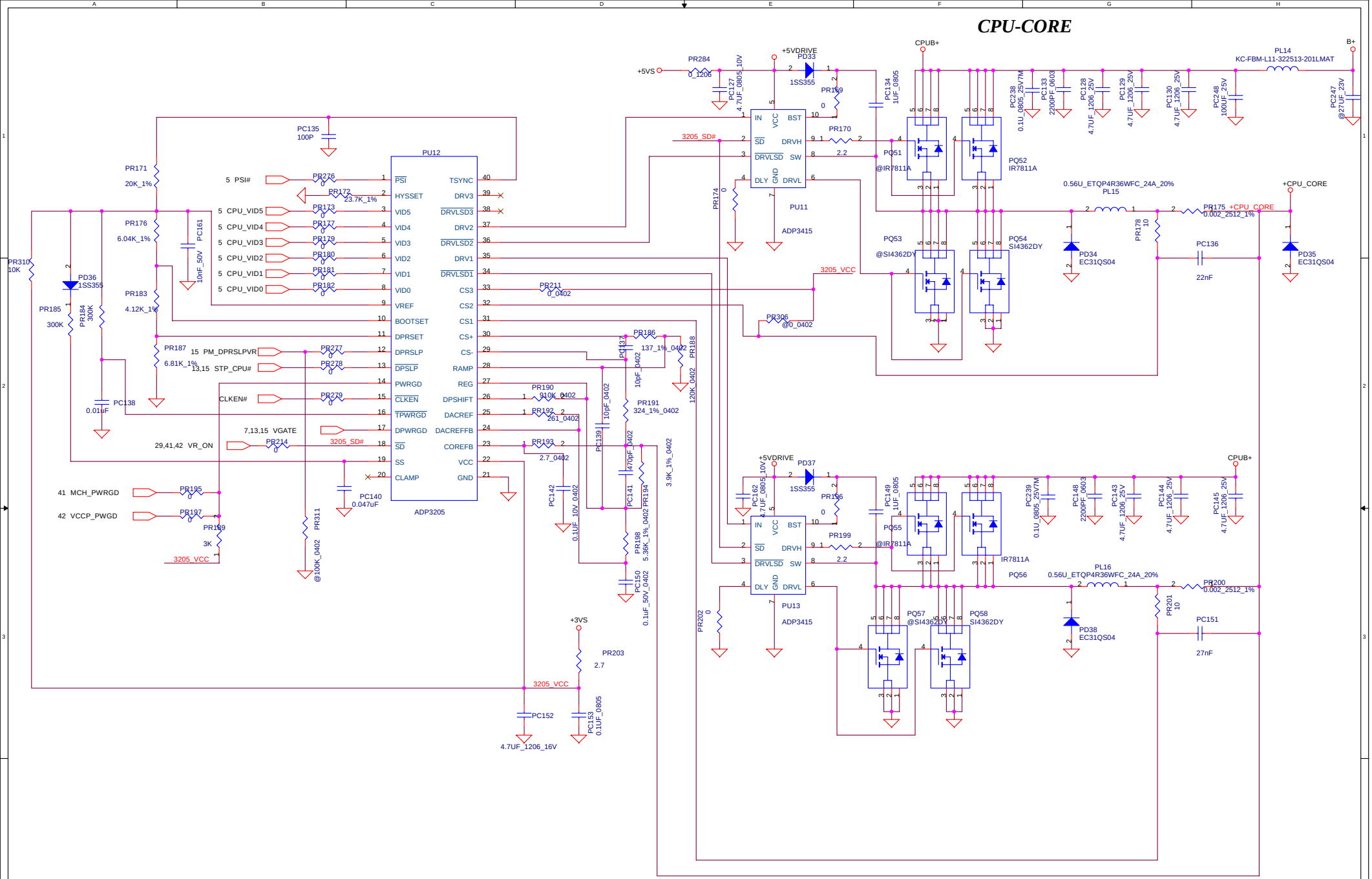
CPU-CORE



Compal Electronics, Inc.

Title: **+VCC_H_CORE**

Size	Document Number	Rev
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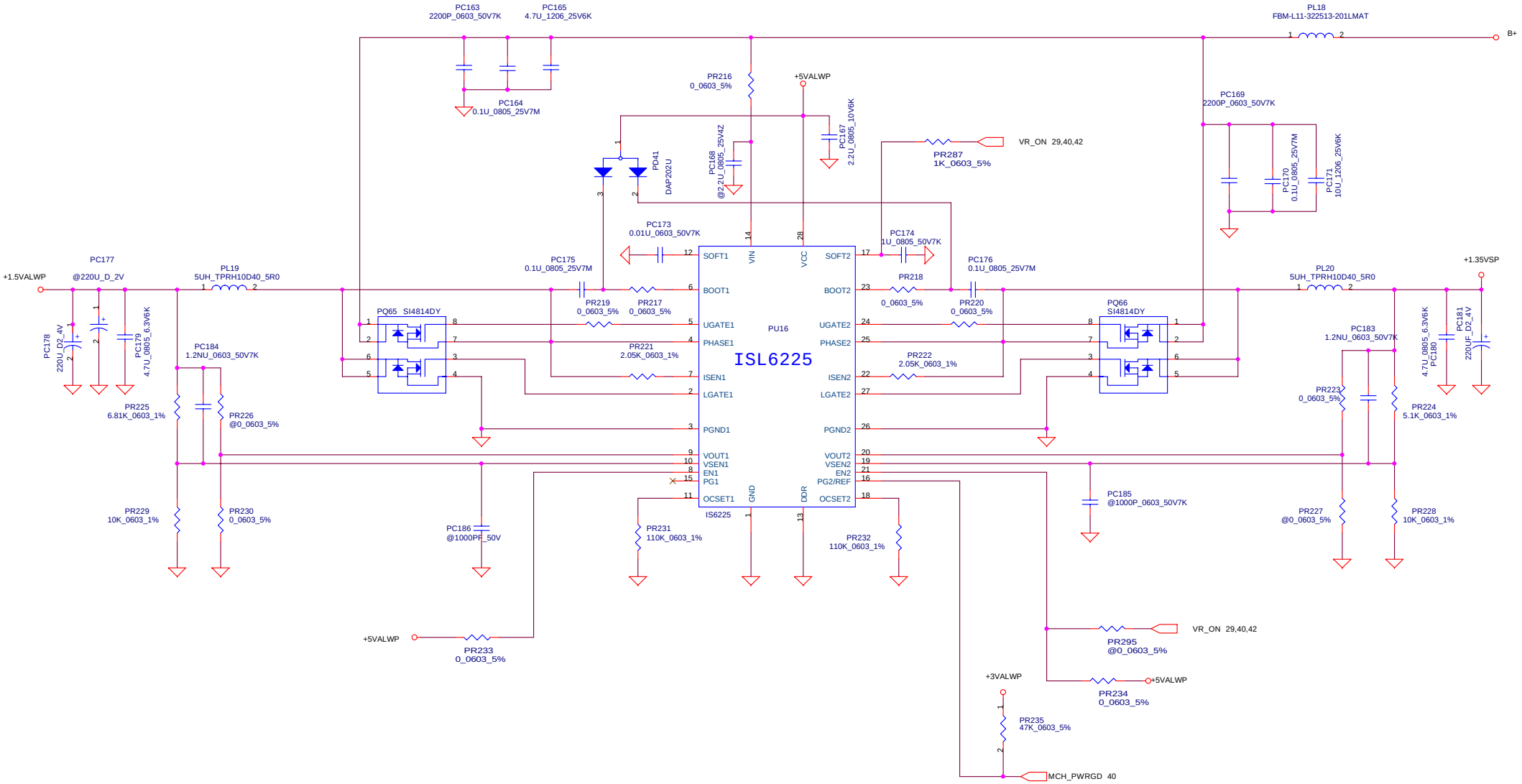
+1.5VALWPV/+1.2VSP



Compal Electronics, Inc.		
Title: 1.5V & 1.2V		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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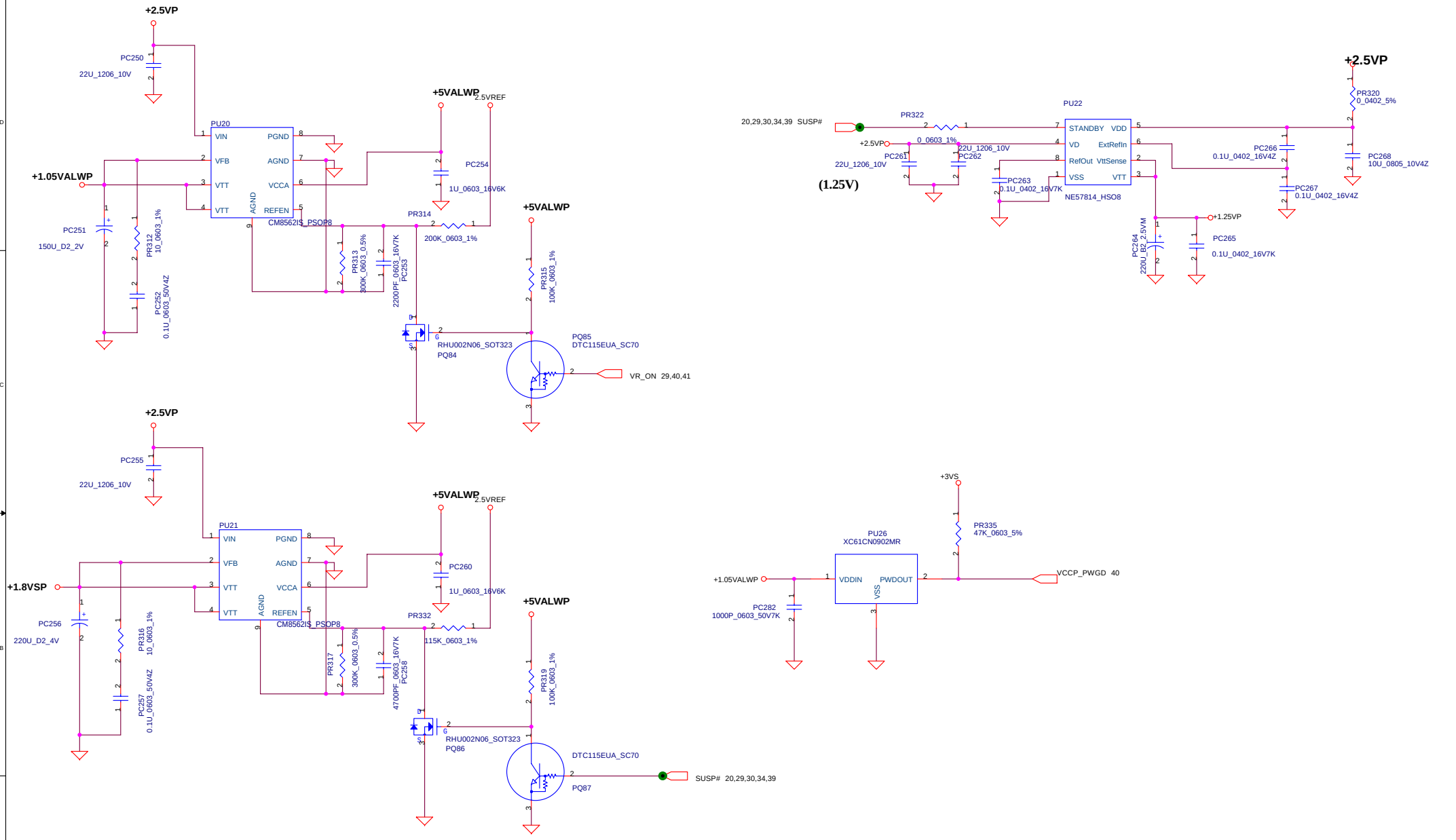
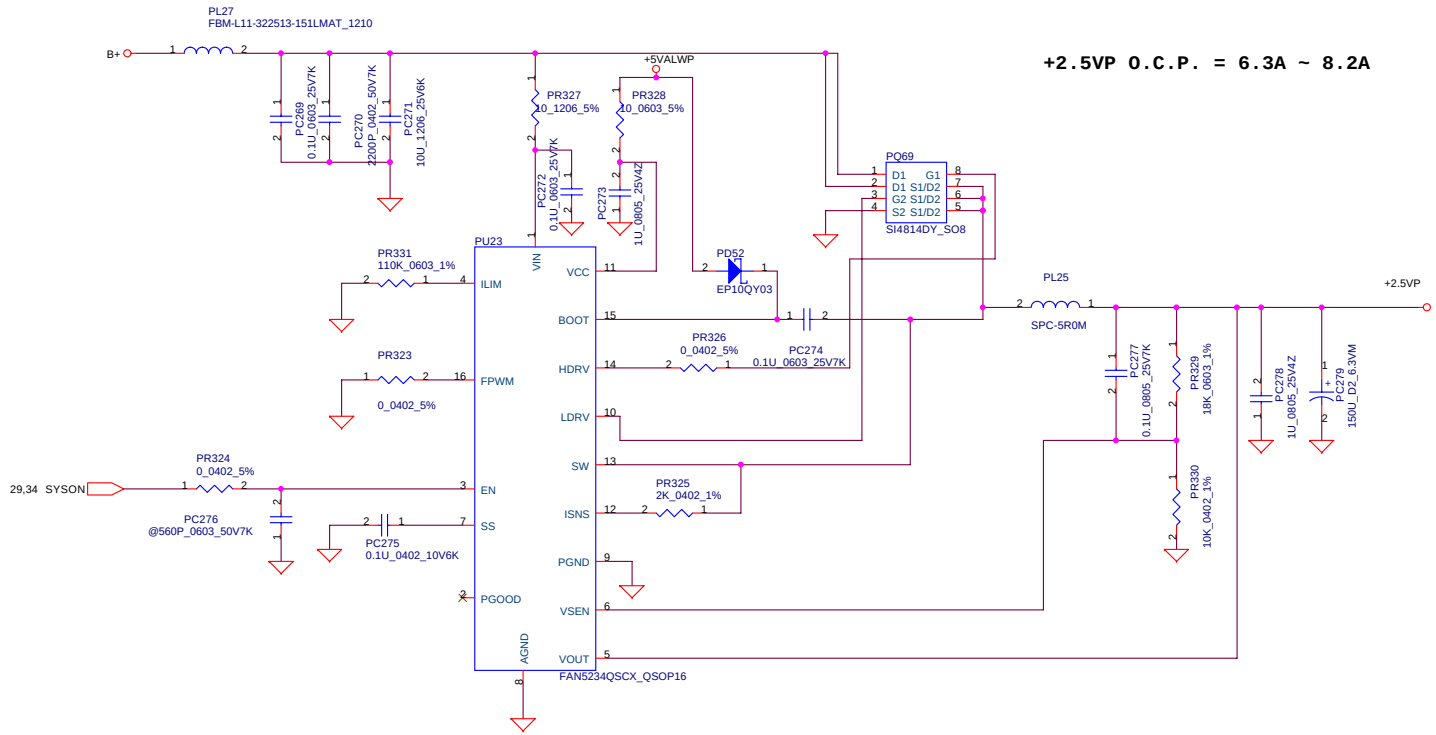
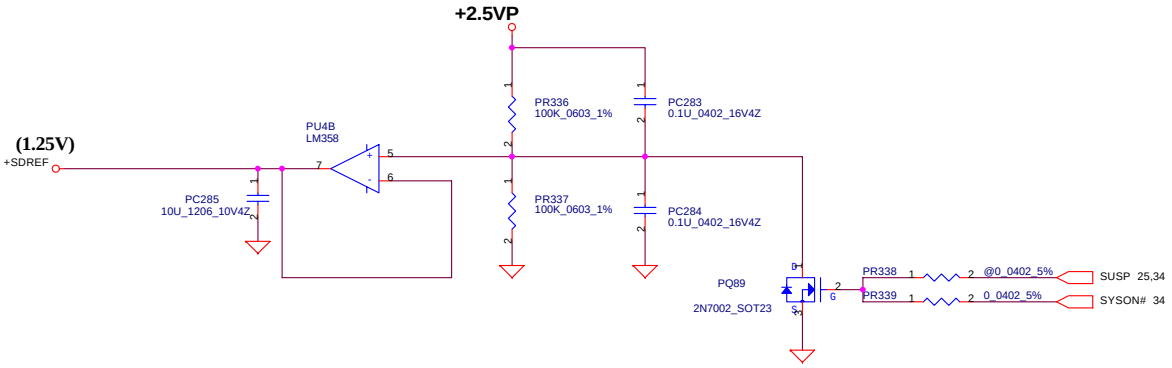


Compal Electronics, Inc.		
Title: 2.5V & 1.25V		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
Date: 星期四, 五月 06, 2004	Sheet 42 of 45	





Compal Electronics, Inc.		
Title: 2.5V & 1.25V		
Size: B	Document Number: DCL55 LA-2201	Rev: 1.0
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Version change list (P.I.R. List)

for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		Set c4 mode controlled by bios based diffference CPU		40	DELETE PR311:100K ; ADD PR277:0		DVT
2		Modify 1.05VP voltage level		42	1. change PR314 to 200k		DVT
3		modify 1.05VP,1.8VP soft start time		42	add PC253, PC258 to be 0.1U		DVT
4		add +SDREF signal circuit		43	add PR339:0; PQ89: 2N7002; PC283,PC284 :0.1U 16V;PR336,PR337:100K;PC285:10U 1206		PVT
5		Add one set power good:VCCP_PWGD		42,41	ADD LDO PU26 :XC61CN;PR335:47K;PC282:1000P 50V,PCHANGE R235 FROM 10K TO 47K		PVT
6		modify 1.05VP,1.8VP soft start time		42	CHANGE PC253 from 0.1u to 2200PF, PC258 from 0.1U to 4700PF		PVT
7		CHANGE PU5 from MAX1631 to MAX1902 (BECAUSE MAX1631 shortage)		38	CHANGE PT1 from 10u_1205P to 9u_sdt-1204p-9r0-120;delete PU19;CHANGE pl5 FROM 10U_SPC-1205P-100 to 10u_1204p-100_4.5A;change PC39 from 180PF to 470PF;DELETE PZD6;change PU5 from max1631 to max1902		MP
8		FOR CL53A ; To change 1.2VSP to 1.35 VSP		37,41	CHANGE PR224 from 3.48K to 5.1K, delete PD49,DELETE PZD8		
9							

Compal Electronics, Inc.		
Title: **PIR (PWR)**		
Size	Document Number: **DCL55**	Rev: 1.0
Date: 星 期日, 五月 06, 2004	Sheet 44 of 45	

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		~~reserve +SDREF signal circuit~~		~~8~~	~~reserve R477,R479~~		PVT
2		~~reserve +SDREF signal circuit~~		~~10~~	~~reserve R480,R481~~		PVT
3		EMI performance		8	change C420_net from MRCOMP to +2.5V		PVT
4		reserve syson,susp#,vr_on pull low resister		29	reserve R482,R483,R484		
5		cost down			change L18,L19,L14,L13,L12,L8,L9,L7 to 0 ohm del C534,C45		PVT
6		board version update		29	change R361 from 8.2k to 18k		PVT
7		for customer request			del TV_out , 1394 funtion		PVT
8		solove leakage			del D15		preMP
9		board version update		29	change R361 from 8.2k to 33k		preMP

Compal Electronics, Inc.

Title: **PIR (PWR)**		
Size	Document Number: **DCL55**	Rev: 1.0
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