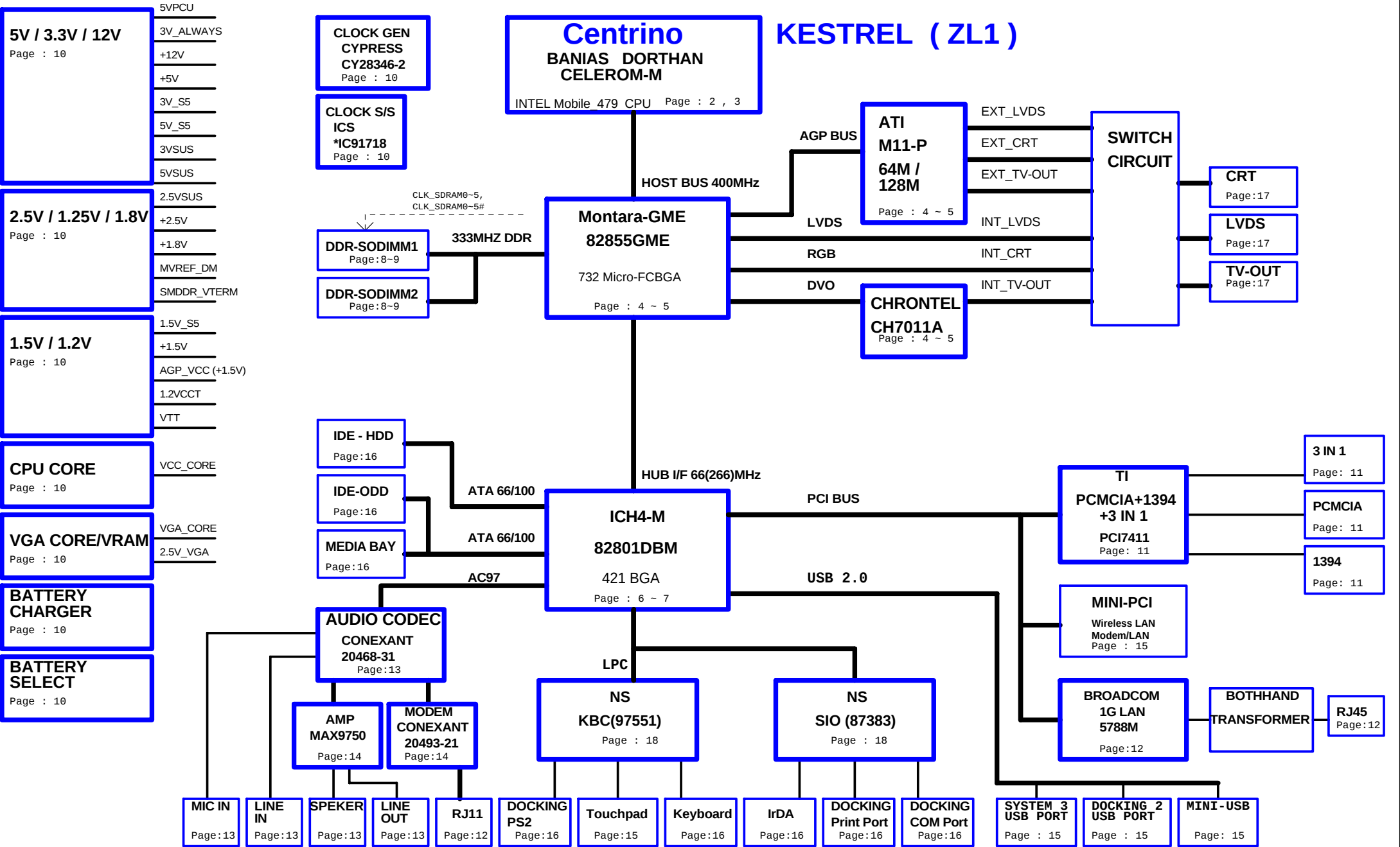
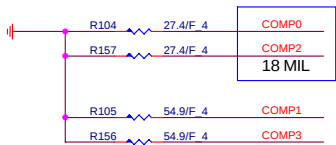


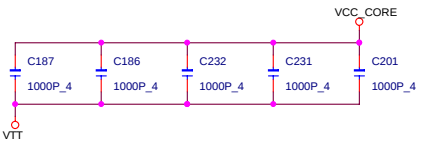


PM : 紀明進 Sunyu Jih
EE Laerer : 許高銘 Jim Hsu
ME Leader : 林哲敏 Mill Lin

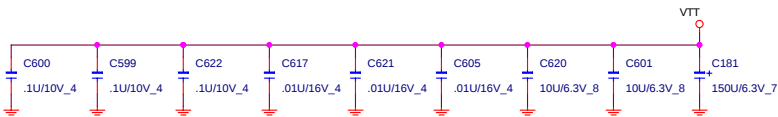
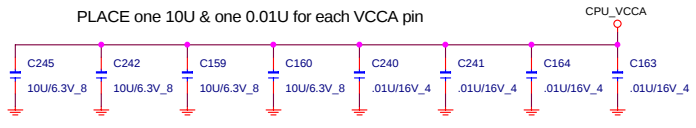
PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
REQ0# / GNT0#	AD18	INTA#	M11
REQ1# / GNT1#	AD20	INTD#	BROADCOM LAN
REQ2# / GNT2#	AD23	INTB# , INTC#	MINI-PCI
REQ3# / GNT3#	AD22	INTE#, INTF#, INTG#	TI 7411



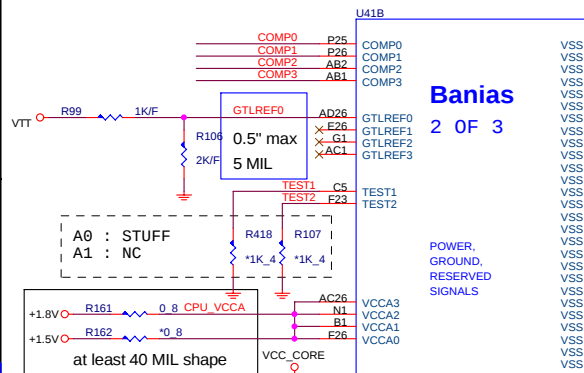
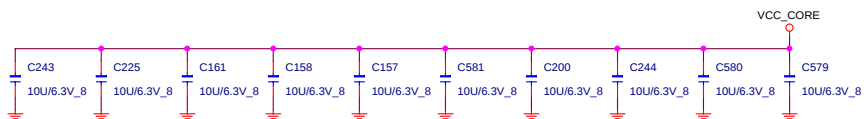
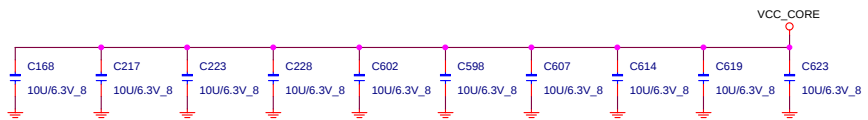
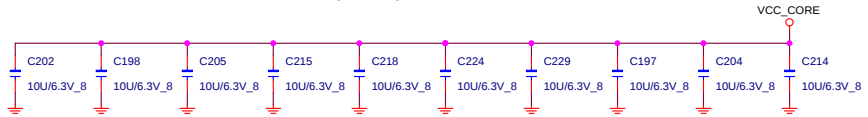
COMP0 ~ 4
max length
500 MIL



PLACE one 10U & one 0.01U for each VCCA pin



10U/6.3V/X5R(CC0805) *30



1.5V FOR DOTHAN ONLY

at least 40 MIL shape

0.5" max

5 MIL

A0 : STUFF

A1 : NC

0.8 CPU_VCCA

*0.8

0.8 CPU_VCCA

0.8 CPU_VCCA

0.8 CPU_VCCA

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0.8 CPU_VCCA

0.8 CPU_VCCA

0.8 CPU_VCCA

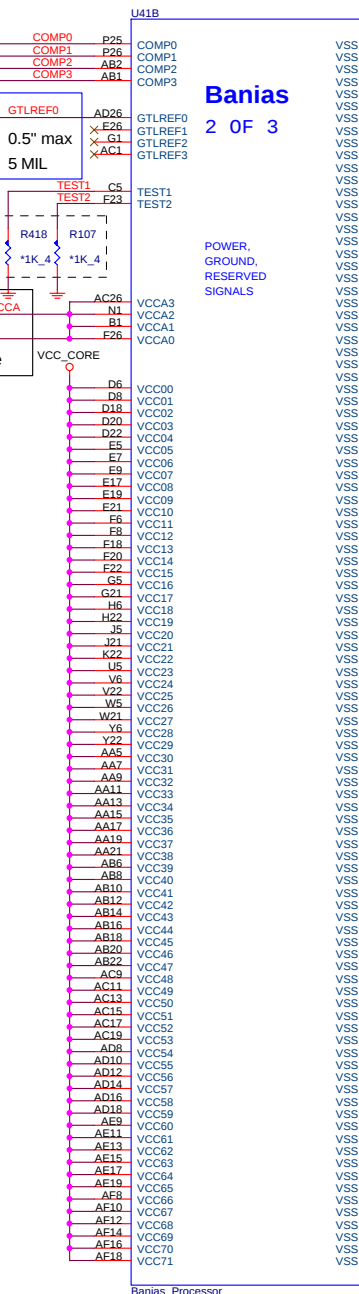
0.8 CPU_VCCA

0.8 CPU_VCCA

0.8 CPU_VCCA

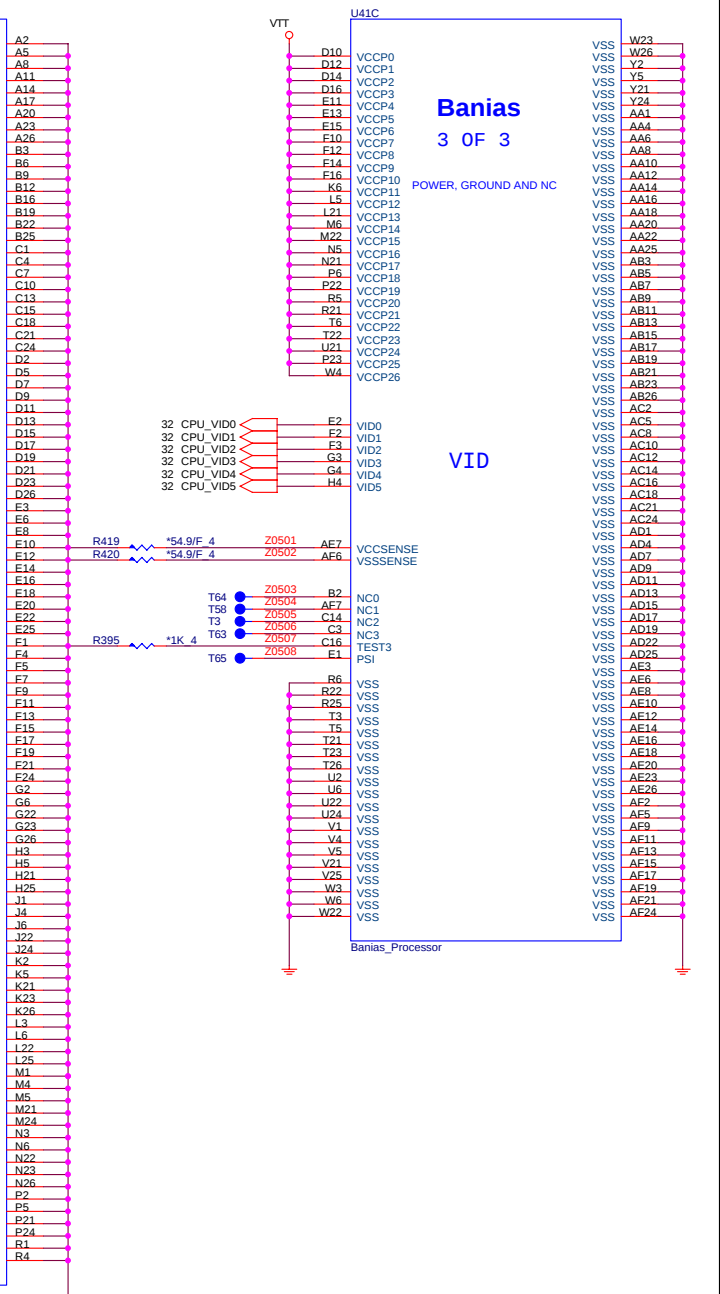
0.8 CPU_VCCA

0.8 CPU_VCCA



Banias
2 OF 3

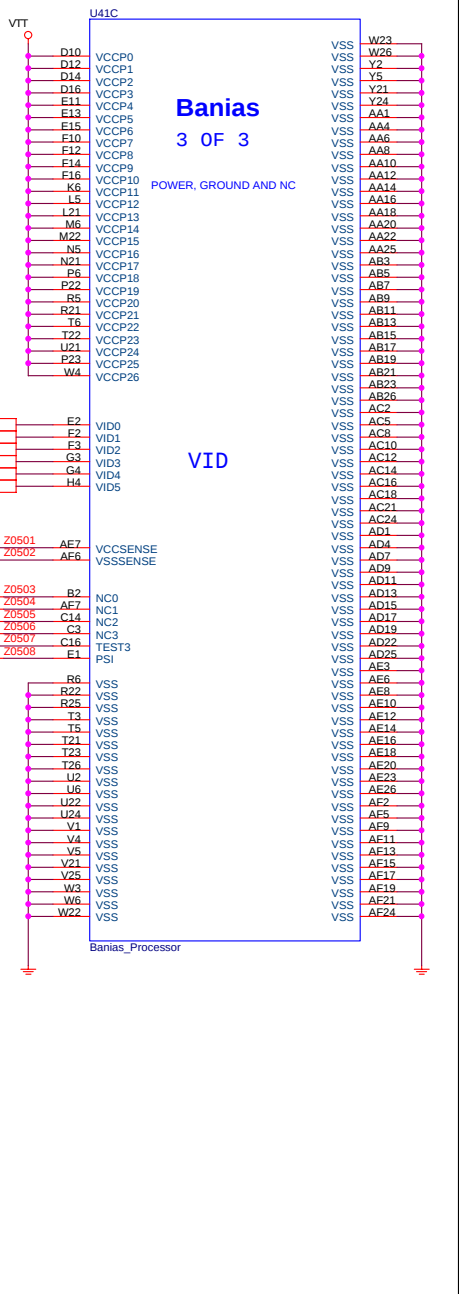
POWER,
GROUND,
RESERVED
SIGNALS



Banias
3 OF 3

POWER, GROUND AND NC

VID

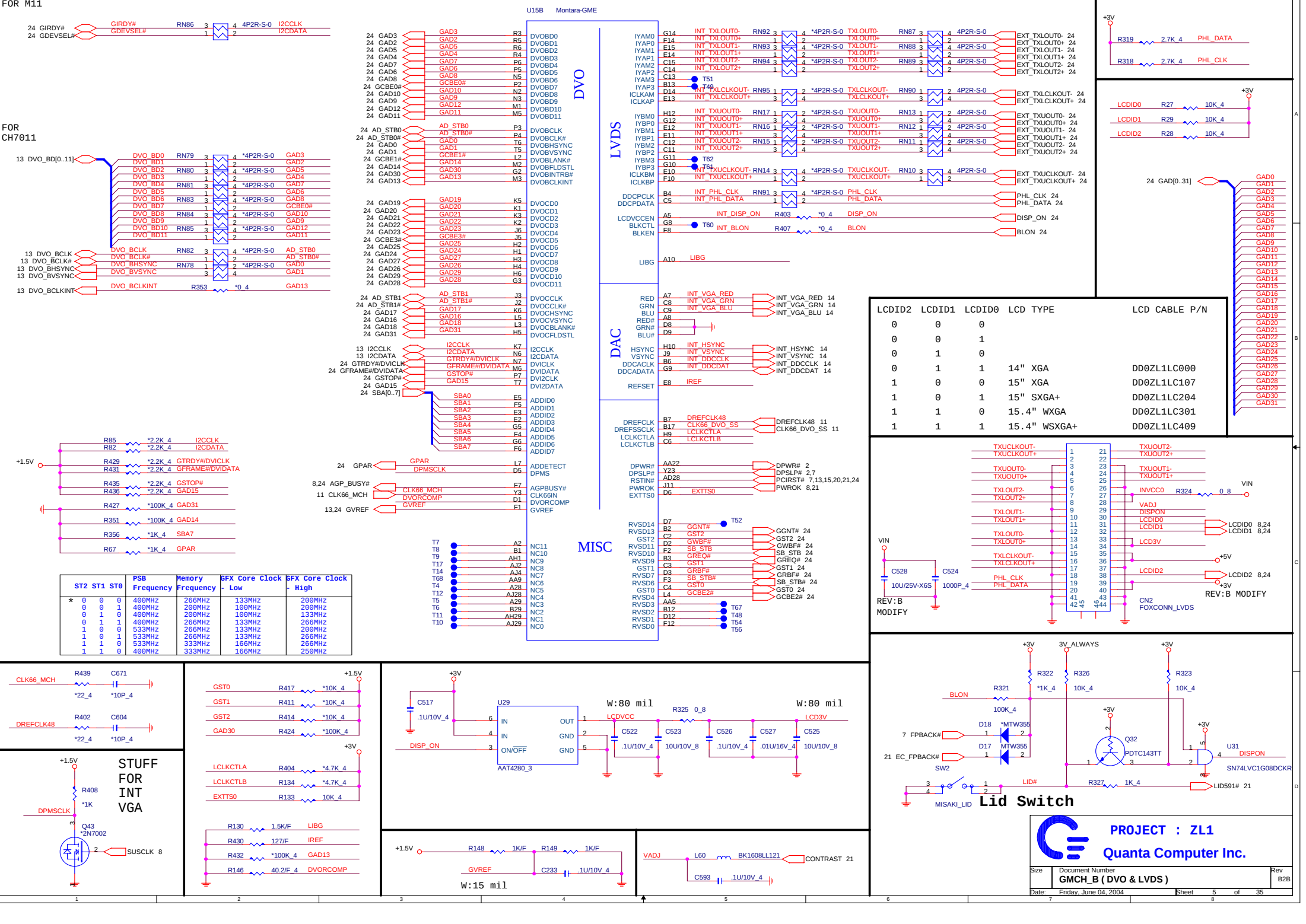


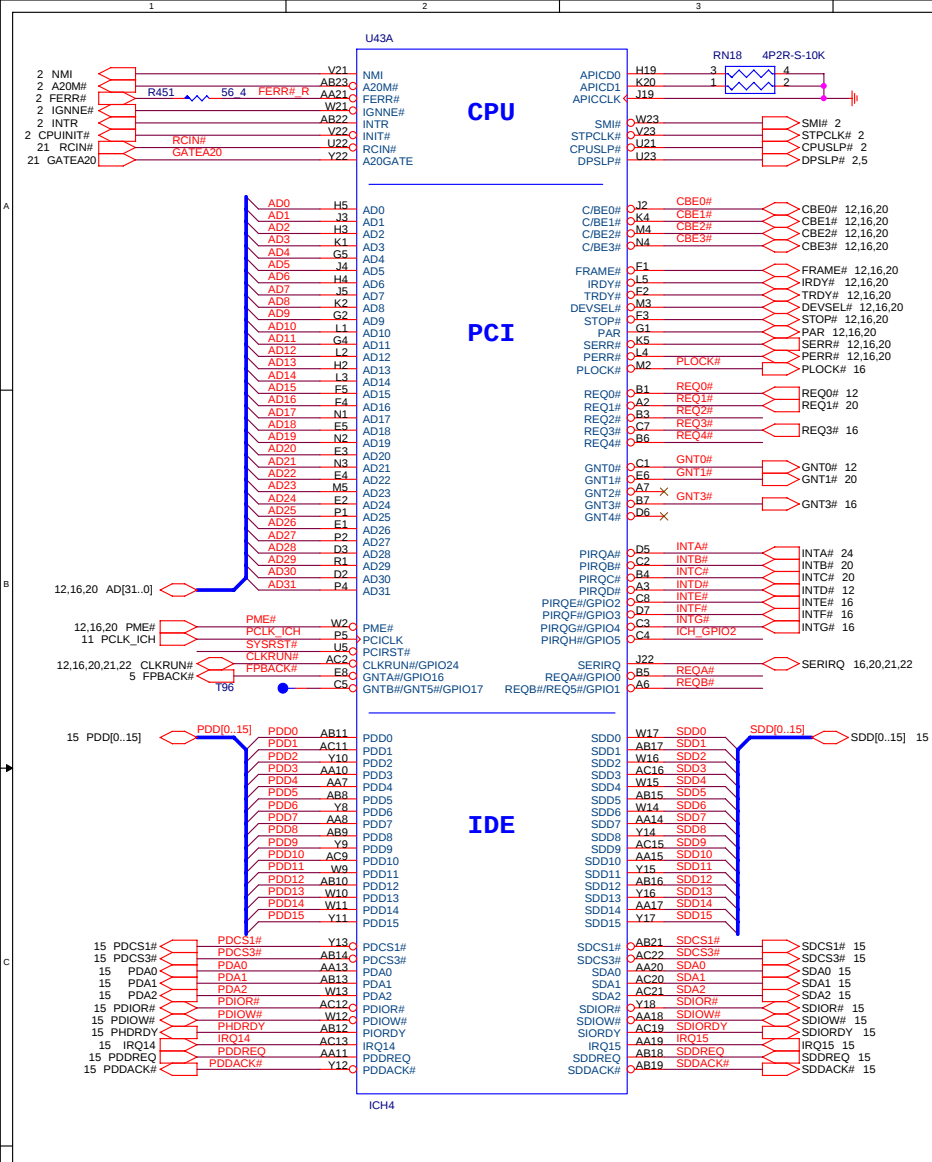
PROJECT : ZL1
Quanta Computer Inc.

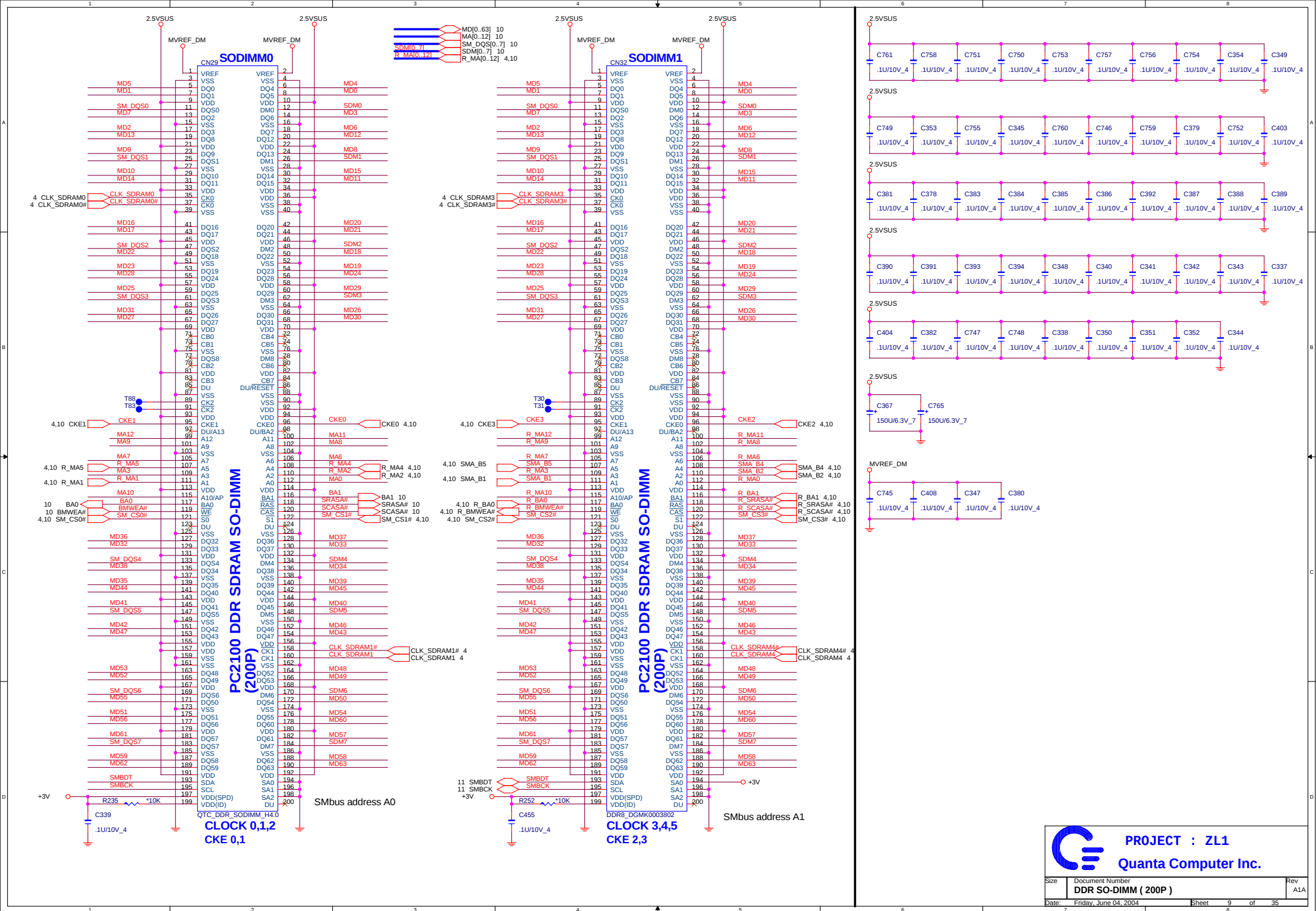
Size	Document Number	Rev
	CPU (POWER)-2	A1A
Date:	Friday, June 04, 2004	Sheet 3 of 35

FOR M11

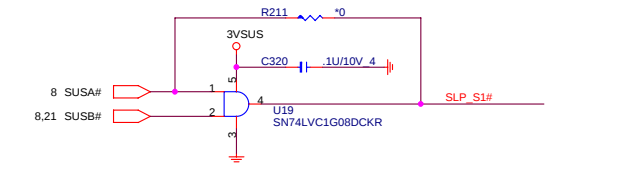
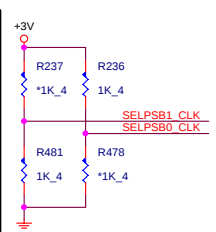
FOR CH7011







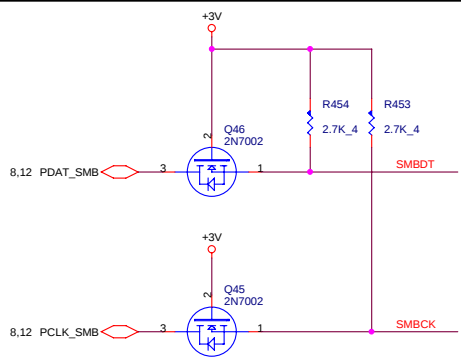
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66
0	0	1	100	66	66
0	1	0	200	66	66
0	1	1	133	66	66



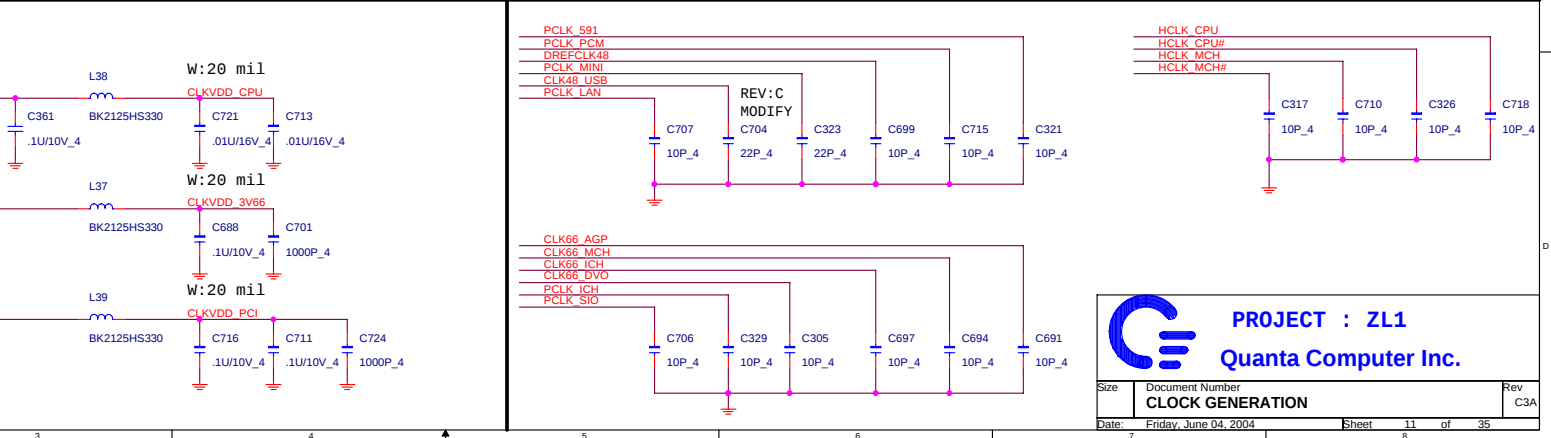
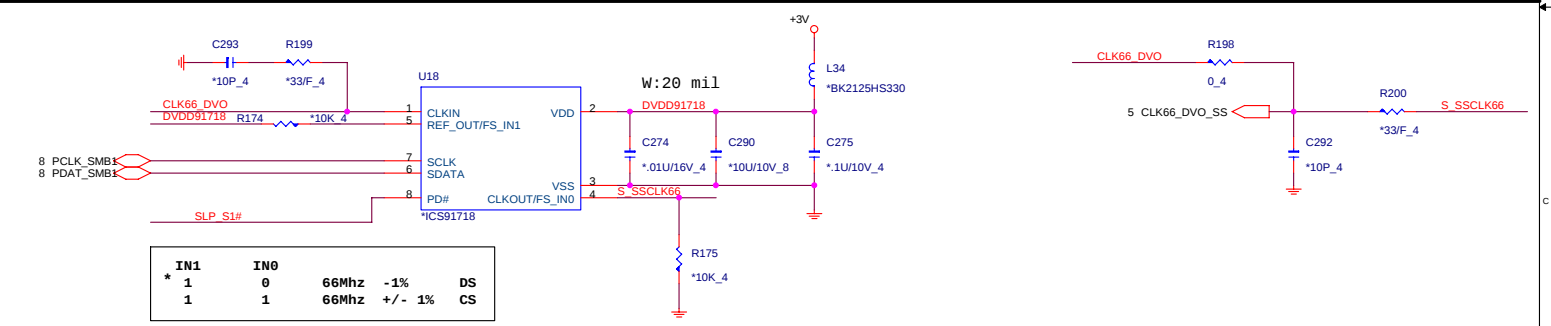
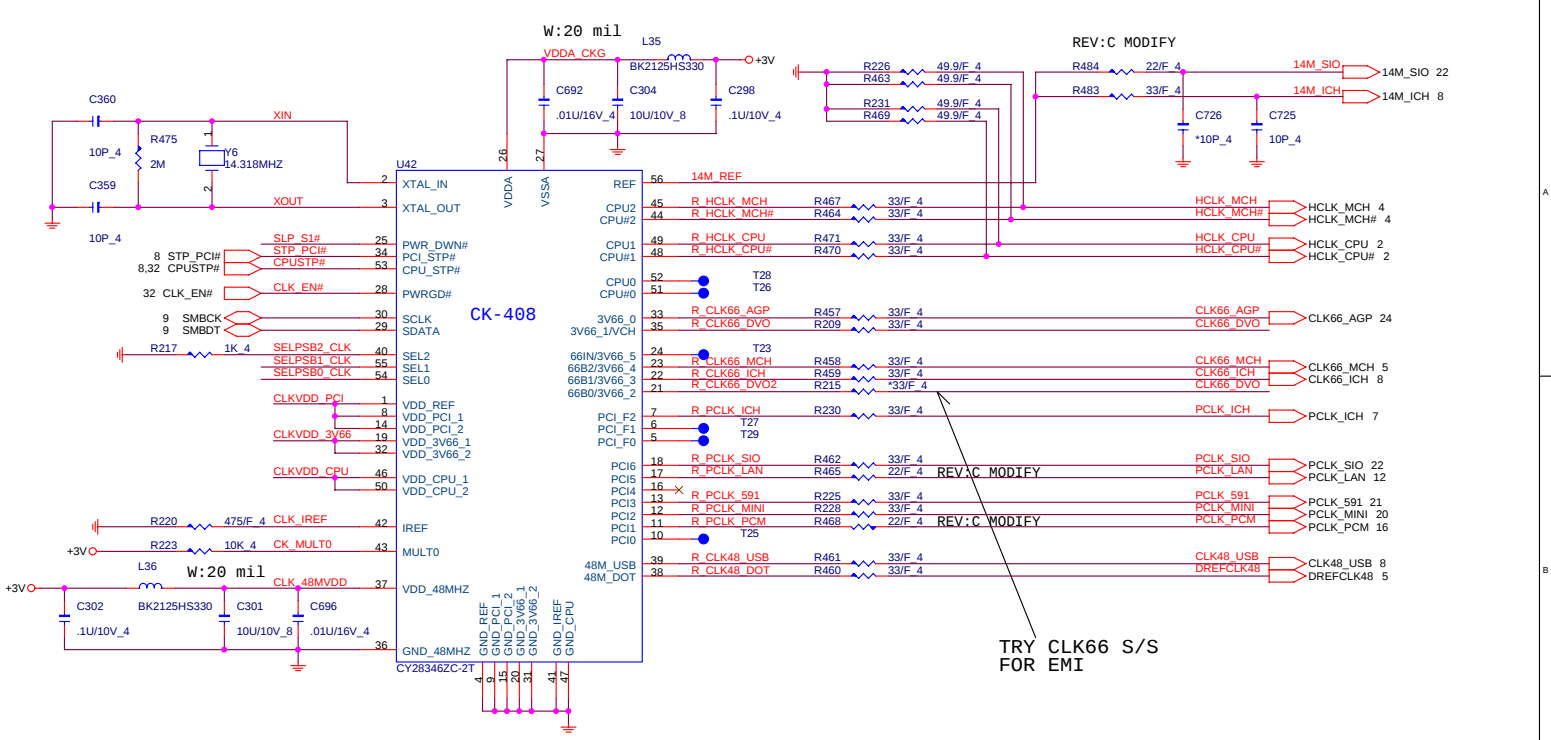
SMBus Byte 0 , Bit 5 = 0	66 MHz	W	S/S
SMBus Byte 0 , Bit 5 = 1	48 MHz	W/O	S/S

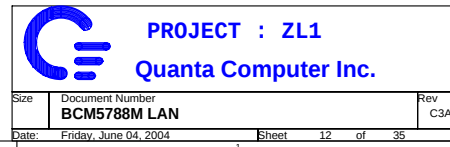
Byte 0 , Bit 7 = 0 Disable Spread Spectrum
Byte 0 , Bit 7 = 1 Enable Spread Spectrum

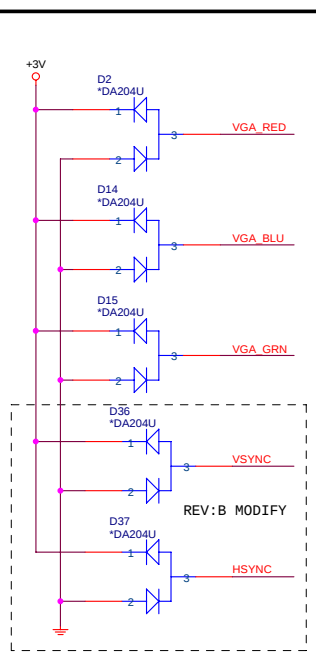
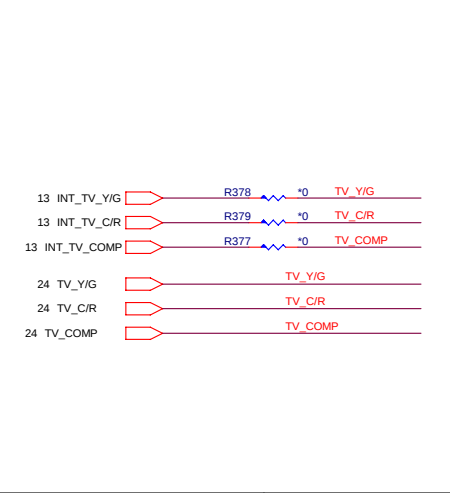
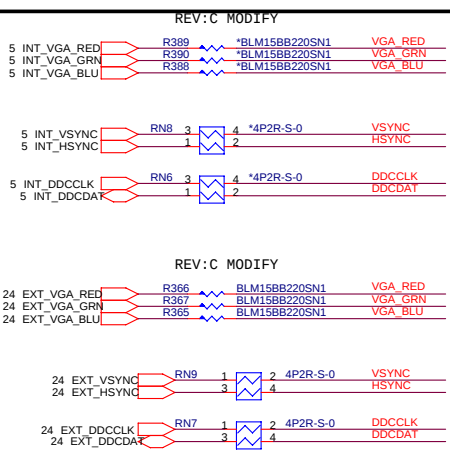
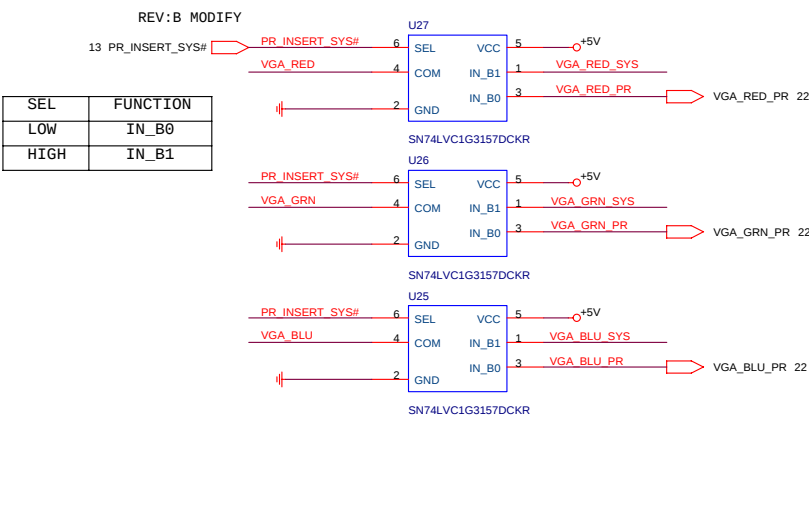
Byte 4 Bit 7	Byte 5 Bit 7	Byte 5 Bit 6	Spread Mode	Spread %
SS2	SS1	SS0		
0	0	0	DOWN	+0.00 , -0.25
0	0	1	DOWN	+0.00 , -0.50
0	1	0	DOWN	+0.00 , -0.75
0	1	1	DOWN	+0.00 , -1.00
1	0	0	CENTER	+0.13 , -0.13
1	0	1	CENTER	+0.25 , -0.25
1	1	0	CENTER	+0.37 , -0.37
1	1	1	CENTER	+0.50 , -1.50



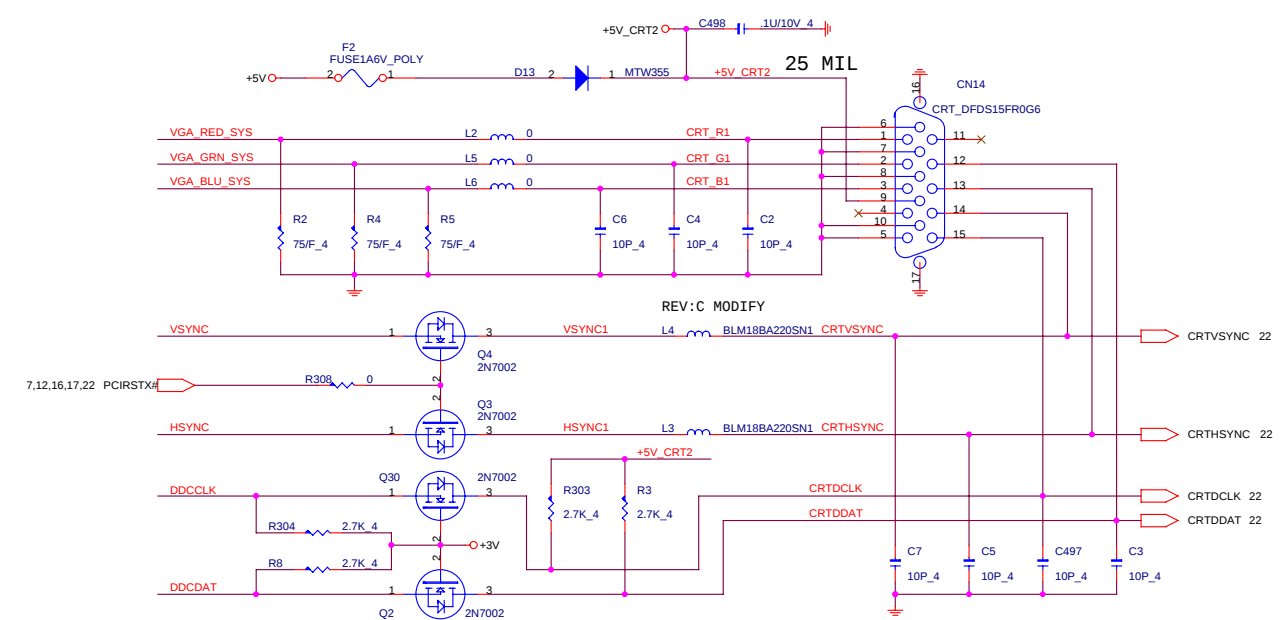
These are for backdrive issue



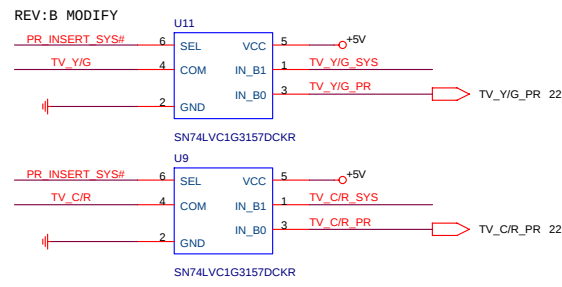
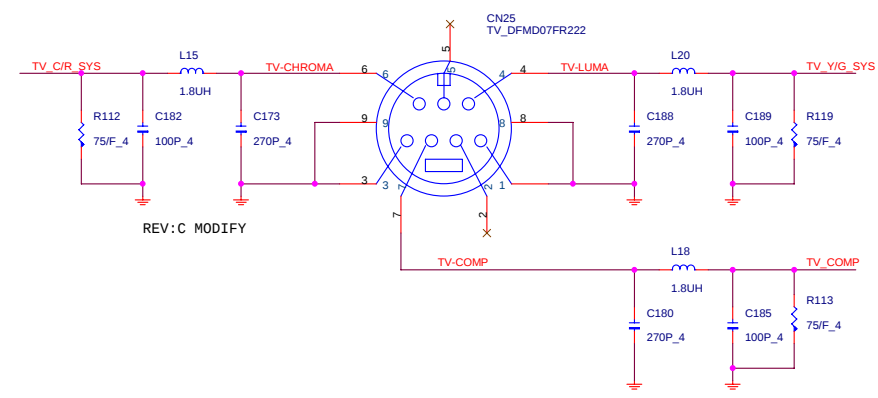


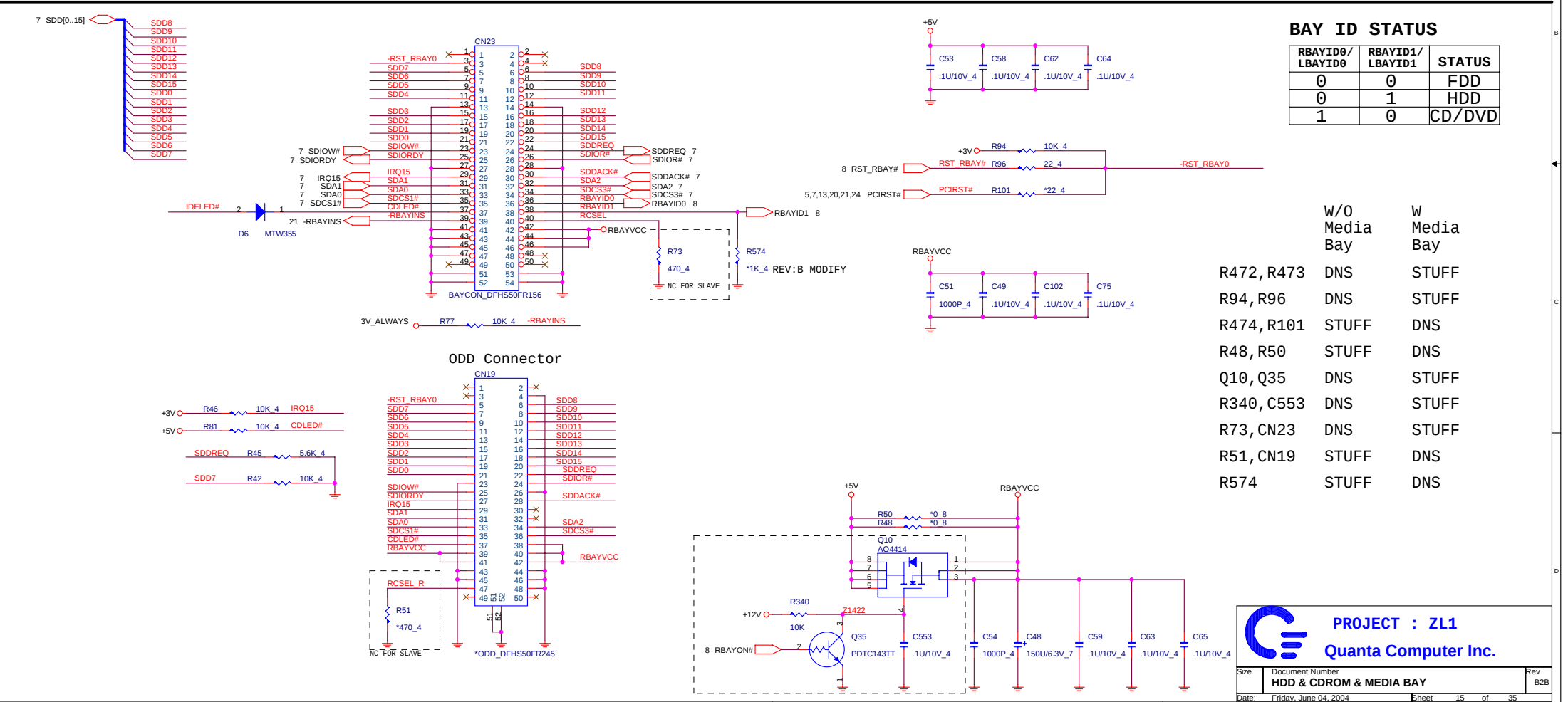
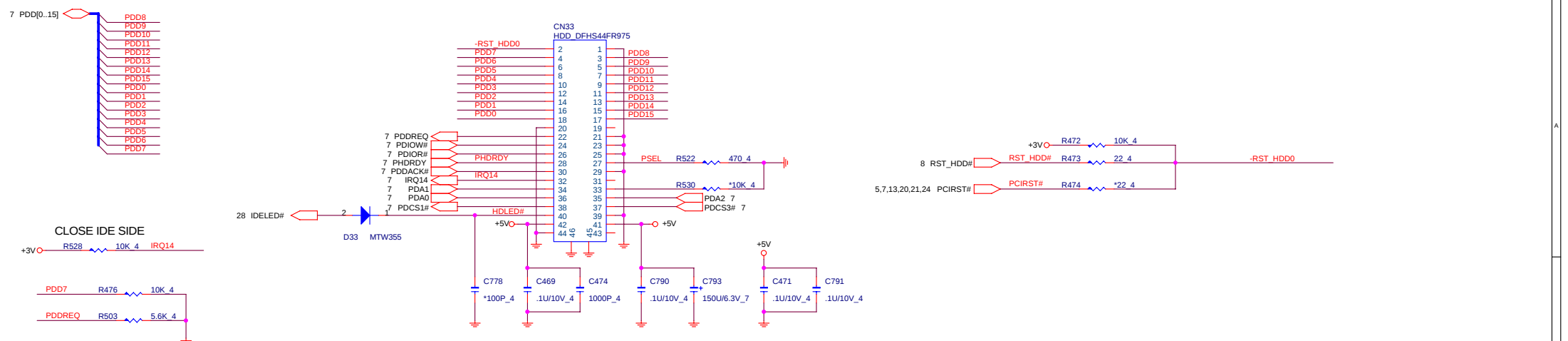


CRT PORT



S-VIDEO



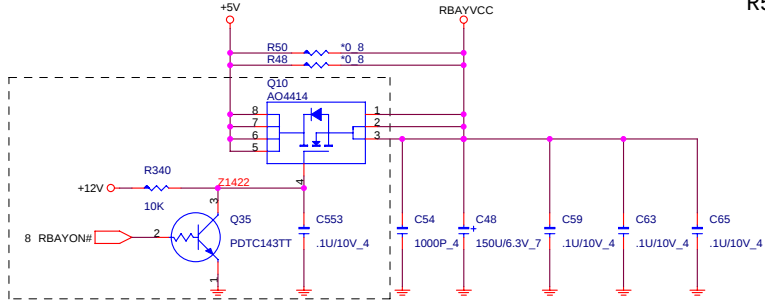


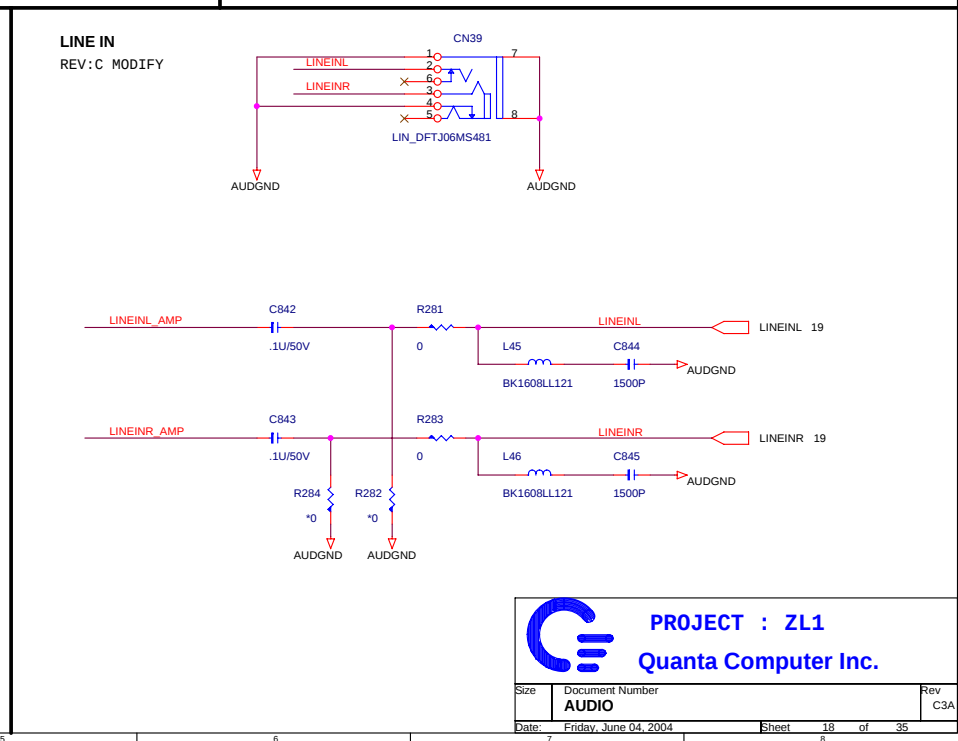
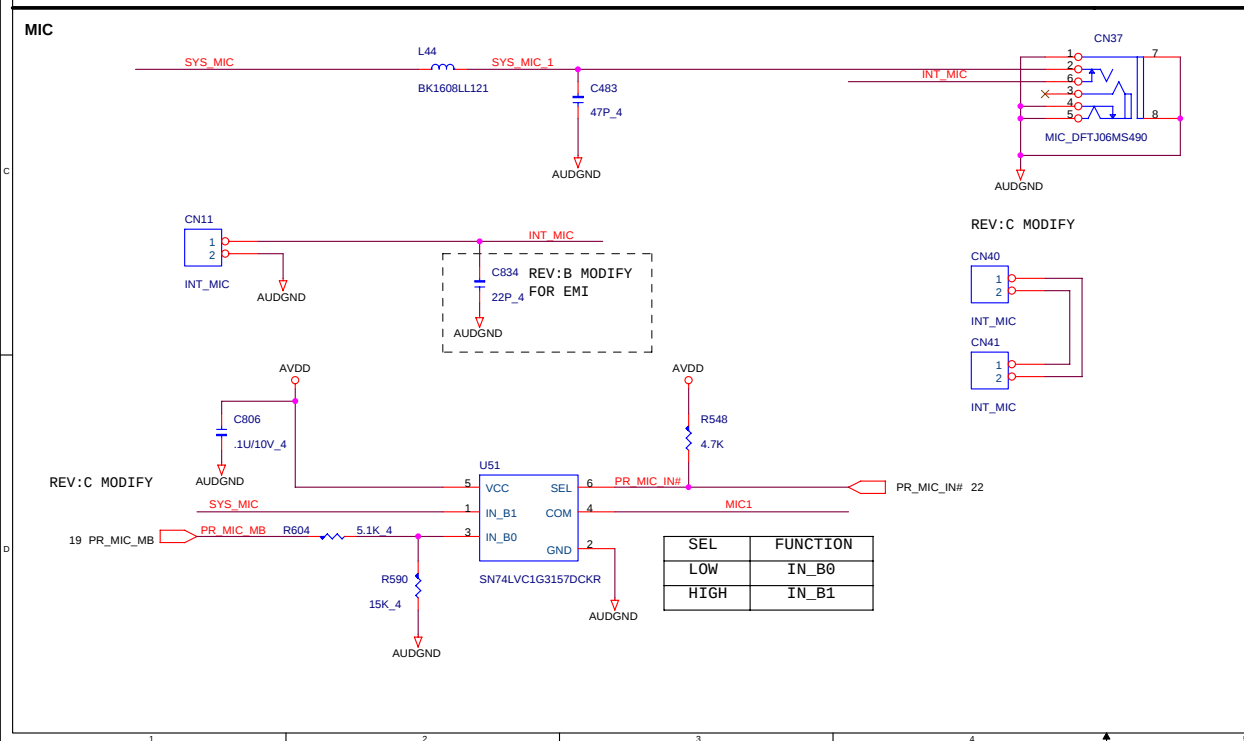
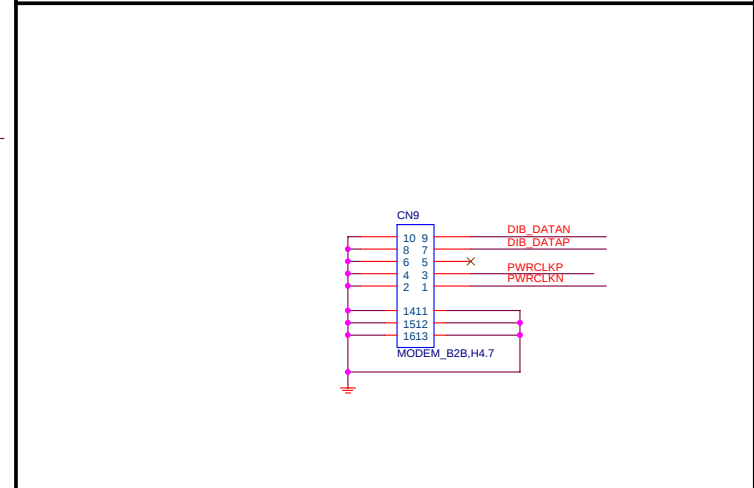
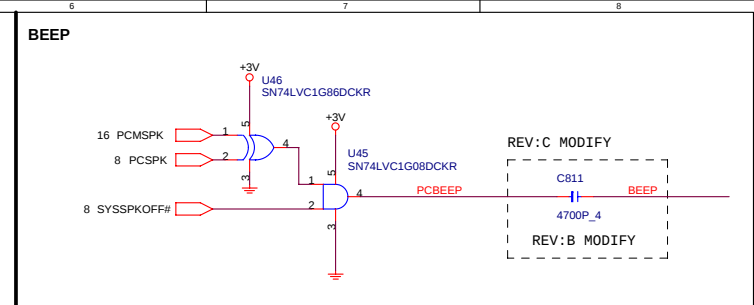
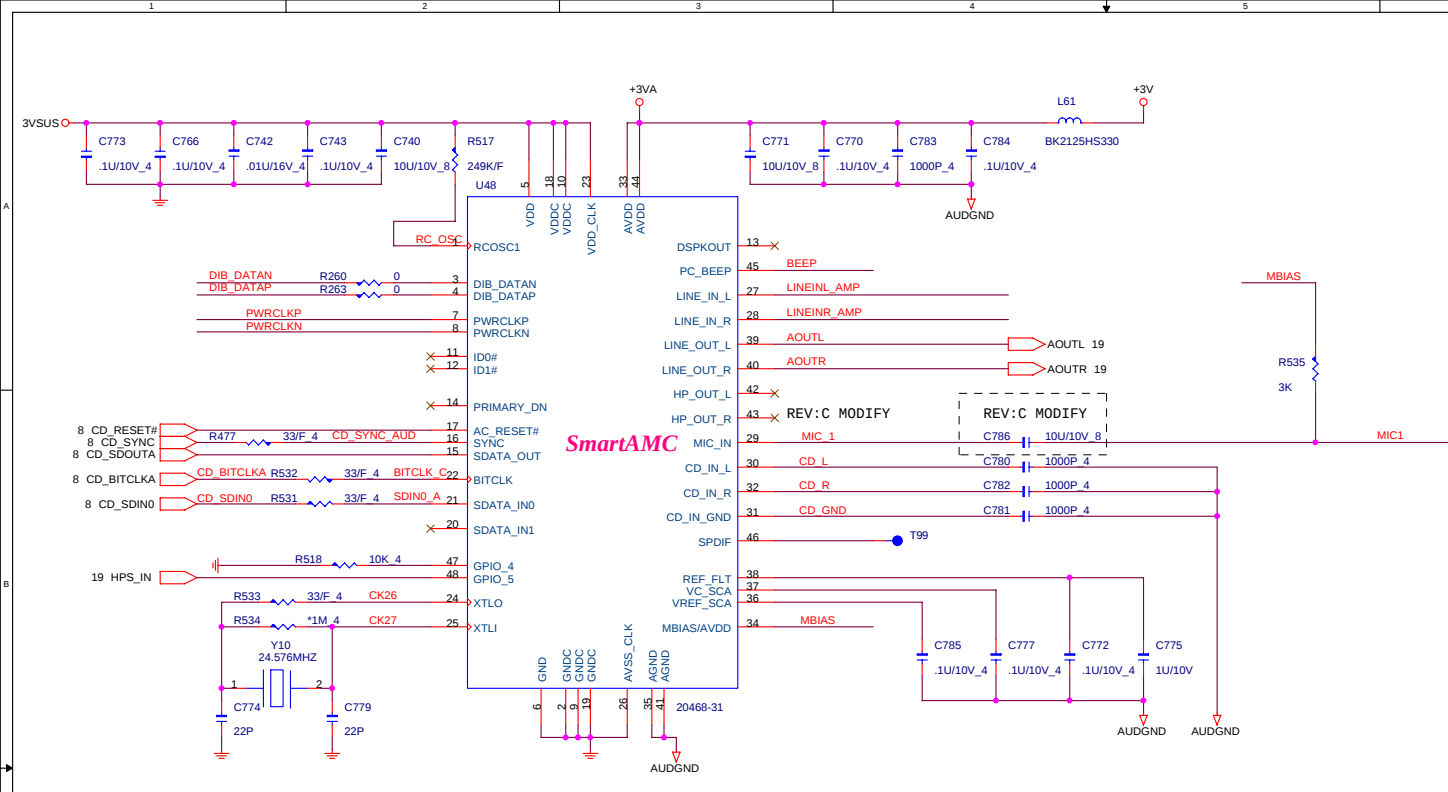
BAY ID STATUS

RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD

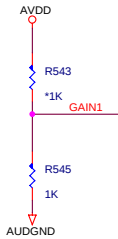
	W/O Media Bay	W Media Bay
R472, R473	DNS	STUFF
R94, R96	DNS	STUFF
R474, R101	STUFF	DNS
R48, R50	STUFF	DNS
Q10, Q35	DNS	STUFF
R340, C553	DNS	STUFF
R73, CN23	DNS	STUFF
R51, CN19	STUFF	DNS
R574	STUFF	DNS

ODD Connector

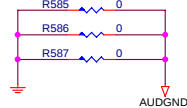
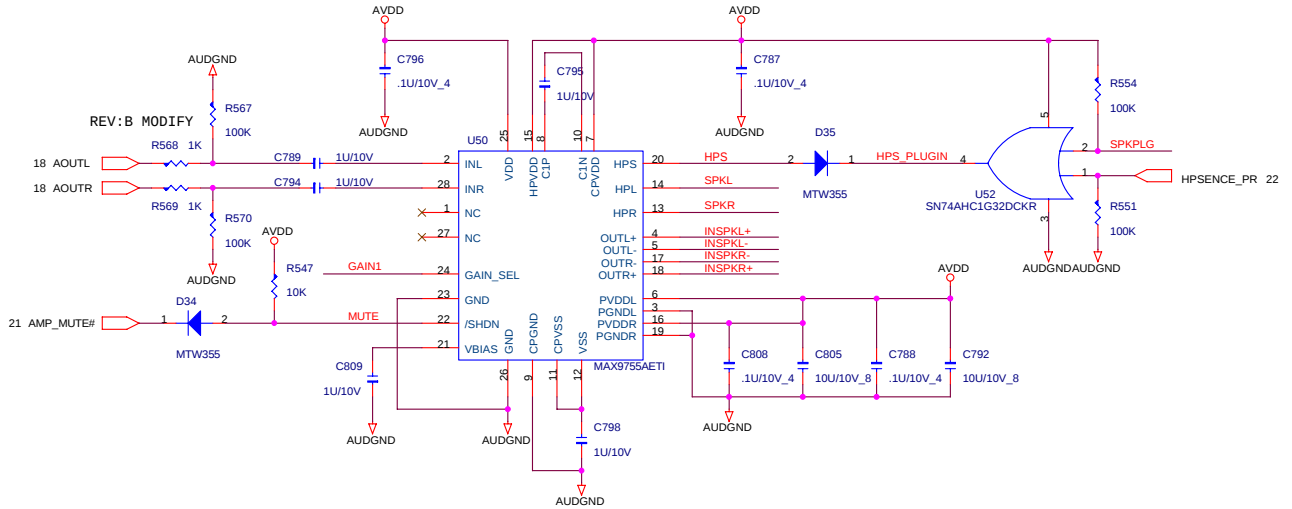
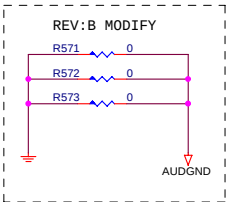
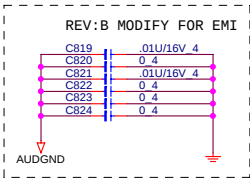
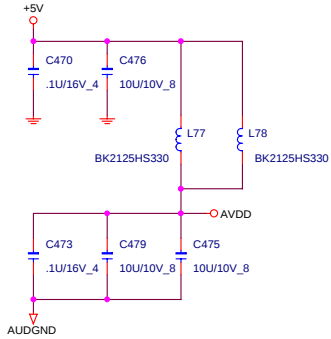




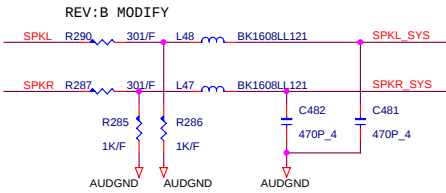
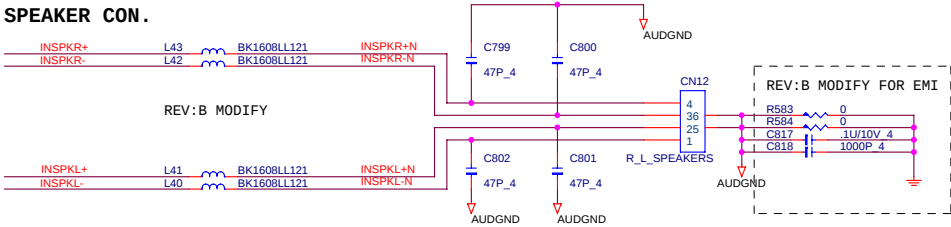
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



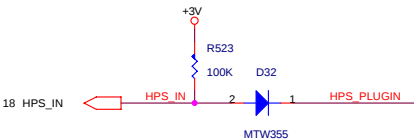
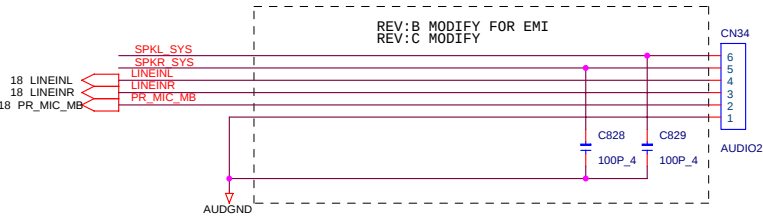
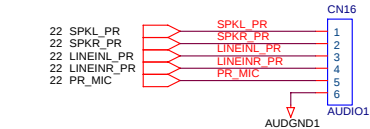
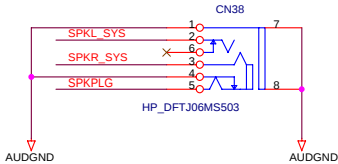
AMP POWER



SPEAKER CON.



LINE OUT



PROJECT : ZL1
Quanta Computer Inc.

3

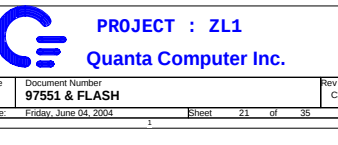
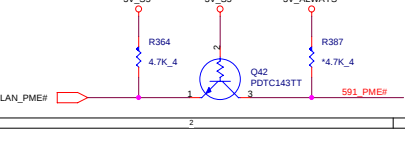
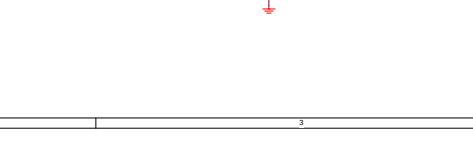


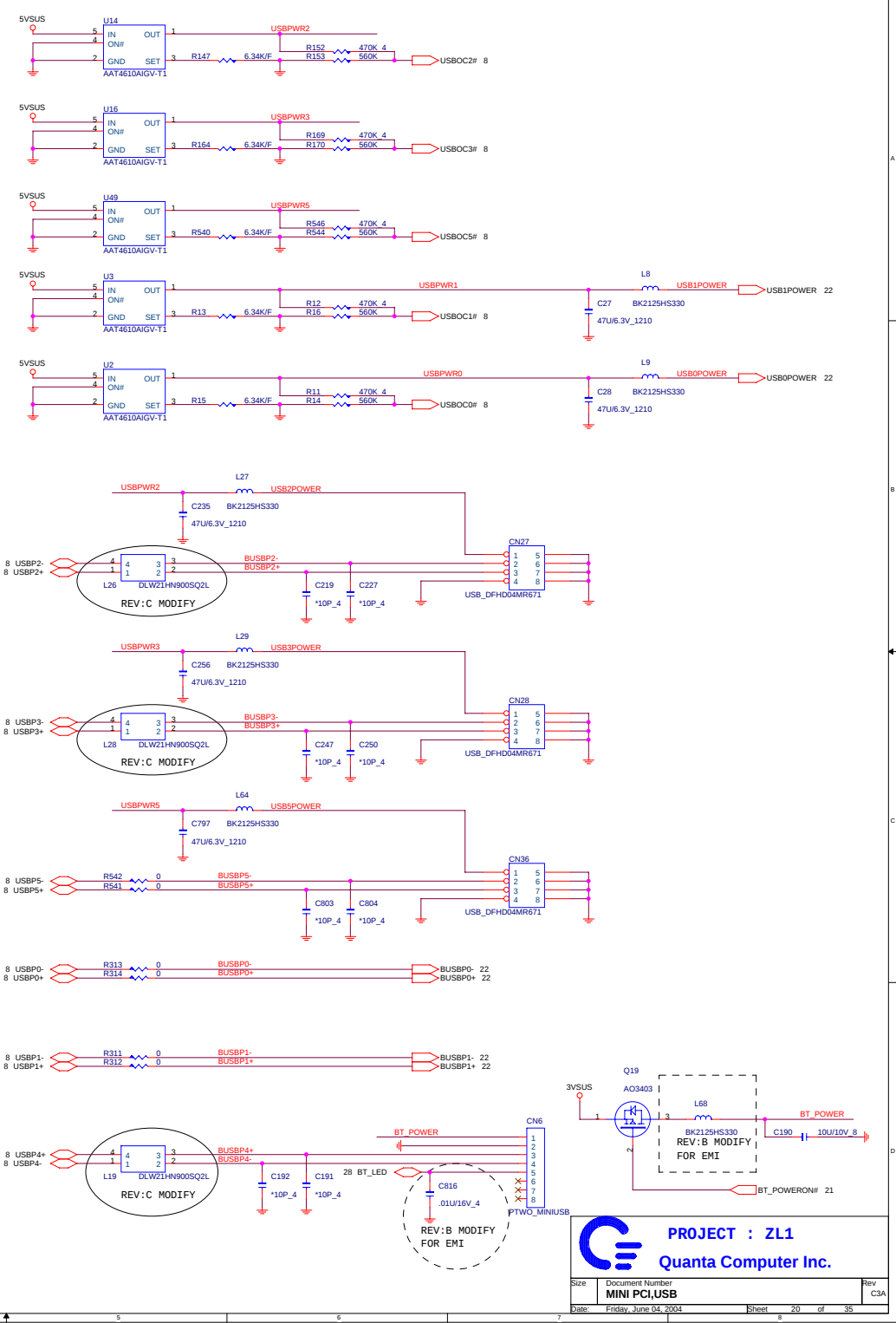
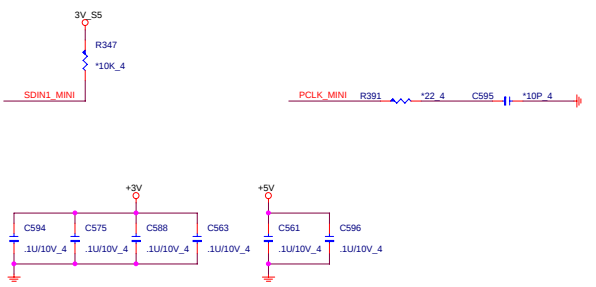
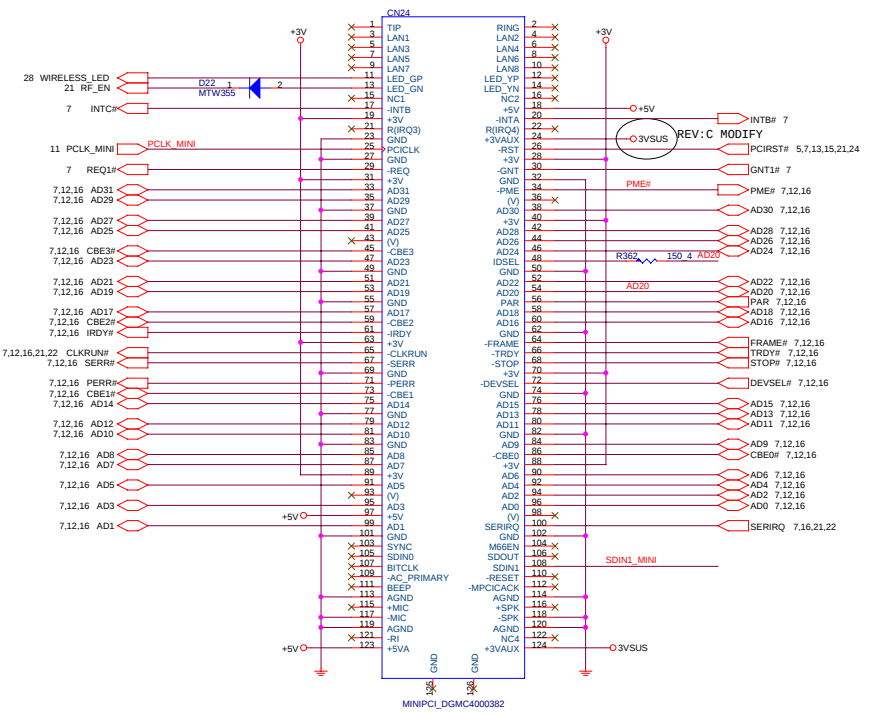
Figure 1 shows the pin connections for the module. The connections are as follows:

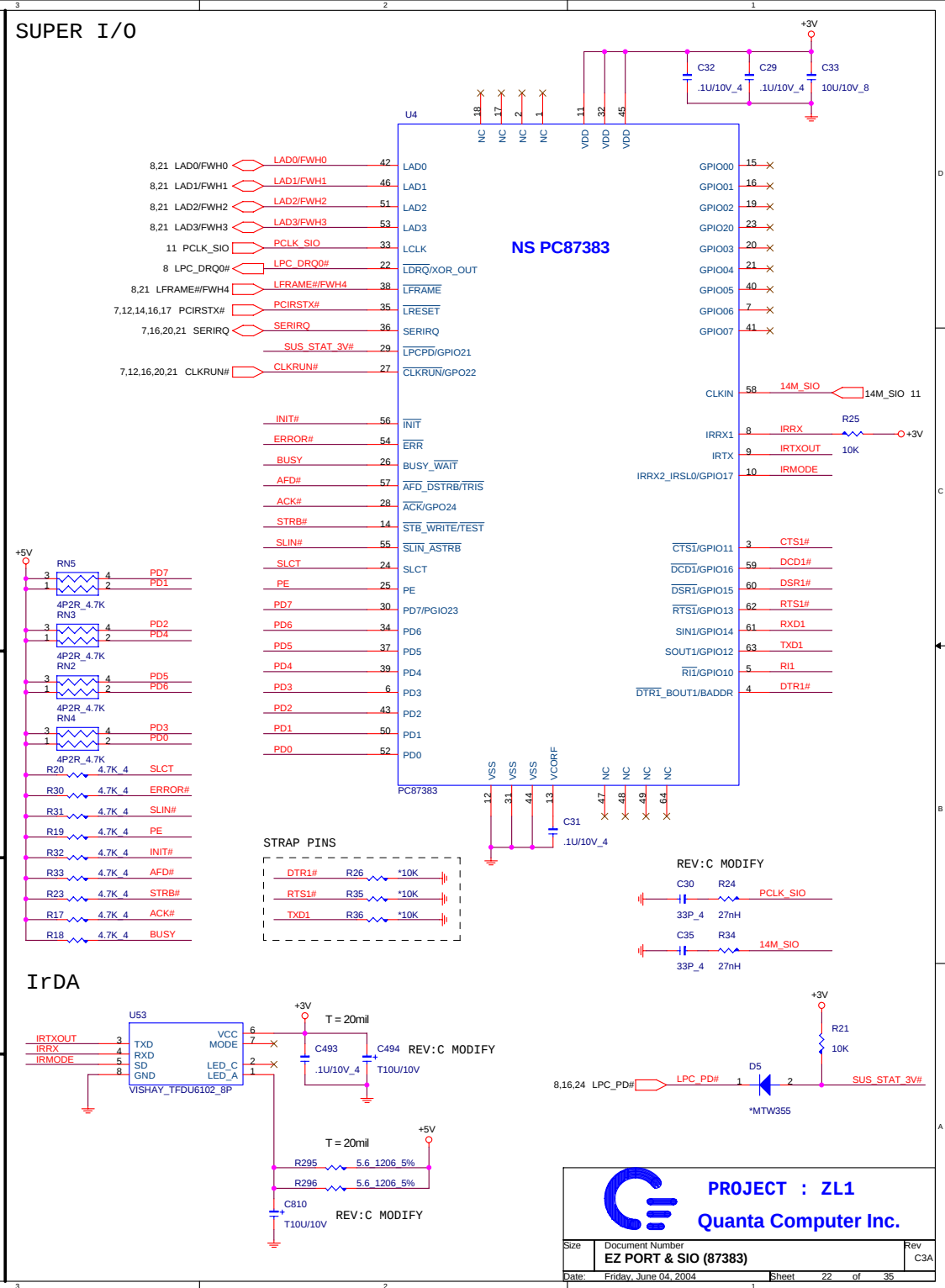
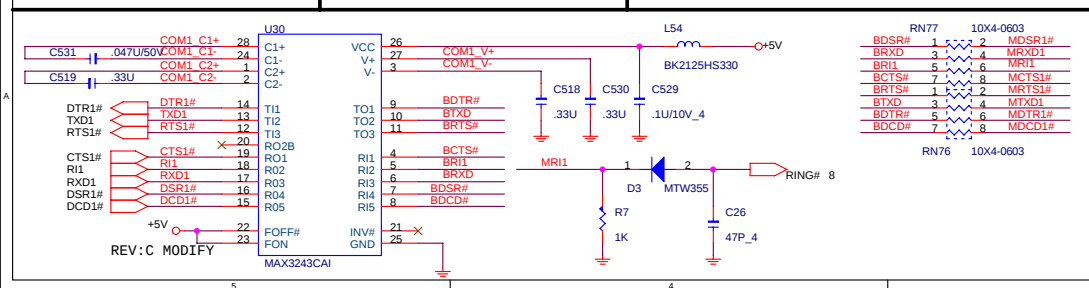
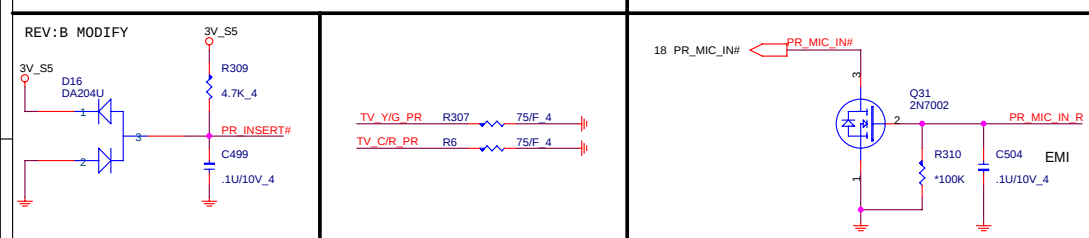
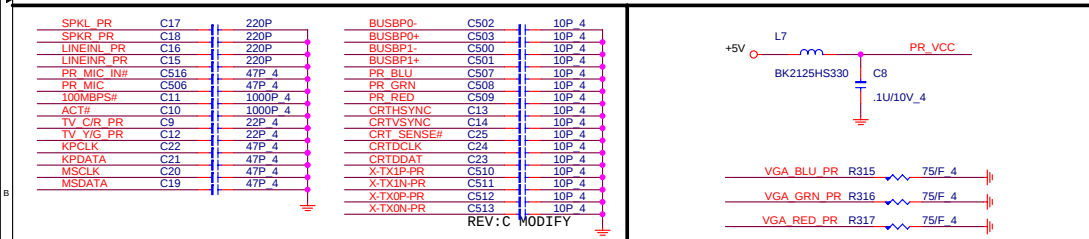
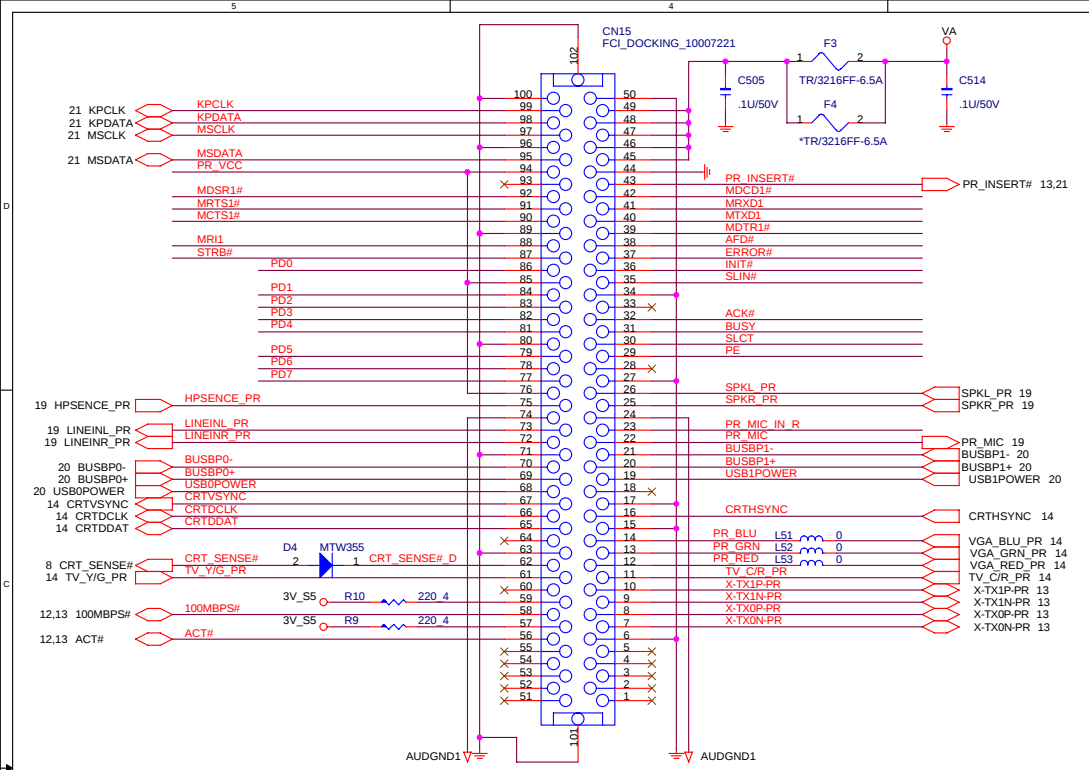
- MBCLK** is connected to **R125** (4.7K) and then to **3V_ALWAYS**.
- MBDATA** is connected to **R131** (4.7K) and then to **3V_ALWAYS**.
- WIRELESS_SW#** is connected to **R360** (4.7K) and then to **+3V**.
- BLUETOOTH_SW#** is connected to **R358** (4.7K) and then to **+3V**.

04, 2004	Sheet	21	01	33
1				

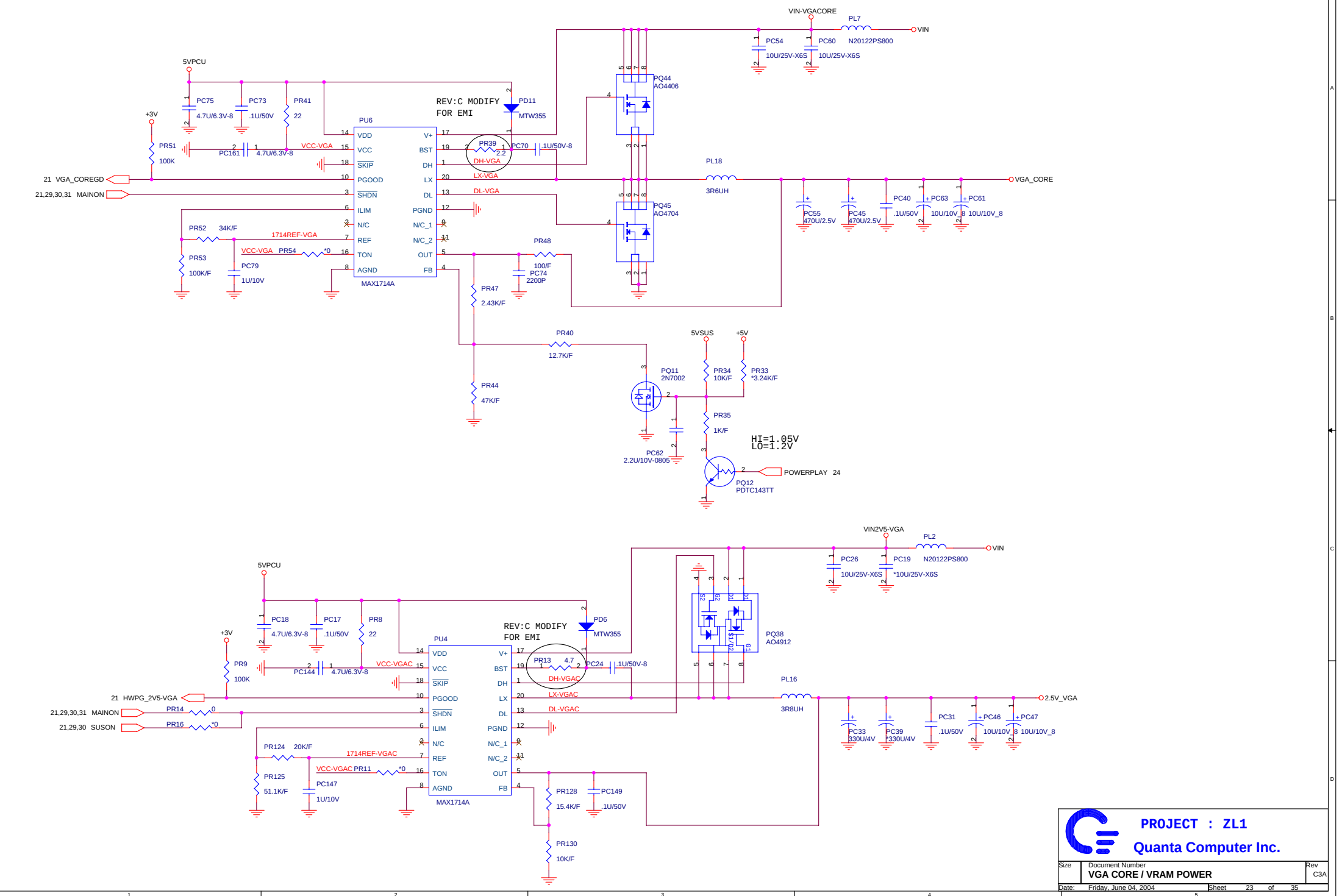
ID Select : AD20
Interrupt Pin : INTB#, INTCH#
Request Indicate : REQ1#
Grant Indicate : GNT1#

MINI-PCI

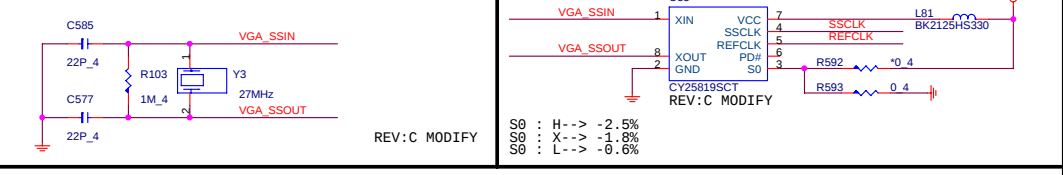
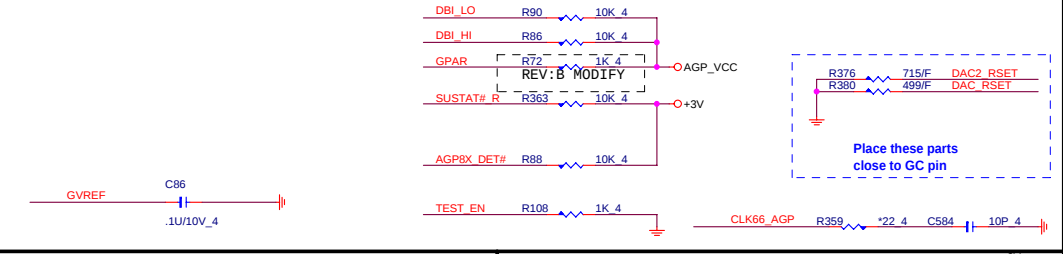
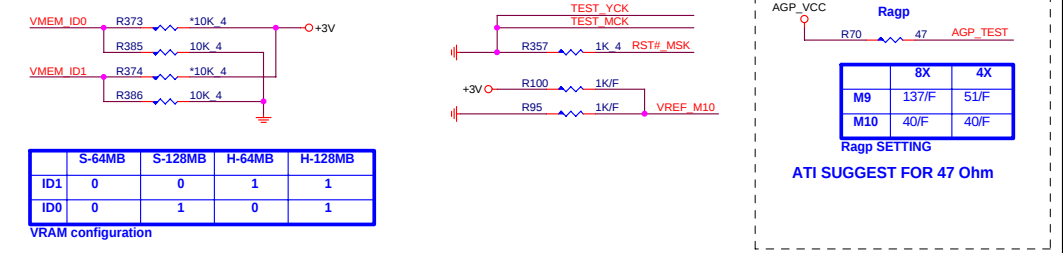
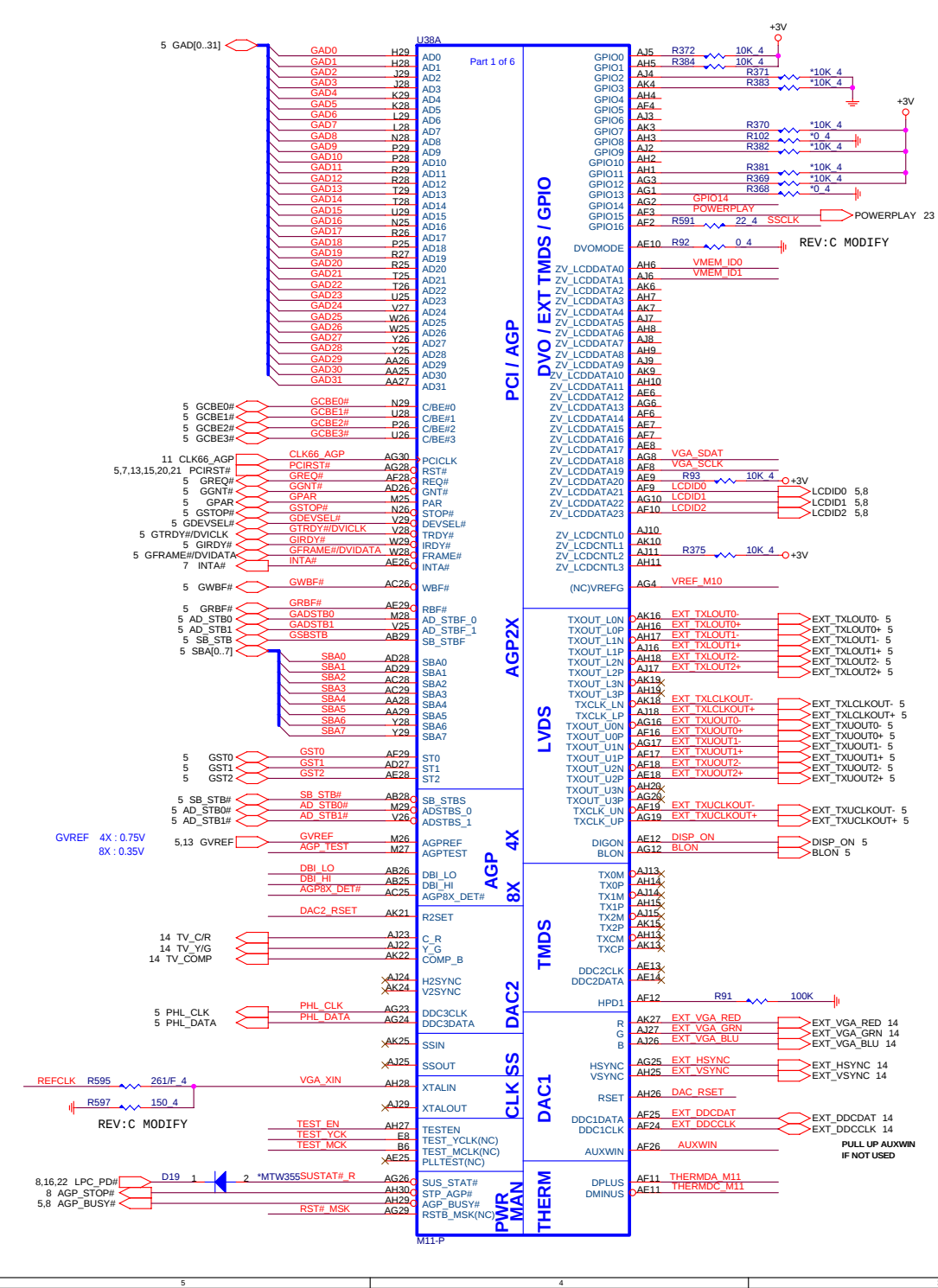




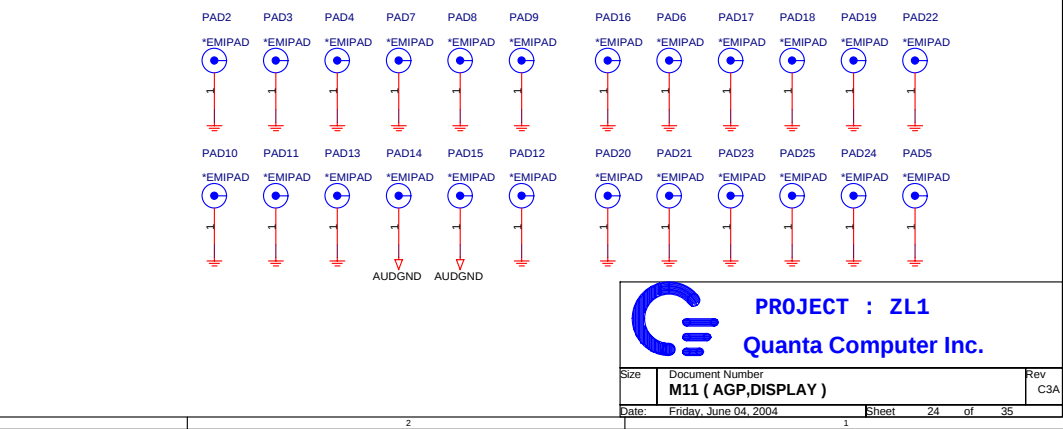
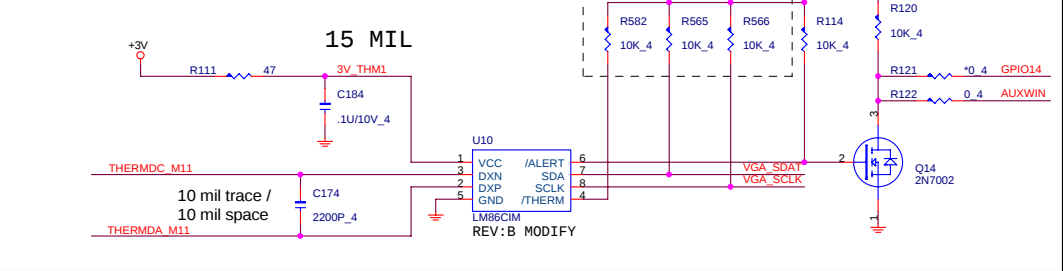
M11 Core & BATTERY



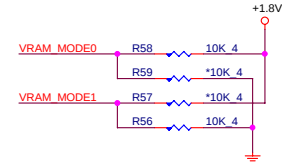
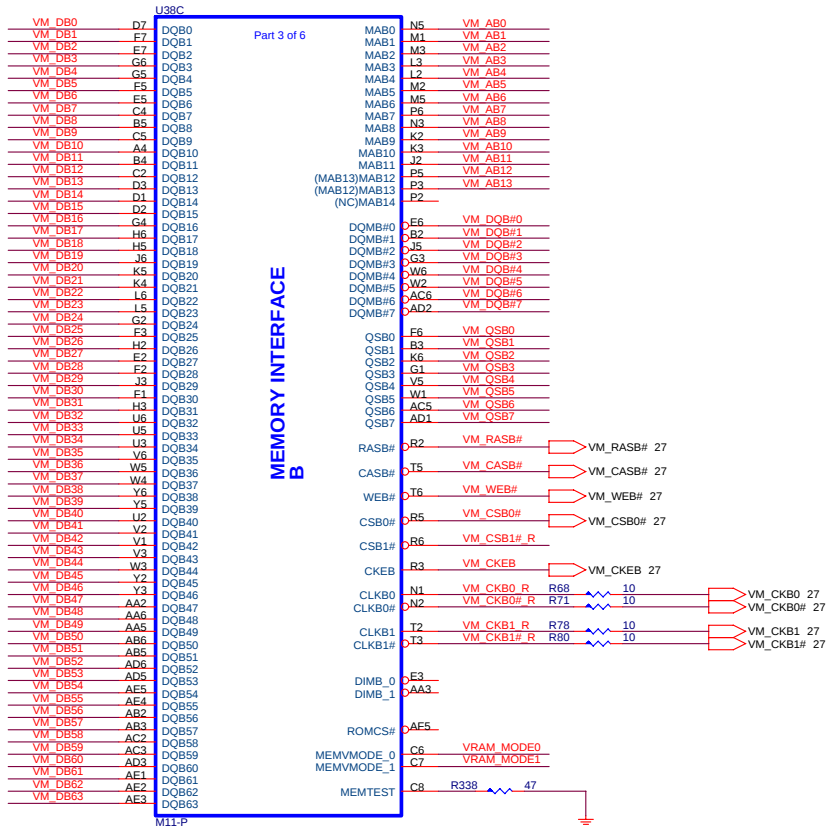
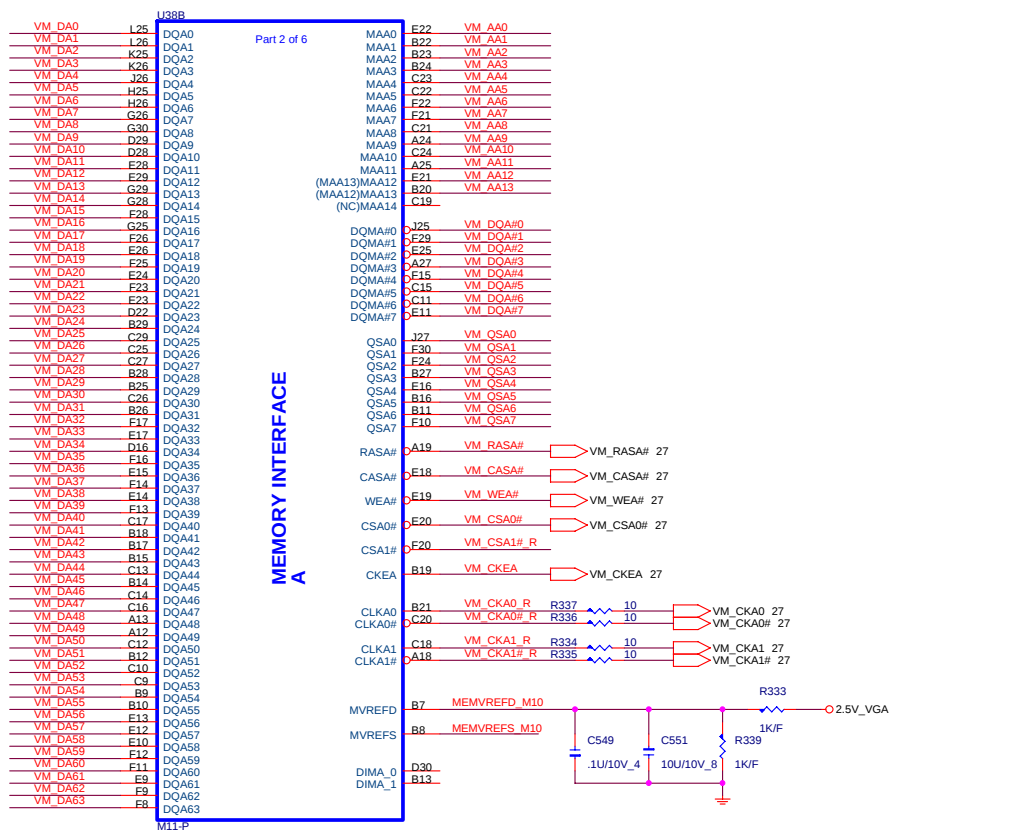
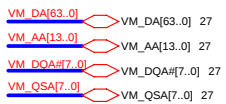
VGA(AGP,DISPLAY)



Thermal Monitor



VGA(MEMORY I/F)



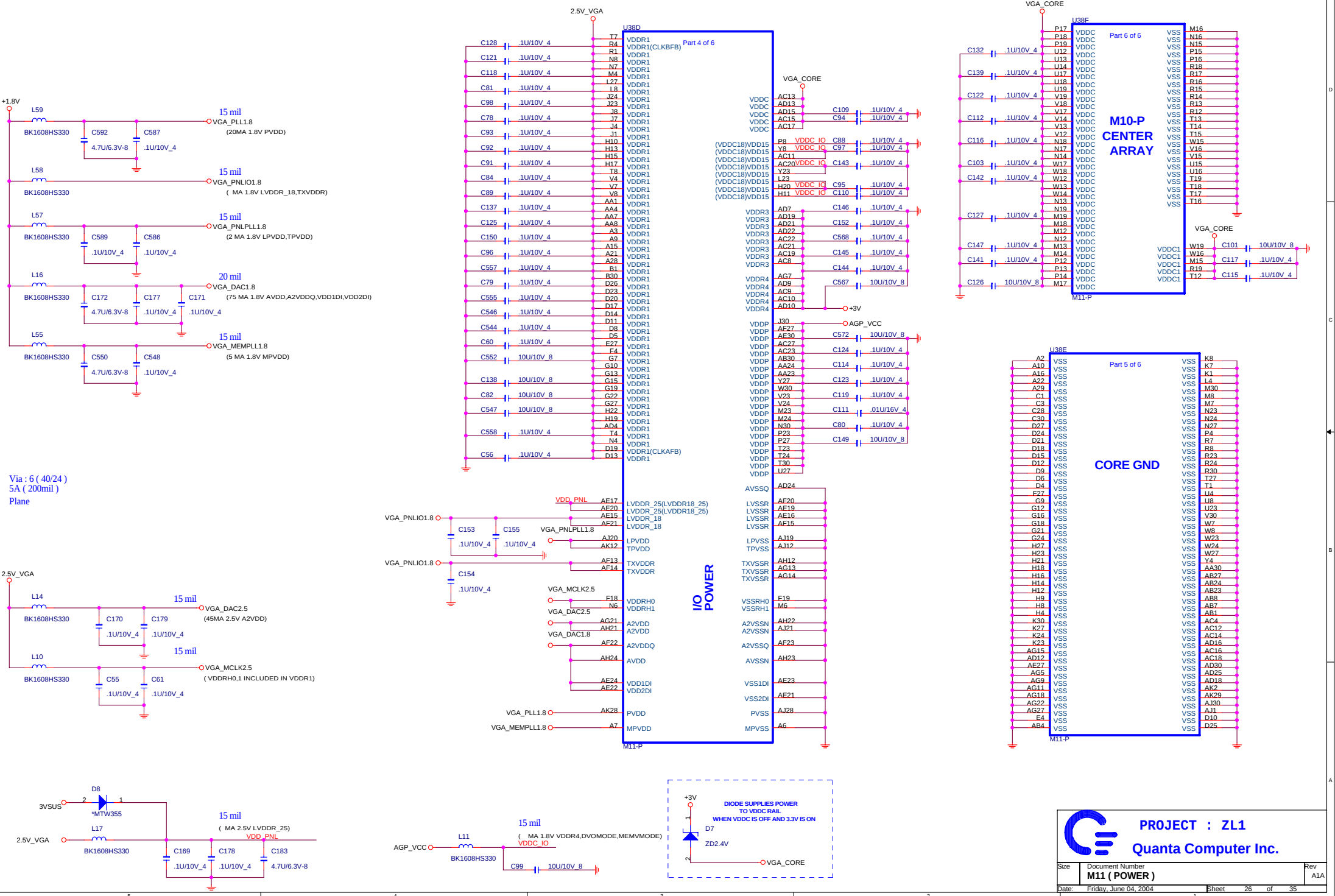
	VRAM_MODE1	VRAM_MODE0
2.5V	0	1
1.8V	1	0
2.8V	1	1

VRAM type setting

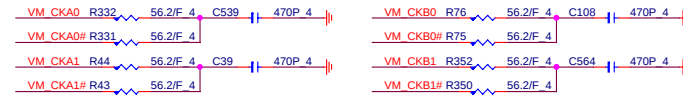


PROJECT : ZL1
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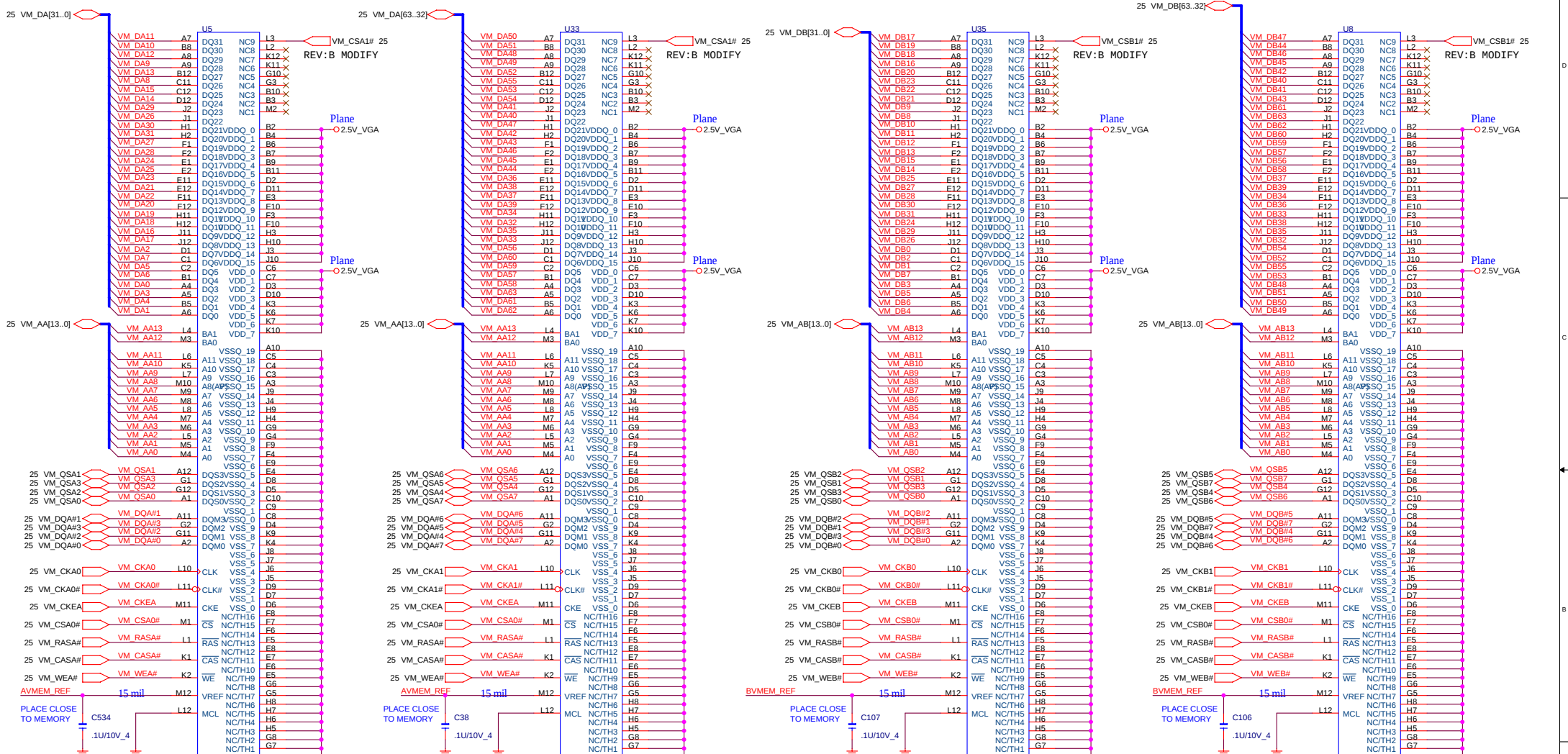
VGA(POWER)



VRAM Channel A



VRAM Channel B

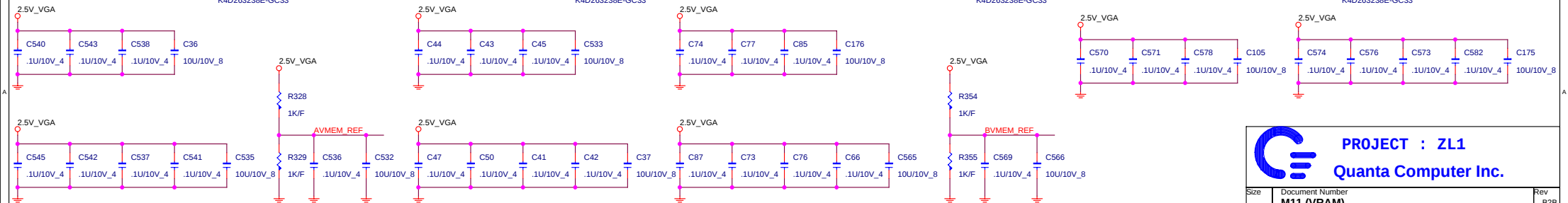


4Mx32 or 8Mx32
K4D263238E-GC33

4Mx32 or 8Mx32
K4D263238E-GC33

4Mx32 or 8Mx32
K4D263238E-GC33

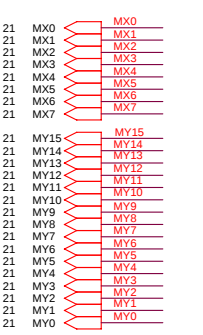
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K4D263238E-GC33



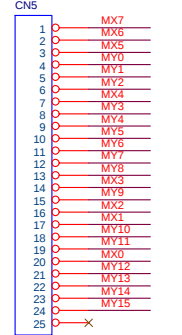
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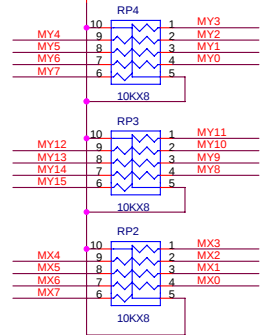
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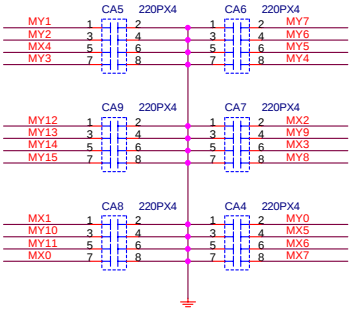
REV:C MODIFY



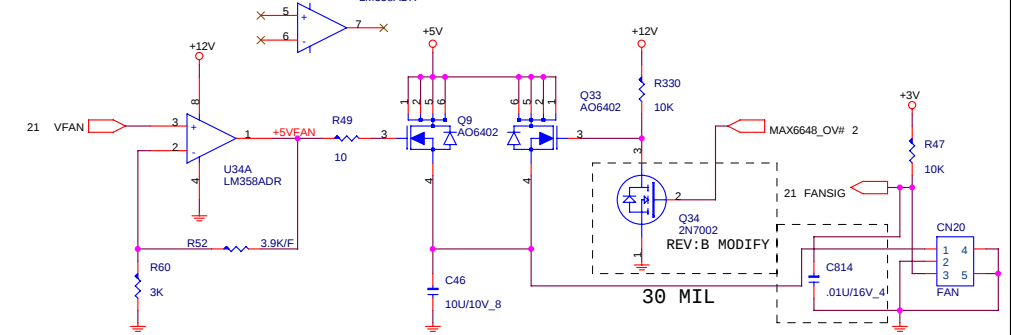
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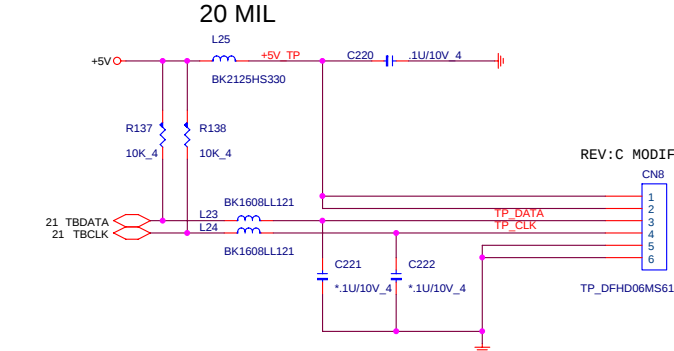
CA5 220PX4



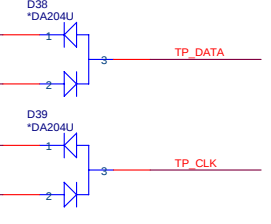
FAN CONTROL



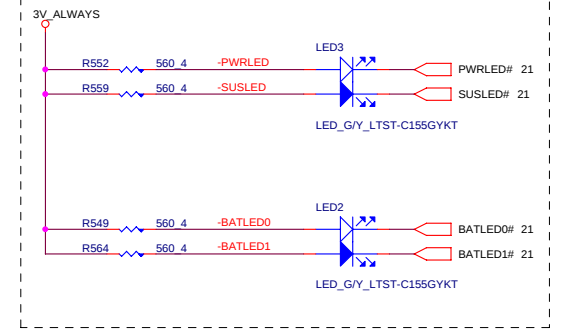
TOUCH PAD



REV:C MODIFY

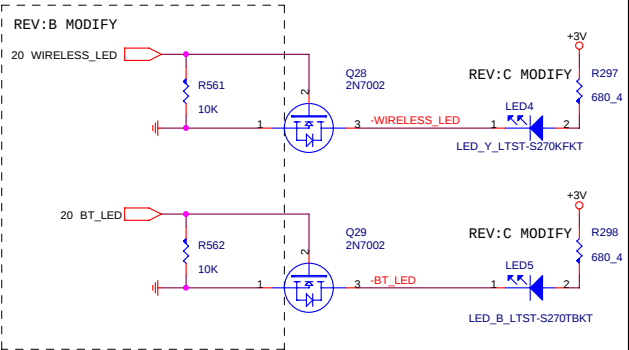
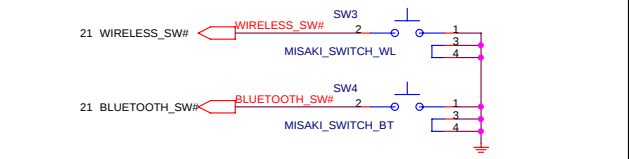
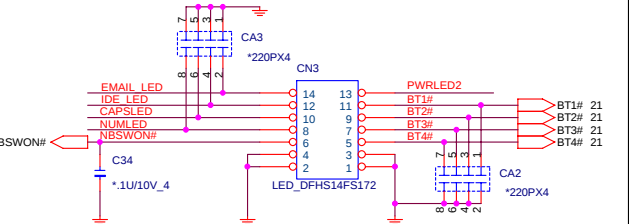
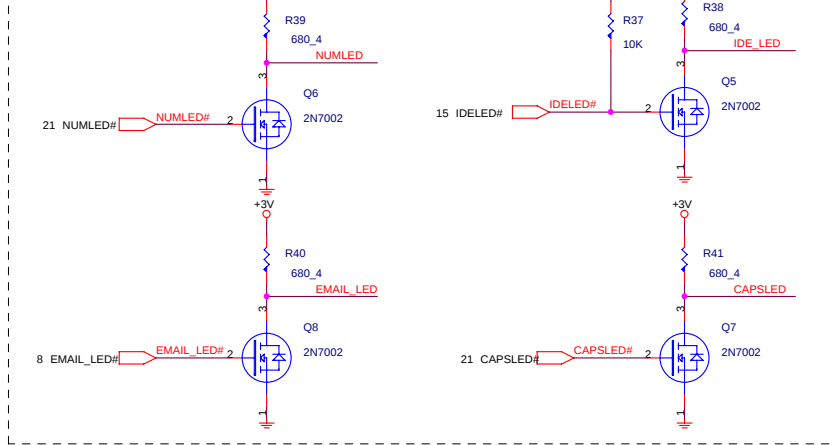


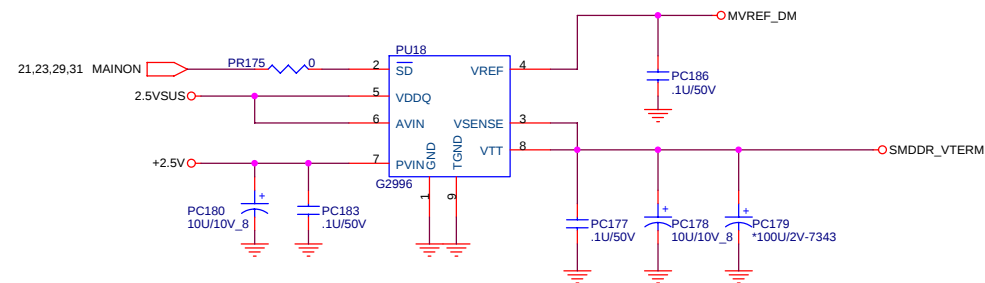
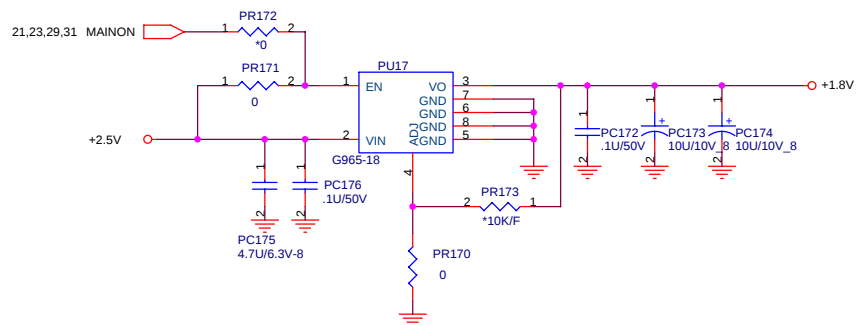
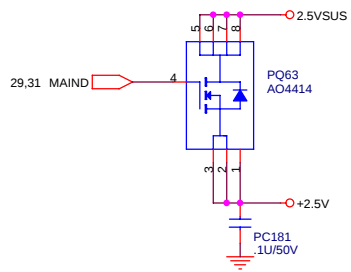
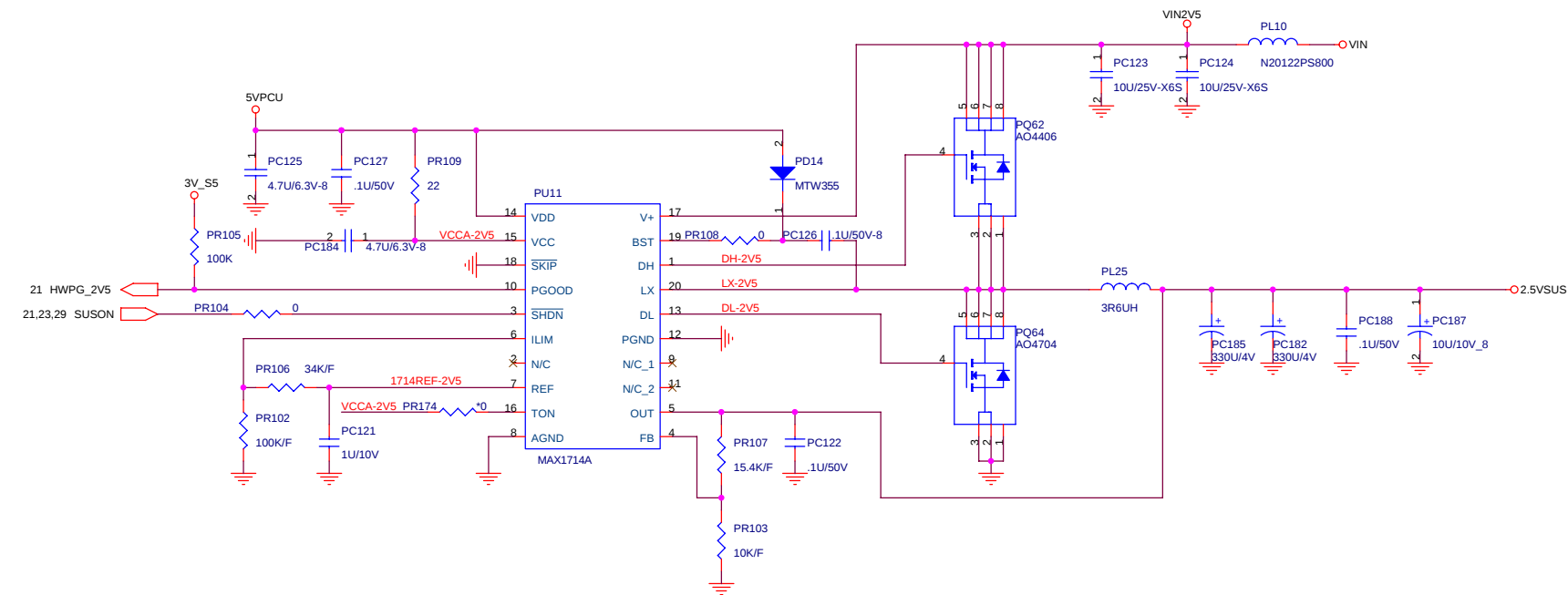
REV:C MODIFY



REV:B MODIFY

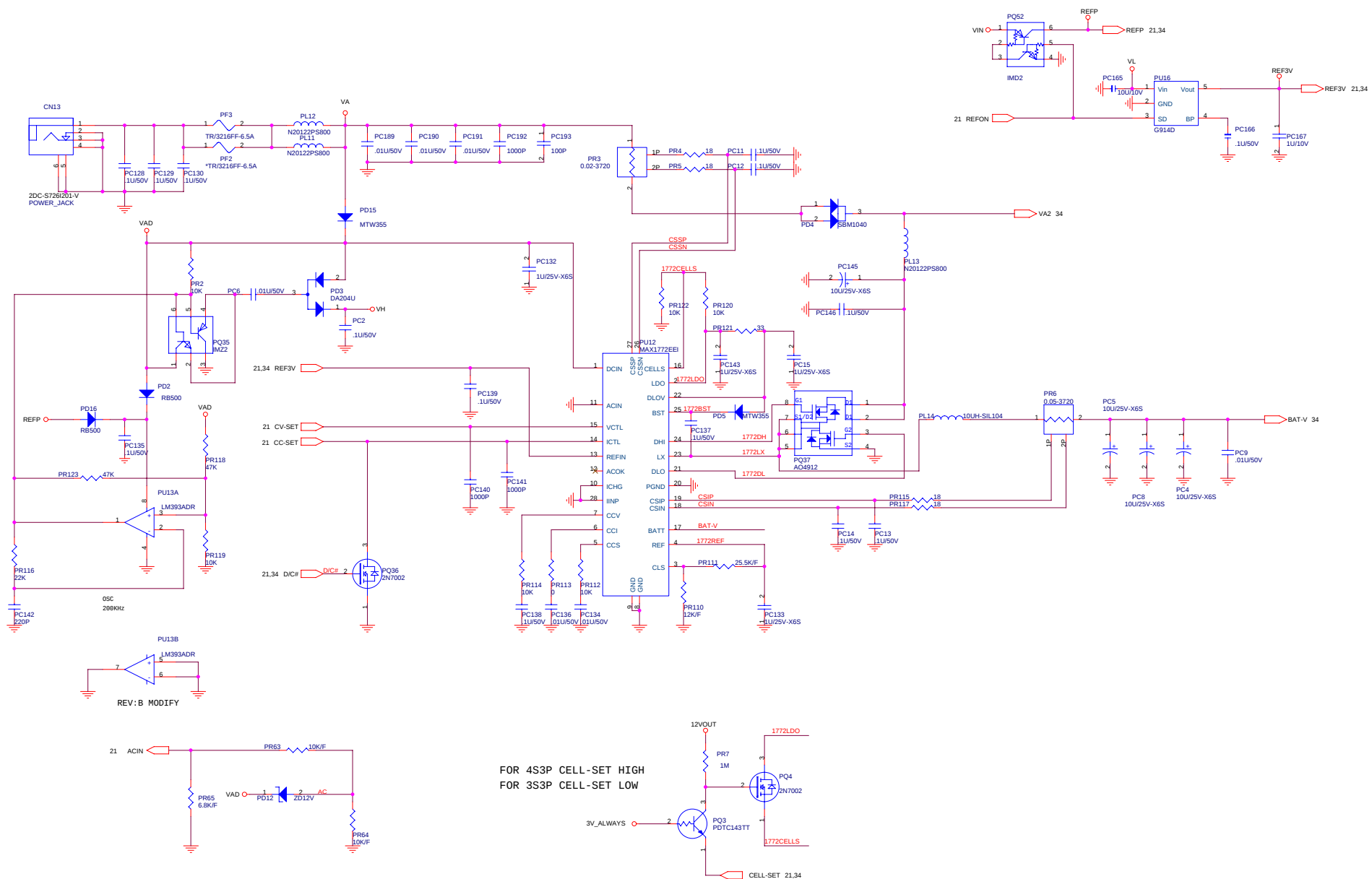
REV:C MODIFY



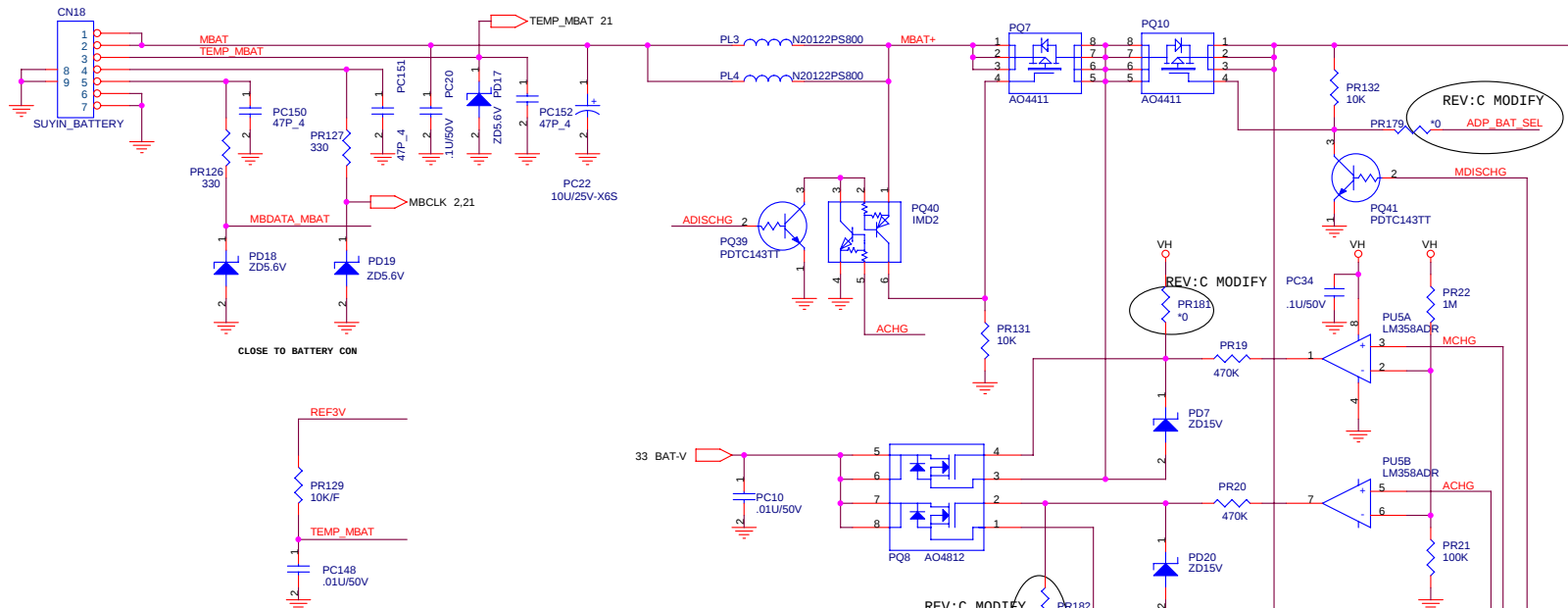


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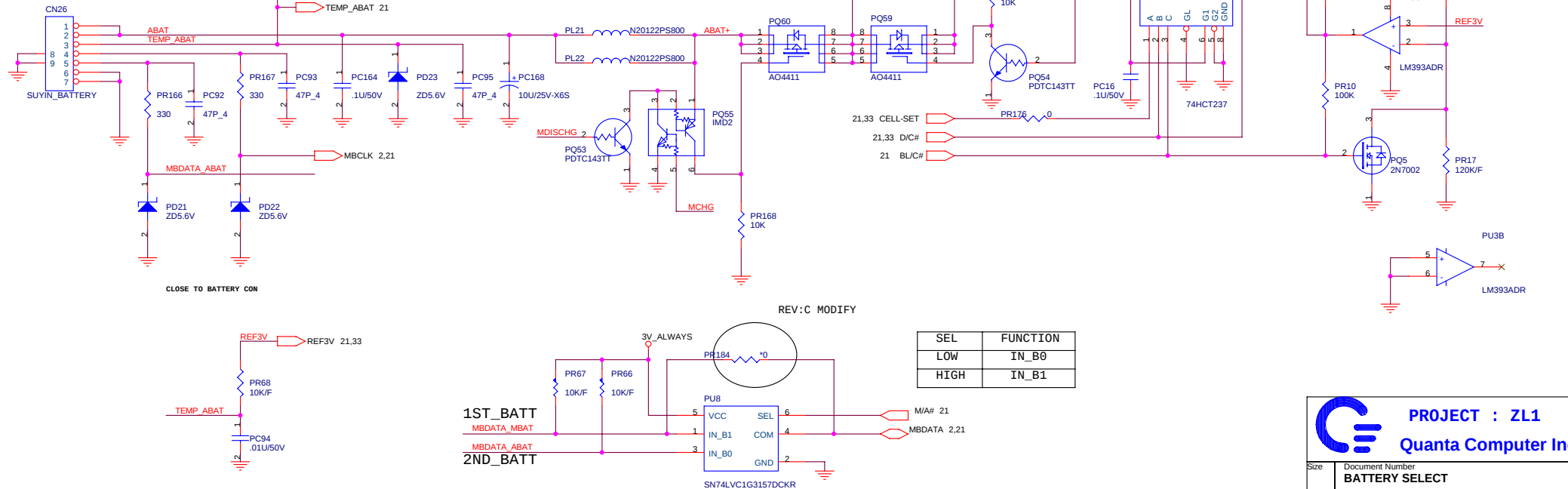
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1ST_BATT_CONN



2ND_BATT_CONN



SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

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	BATTERY SELECT	C3A
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MODEL :		REV :	CHANGE LIST :	PAGE	FROM	TO
ZL1 MotherBoard	B2B		PAGE2 . 1.UN-STUFF R392 FOR THERMTRIP 2. STUFF NUTS	1	A1A	
			PAGE5 . 1.ADD +3V FOR LCD EDID 2.MODIFY C528 VALUE	2	B2B	C3A
			PAGE8 . 1.STUFF R204 FOR THERMTRIPS	3	A1A	
			PAGE12. 1.ADD 10/100 CO LAYOUT CIRCUIT	4	A1A	
			PAGE13. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT 2.MODIFY LAN SWITCH POWER	5	B2B	
			PAGE14. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT	6	A1A	
			PAGE15. 1.ADD RBAYID1 PULL DOWN RESISTOR	7	A1A	
			PAGE18. 1.MODIFY FOR AUDIO QUALITY	8	B2B	
			PAGE19. 1.MODIFY FOR AUDIO QUALITY	9	A1A	
			PAGE20. 1.ADD EMI SOLUTION	10	A1A	
	C3A		PAGE22. 1.MODIFY PR INSERT# PULL HIGH VOLTAGE	11	A1A	C3A
			PAGE23. 1.STUFF PC45 FOR VGA CORE 2. MODIFY 2.5V VGA POWER	12	B2B	C3A
			PAGE24. 1.MODIFY GPAR PULL HIGH RESISTOR 2. ADD PULL HIGH FOR I2C BUS	13	B2B	C3A
			PAGE27. 1.MODIFY CS PIN FOR 128MB VRAM	14	B2B	C3A
			PAGE28. 1.MODIFY LED CIRCUIT 2. MODIFY Q34 TO 2N7002	15	B2B	
			PAGE29. 1.MODIFY MAX6648 OV# CIRCUIT	16	A1A	C3A
			PAGE32. 1.MODIFY CPU CORE VOLTAGE LEVEL	17	A1A	C3A
			PAGE33. 1.MODIFY CHARGER CIRCUIT	18	B2B	C3A
			PAGE34. 1.MODIFY CHARGER CIRCUIT	19	B2B	C3A
			PAGE02. 1.MODIFY MAX6648 AL# TO HIGH ACTIVE	20	B2B	C3A
			PAGE11. 1.MODIFY FOR SIGNAL QUALITY	21	A1A	C3A
			PAGE12. 1.MODIFY LAN LED 2. MODIFY LAN TRACE PULL HIGH	22	B2B	C3A
			PAGE13. 1.MODIFY LAN LED 2. MODIFY DVO_AVDD	23	B2B	C3A
			PAGE14. 1.MODIFY RGB BEAD VALUE	24	B2B	C3A
			PAGE16. 1.REMOVE COMMON MODE CHOKE	25	A1A	
			PAGE17. 1.MODIFY 3 IN 1 CARD READER FOOTPRINT	26	A1A	
			PAGE18. 1.MODIFY LINE IN SIGNAL	27	B2B	
			PAGE19. 1.MODIFY LINE IN SIGNAL	28	B2B	C3A
			PAGE20. 1.MODIFY USB COMMON MODE CHOKE 2. MODIFY MINI-PCI 3V_S5 TO 3VSUS	29	B2B	C3A
			PAGE21. 1.MODIFY 97551 PIN 21	30	A1A	
			PAGE22. 1.MODIFY U30 PIN23 TO +5V	31	A1A	C3A
			PAGE23. 1.MODIFY FOR EMI SOLUTION	32	B2B	C3A
			PAGE24. 1.ADD EMI SOLUTION	33	B2B	
			PAGE28. 1.MODIFY PWRLED2 TO +3V 2. ADD TP ESD PROTECT 3. MODIFY LED CIRCUIT	34	B2B	C3A
			PAGE29. 1.MODIFY FOR POWER SUHTDOWN ISSUE	35	B2B	C3A
PAGE31. 1.MODIFY FOR EMI SOLUTION						
PAGE32. 1.CHANGE SOURCE						
PAGE34. 1.MODIFY CHARGER CIRCUIT FOR COST DOWN						
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	CHANGE LIST	C3A	MB ASSY'S P/N : 31ZL1MB0004	PROJECT LEADER: JIM HSU	DOCUMENT NO: 204	DATE :2004/06/01
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