

ZI6 BLOCK DIAGRAM

CENTRINO

@:ZI66 Internal Gra.

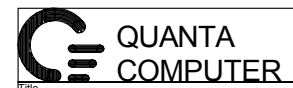
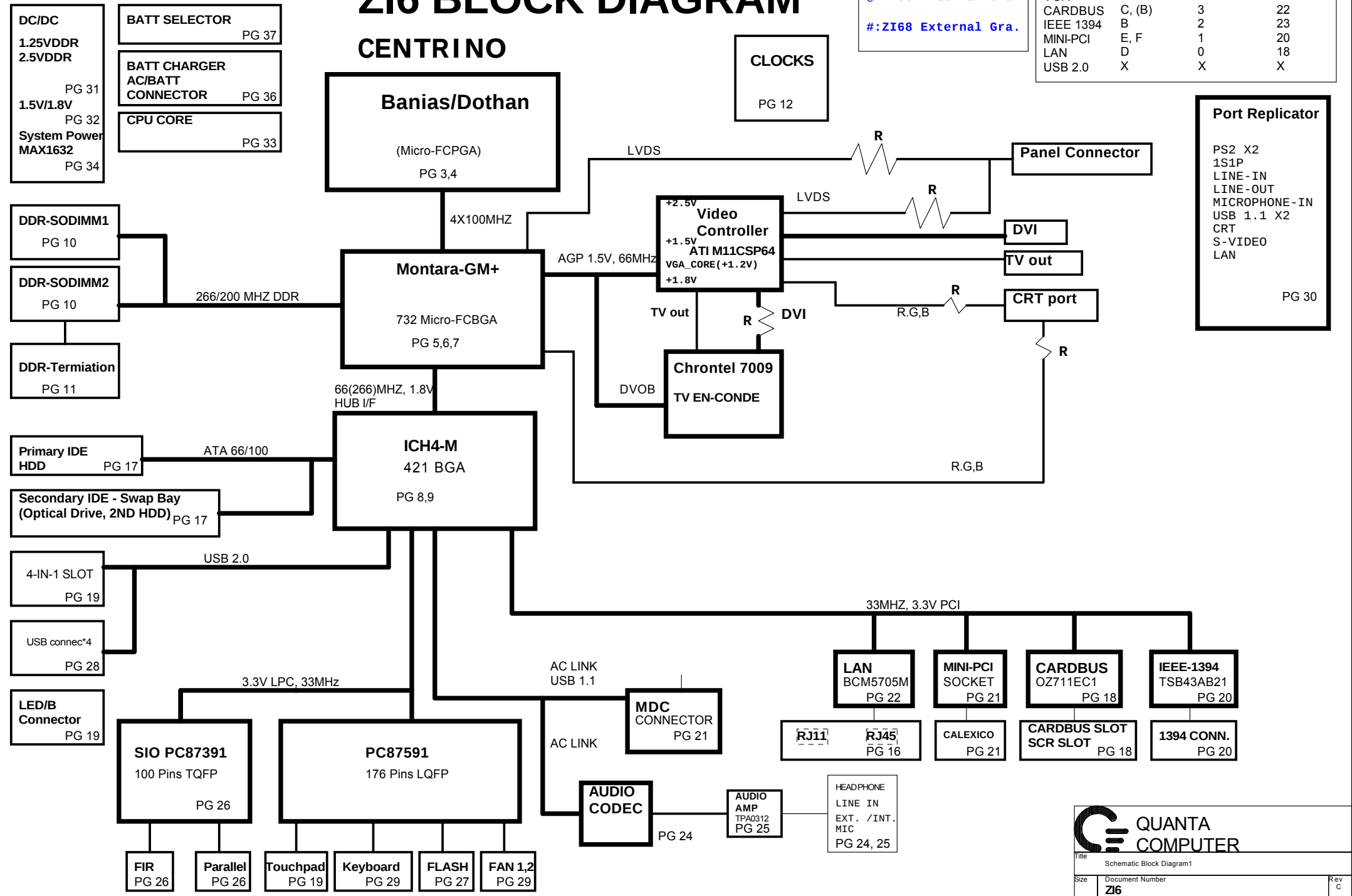
#:ZI68 External Gra.

	PIRQ#	REQ#/GNT#	IDSEL
VGA	X	X	X
CARDBUS	C, (B)	3	22
IEEE 1394	B	2	23
MINI-PCI	E, F	1	20
LAN	D	0	18
USB 2.0	X	X	X

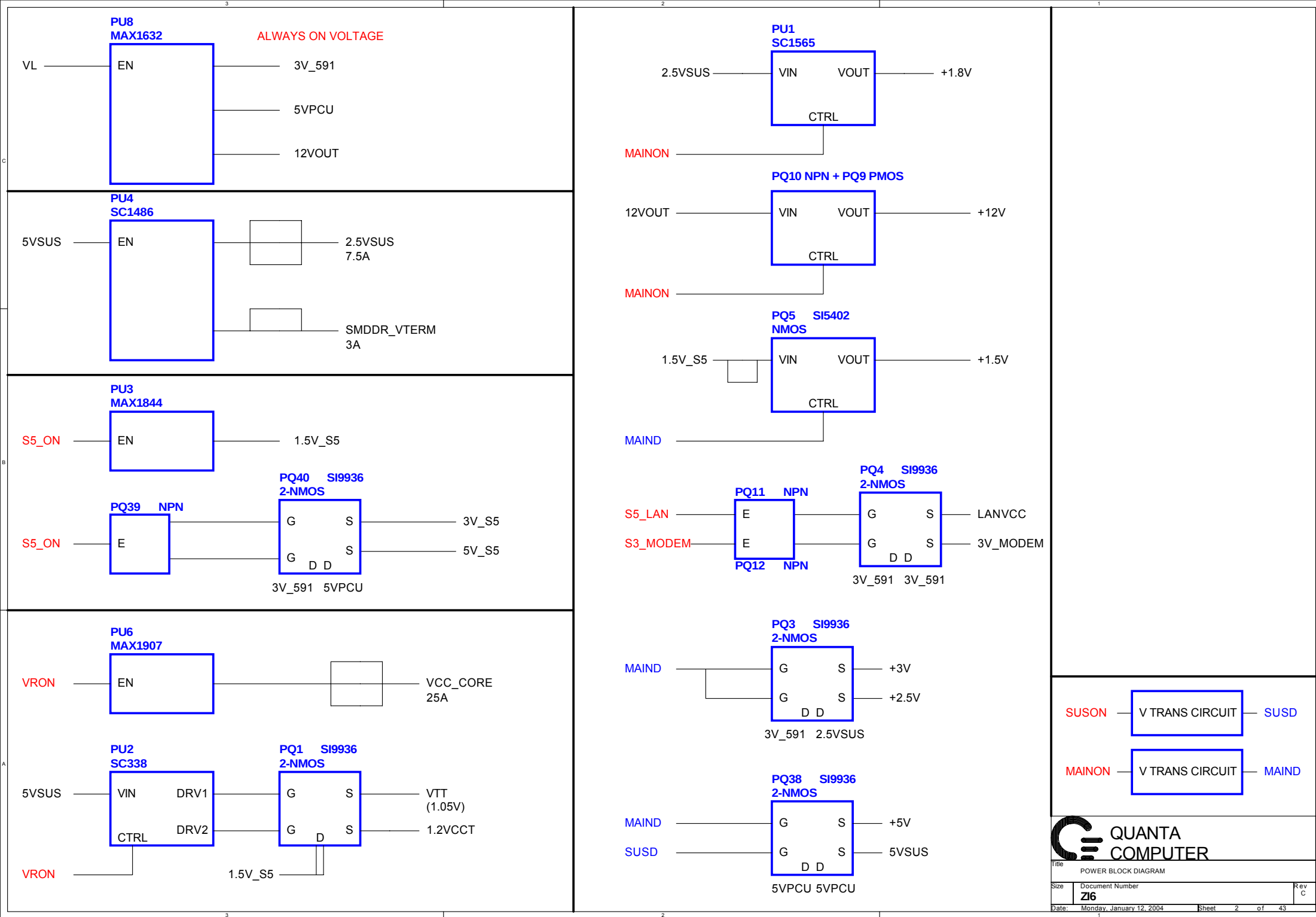
Port Replicator

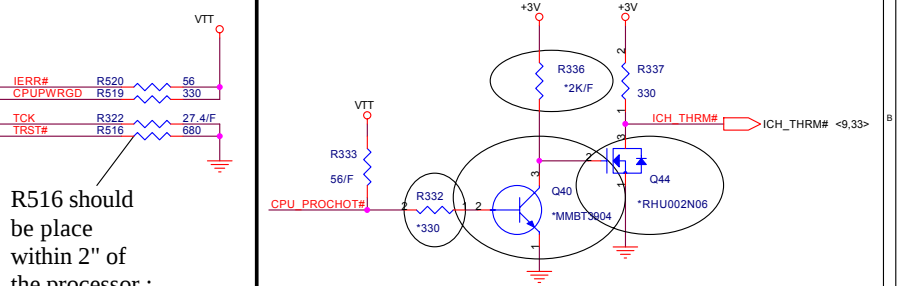
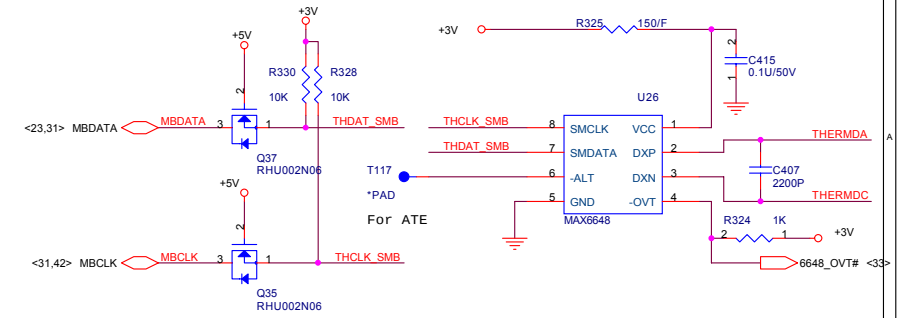
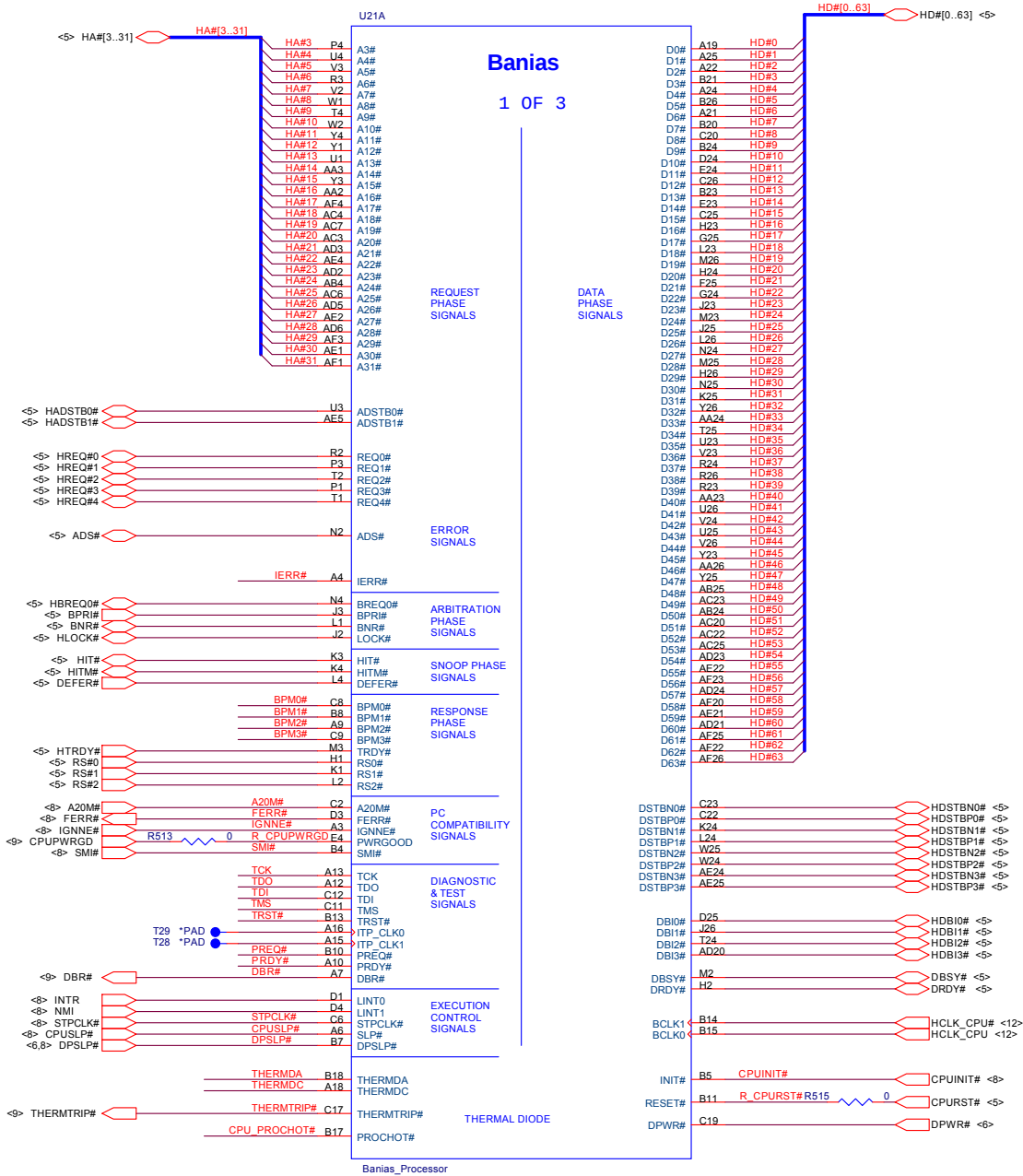
PS2 X2
1S1P
LINE-IN
LINE-OUT
MICROPHONE-IN
USB 1.1 X2
CRT
S-VIDEO
LAN

PG 30

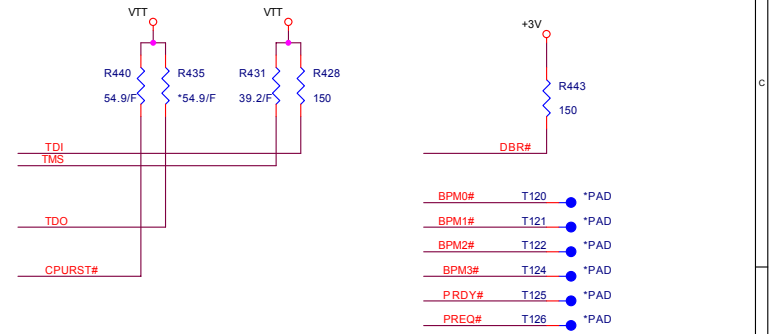


Title Schematic Block Diagram1		
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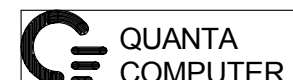


R516 should be place within 2" of the processor ; others place near ITP



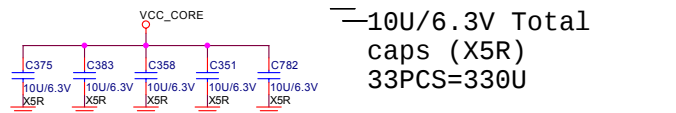
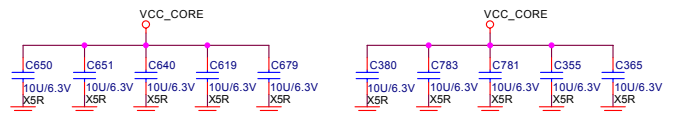
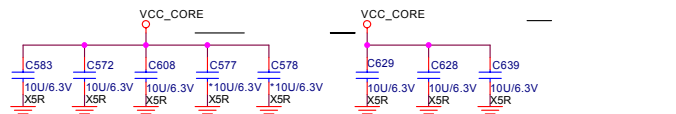
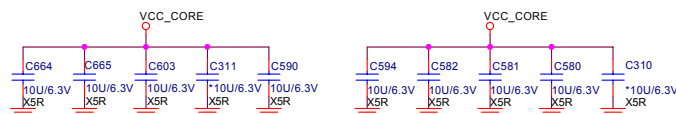
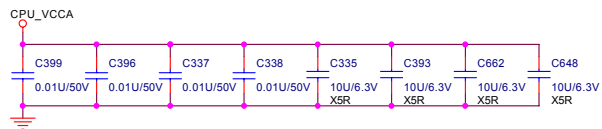
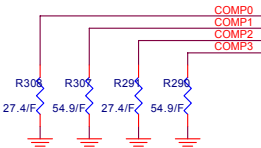
De1 ITP700

De1 R434, R439, C542, R442



Title Banias Processor (HOST)		
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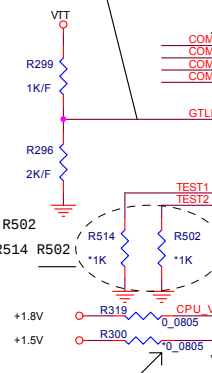
These 4 Resistors need to place with 0.5" of CPU.
Comp0,2 trace need to be zo=27.4 ohm,
comp1,3 traces need to be zo=55 ohm.



—10U/6.3V Total
caps (X5R)
33PCS=330U

GTLREF: 2/3 VCCP+-2%
T<0.5" 25 mils space

25 MILS (>50 MILS PREFERRED) SPACE
T < 0.5"



A0 CPU install R514 R502

A1 CPU not install R514 R502

1.5V option is not used.
It is ok to leave in there.

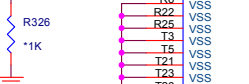
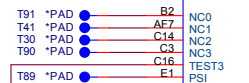
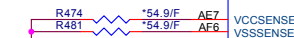
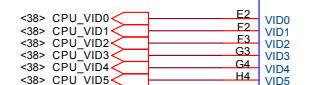
Banias
2 OF 3

POWER, GROUND, RESERVED
SIGNALS

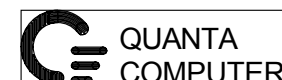
Banias
3 OF 3

POWER, GROUND AND NC

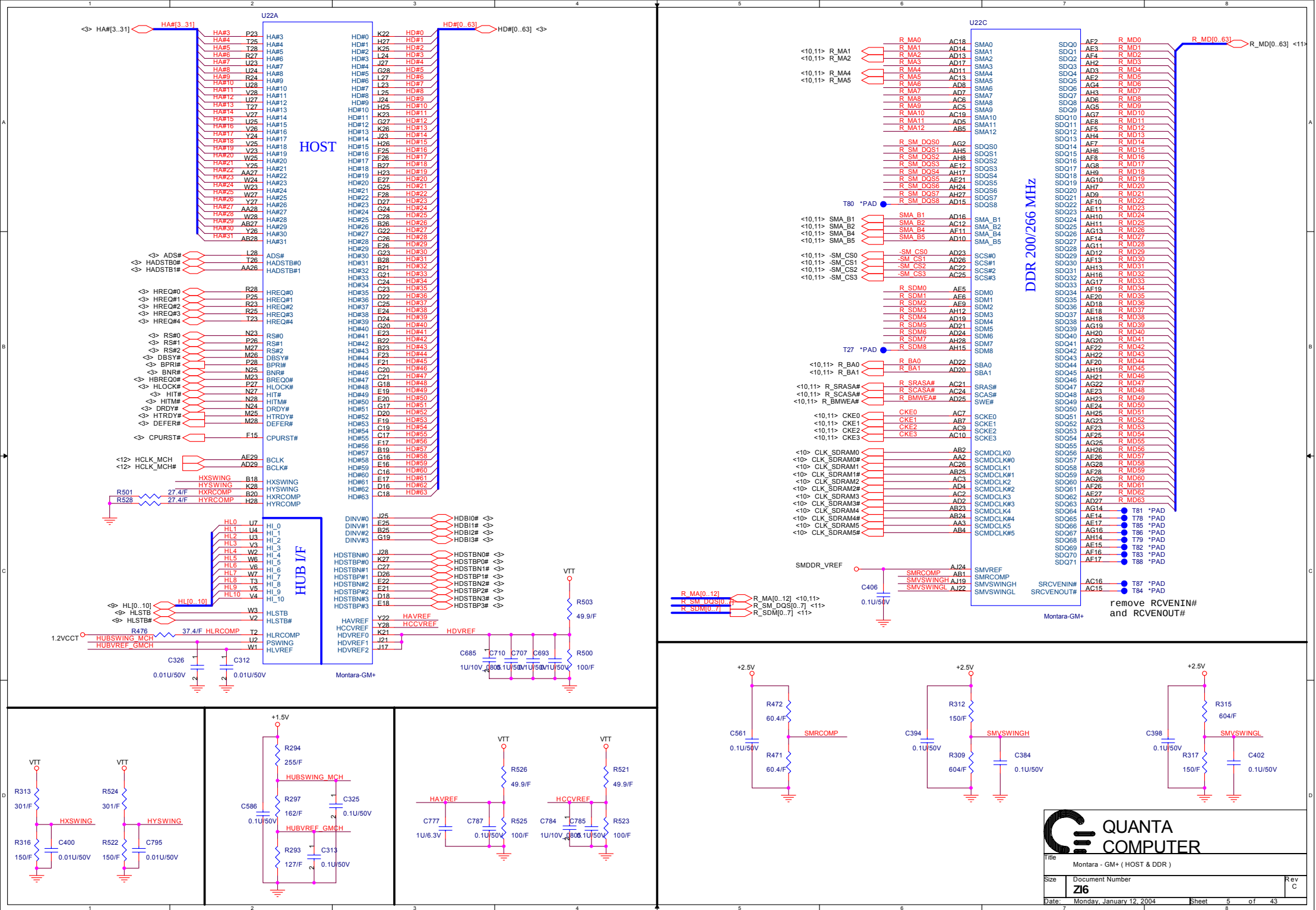
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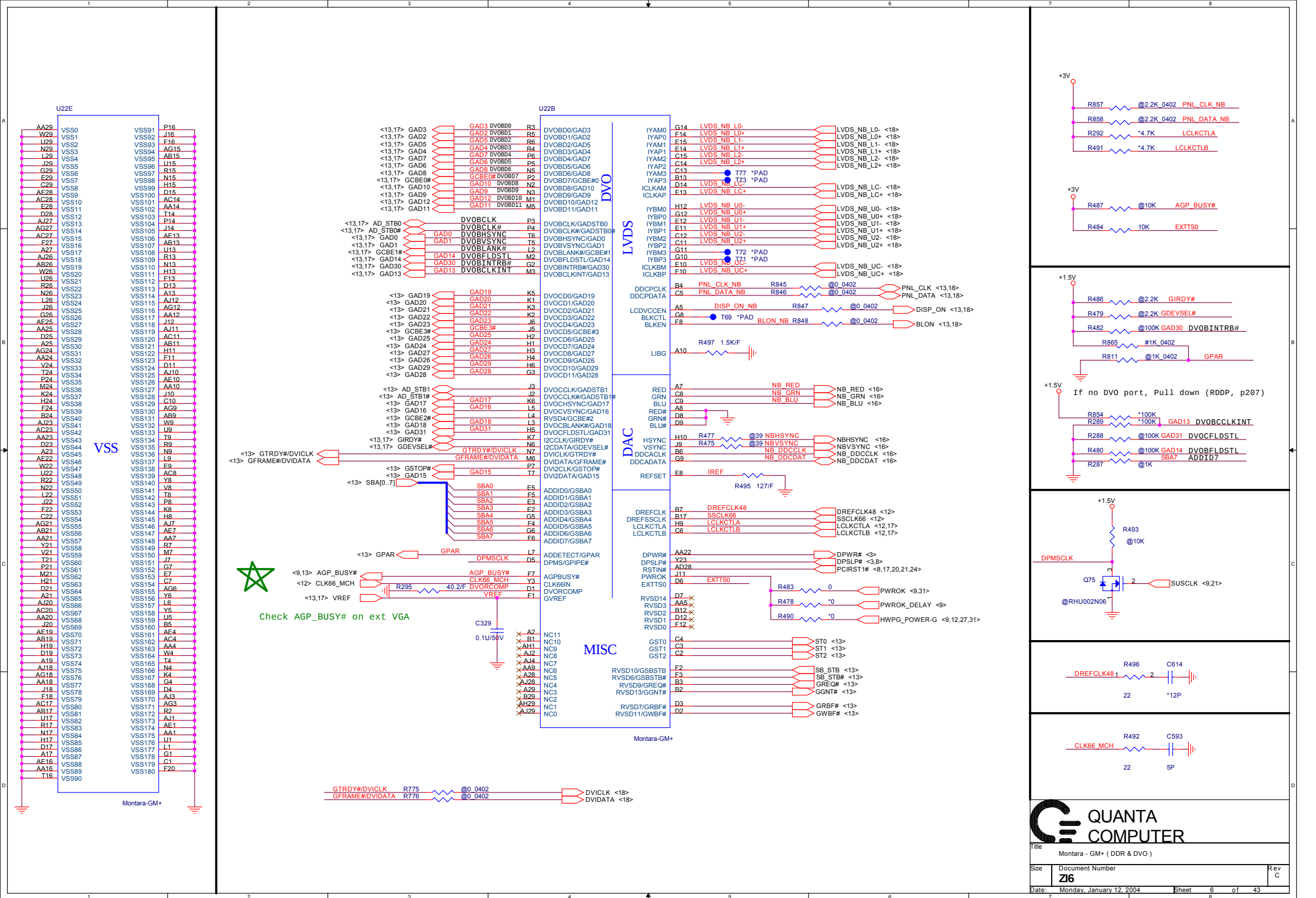


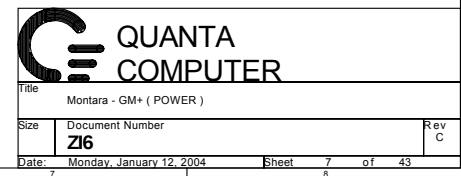
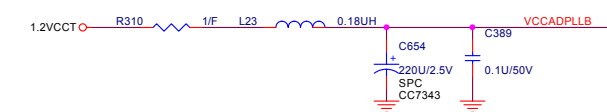
* VCC, B00T 1.2V
VCC_CORE 1.468V
(DEEPER SLEEP) 0.956V
VTT 1.05V
VCCA 1.8V

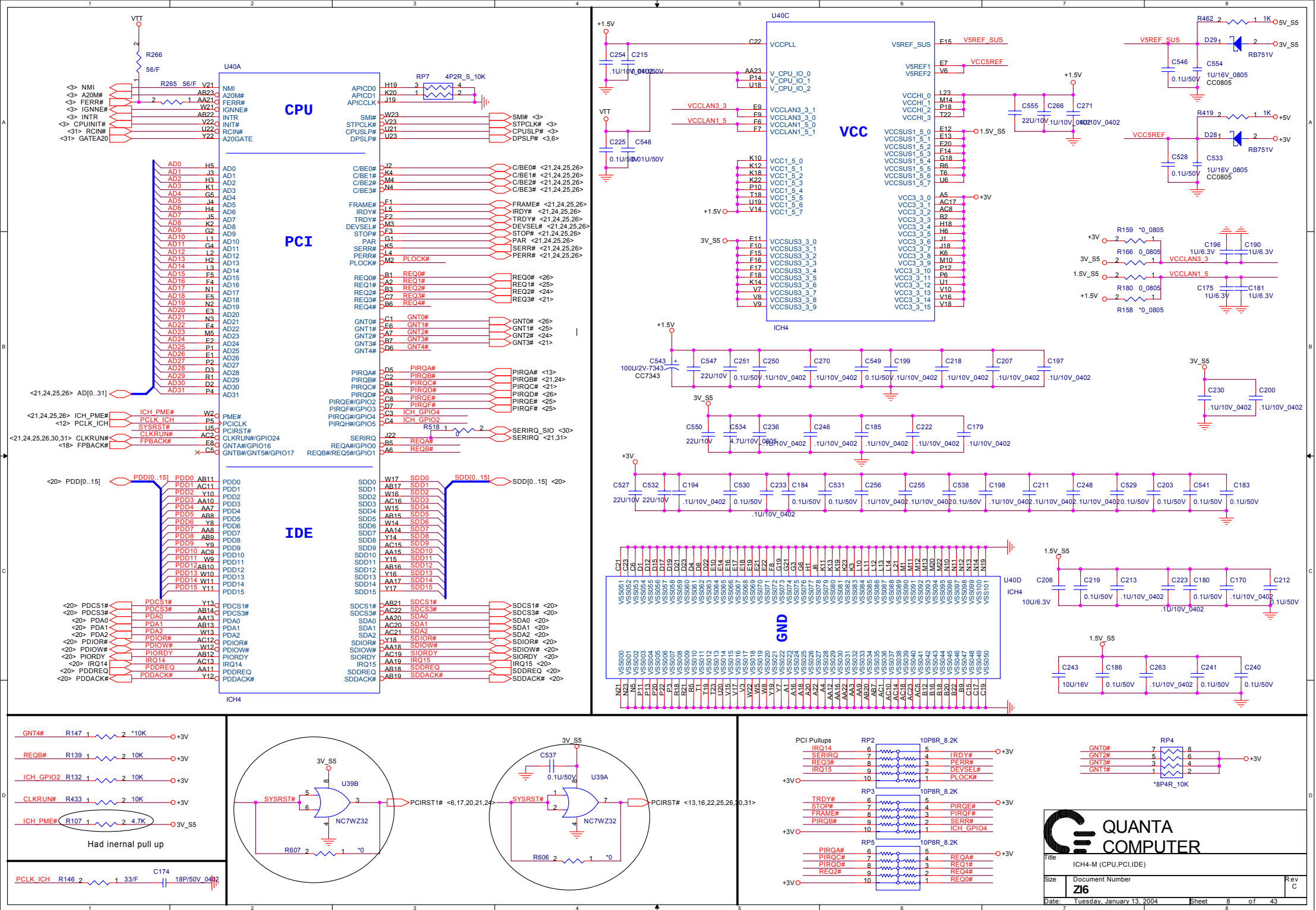


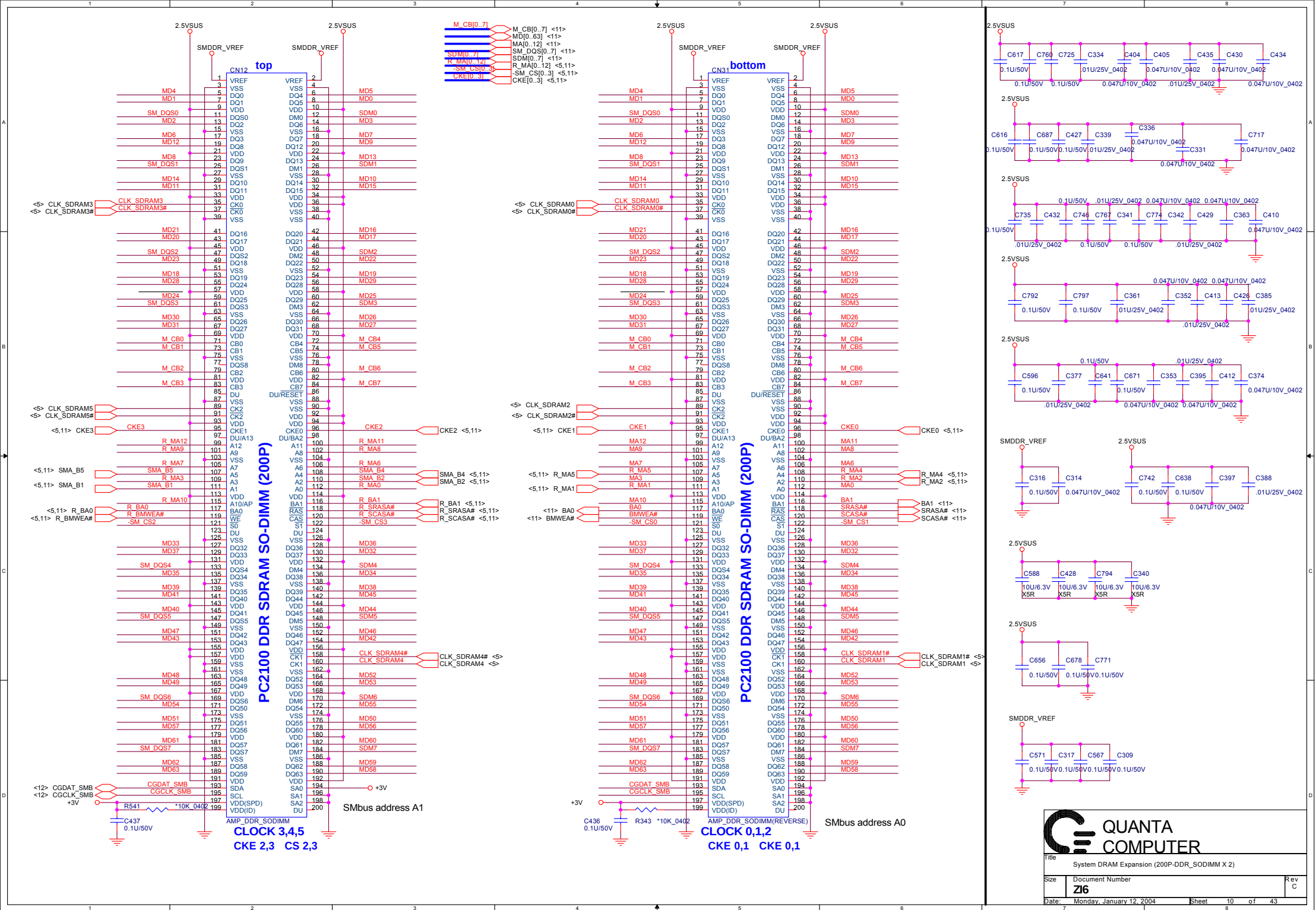
Title Banias Processor (POWER)		
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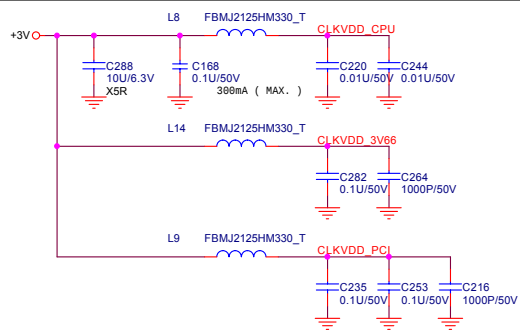
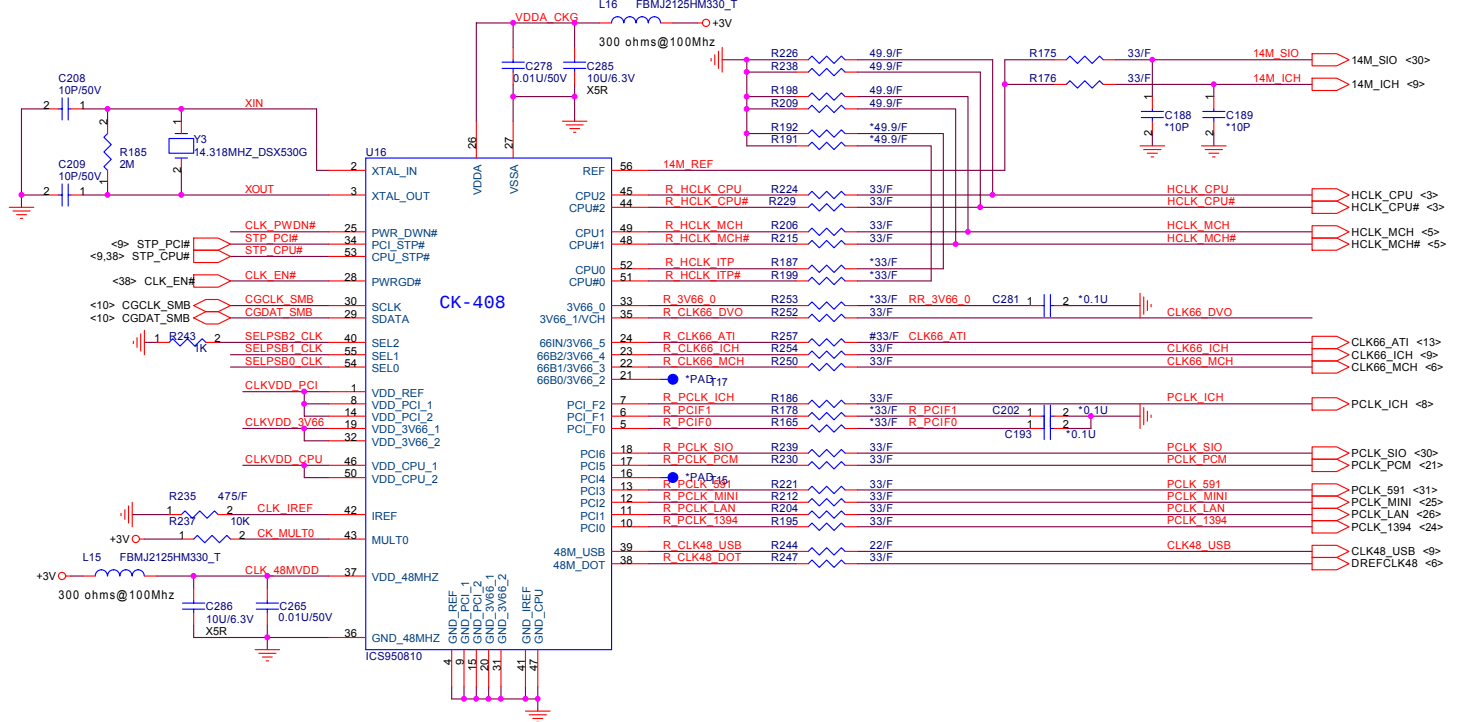
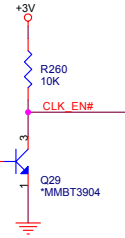
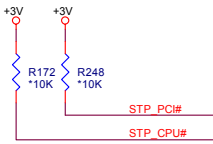






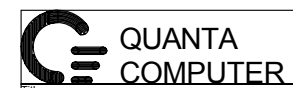
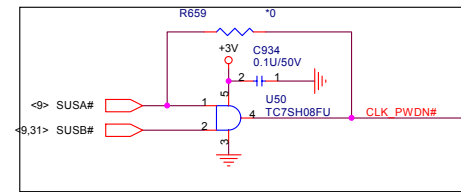
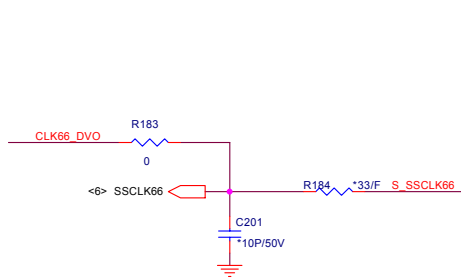
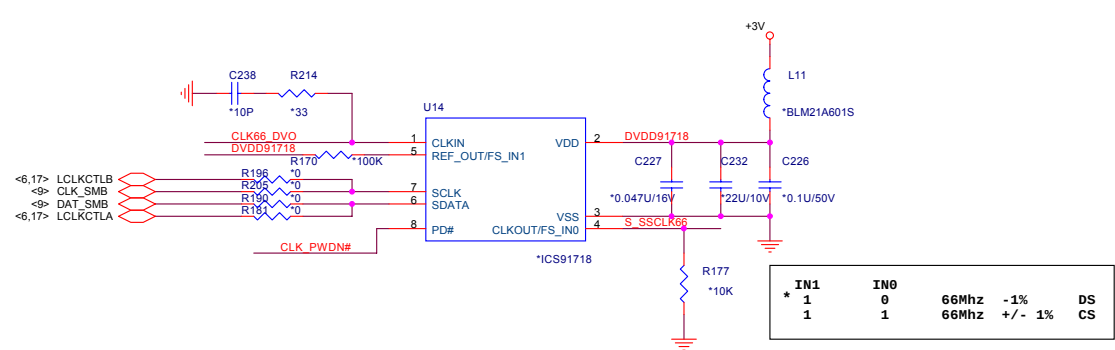
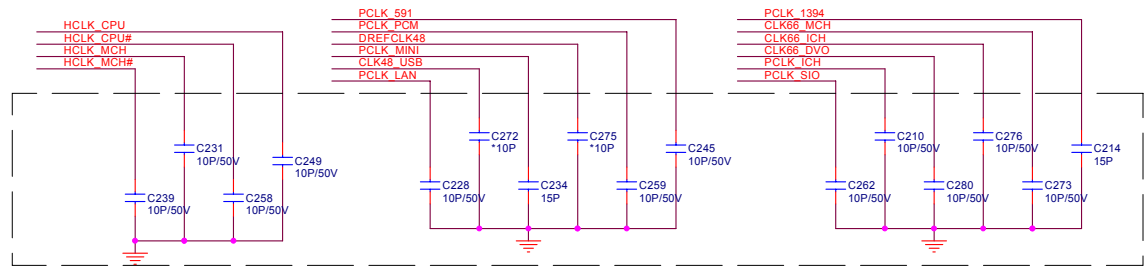






These are for backdrive issue

The image shows two identical circuit diagrams for backdrive protection on SMB lines. Each circuit features a 3-pin connector (pins 1, 2, 3) connected to a MOSFET (Q31 or Q30, RHU002N06). The gate of the MOSFET is connected to pin 2 and a +5V supply. The drain of the MOSFET is connected to pin 1 and the SMB line (CGDAT_SMB or CGCLK_SMB). The source of the MOSFET is connected to pin 3 and ground. A 10K resistor (R259 or R258) is connected between the SMB line and a +3V supply.



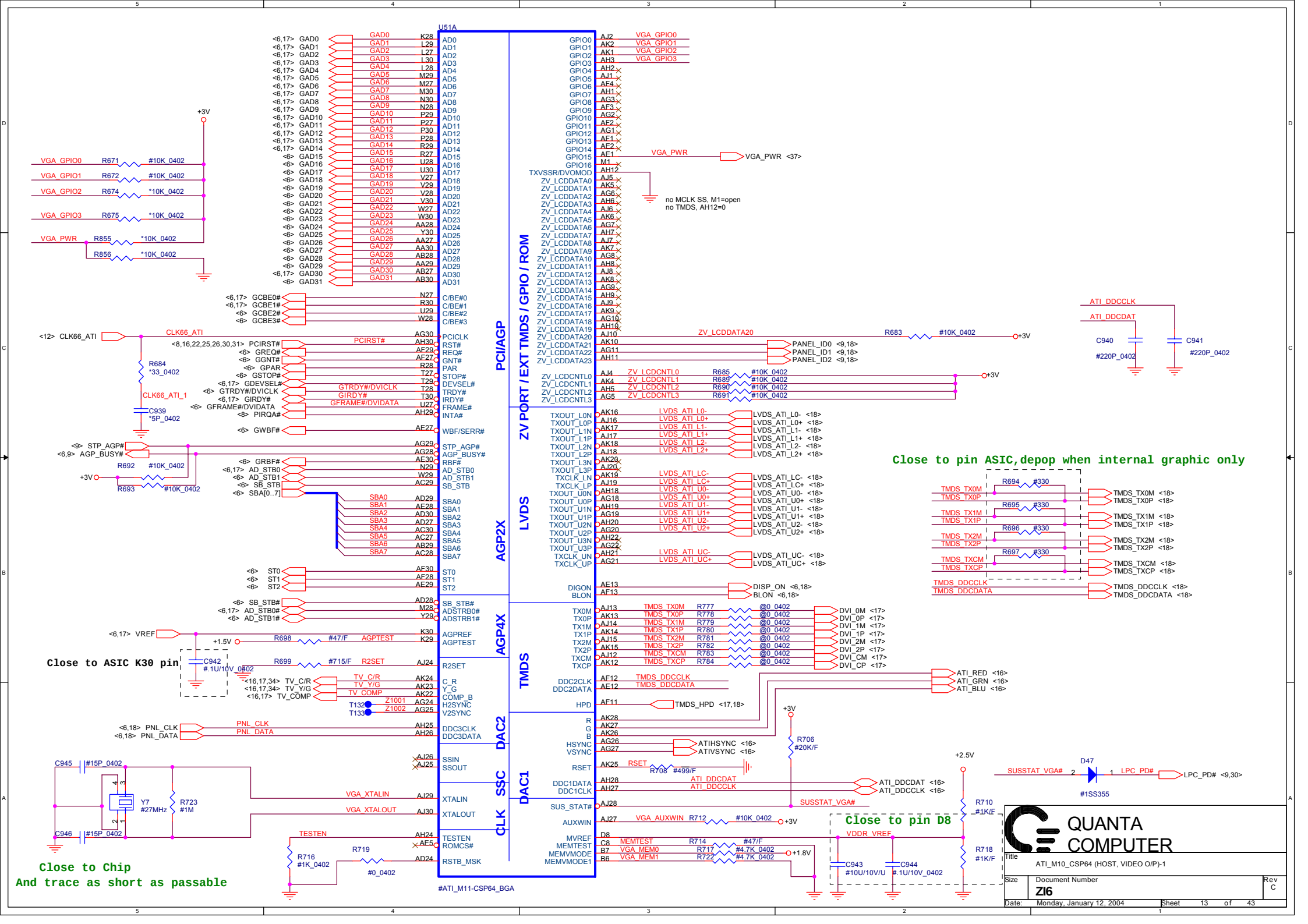
Title	CLOCK GENERATOR
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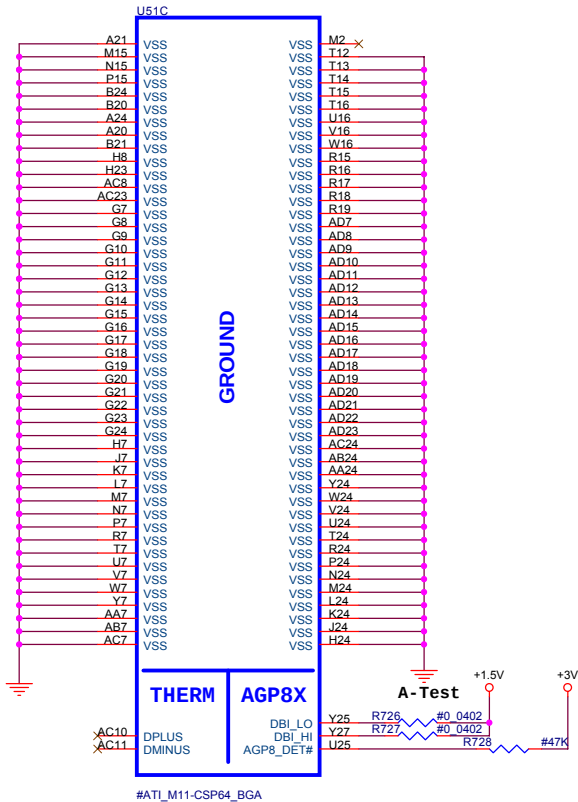
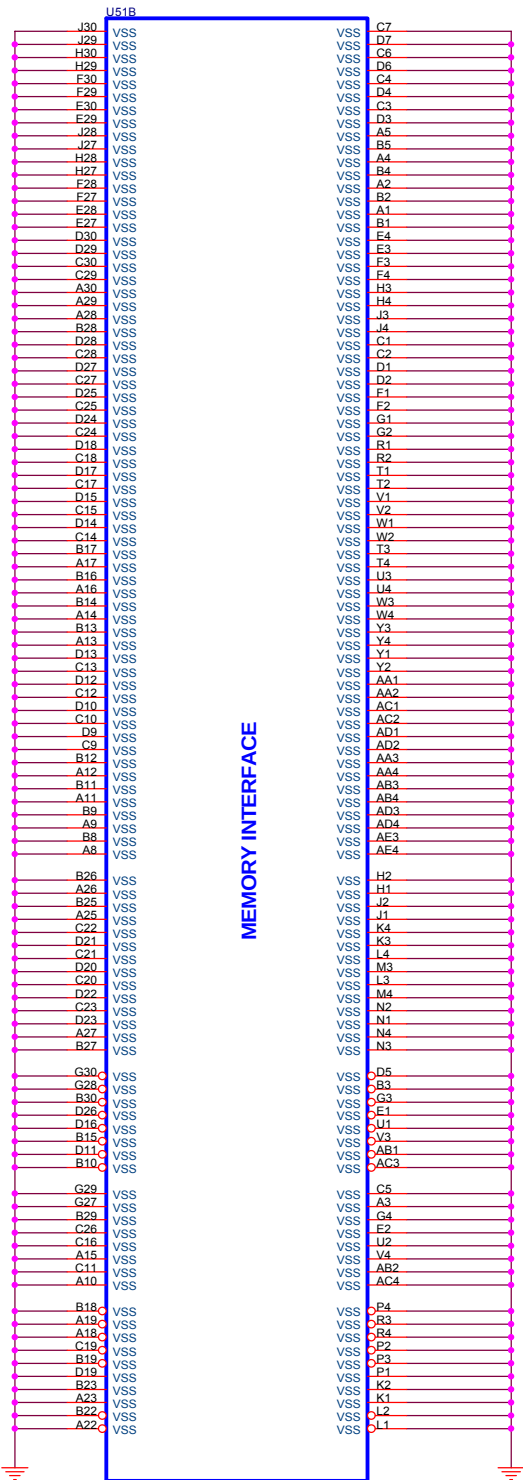
Size	Document Number
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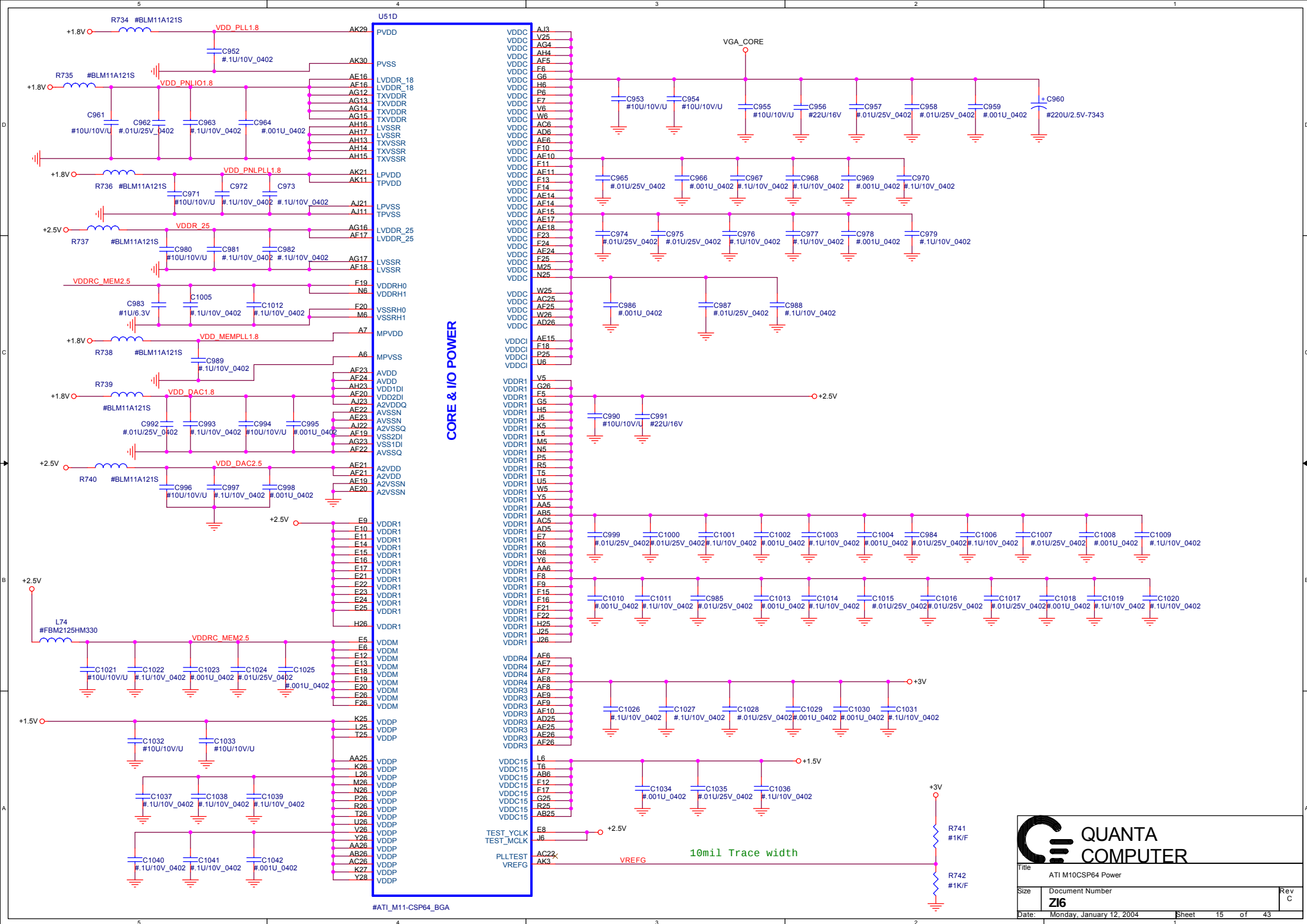
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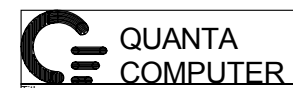
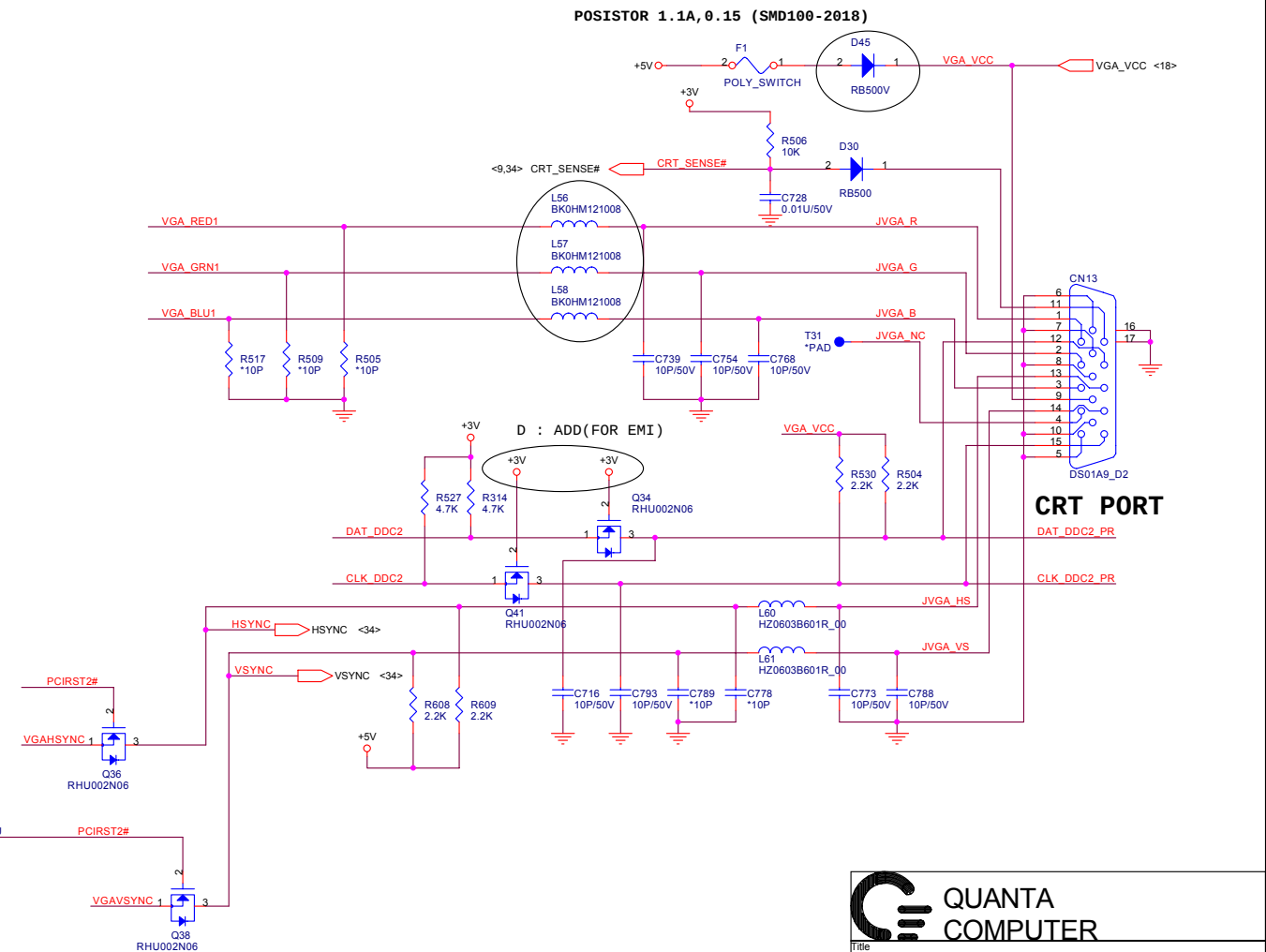
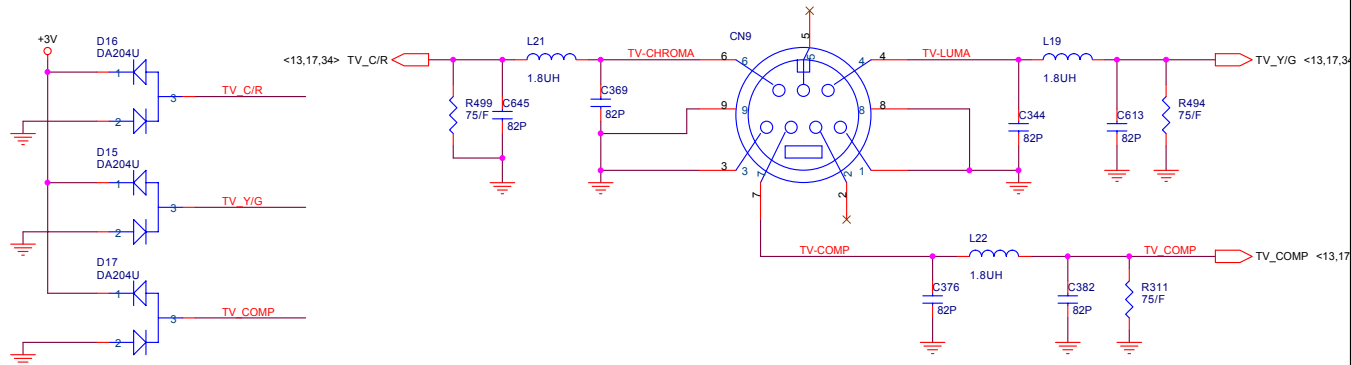
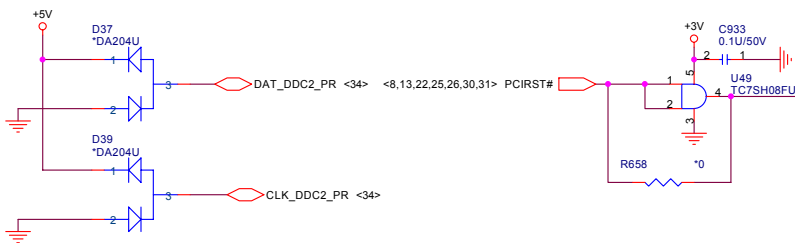
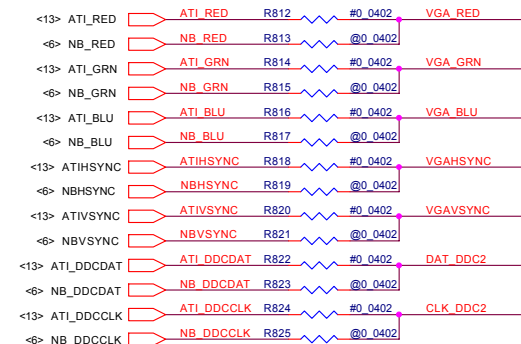
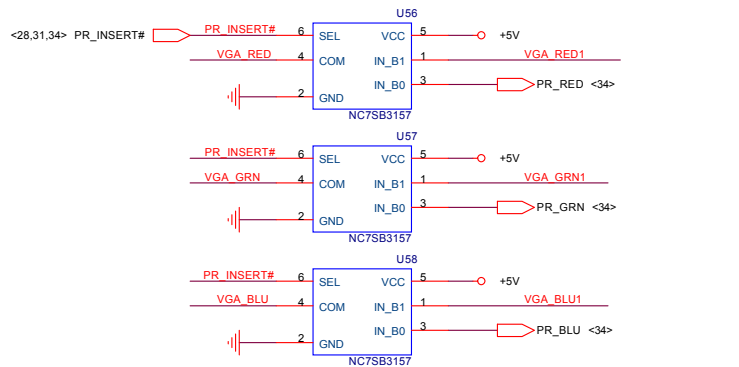
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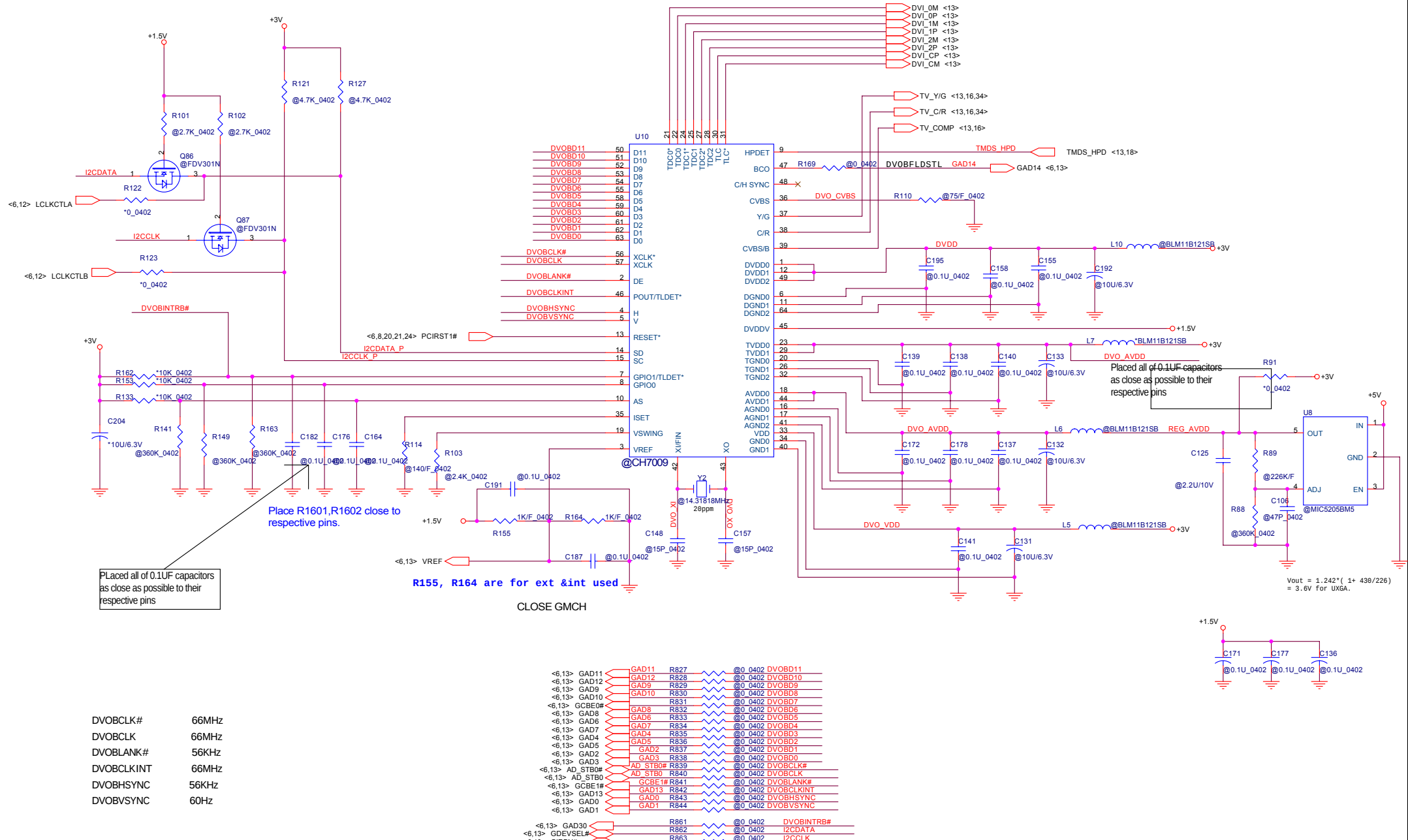








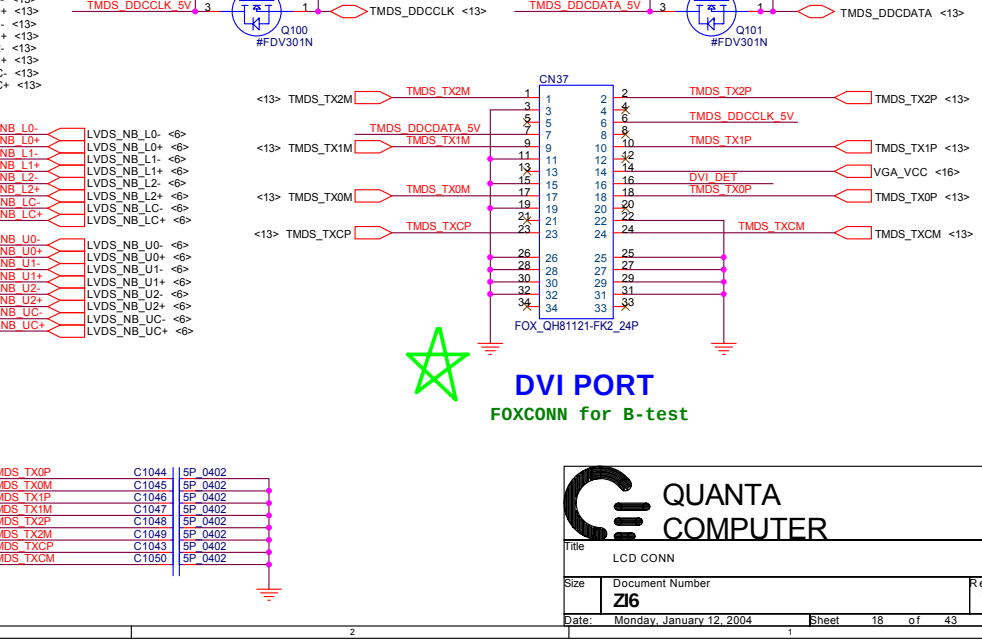
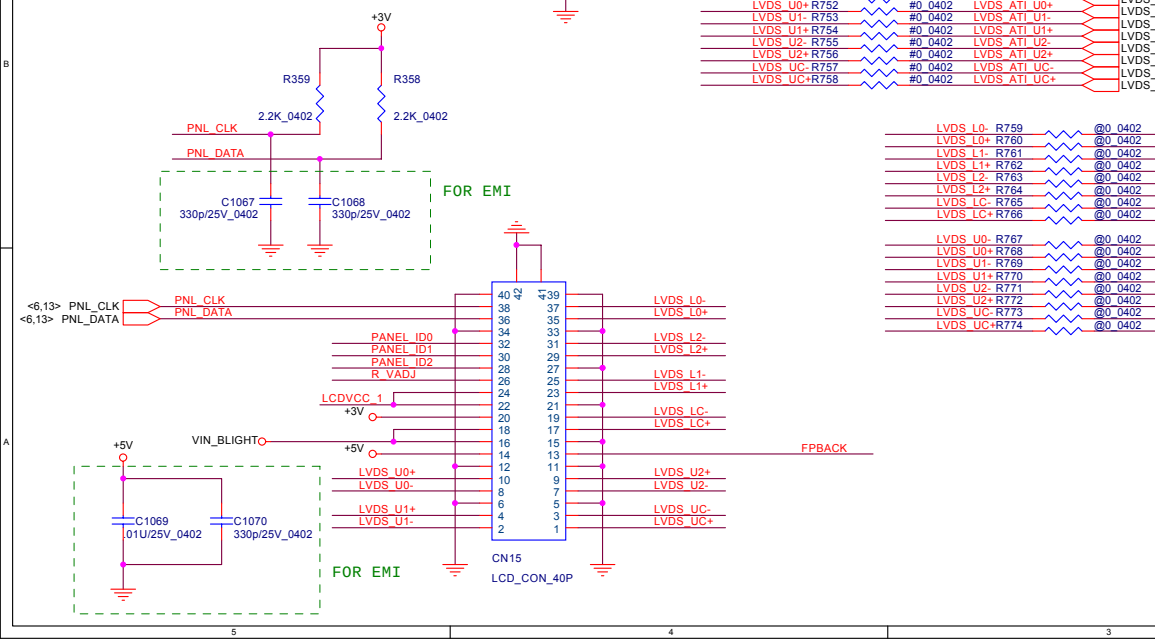
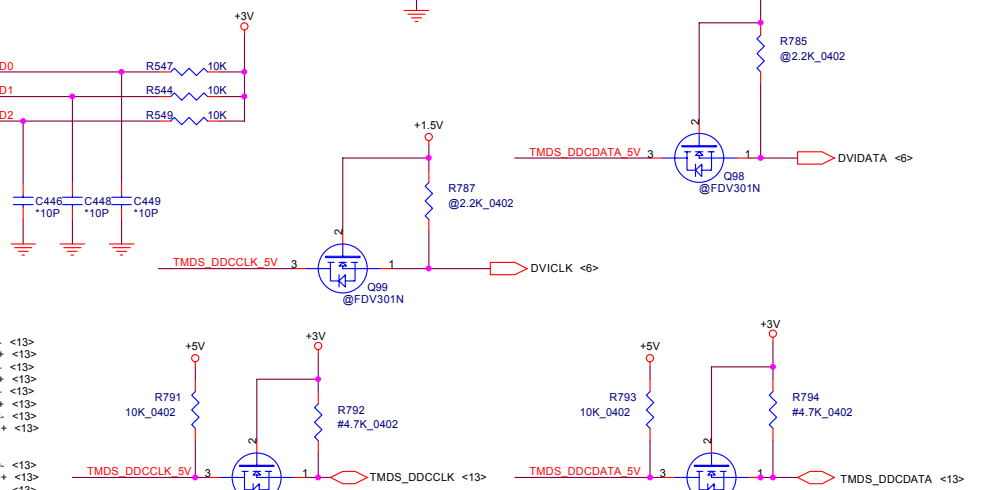
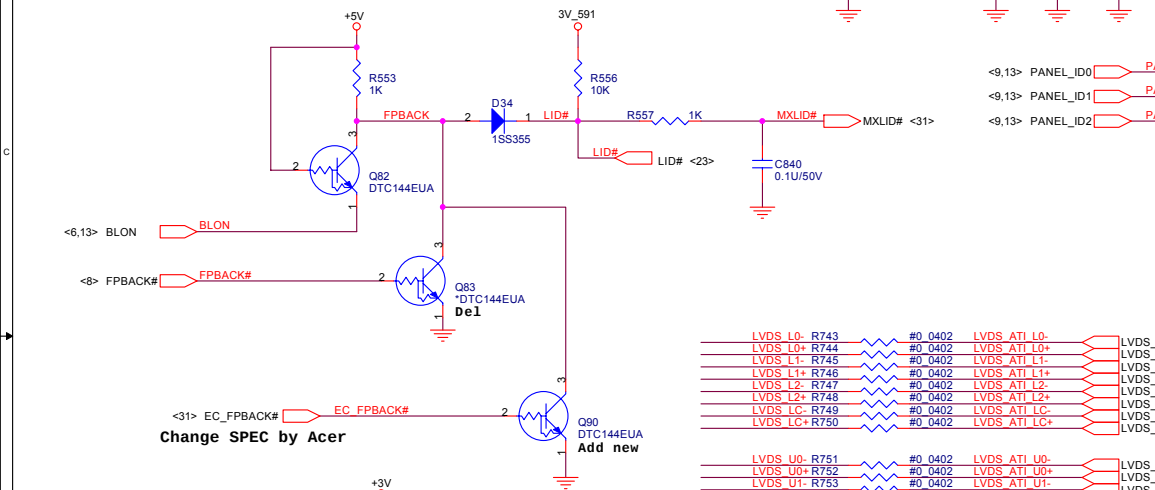
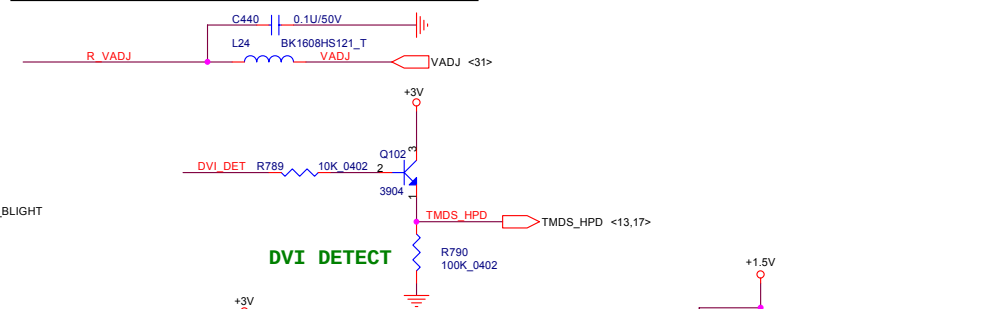
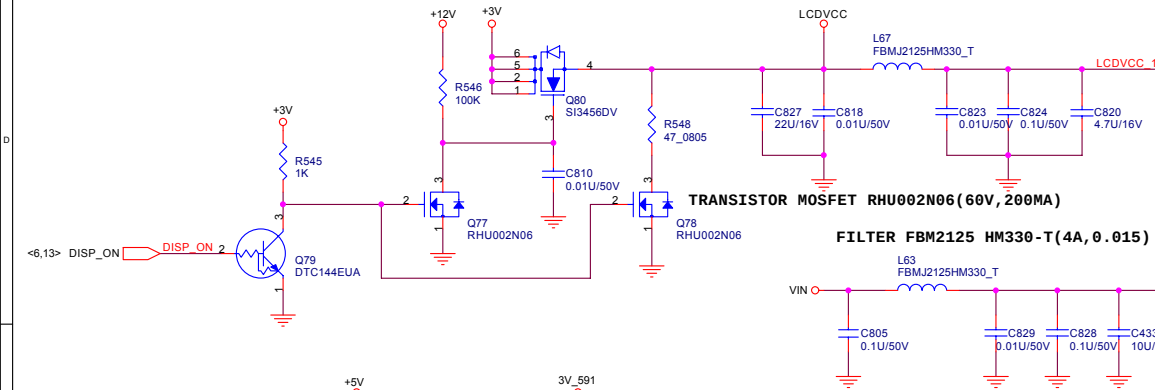
Title CRT & TV-OUT		
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FILTER FBM2125 HM330-T(4A,0.015)

PANEL ID:

	PANEL_ID2	PANEL_ID1	PANEL_ID0
D3	0	1	1
D4	1	0	0
D5	1	0	1
D6	1	1	0
D7	1	1	1



LVDS L0- R743	#0 0402	LVDS ATI L0-	LVDS ATI L0- <13>
LVDS L0+ R744	#0 0402	LVDS ATI L0+	LVDS ATI L0+ <13>
LVDS L1- R745	#0 0402	LVDS ATI L1-	LVDS ATI L1- <13>
LVDS L1+ R746	#0 0402	LVDS ATI L1+	LVDS ATI L1+ <13>
LVDS L2- R747	#0 0402	LVDS ATI L2-	LVDS ATI L2- <13>
LVDS L2+ R748	#0 0402	LVDS ATI L2+	LVDS ATI L2+ <13>
LVDS LC- R749	#0 0402	LVDS ATI LC-	LVDS ATI LC- <13>
LVDS LC+ R750	#0 0402	LVDS ATI LC+	LVDS ATI LC+ <13>
LVDS U0- R751	#0 0402	LVDS ATI U0-	LVDS ATI U0- <13>
LVDS U0+ R752	#0 0402	LVDS ATI U0+	LVDS ATI U0+ <13>
LVDS U1- R753	#0 0402	LVDS ATI U1-	LVDS ATI U1- <13>
LVDS U1+ R754	#0 0402	LVDS ATI U1+	LVDS ATI U1+ <13>
LVDS U2- R755	#0 0402	LVDS ATI U2-	LVDS ATI U2- <13>
LVDS U2+ R756	#0 0402	LVDS ATI U2+	LVDS ATI U2+ <13>
LVDS UC- R757	#0 0402	LVDS ATI UC-	LVDS ATI UC- <13>
LVDS UC+ R758	#0 0402	LVDS ATI UC+	LVDS ATI UC+ <13>

LVDS L0- R759	@0 0402	LVDS NB L0-	LVDS NB L0- <6>
LVDS L0+ R760	@0 0402	LVDS NB L0+	LVDS NB L0+ <6>
LVDS L1- R761	@0 0402	LVDS NB L1-	LVDS NB L1- <6>
LVDS L1+ R762	@0 0402	LVDS NB L1+	LVDS NB L1+ <6>
LVDS L2- R763	@0 0402	LVDS NB L2-	LVDS NB L2- <6>
LVDS L2+ R764	@0 0402	LVDS NB L2+	LVDS NB L2+ <6>
LVDS LC- R765	@0 0402	LVDS NB LC-	LVDS NB LC- <6>
LVDS LC+ R766	@0 0402	LVDS NB LC+	LVDS NB LC+ <6>
LVDS U0- R767	@0 0402	LVDS NB U0-	LVDS NB U0- <6>
LVDS U0+ R768	@0 0402	LVDS NB U0+	LVDS NB U0+ <6>
LVDS U1- R769	@0 0402	LVDS NB U1-	LVDS NB U1- <6>
LVDS U1+ R770	@0 0402	LVDS NB U1+	LVDS NB U1+ <6>
LVDS U2- R771	@0 0402	LVDS NB U2-	LVDS NB U2- <6>
LVDS U2+ R772	@0 0402	LVDS NB U2+	LVDS NB U2+ <6>
LVDS UC- R773	@0 0402	LVDS NB UC-	LVDS NB UC- <6>
LVDS UC+ R774	@0 0402	LVDS NB UC+	LVDS NB UC+ <6>

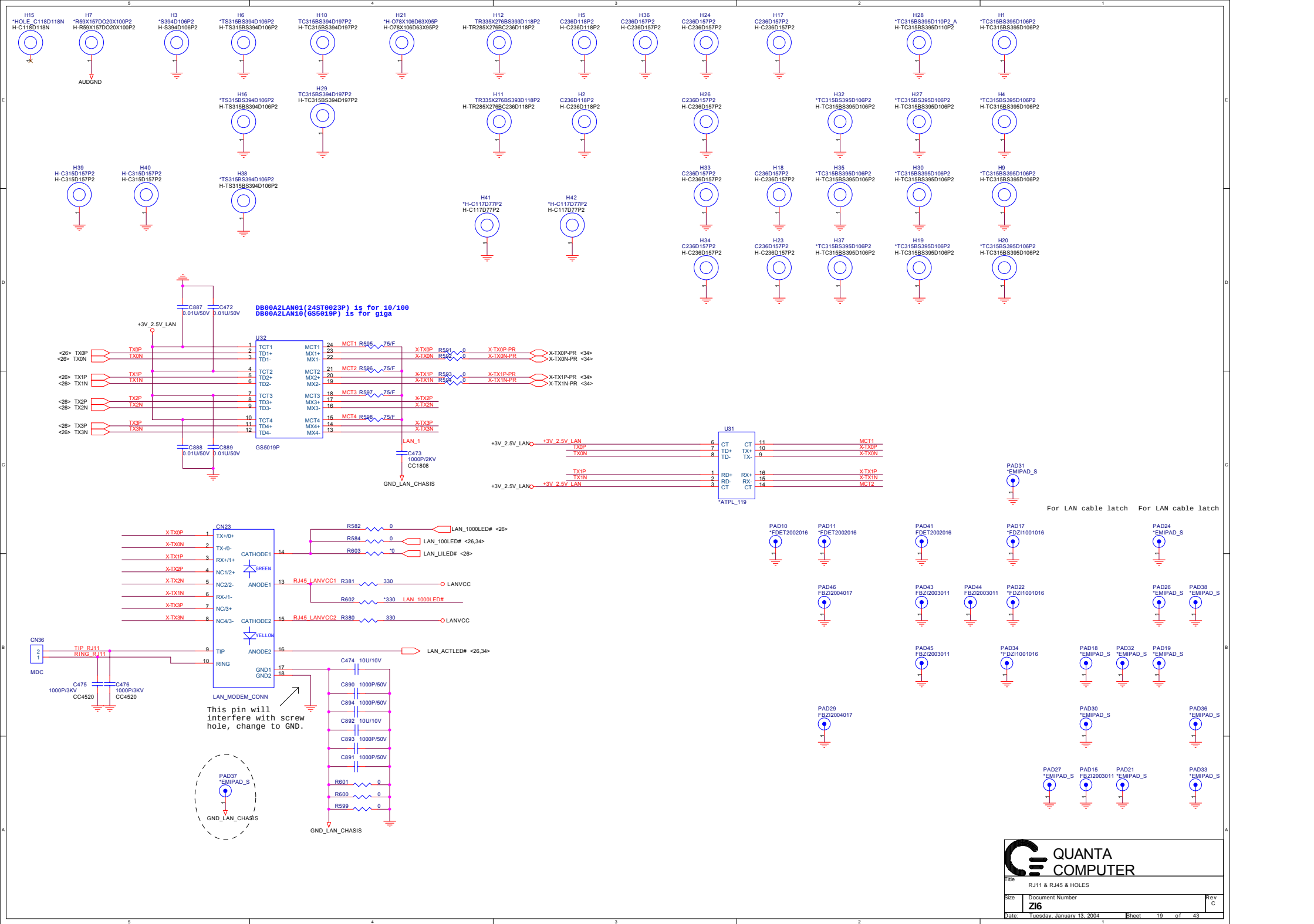
TMDS TX0P	C1044	1P 0402
TMDS TX0M	C1045	1P 0402
TMDS TX1P	C1046	1P 0402
TMDS TX1M	C1047	1P 0402
TMDS TX2P	C1048	1P 0402
TMDS TX2M	C1049	1P 0402
TMDS TXCP	C1043	1P 0402
TMDS TXCM	C1050	1P 0402

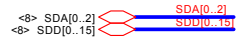


DVI PORT
FOXCONN for B-test

QUANTA
COMPUTER

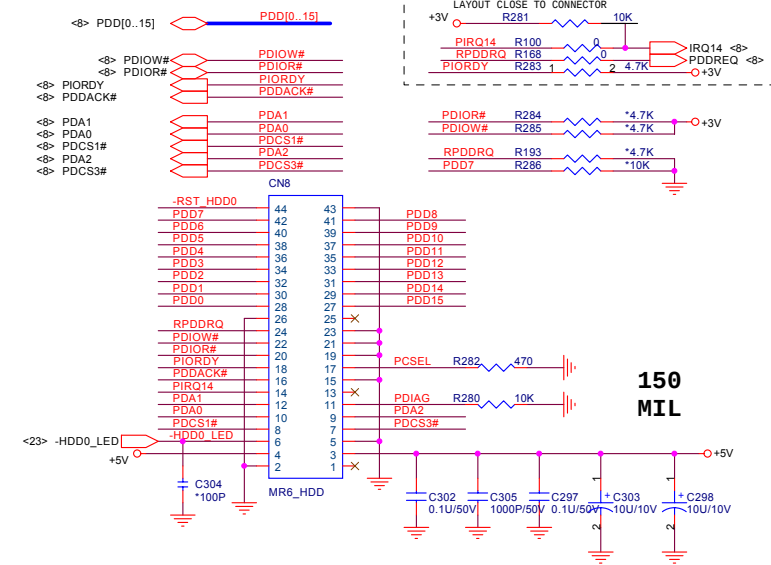
Title	LCD CONN
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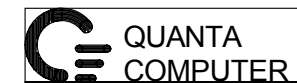
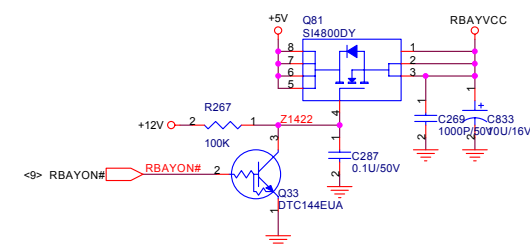
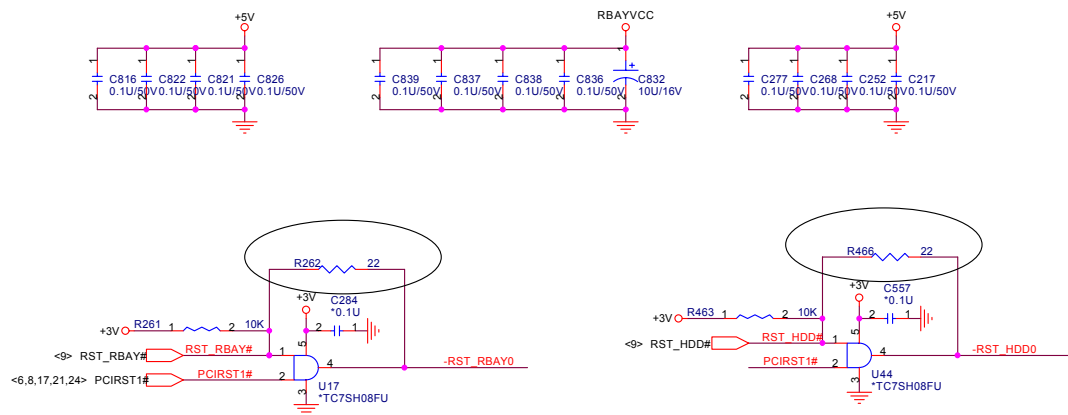


BAY ID STATUS

RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD



HDD CONNECTOR



Title
DEVICE BAY

Size	Document Number
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Z16

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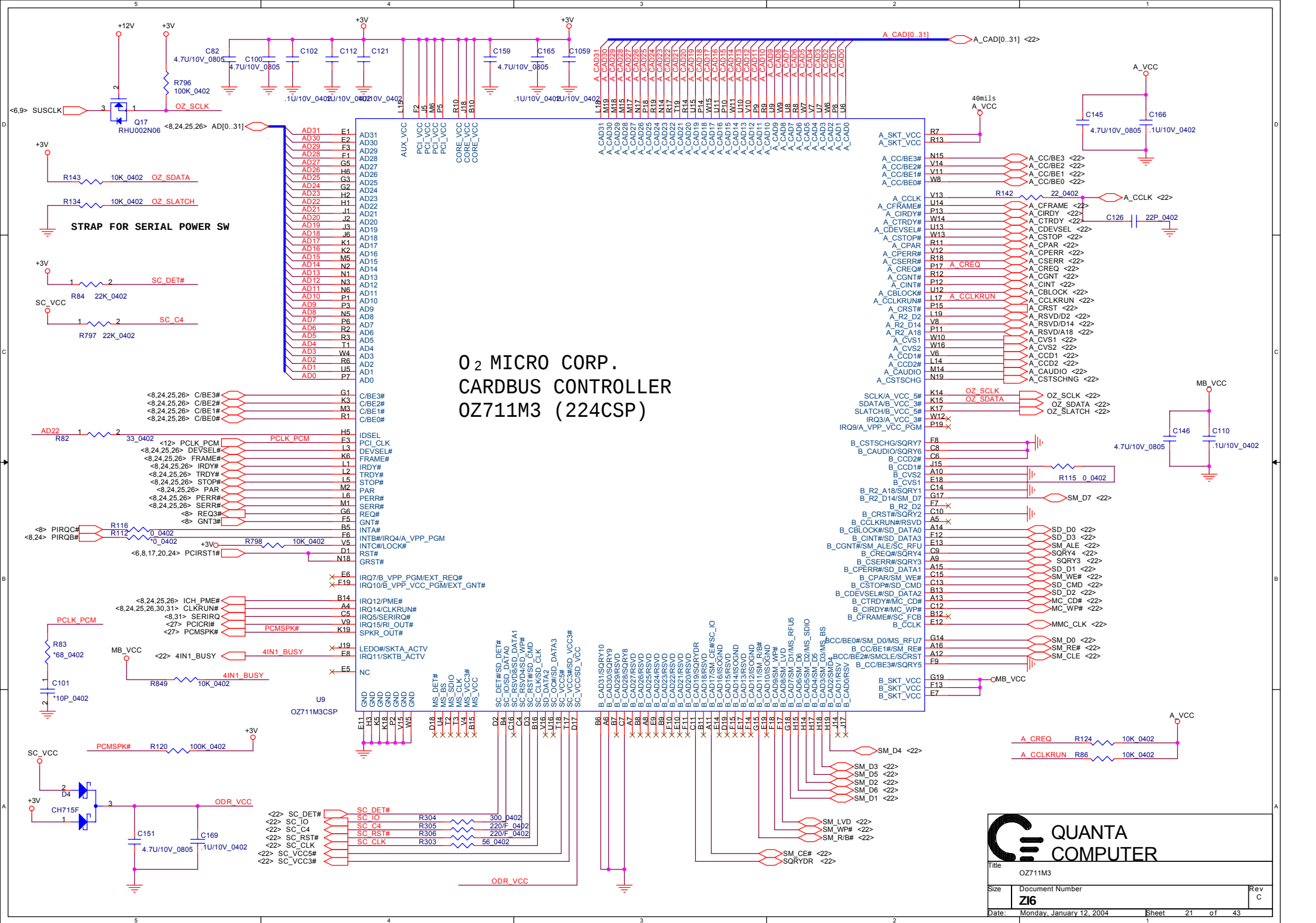
	Rev C
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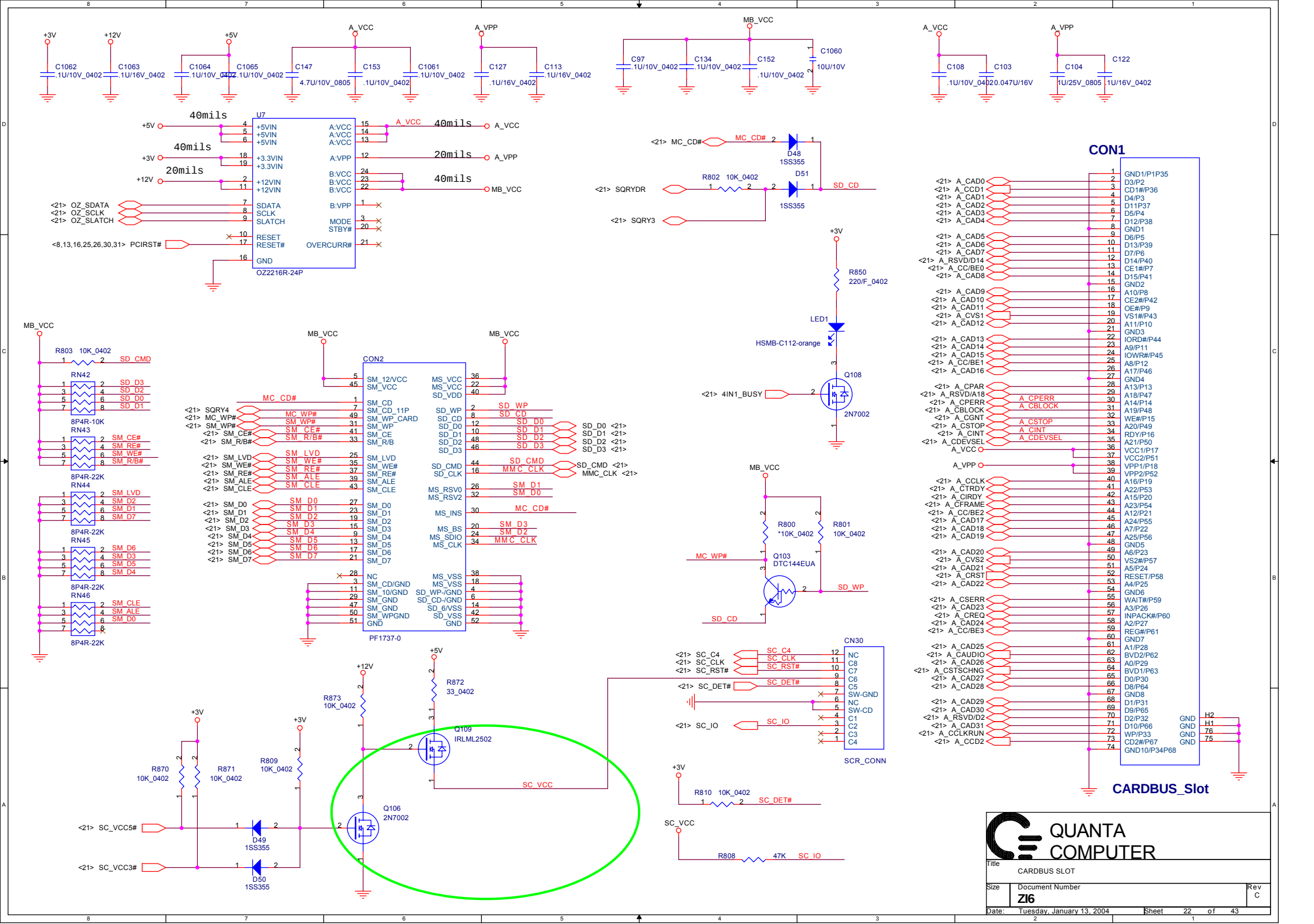
ev
C

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SWAP BAY POWER CONTROL& RESET





QUANTA COMPUTER

Title CARDBUS SLOT

Size Document Number

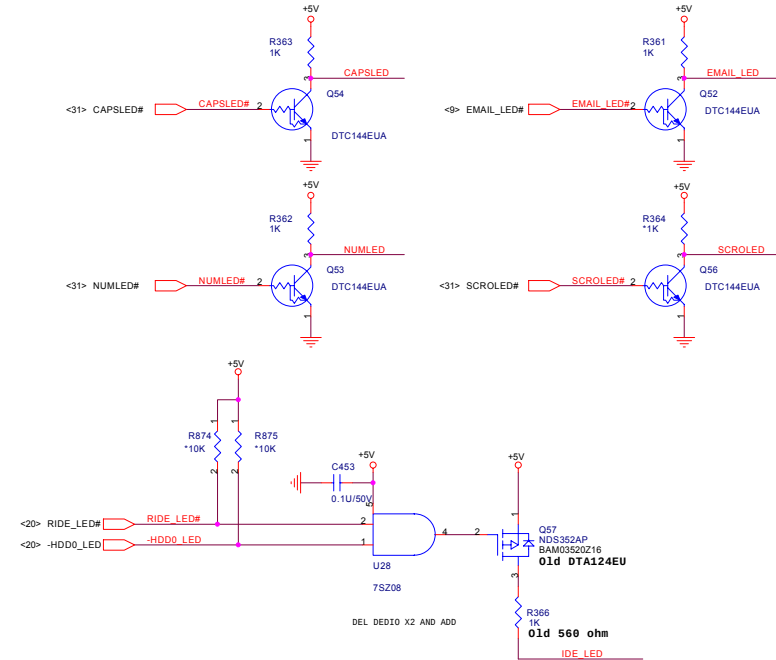
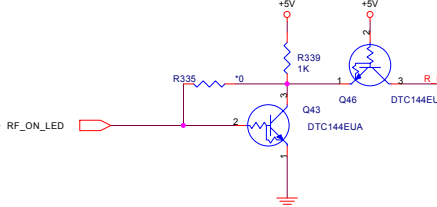
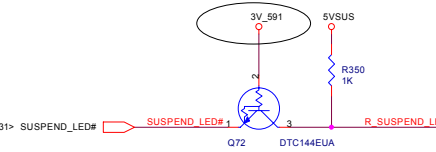
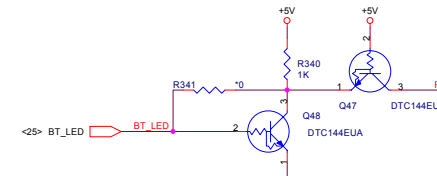
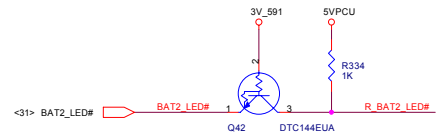
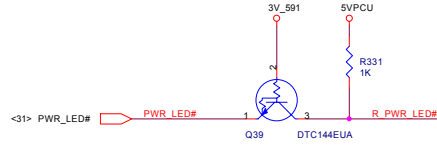
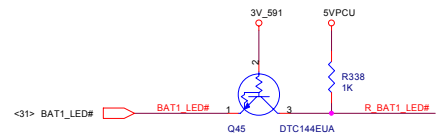
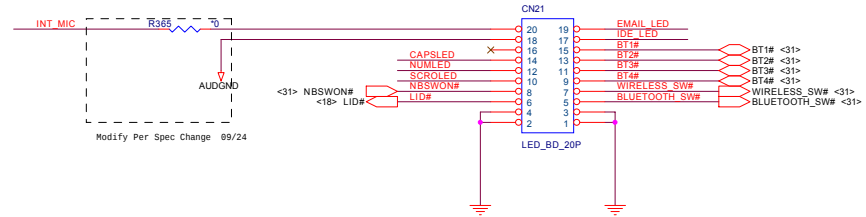
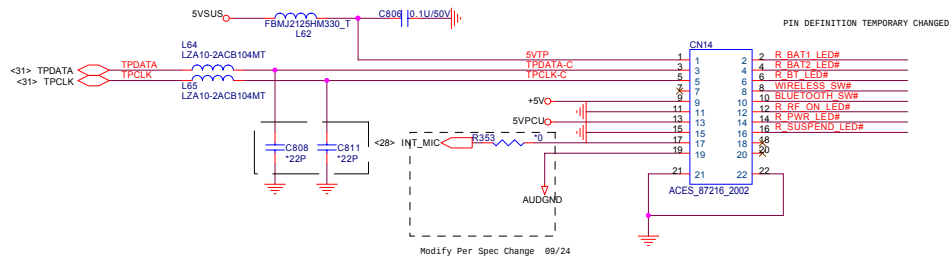
Z16

Date: Tuesday, January 13, 2004

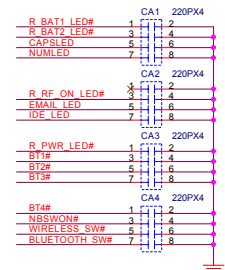
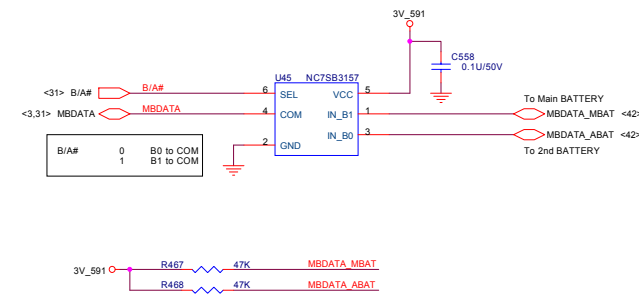
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Rev C

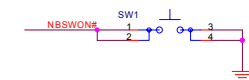
12 MIL

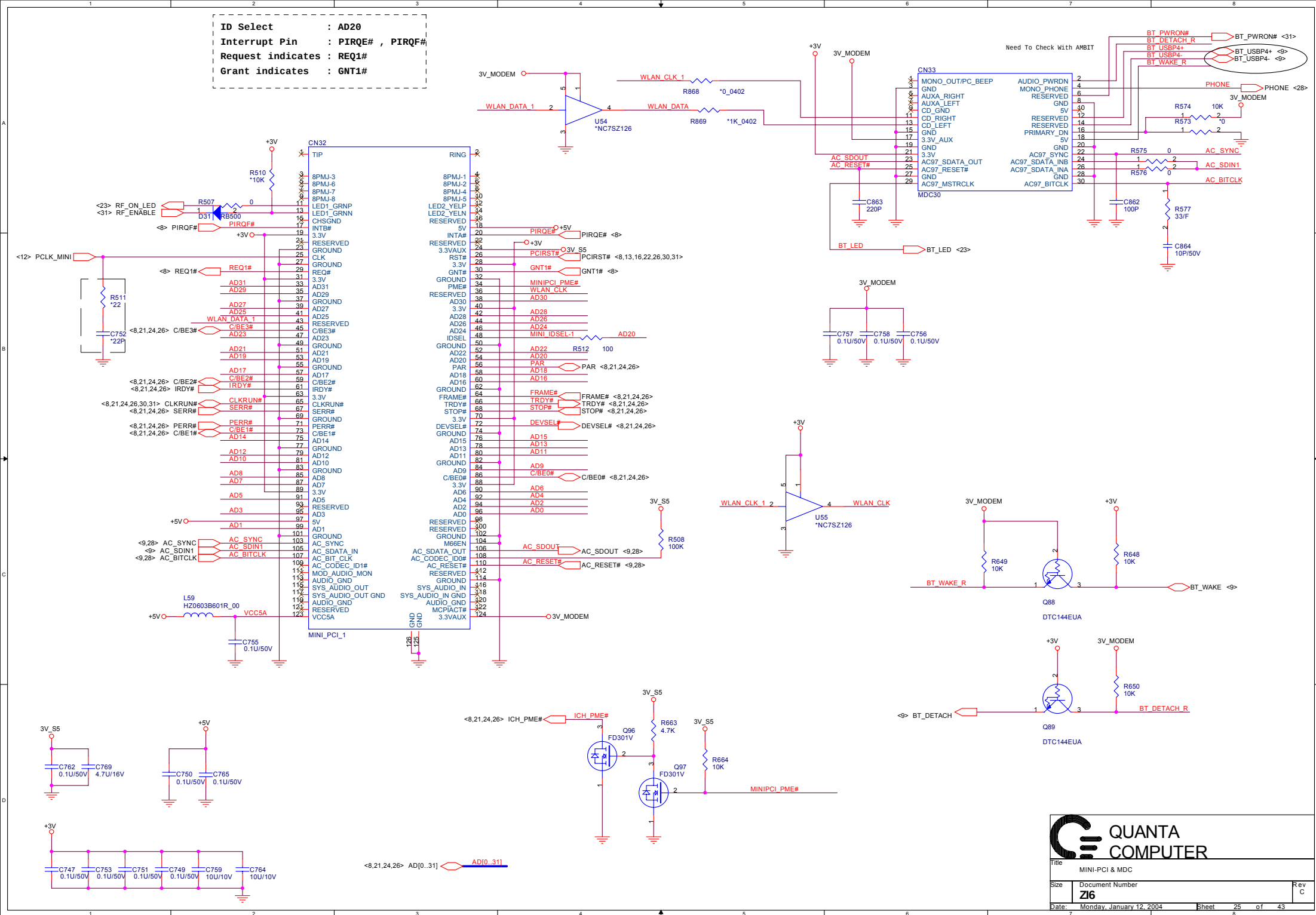


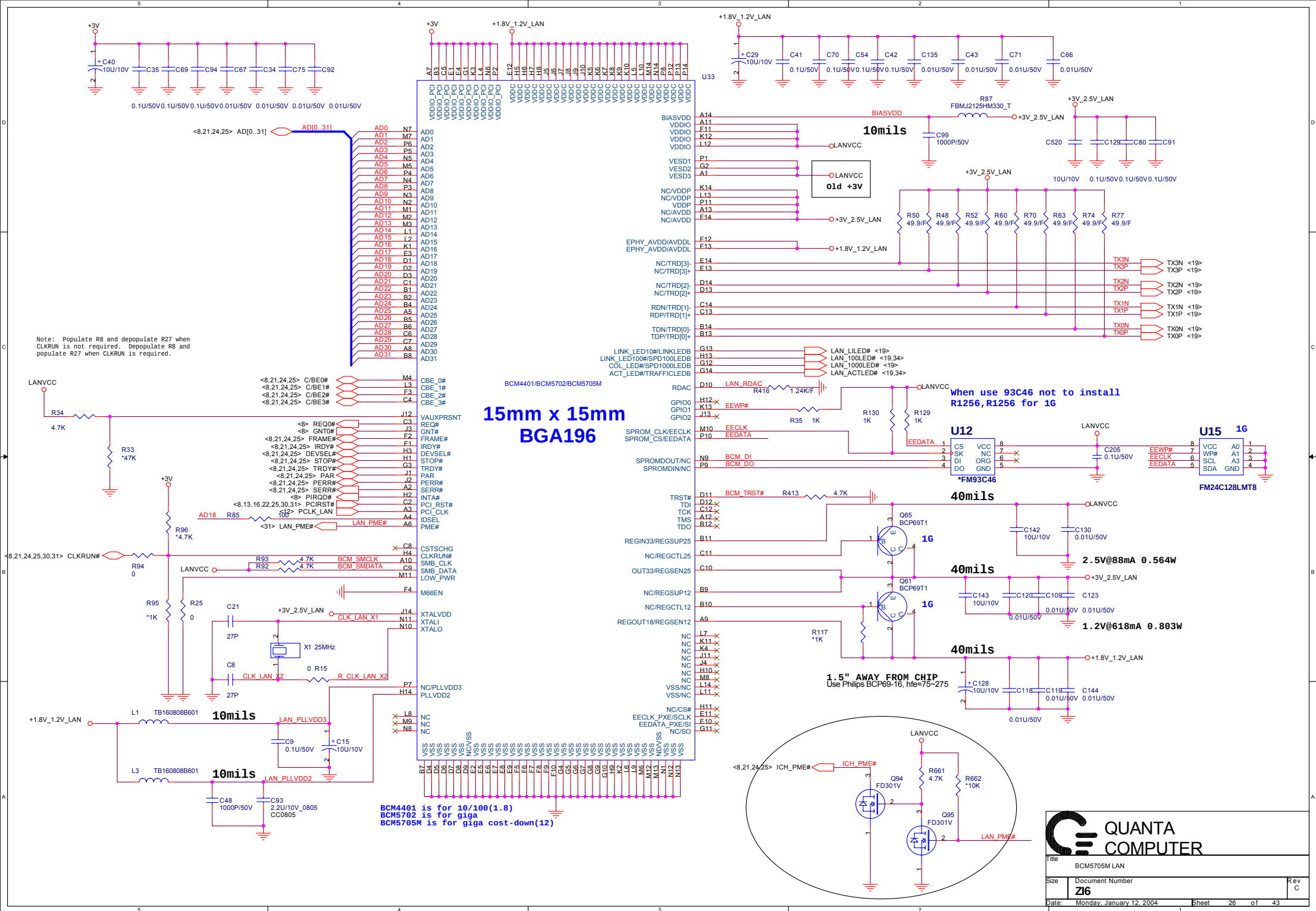
IDE LED CONTROL LOGIC



Del SW2



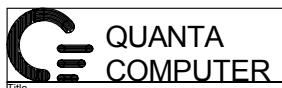




Note: Populate R8 and depopulate R27 when CLKRUN is not required. Depopulate R8 and populate R27 when CLKRUN is required.

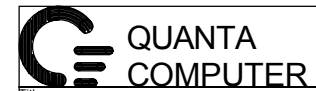
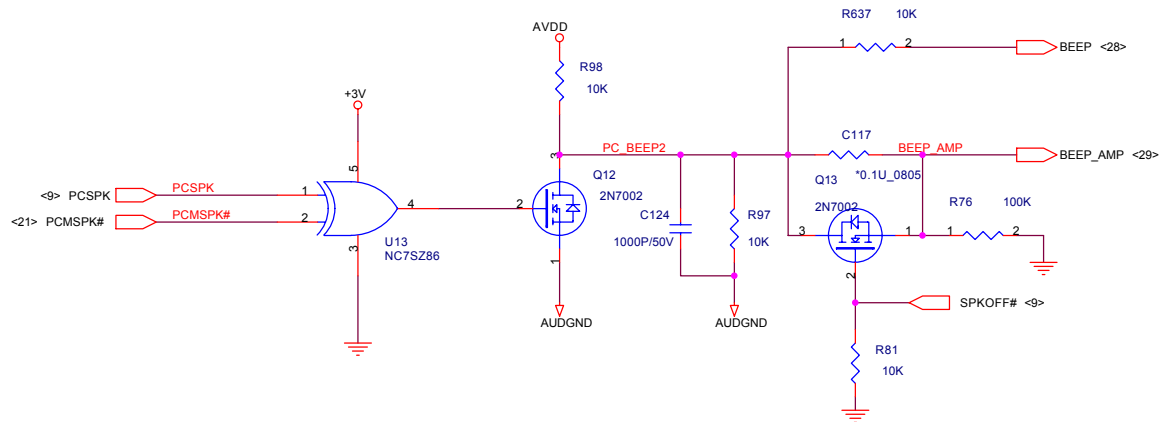
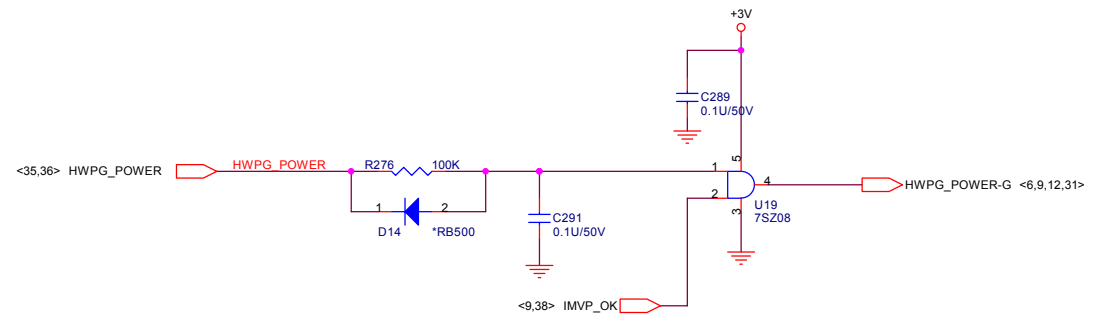
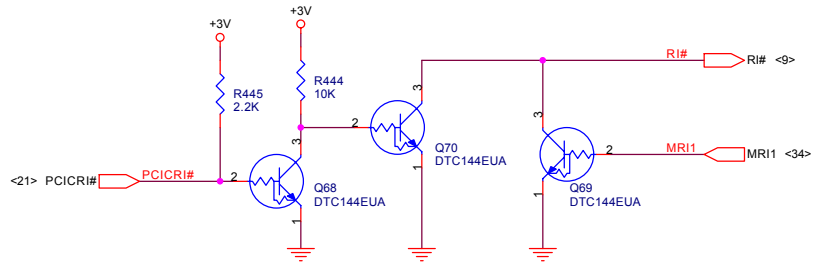
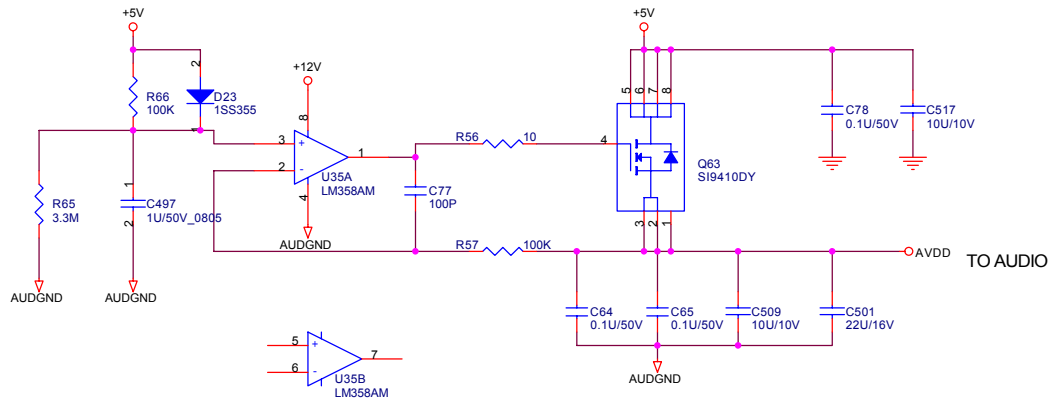
15mm x 15mm
BGA196

BCM4401 is for 10/100(1.8)
BCM5702 is for giga
BCM5705M is for giga cost-down(12)



Title			BCM5705M LAN
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AUDIO POWER



Title	AUDIO POWER & BEEP & FDD SEL
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Size	Document Number
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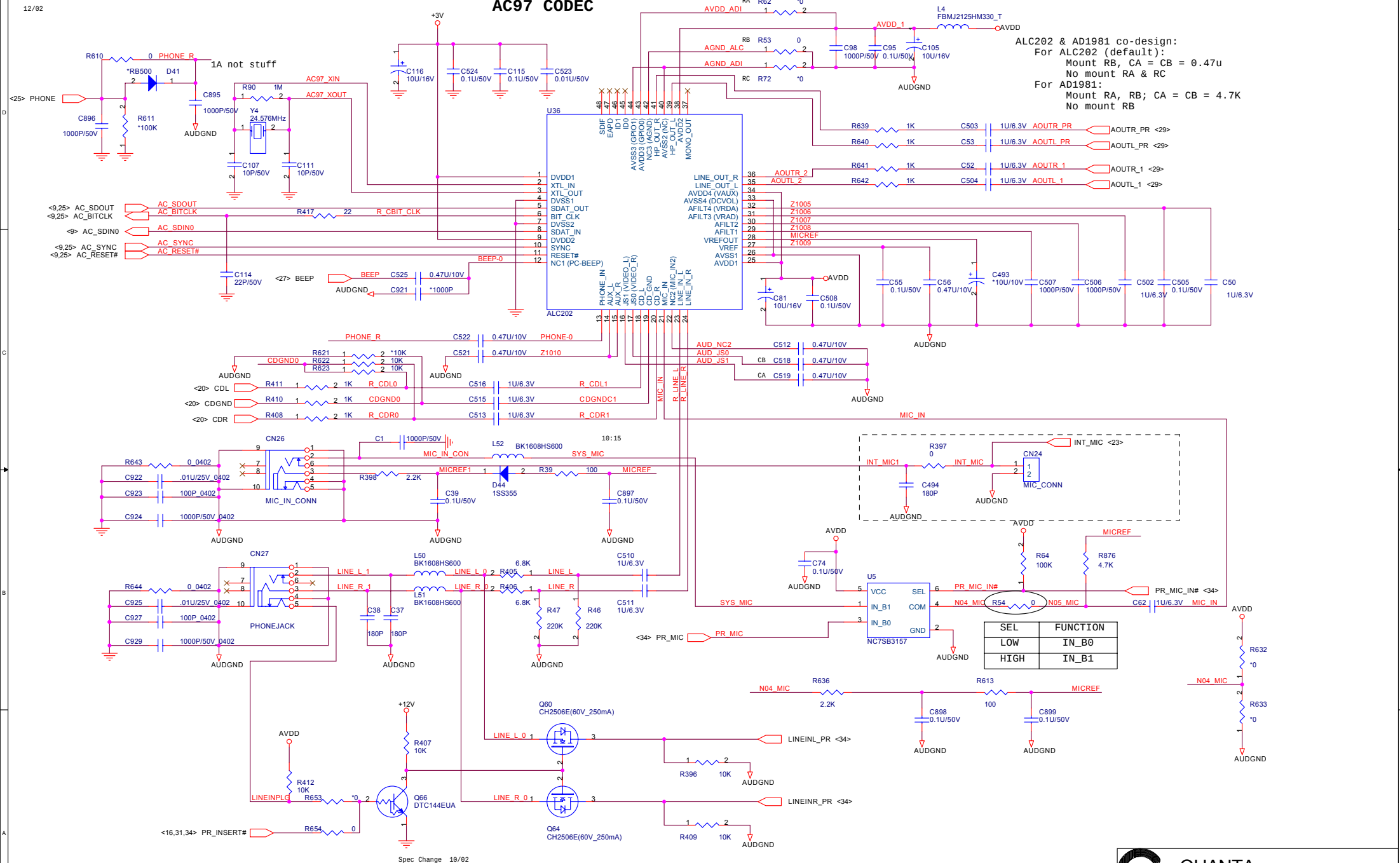
Z16

Date: Monday, January 12, 2004

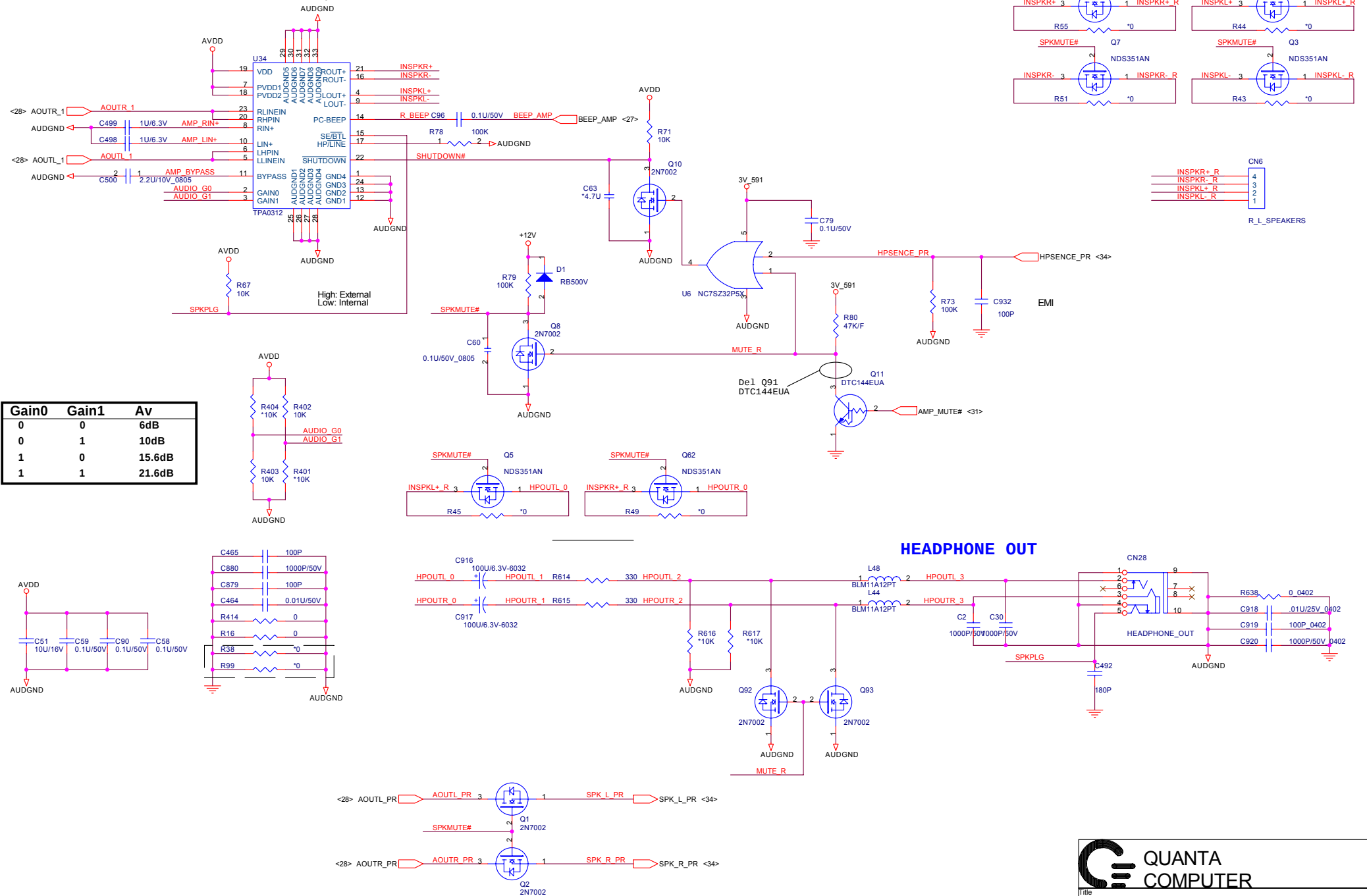
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ev
C

AC97 CODEC

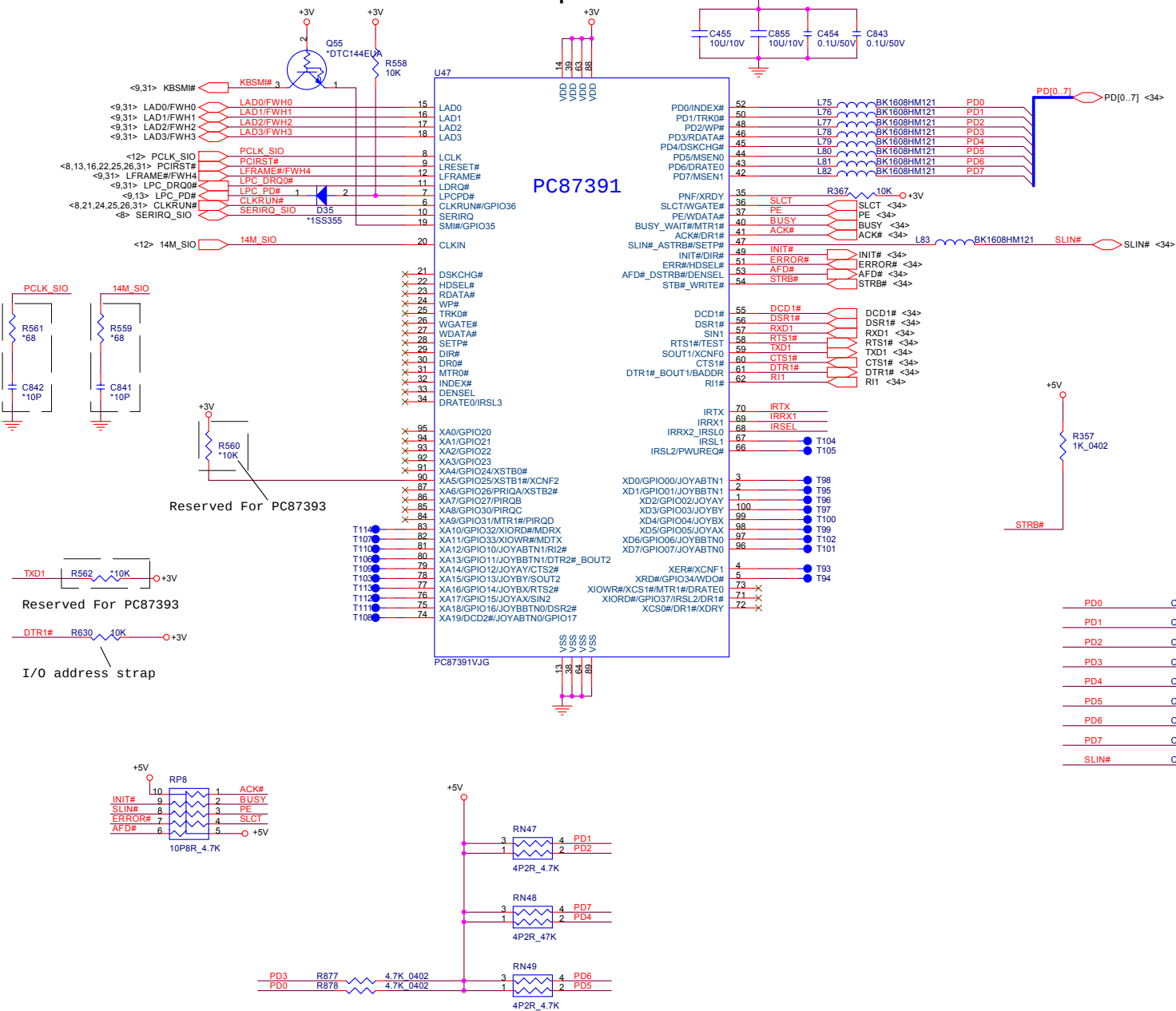


Audio Amplifier

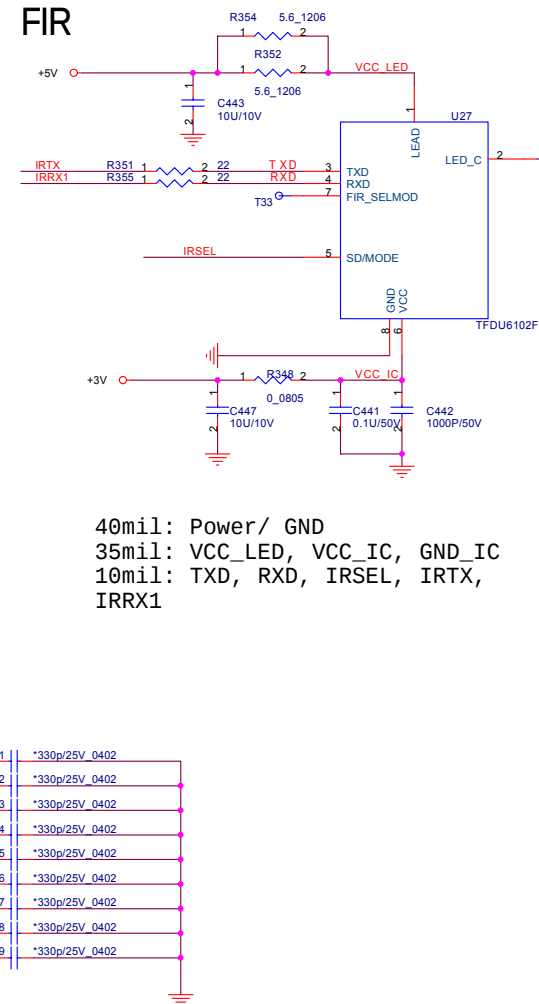


Super I/O

PC87391



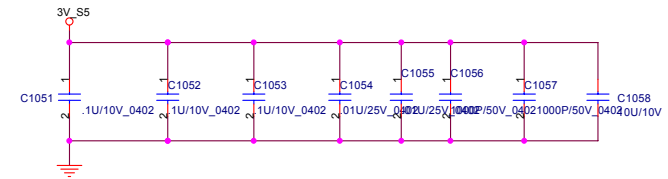
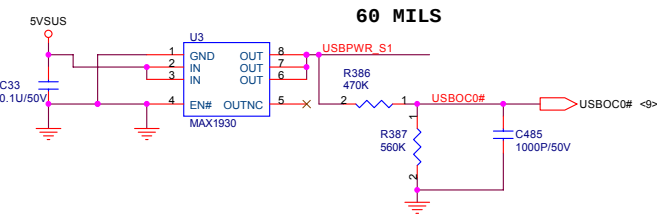
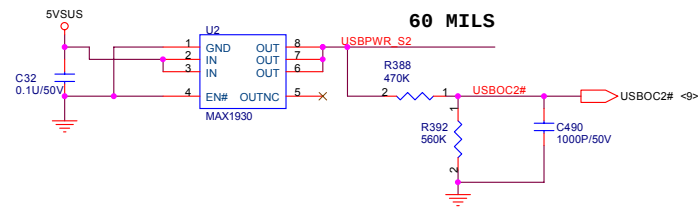
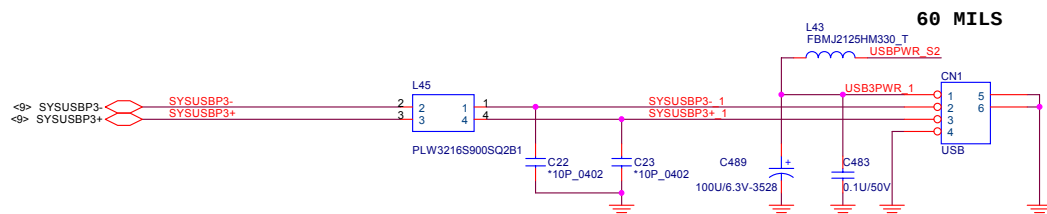
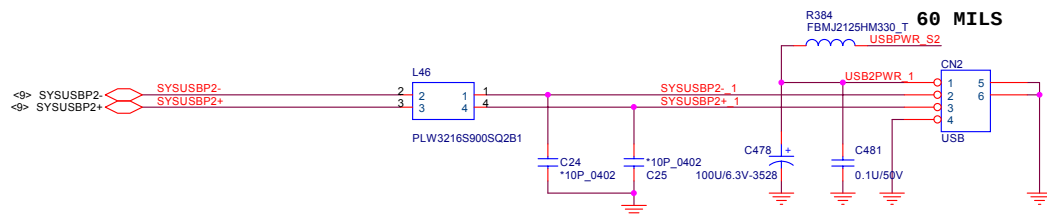
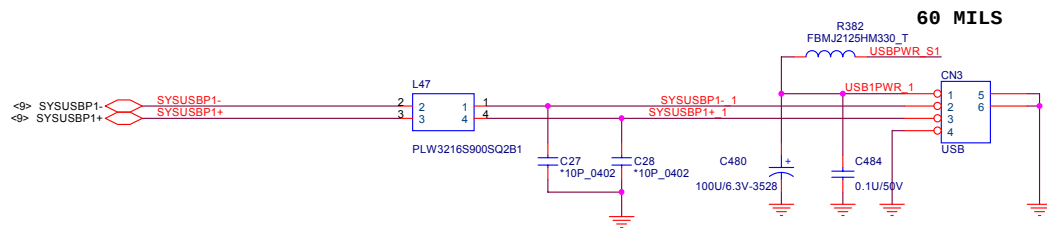
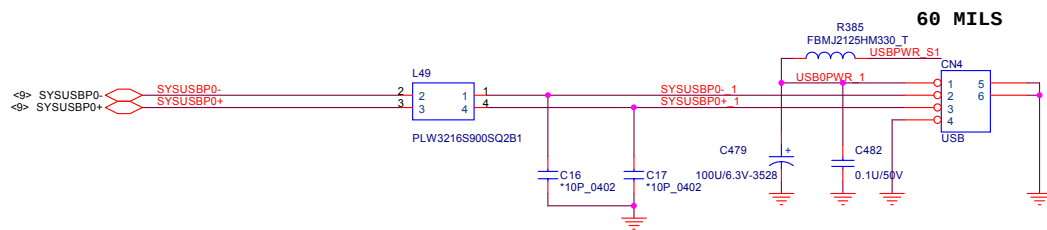
FIR



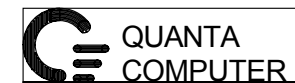
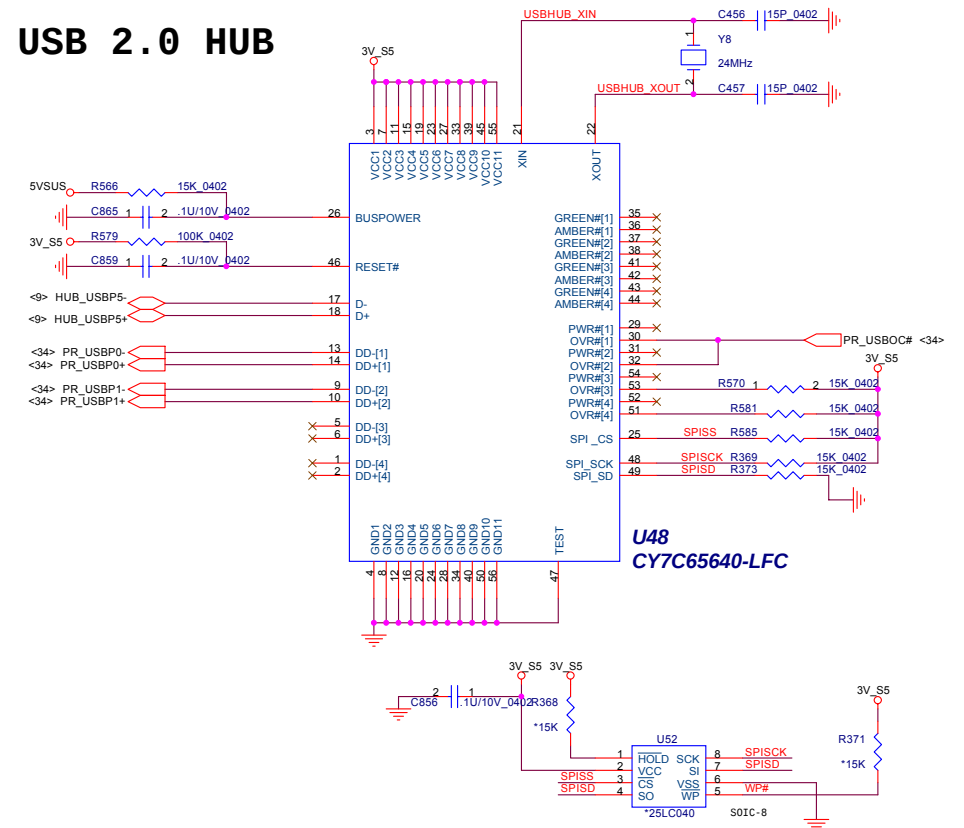
40mil: Power/ GND
35mil: VCC_LED, VCC_IC, GND_IC
10mil: TXD, RXD, IRSEL, IRTX, IRRX1



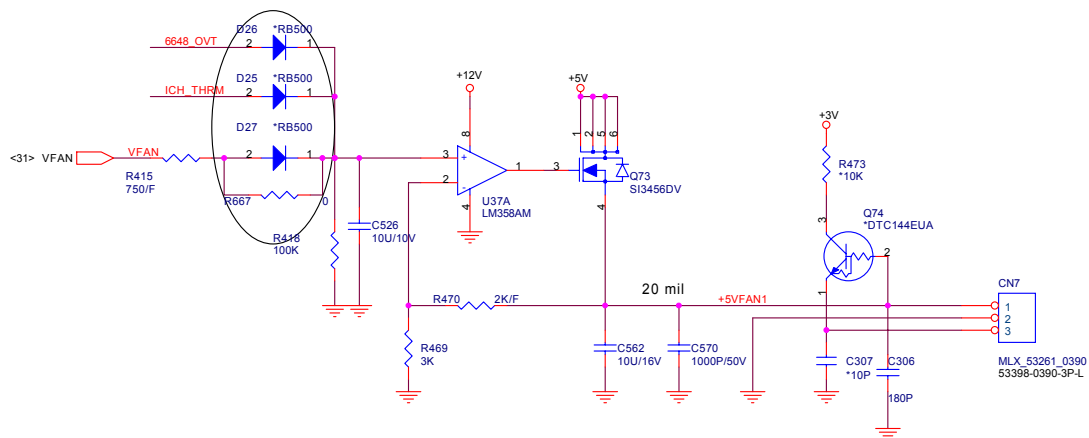




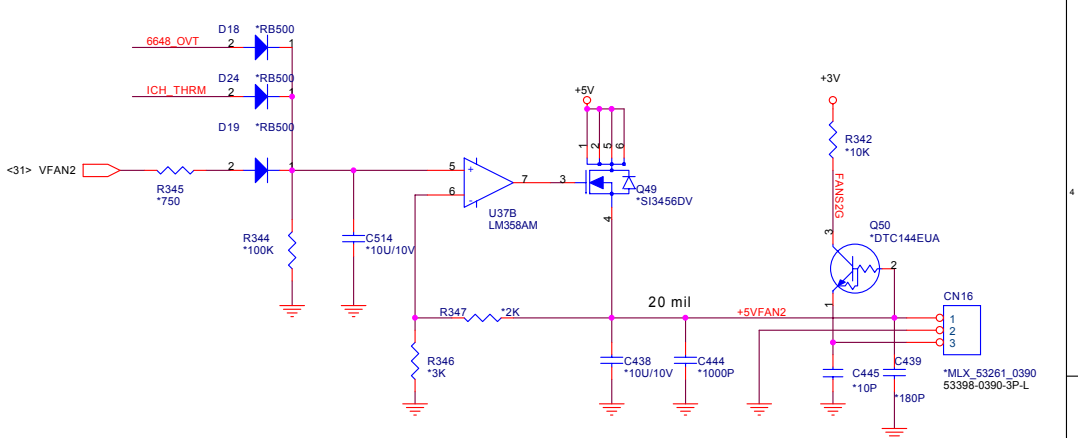
USB 2.0 HUB



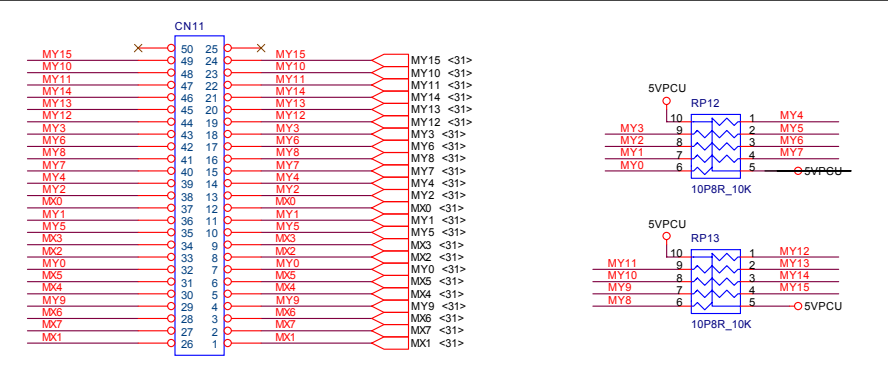
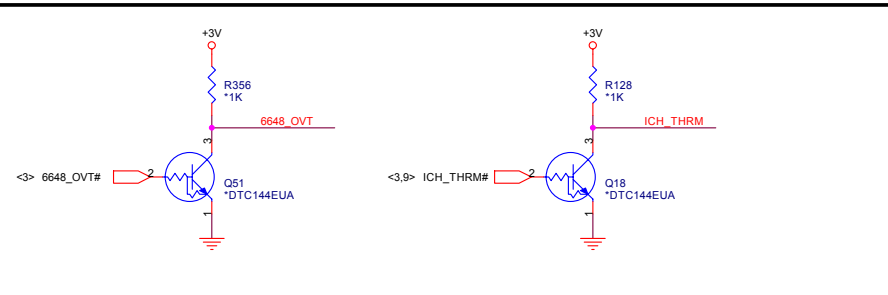
Title			USB CONN.
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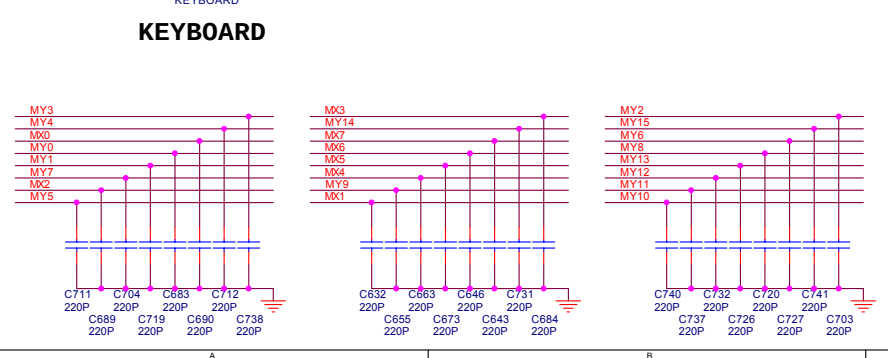
1st FAN OUT CONNECTOR

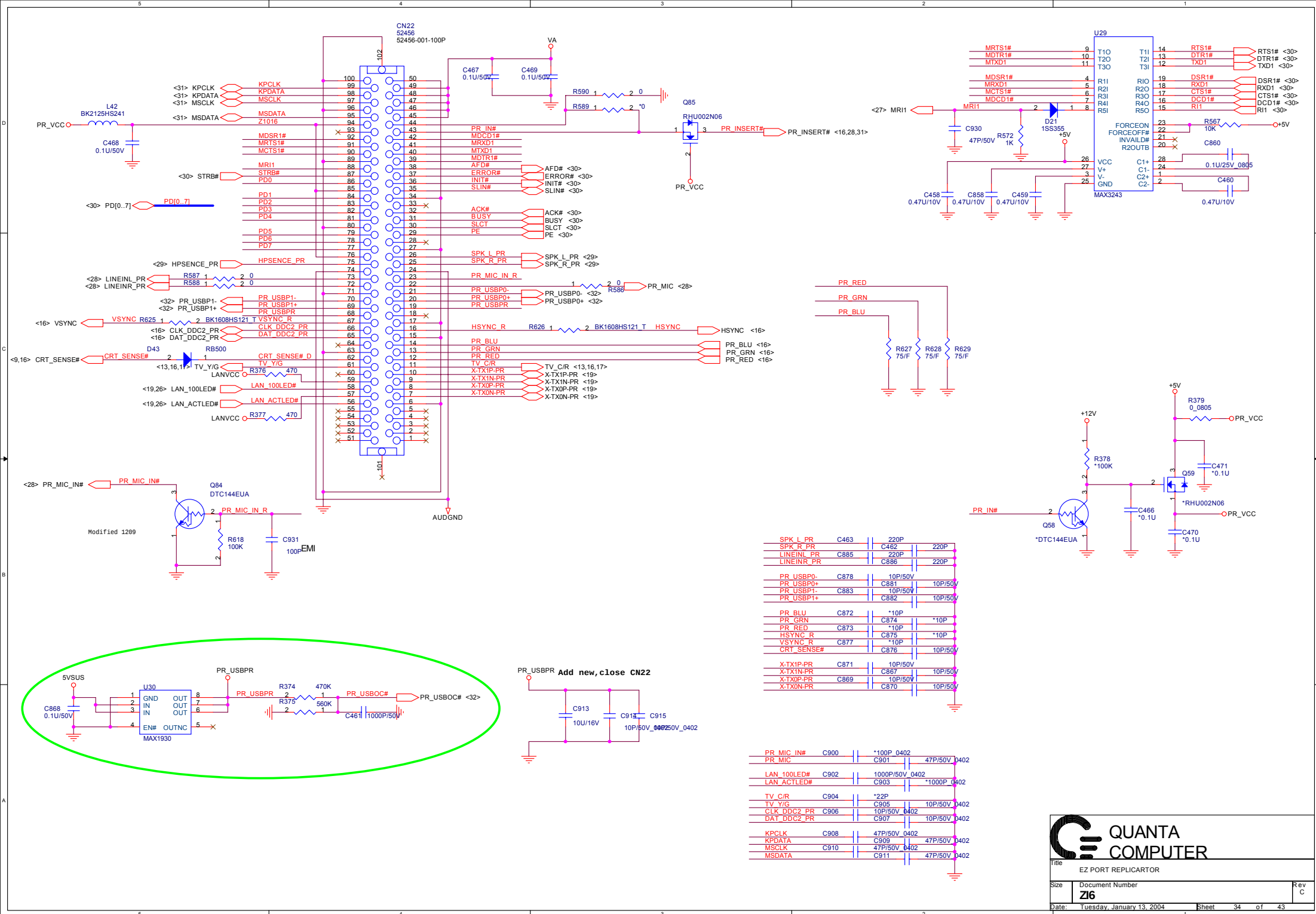


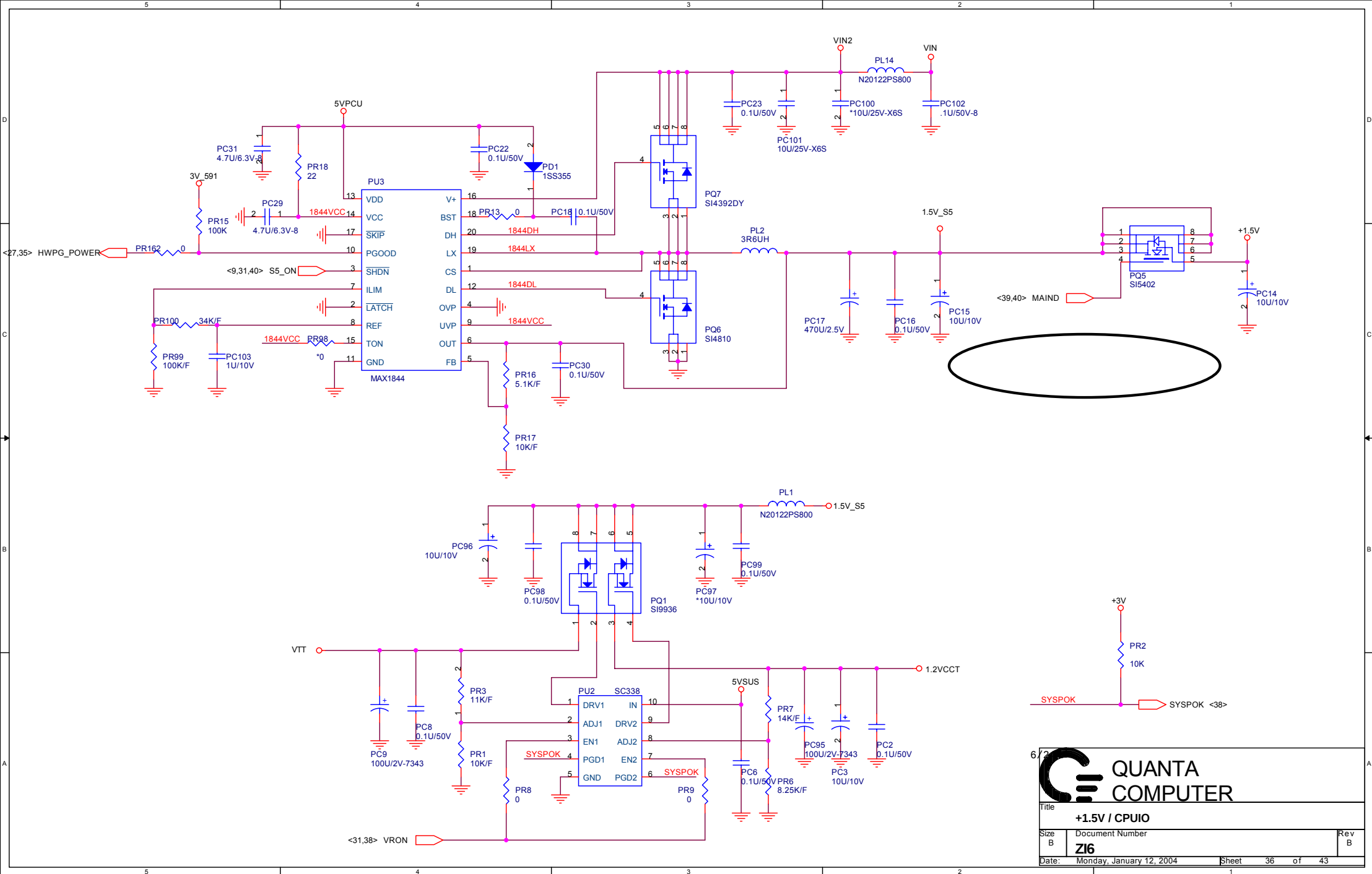
2nd FAN OUT CONNECTOR

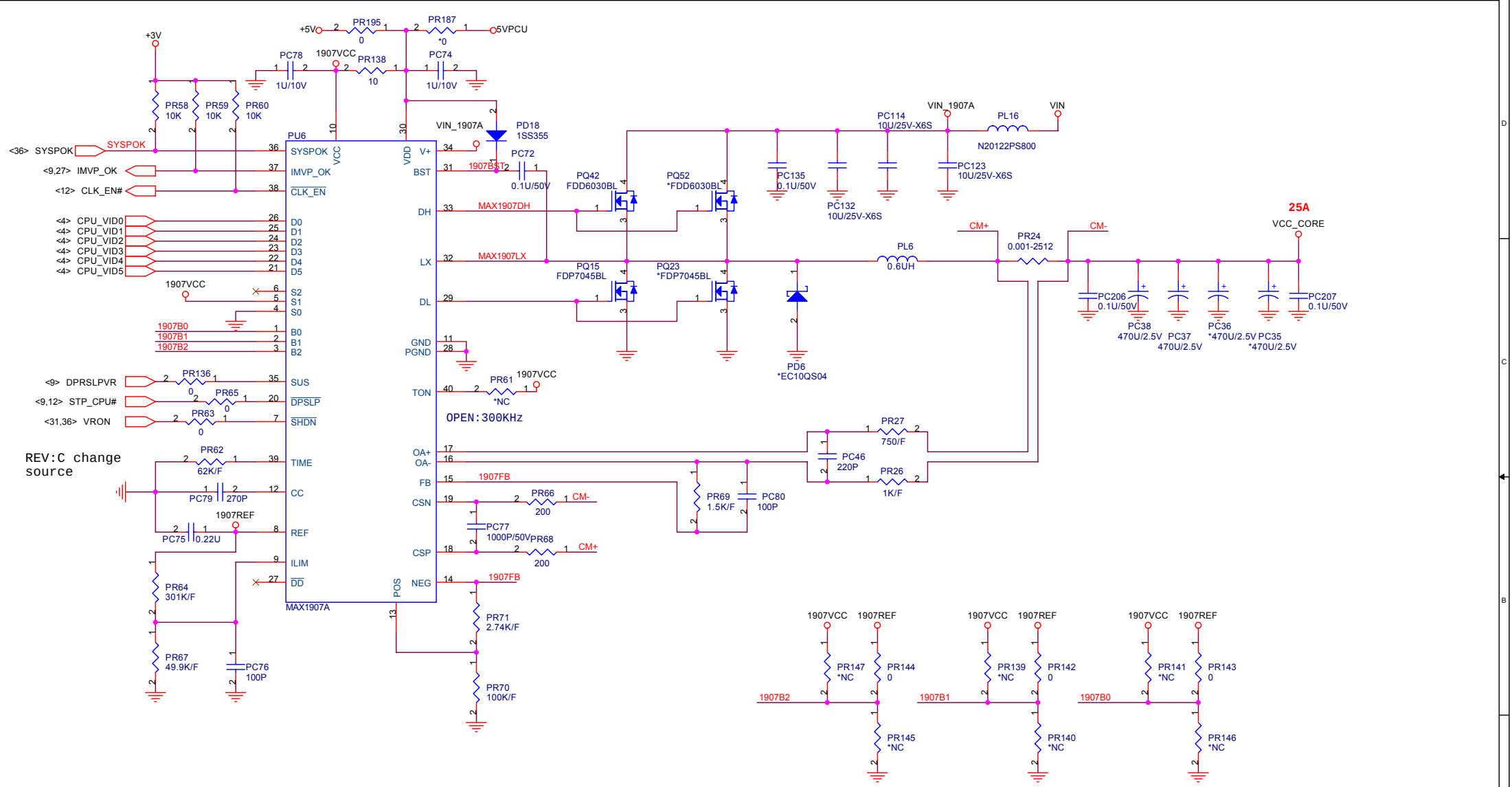


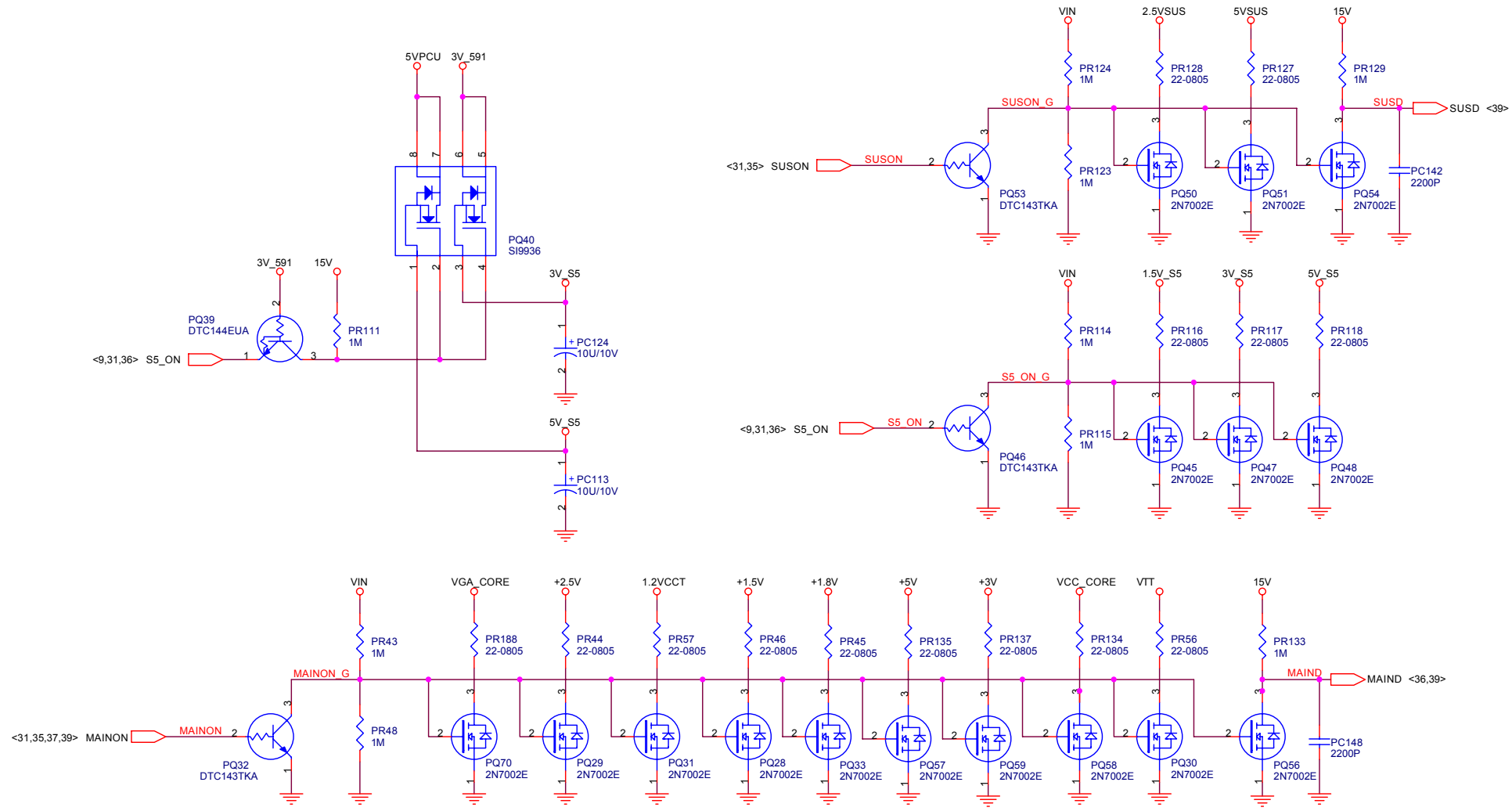
KEYBOARD







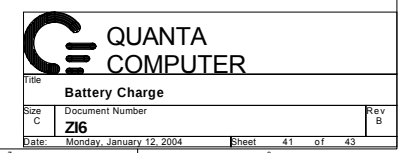




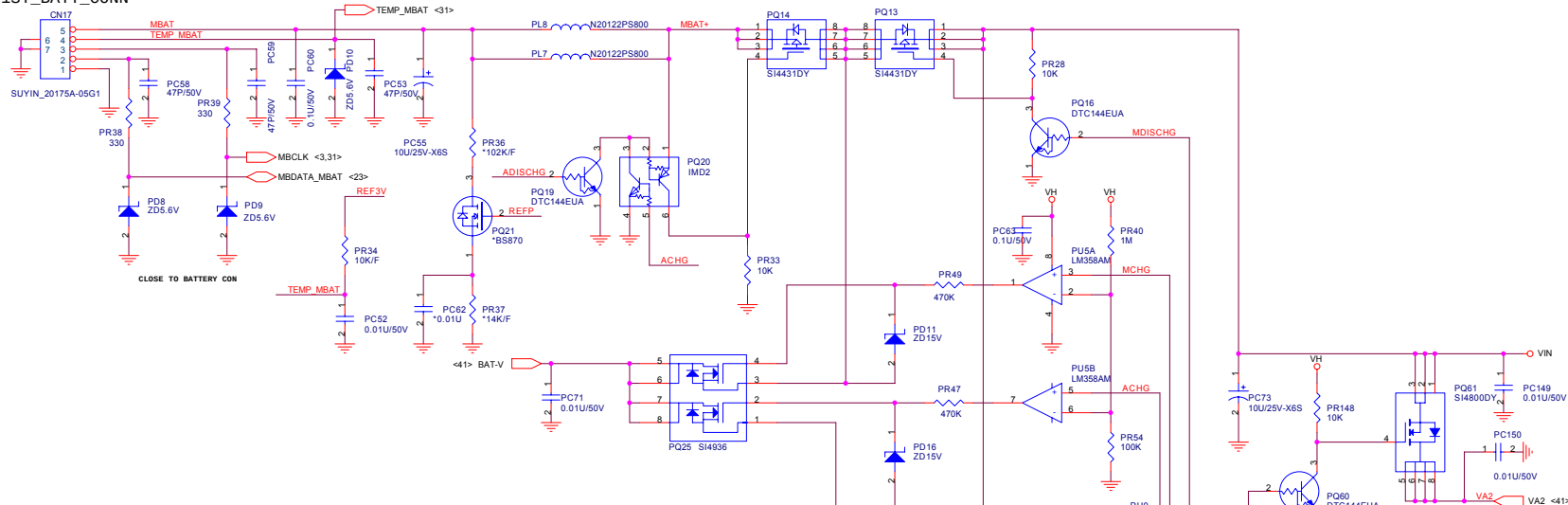


QUANTA
COMPUTER

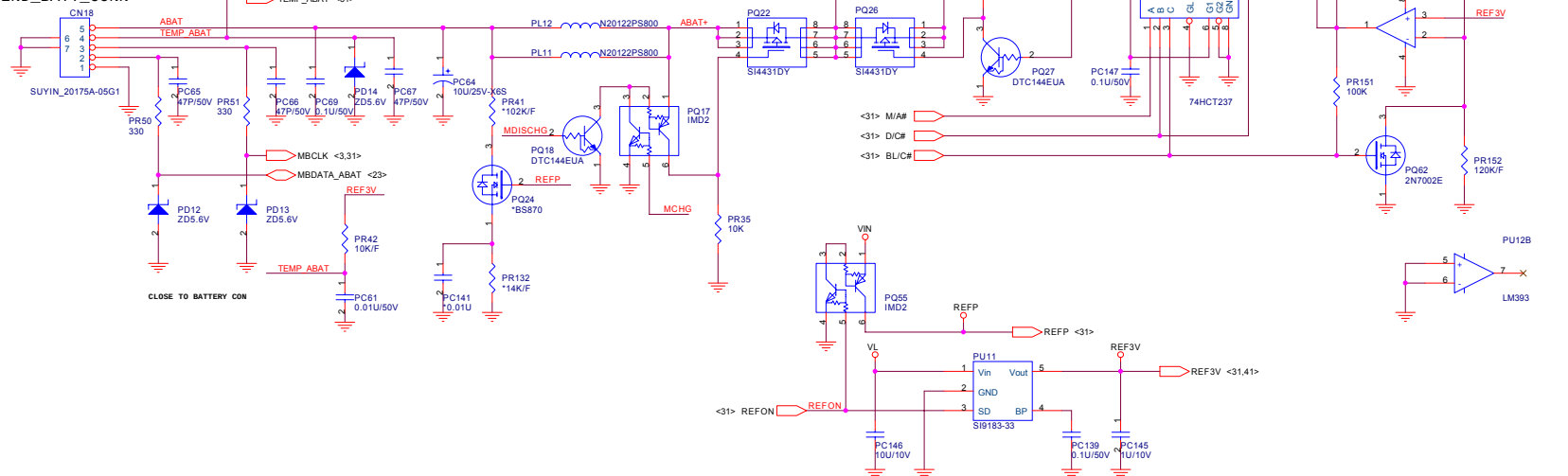
Title		
Discharge Circuit		
Size	Document Number	Rev
B	ZI6	B
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1ST_BATT_CONN



2ND_BATT_CONN



Change list

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	B.Ver #
1	ZI68 can't detect AGP device	855GME GPAR pin has internal 8.2k pull up, but still need 1k external pull up to +1.5V	A	6	Add R865 pull up res. to +1.5V	Rev B
2	Always power fail	Power net VL short to GND via PR19	A	39	Delete PR19	Rev B
3	2.5VSUS unstable	2.5VSUS may be unstable	A	35	Delete PC170	Rev B
4	None	Intel recommend	A	5	Change R476 from 27.4 ohm 1% to 37.4 ohm 1%	Rev B
5	ZI66 DVI fail	RHU002N06 slew rate is too bad, cause system can't detect DVI device.	A	18	Change Q98-Q101 from RHU002N06 to FDV301N	Rev B
6	Smart card power	Smart card power will always turn on	A	22	Change PMOS switch to NMOS avoid SC power always turn on.	Rev B
7	None	Improve CRT signal quality	A	16	Delete R531, R532, R534, R535, R537, R538	Rev B
8	None	Reserve Res for remove IDE device LED always light.	B	23	Add R874, R875 (10K ohm_0603) pull up to +5V	Rev C
9	None	Codec vendor recommend	B	28	Add R876(4.7K_0603) pull up to MICREF	Rev C
10	None	Save cost	B	22	Del. U53	Rev C
11	4 in1 1 LED power on light	4in1 LED incorrect behavior	B	21	Change net: 4in1_busy connection from U9.J19 to U9.E8	Rev C
12	EMI Request	EMI Request	B	18	Add C1067 ~ C1070 for EMI request	Rev C
13	None	Acer Request	C	16	Add U56-58 for Acer request	Rev D
14	None	Save cost	C	24	Add R879,R880 to remove 1394 EEPROM	Rev D