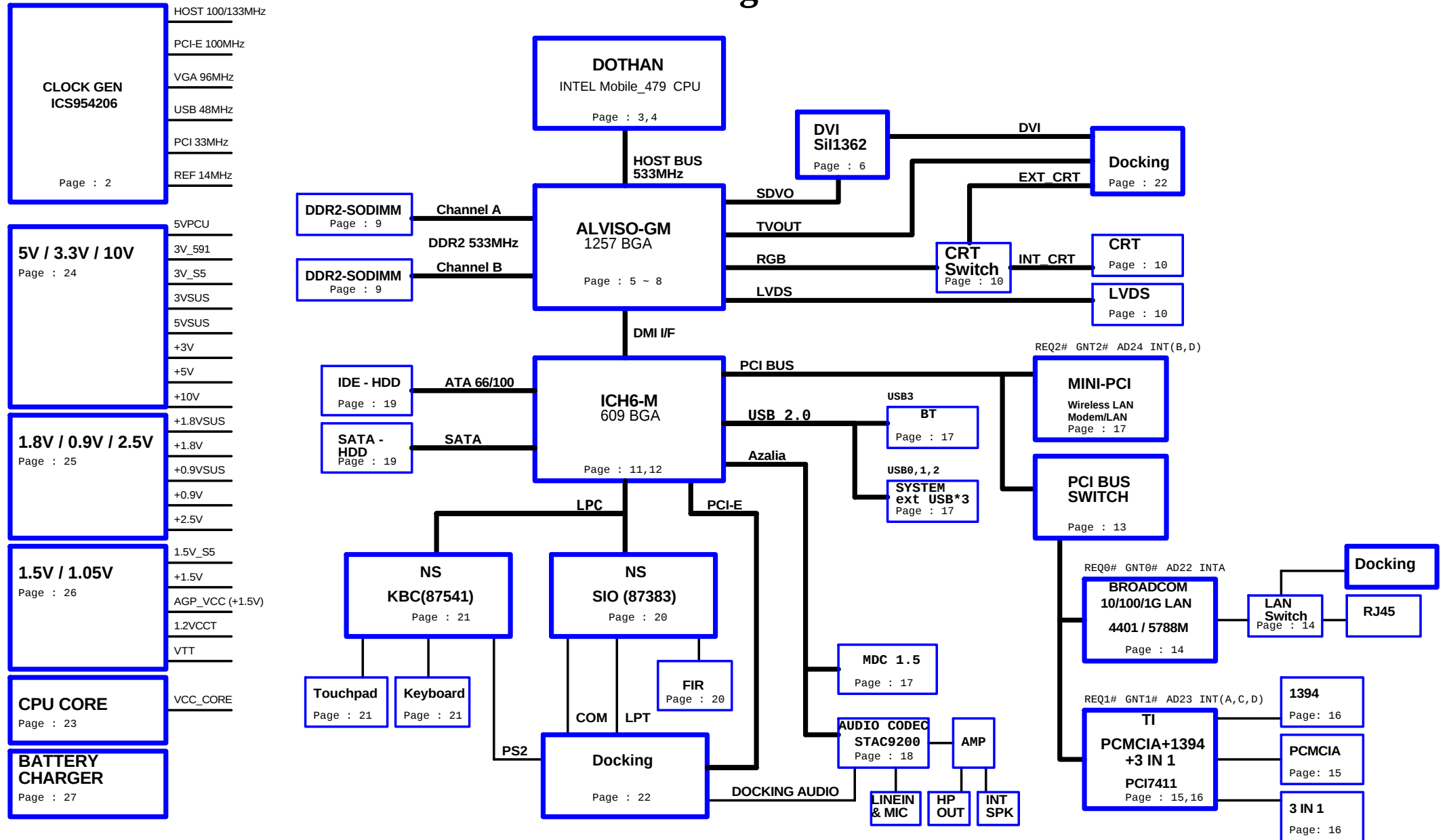


MODEL :		REV:	CHANGE LIST:	MODEL : ZH1 MB		
ZH1 MotherBoard	1A	FIRST RELEASE		PAGE	FROM	TO
	2A	PAGE02. 1.MODIFY BSEL0 & BSEL1 2.ADD PCIE CLOCK FOR SATA PAGE03. 1.UN-STUFF R218 PAGE05. 1.MODIFY BSEL0 & BSEL1 2. REMOVE M_OCDCOMP RESISTOR (Intel Errta) PAGE06. 1.MODIFY SDVO I2C BUS PULL HIGH TO 5.6K PAGE08. 1.MODIFY CAPACITOR P/N DUE TO ME ISSUE PAGE09. 1.MODIFY SO-DIMM SA0 & SA1 PAGE10. 1.MODIFY Q9,Q10 TO 2N7002 2. MODIFY RGB BEAD VALUE 3. DELETE SYNC BEFORE BUFFER RESISTOR 4. DELETE LCD_ON PULL DOWN RESISTOR PAGE11. 5.MODIFY LVDS CONNECTOR PAGE12. 1.MODIFY ICH-6 PULL HIGH RESISTOR VALUE 2. MODIFY Azalia LAYOUT TOPOLOGY 3.ADD SATA CIRCUIT 4. LAN_RST# CONNECT TO PLTRST# PAGE14. 1.MODIFY LAN POWER TO +1.5V & +3V (S3-S4 OFF) PAGE15. 1.MODIFY LAN RDAC TO 1.18K FOR IEEE TEST PAGE16. 1.MODIFY IDSEL RESISTOR TO 150 OHM PAGE17. 1394 POWER CLASS TO 30W 2.MODIFY CRYSTAL CL VALUE 3.ADD MOS FOR 1394 POWER PAGE18. 1.MODIFY IDSEL RESISTOR TO 150 OHM 2. MODIFY Azalia LAYOUT TOPOLOGY 3. MODIFY USB COMMON CHOKE FOOTPRINT & P/N PAGE19. 1.MODIFY Azalia LAYOUT TOPOLOGY 2. DELETE R458 3. ADD DIODE FOR HPS PAGE20. 1.MODIFY LED7,LED8 FOOTPRINT 2.ADD SATA INTERFACE HDD 3.REMOVE R465 , 6648_OV# USE PULL HIGH +3V PAGE21. 1.MODIFY IrDA C504 LOCATION PAGE22. 1.MODIFY KB MATRIX 2. ADD PME & MAX6648_AL# PAGE23. 1.MODIFY U18 FOOTPRINT PAGE24. 1.MODIFY PR116 FROM 787/F TO 1.54K/F FOR CPU LOAD LINE 2.MODIFY P041 FROM SUD50N03-09P TO A0D404 FOR CPU EFFICIENCY PAGE25. 1.REMOVE P02,P03,PR74 AND MOUNT PR57 FOR POWER SOURCE OF TURN ON MOSFET 2.ADD PR136,PR137,PC132,PC133 FOR EMI REQUEST PAGE26. 1.MODIFY PR78 NETNAME S5 ON CHANGE TO NETNAME SUSON 2.MODIFY PR80,PC89 FOR EMI REQUEST 3.MODIFY +VCCP POWER UP SEQUENCE PAGE27. 1.ADD PR138,PR139,PC134,PC135 FOR EMI REQUEST 2.REMOVE JP2,JP3 PAGE27. 1.REMOVE PC98 FOR ME REQUEST 2.MODIFY PR26 FROM 22K/F TO 12K/F FOR CONSTANT CURRENT LIMIT		1	1A	
	2B	PAGE 05. MODIFY NB P/N	2	2A		
		PAGE 06. MODIFY NB P/N	3	2A		
		PAGE 07. MODIFY NB P/N	4	2A		
		PAGE 08. MODIFY NB P/N	5	2A		
		PAGE 11. MODIFY SB P/N	6	2B	3A	
		PAGE 12. MODIFY SB P/N	7	2B		
			8	2B		
			9	2A	3A	
			10	2A	3A	
			11	2B		
	3A	PAGE 06. ADD DVI SERIAL RESISTORS FOR EMI	12	2B		
		PAGE 09. MODIFY DDR2 CONNECTOR P/N	13	2A	3A	
		PAGE 10. MODIFY RGB BEAD FROM 22 TO 30	14	2A	3A	
		PAGE 13. MODIFY CRYSTAL SERIAL RESISTOR R354 TO 200 OHM	15	2A	3A	
		PAGE 14. MODIFY PCI BUS	16	2A	3A	
		PAGE 15. MODIFY 1394 POWER SWITCH GATE ENABLE SIGNAL	17	2A	3A	
		PAGE 16. MODIFY USB POWER SWITCH	18	2A	3A	
		PAGE 17. MODIFY AUDIO POWER & AMP INPUT CIRCUIT	19	2A		
		PAGE 18. MODIFY FAN & SW BOARD CONNECTOR P/N	20	2A		
		PAGE 21. MODIFY RGB FOR EMI SOLUTION	21	2A	3A	
		PAGE 22. MODIFY CAPACITOR FOR NOISE	22	2A	3A	
		PAGE 23. STUFF SNUBBER CIRCUIT	23	2A	3A	
		PAGE 24. UN-STUFF PC23 & STUFF SNUBBER CIRCUIT	24	2A	3A	
		PAGE 25. STUFF SNUBBER CIRCUIT	25	2A	3A	
		PAGE 26. STUFF SNUBBER CIRCUIT	26	2A	3A	

ZH1 Block Diagram

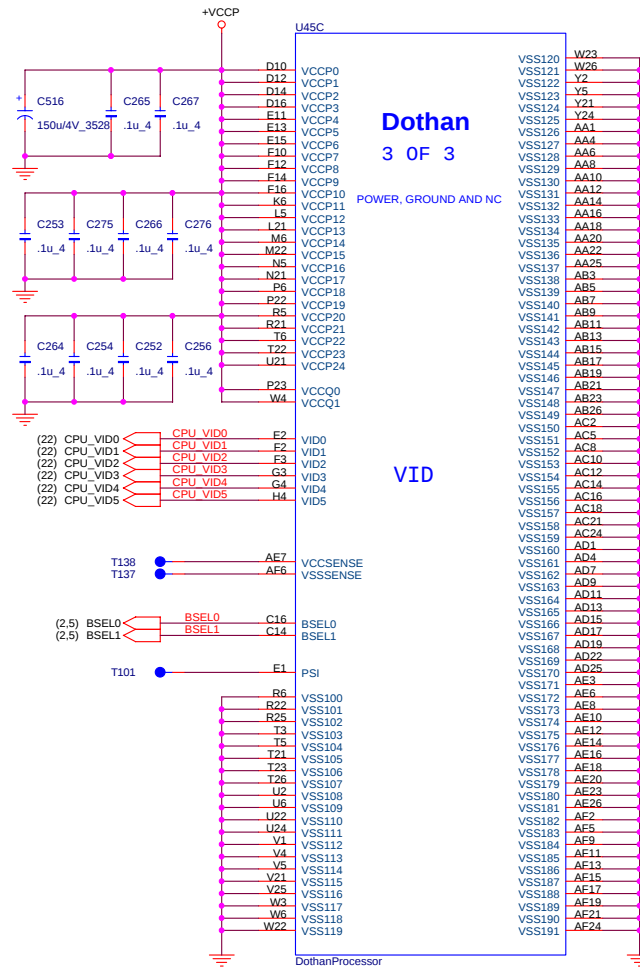
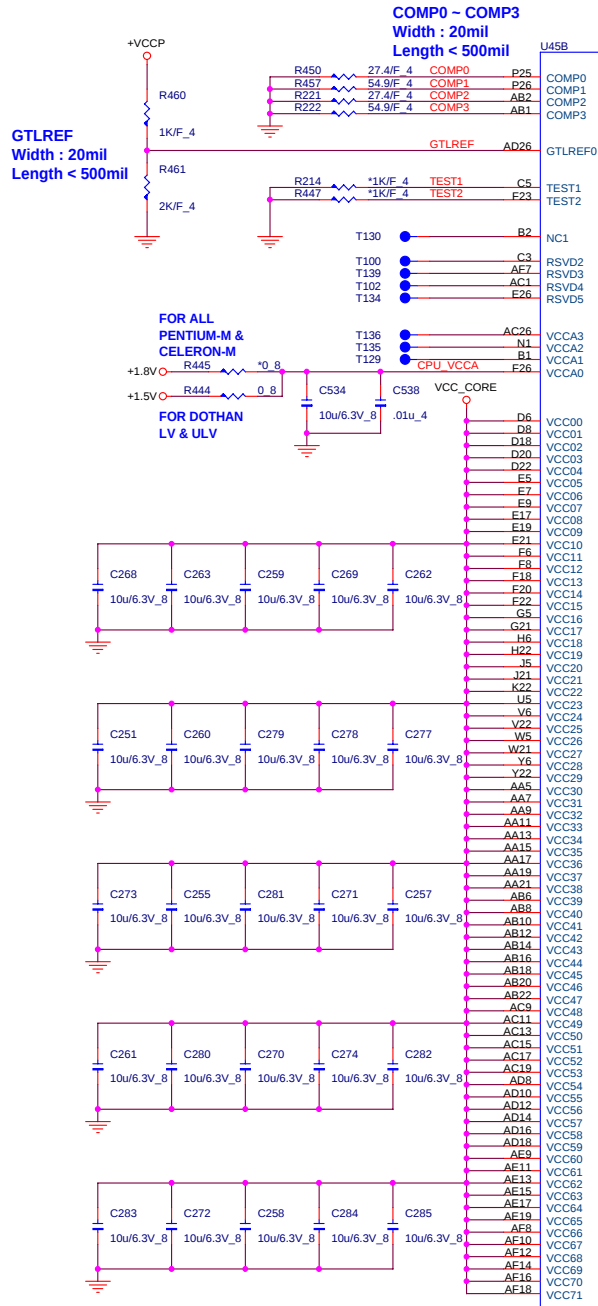


PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
REQ0# / GNT0#	AD22	INTA#	BROADCOM LAN
REQ2# / GNT2#	AD24	INTB# , INTD#	MINI-PCI
REQ1# / GNT1#	AD23	INTC# , INTD# , INTA#	TI 7411

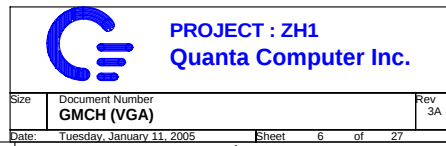
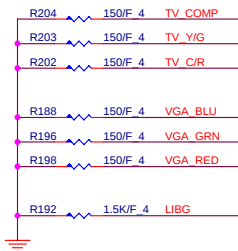


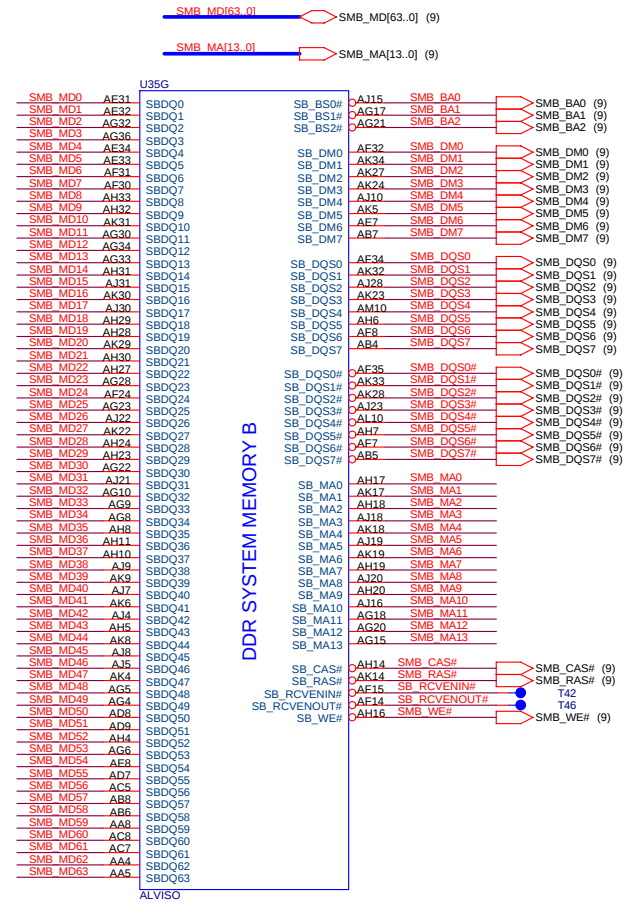
PROJECT : ZH1
Quanta Computer Inc.

Size	Document Number	Rev
	BLOCK DIAGRAM	1A
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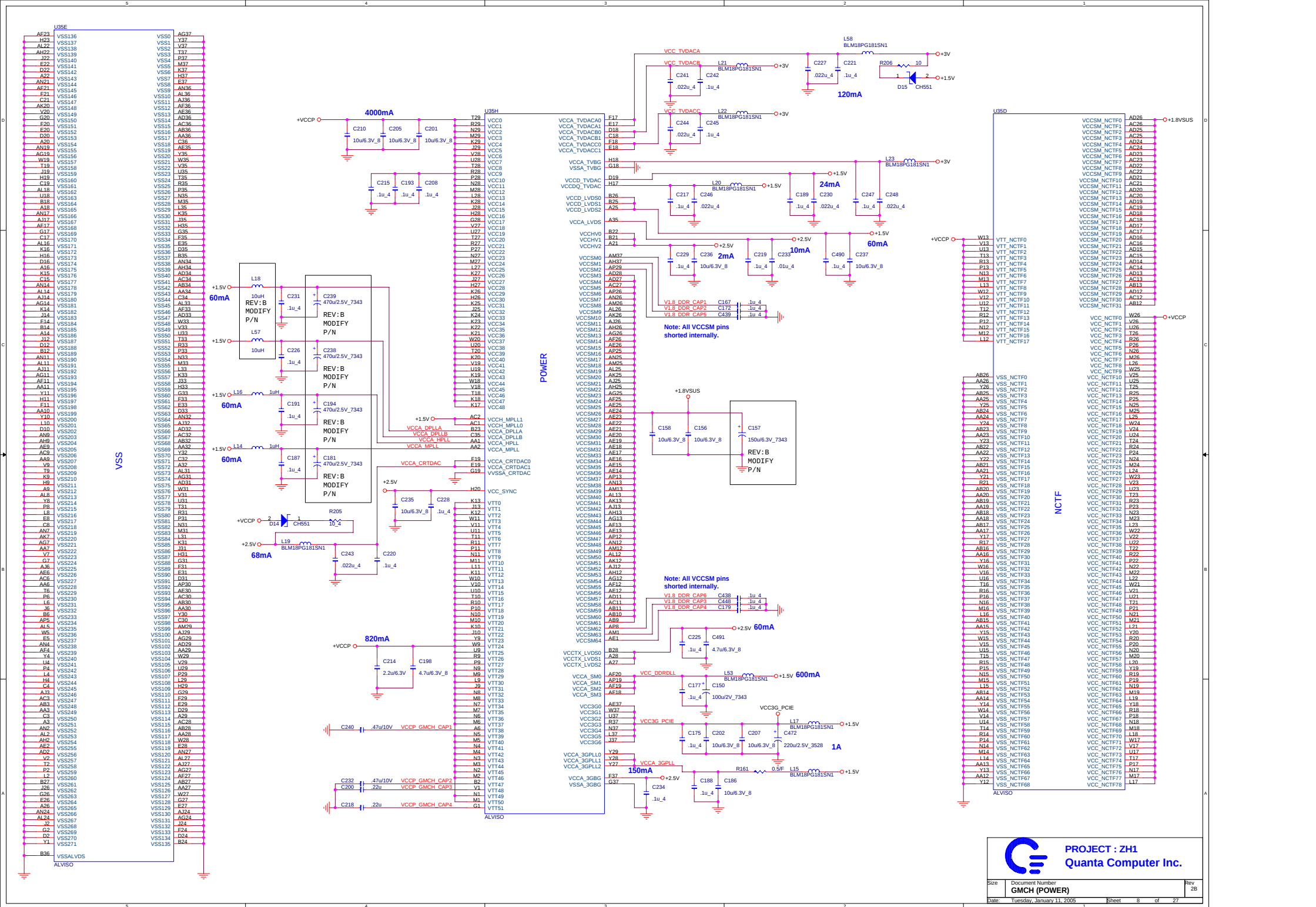


PROJECT : ZH1
Quanta Computer Inc.





PROJECT : ZH1
Quanta Computer Inc.



The top diagram shows a 0.9V supply connected to a series of capacitors (C401 to C430) connected to a 150u/6.3V_7343 capacitor. The bottom diagram shows a 0.9V supply connected to a series of capacitors (C76 to C80) connected to a 150u/6.3V_7343 capacitor.

Signal	Value	Unit
SM CKE0	R84	56.4
SM CKE1	R306	56.4
SM CKE2	R333	56.4
SM CKE3	R67	56.4
SM CS0#	R309	56.4
SM CS1#	R82	56.4
SM CS2#	R64	56.4
SM CS3#	R337	56.4
SM ODT0	R310	56.4
SM ODT1	R83	56.4
SM ODT2	R65	56.4
SM ODT3	R338	56.4

+0.9V				+0.9V			
SMB MA0	RN10	1	2 56 4P2R	SMA MA10	RN14	1	2 56 4P2R
SMB MA2		3	4	SMA BA0		3	4
SMB MA2	RN22	1	2 56 4P2R	SMA MA2	RN19	1	2 56 4P2R
SMB MA3		3	4	SMA MA0		3	4
SMB MA13	R66		56 4	SMA MA5	RN13	1	2 56 4P2R
SMB BA1	R62		56 4	SMA MA1		3	4
SMB MA4	RN12	1	2 56 4P2R	SMA MA6	RN18	1	2 56 4P2R
SMB MA7		3	4	SMA MA4		3	4
SMB MA5	RN21	1	2 56 4P2R	SMA MA8	RN16	1	2 56 4P2R
SMB MA8		3	4	SMA MA3		3	4
SMB BA0	RN23	1	2 56 4P2R	SMA MA11		3	4
SMB MA1		3	4	SMA MA7	RN15	1	2 56 4P2R
SMB MA9	RN20	1	2 56 4P2R	SMA MA12		3	4
SMB MA12		3	4	SMA MA9		3	4
SMB MA6	RN11	1	2 56 4P2R	SMA BA1	R307		56 4
SMB MA11		3	4	SMA BA3	R311		56 4
SMB BA2	R334		56 4	SMA BA2	R38		56 4
SMB RAS#	R63		56 4	SMA RAS#	R308		56 4
SMB CAS#	R336		56 4	SMA CAS#	R81		56 4
SMB WE#	R335		56 4	SMA WE#	R80		56 4

[illegible]

1.8V_VBUS

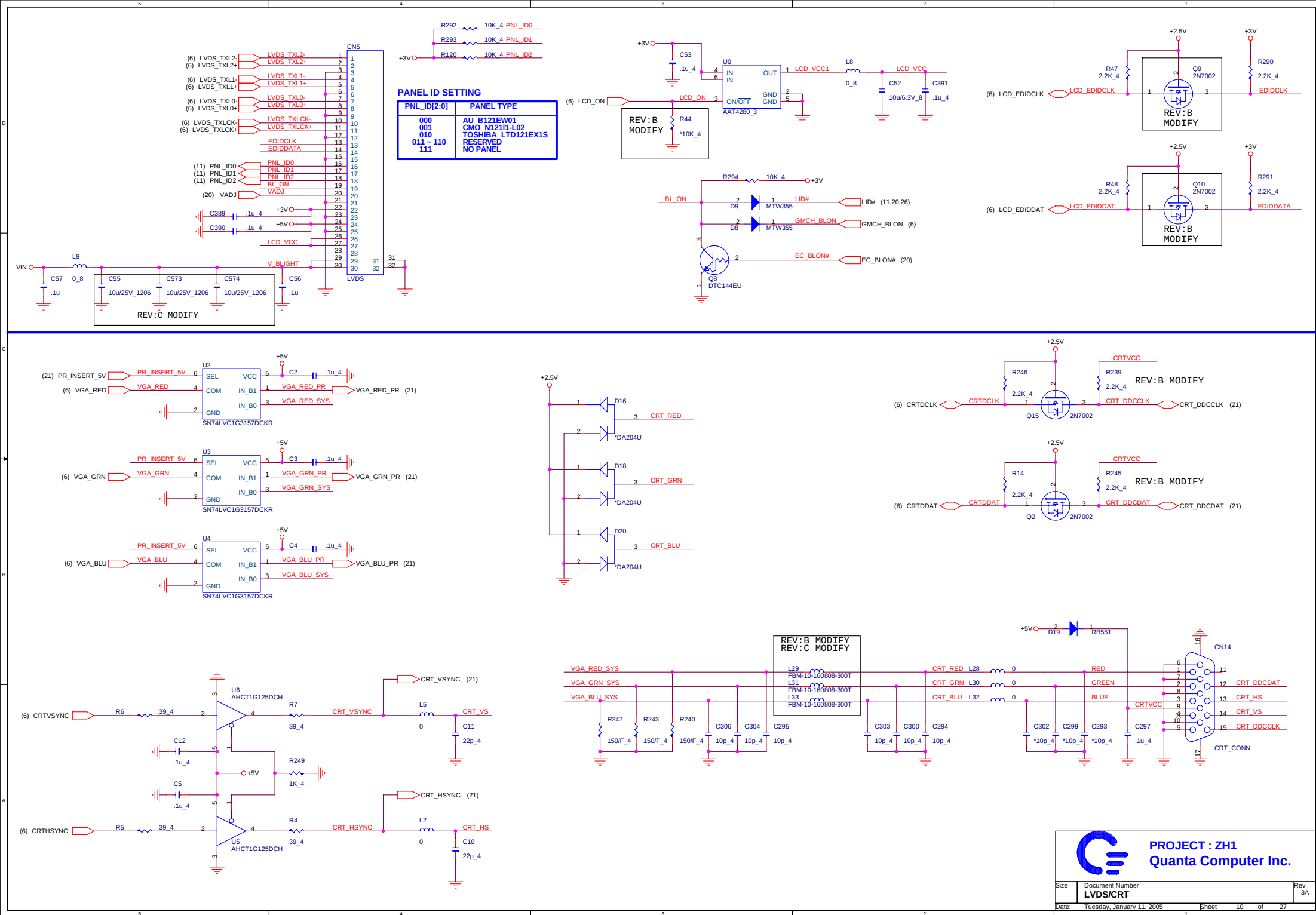
CN20C

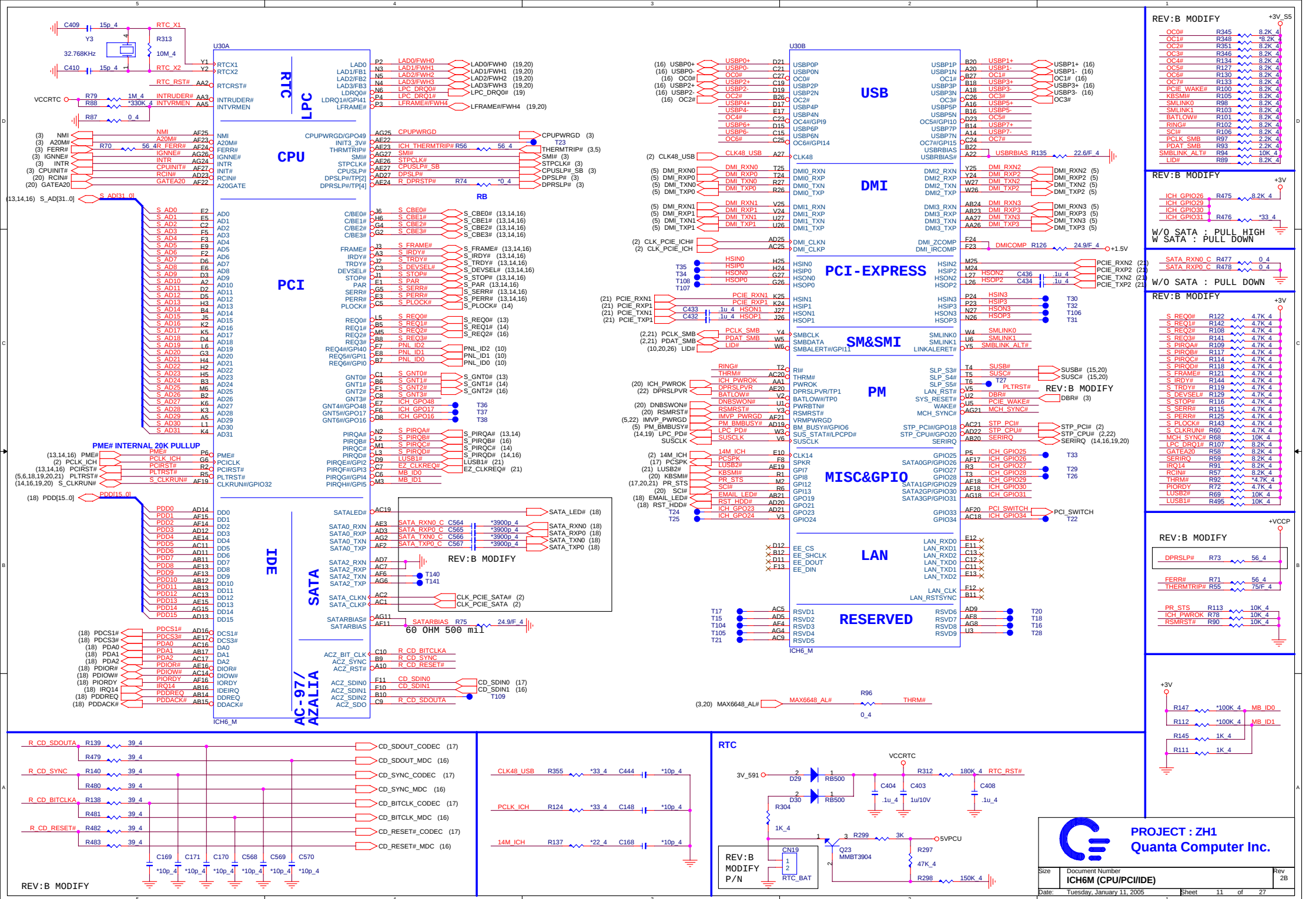
COMMON PIN

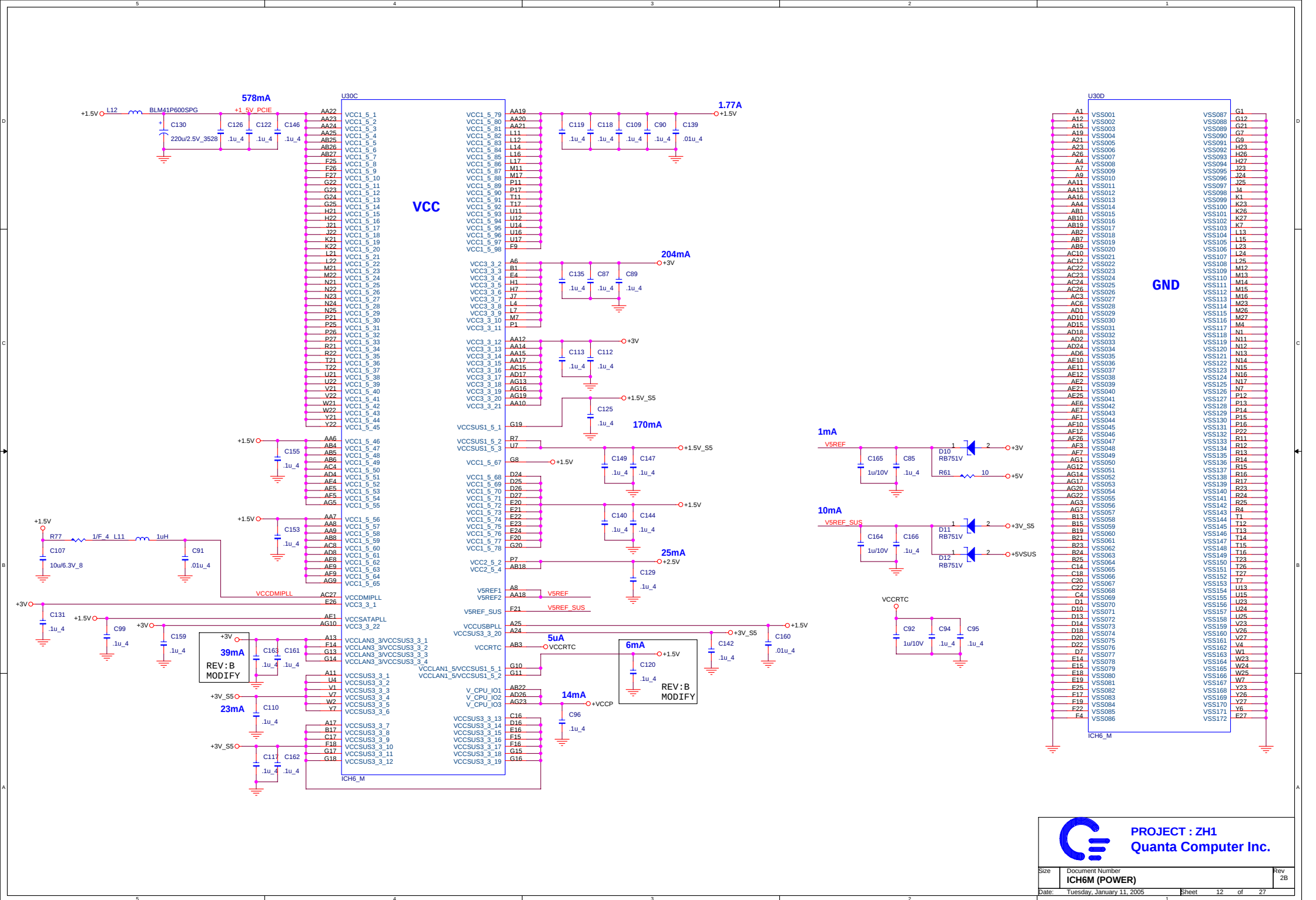
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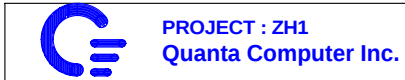


Size	Document Number DDR2 SO-DIMM & TERMINATOR	Rev 3A
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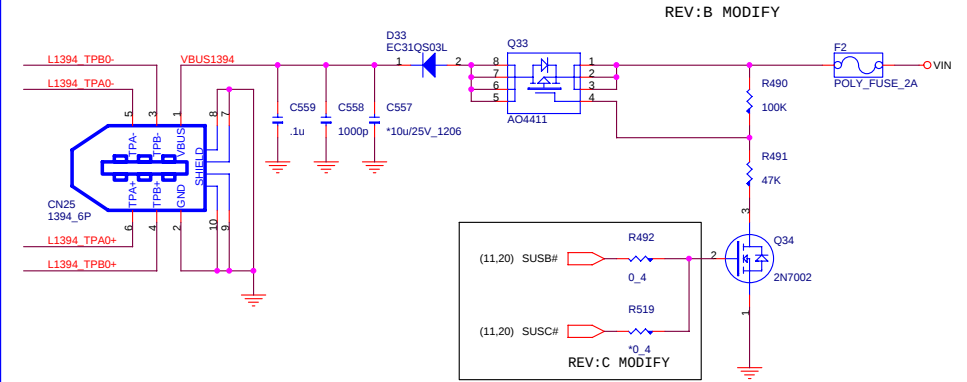
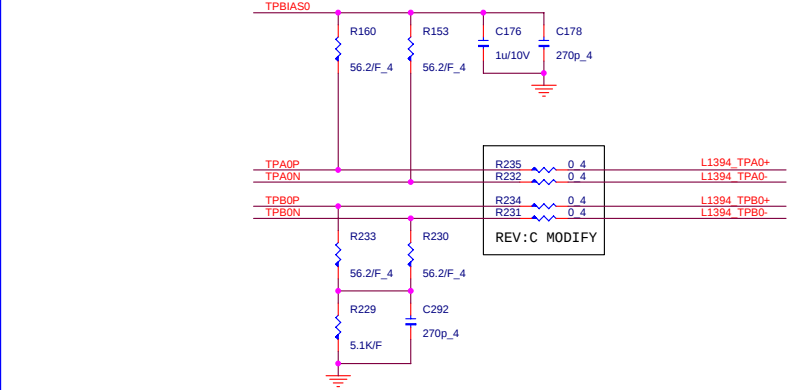
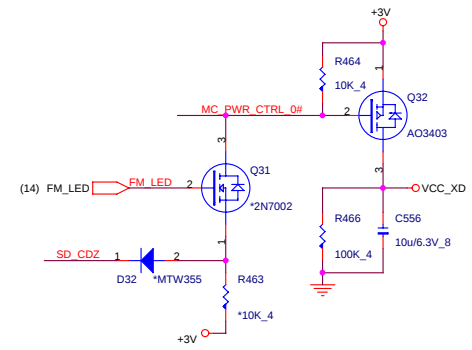
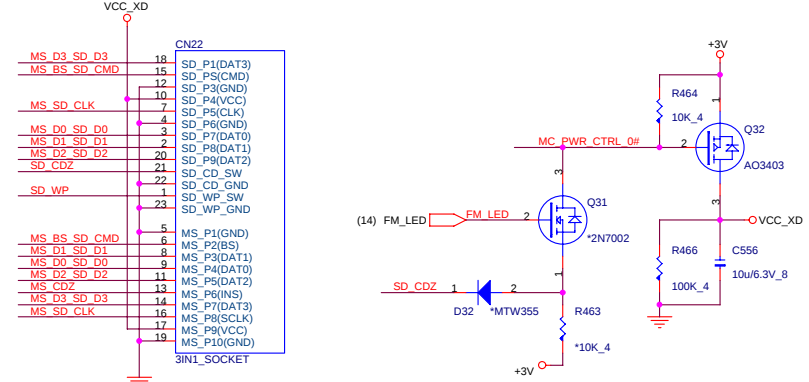
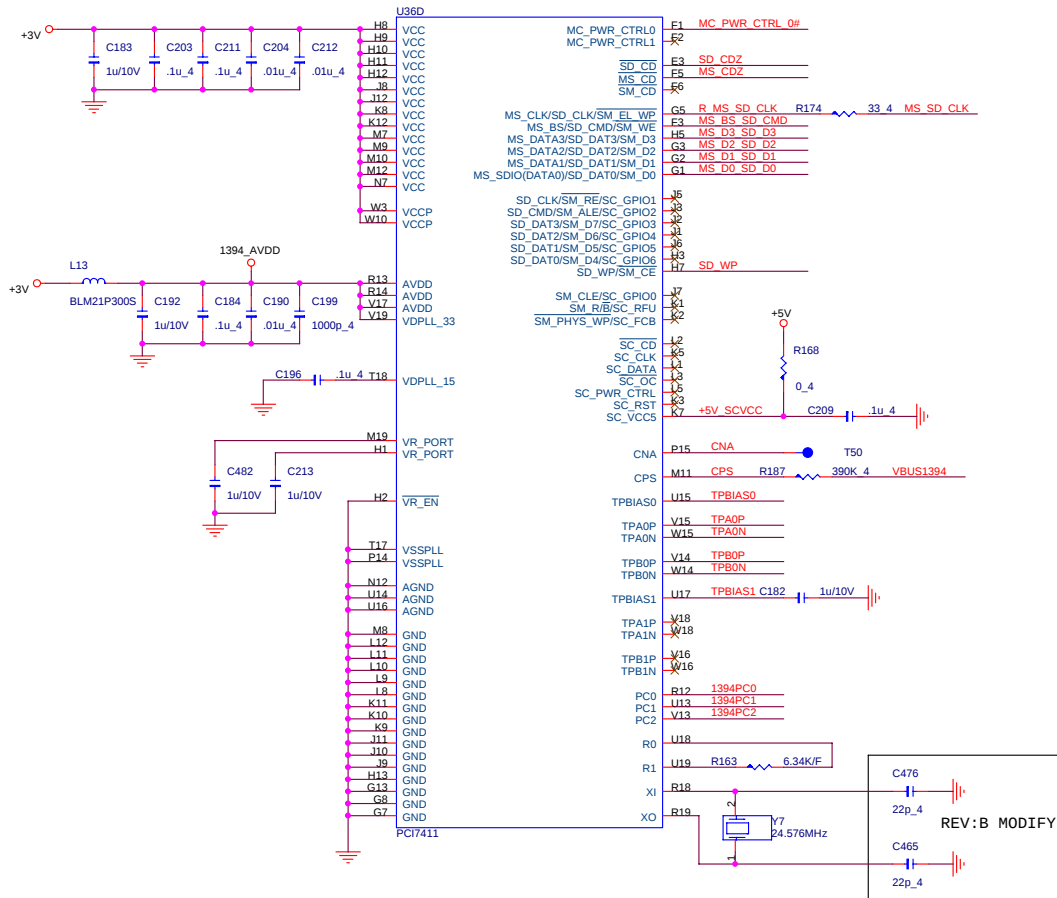


Power Class

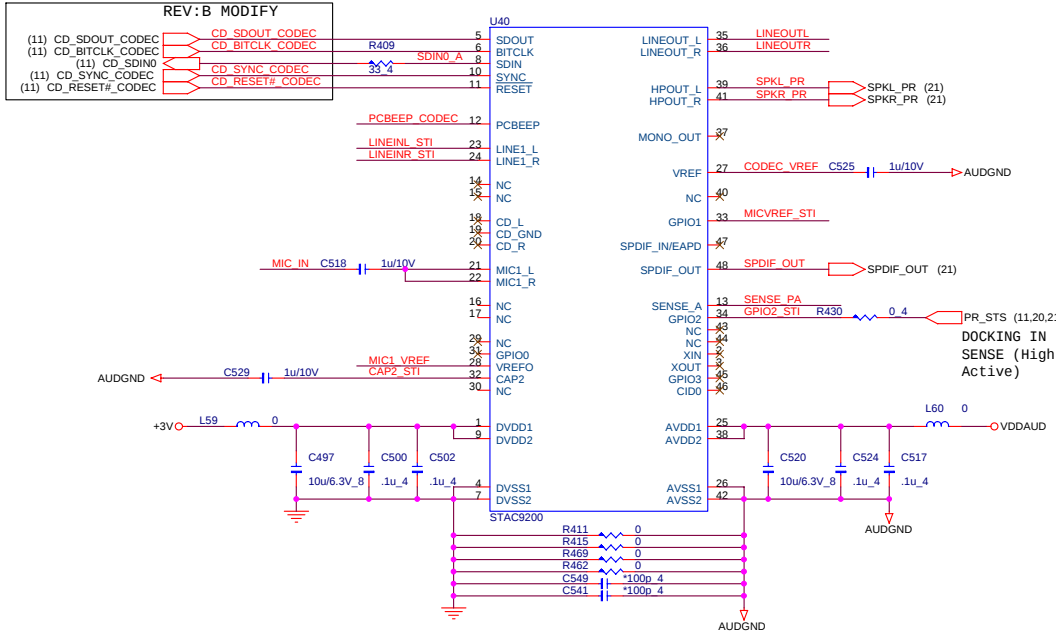
PC[2:0]	VBUS POWER
000	No Power
001	15W
010	30W
011	45W

REV:B MODIFY

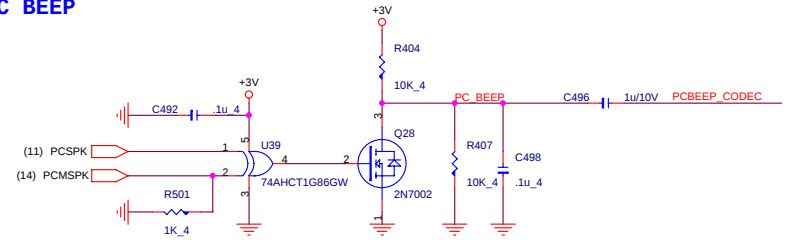
DEFAULT



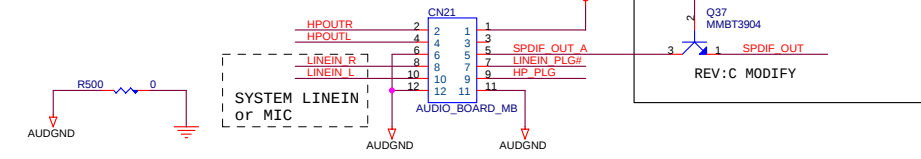
CODEC



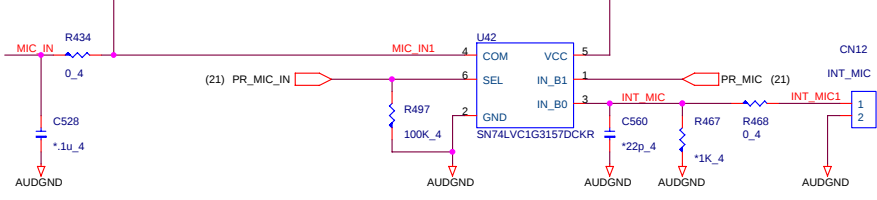
PC BEEP



AUDIO BOARD

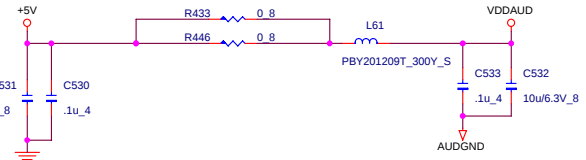


INTERNAL & DOCKING MIC
PORT B



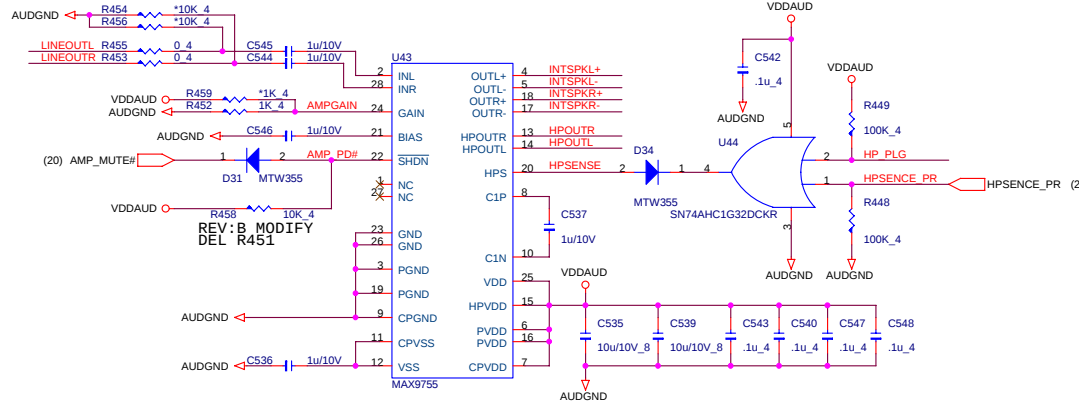
AUDIO POWER

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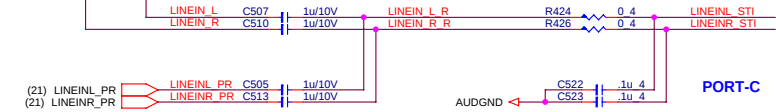


AMP

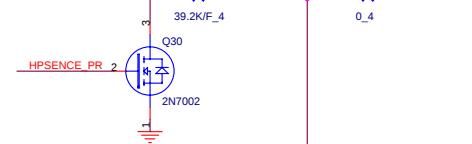
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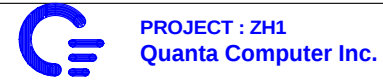
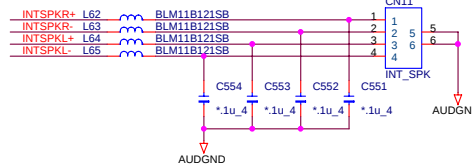
LINEIN PORT C



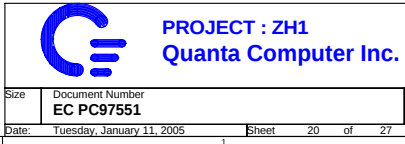
PORT-A SENSE



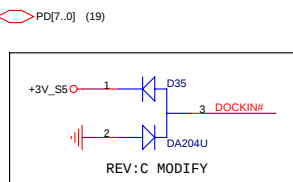
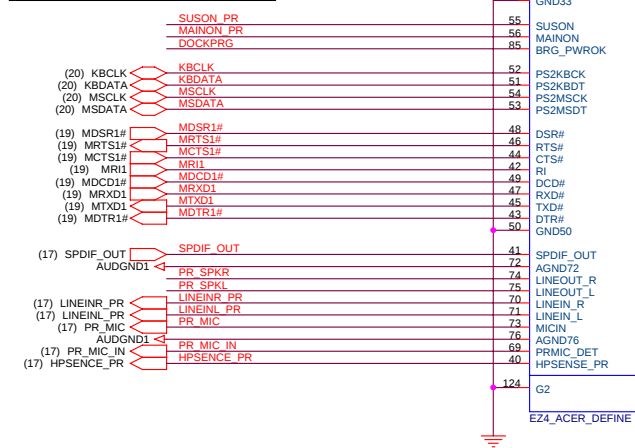
PORT-C SENSE



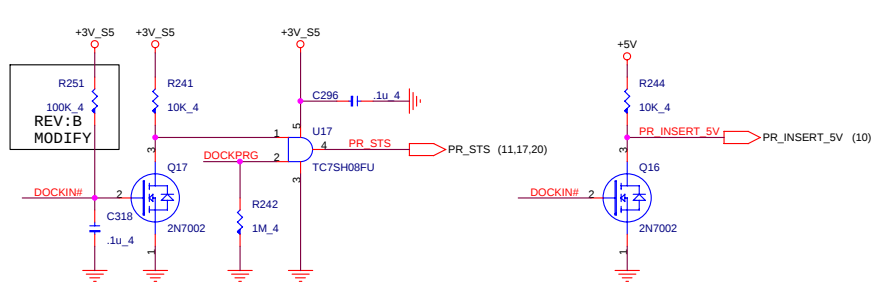
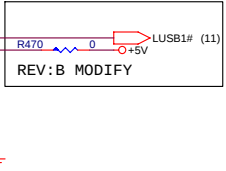
Size	Document Number	Rev
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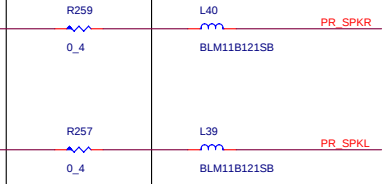
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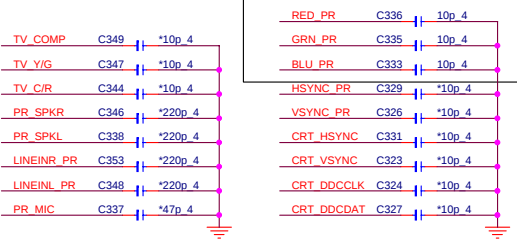
REV:B MODIFY



REV:C MODIFY



REV:C MODIFY BY EMI



REV:B MODIFY FOOTPRINT TO SSOP8-4-65

