

ZW6 BLOCK DIAGRAM

CENTRINO

@:ZI66 Internal Gra.

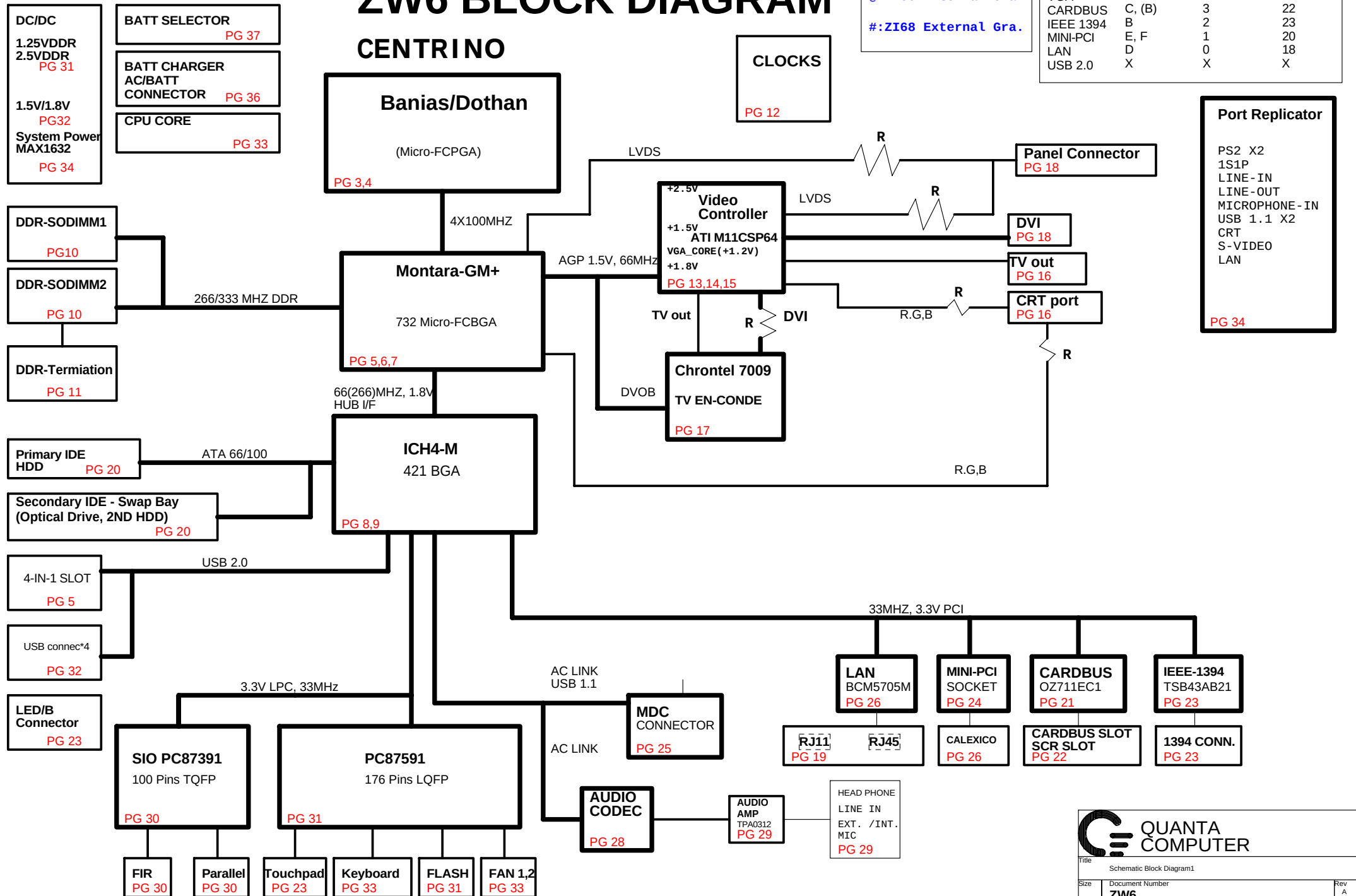
#:ZI68 External Gra.

	PIRQ#	REQ#/GNT#	IDSEL
VGA	X	X	X
CARDBUS	C, (B)	3	22
IEEE 1394	B	2	23
MINI-PCI	E, F	1	20
LAN	D	0	18
USB 2.0	X	X	X

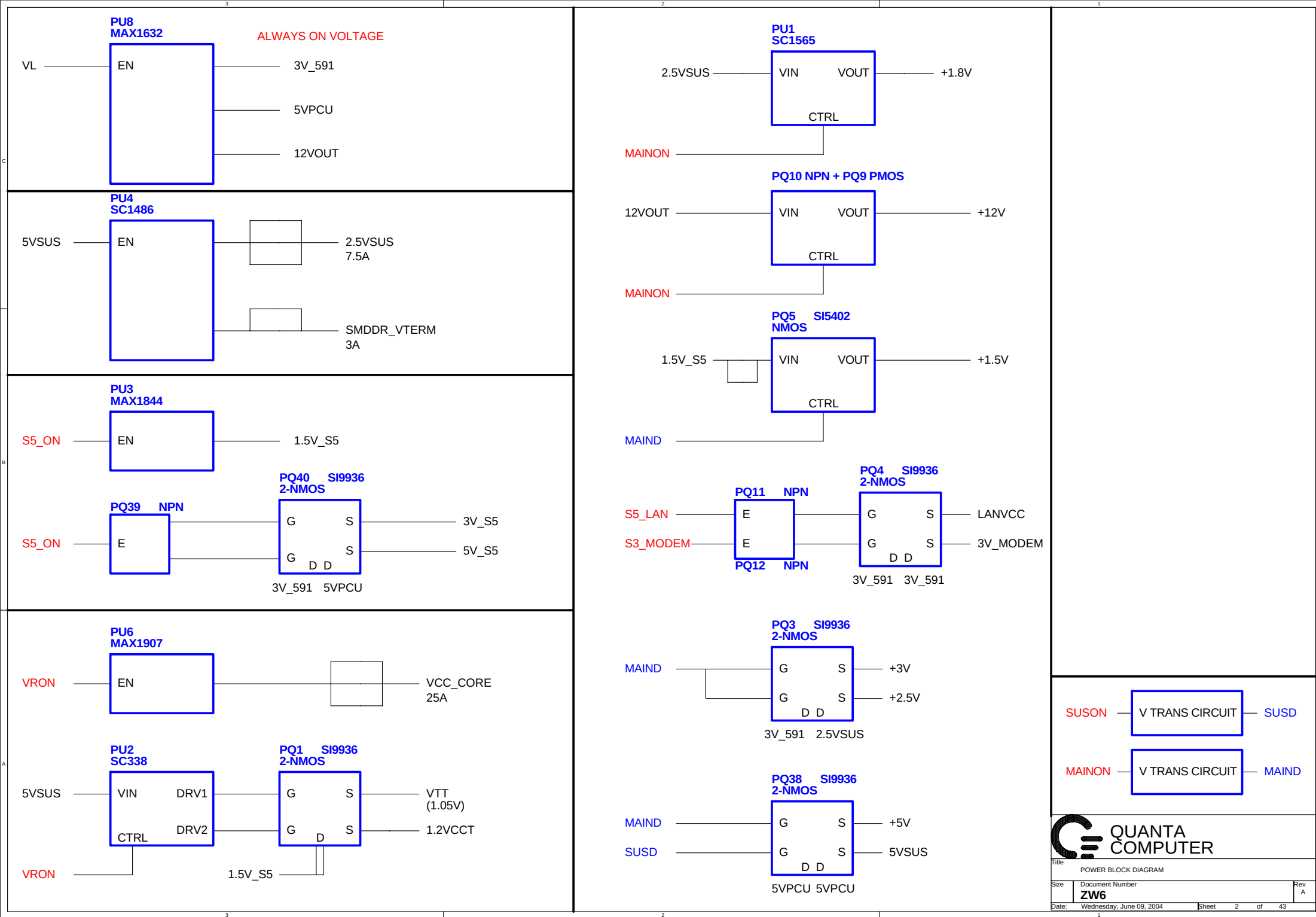
Port Replicator

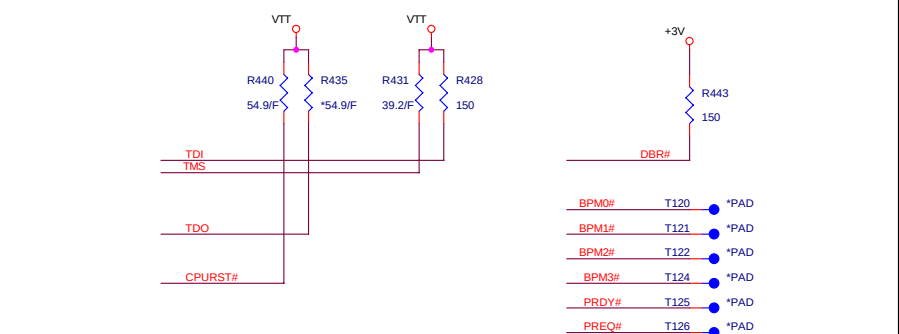
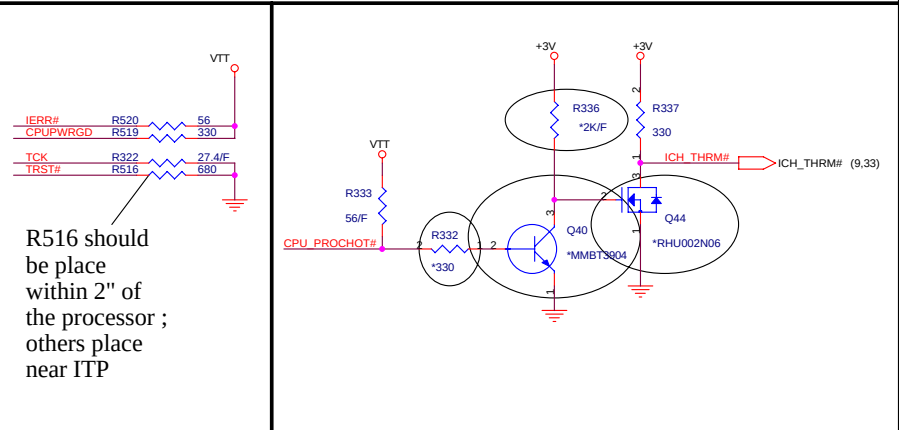
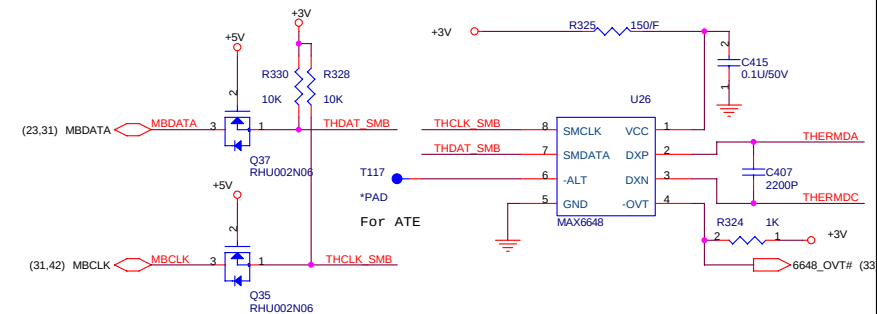
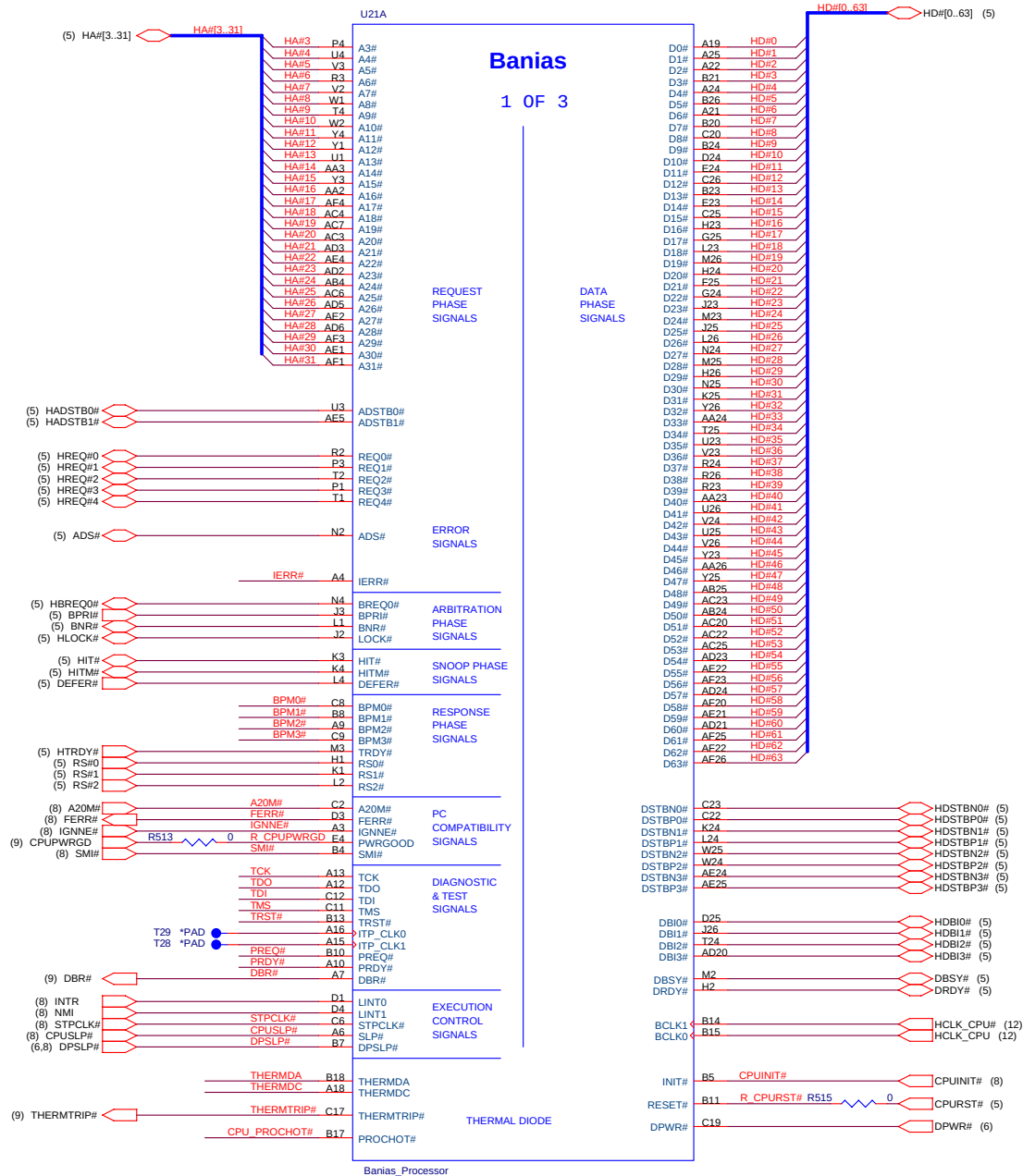
PS2 X2
1S1P
LINE-IN
LINE-OUT
MICROPHONE-IN
USB 1.1 X2
CRT
S-VIDEO
LAN

PG 34

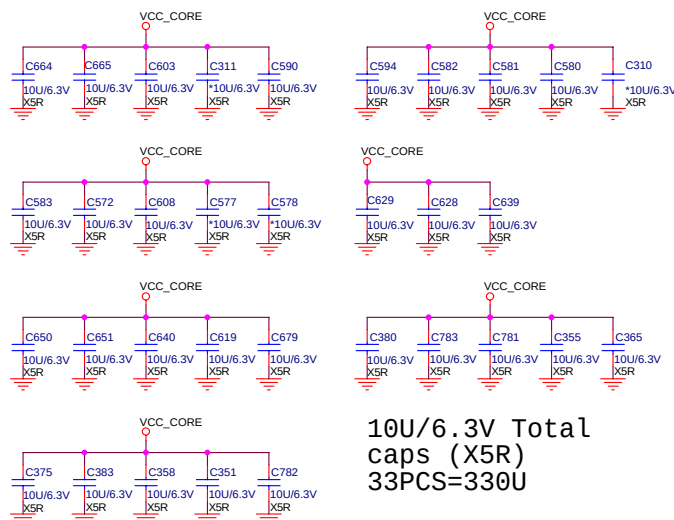
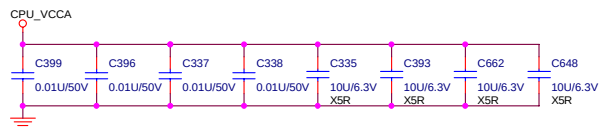


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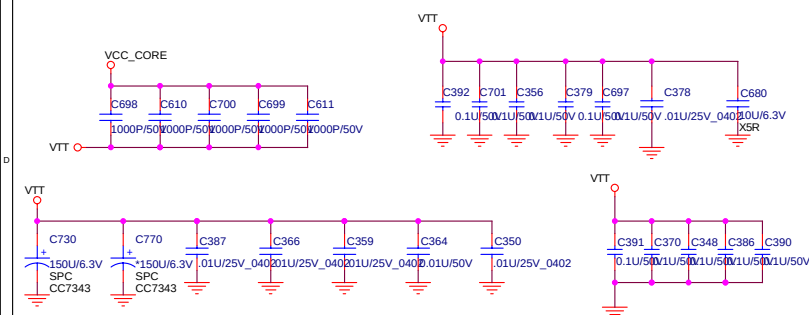




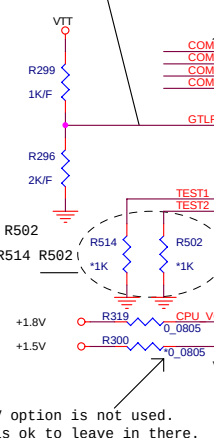
Del ITP700
Del R434, R439, C542, R442



10U/6.3V Total
caps (X5R)
33PCS=330U

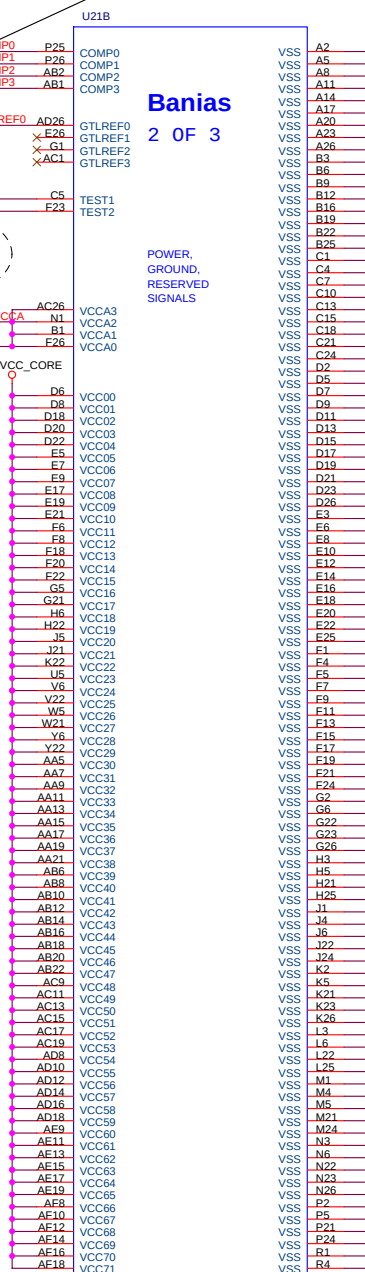


25 MILS (>50 MILS PREFERRED) SPACE
T < 0.5"



A1 CPU not install R514

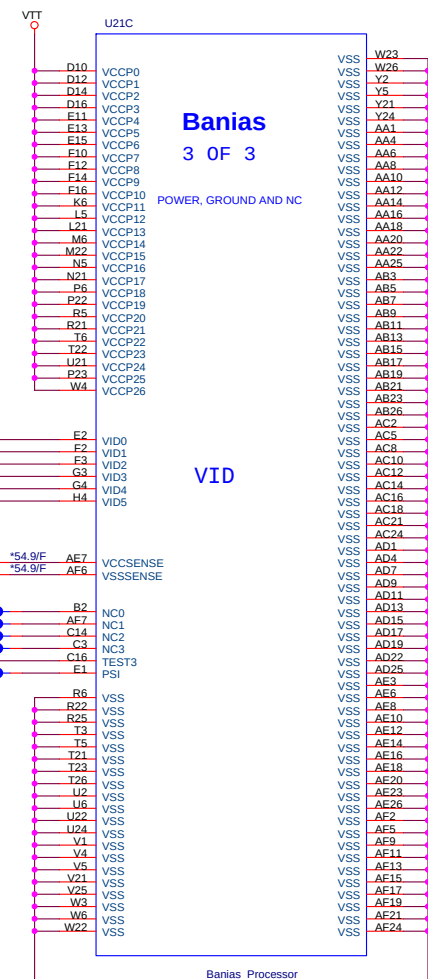
1.5V option is not used.
It is ok to leave in there.



Banias
2 OF 3

POWER,
GROUND,
RESERVED
SIGNALS

Banias Processor



Banias

POWER, GROUND AND NC

VID

Banias Processor

```
*
VCC,BOOT 1.2V
VCC_CORE 1.468V
(DEEPER SLEEP) 0.956V
VTT 1.05V
VCCA 1.8V
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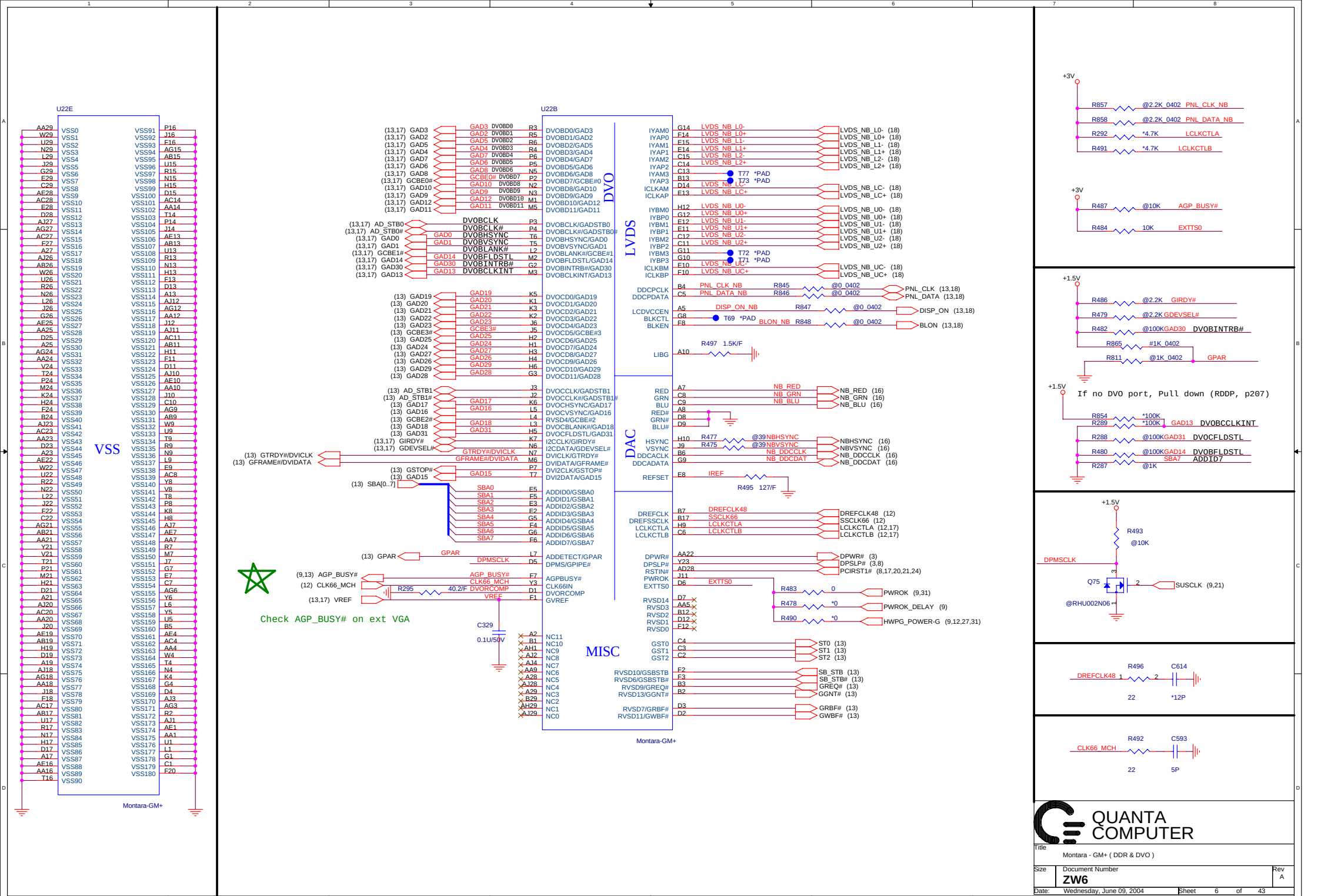


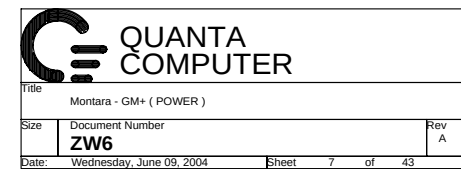
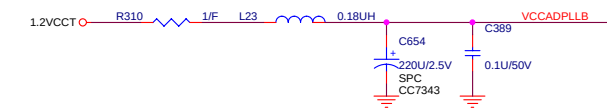
Title	Banias Processor (POWER)
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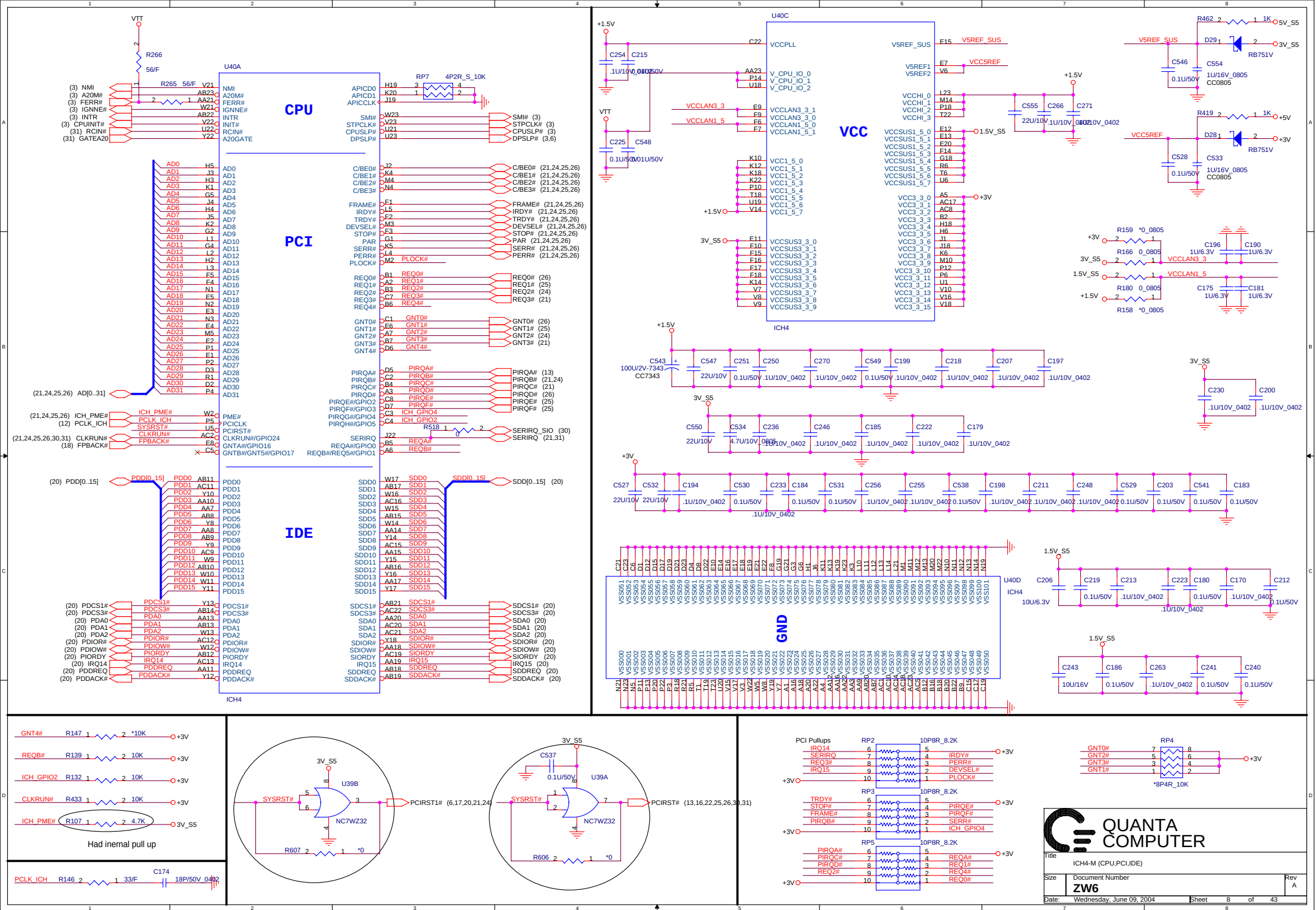
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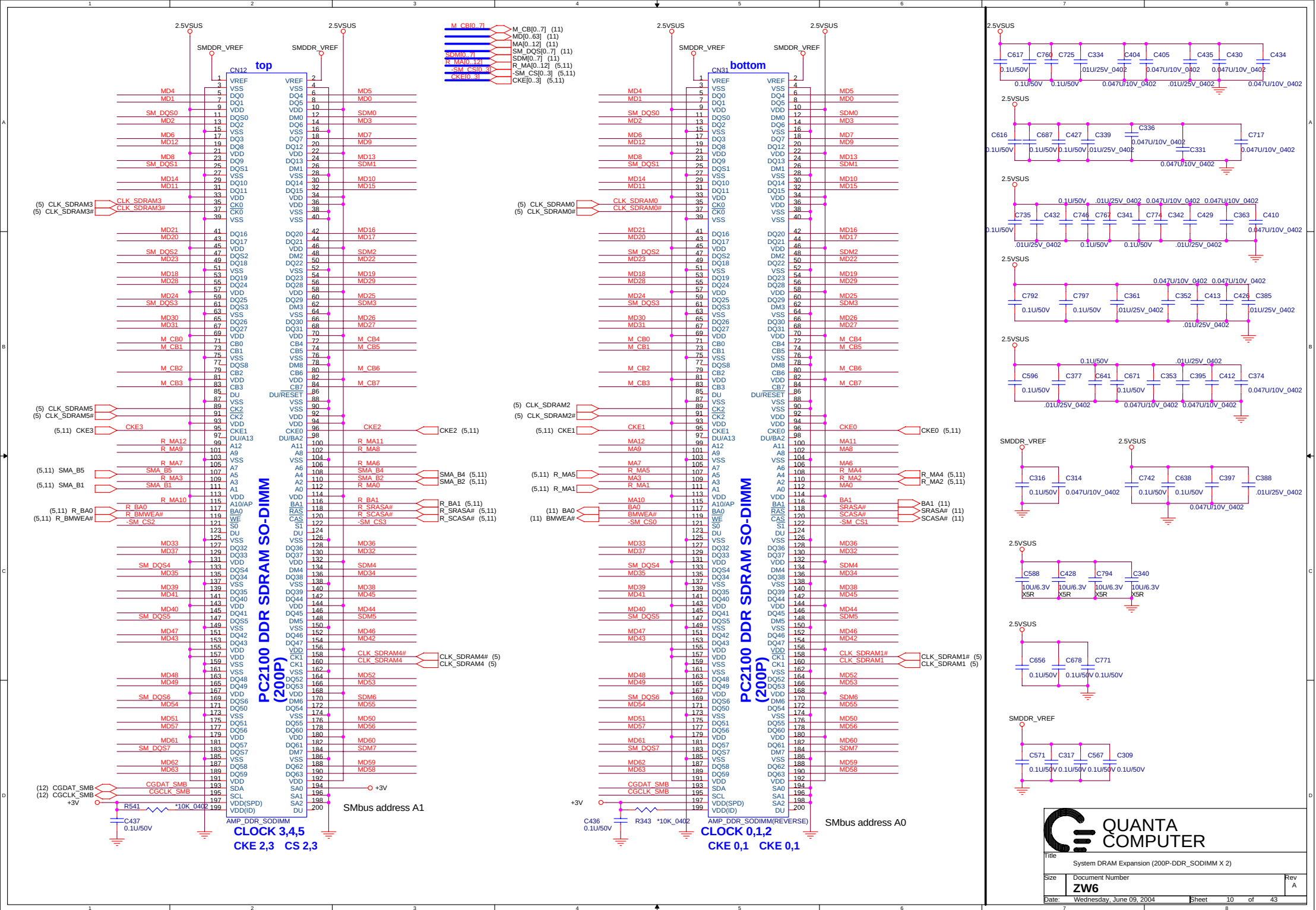
ZW6

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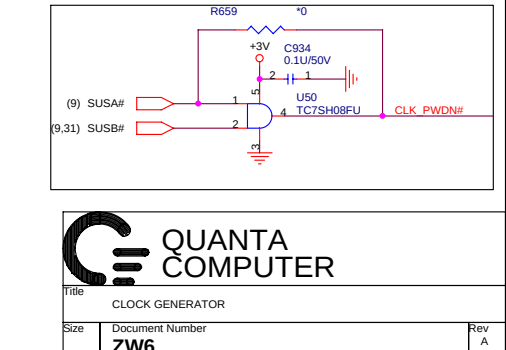
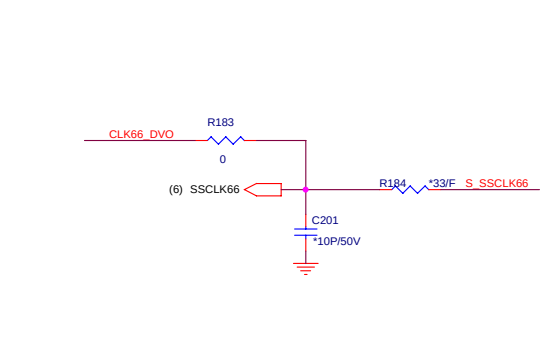
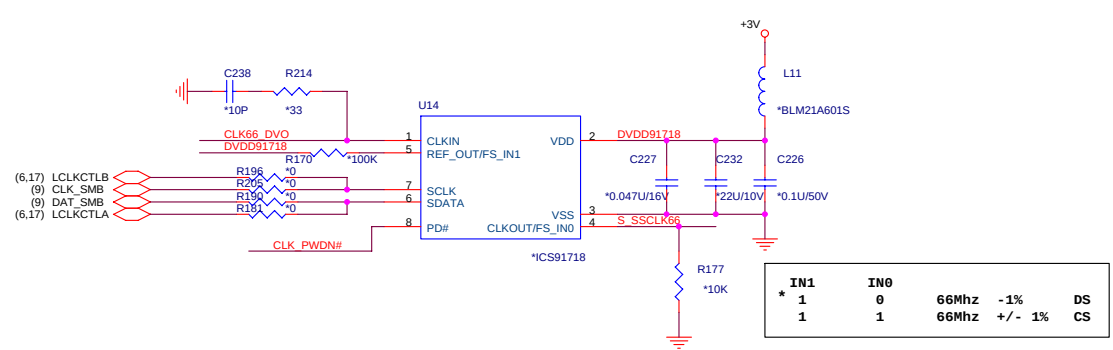
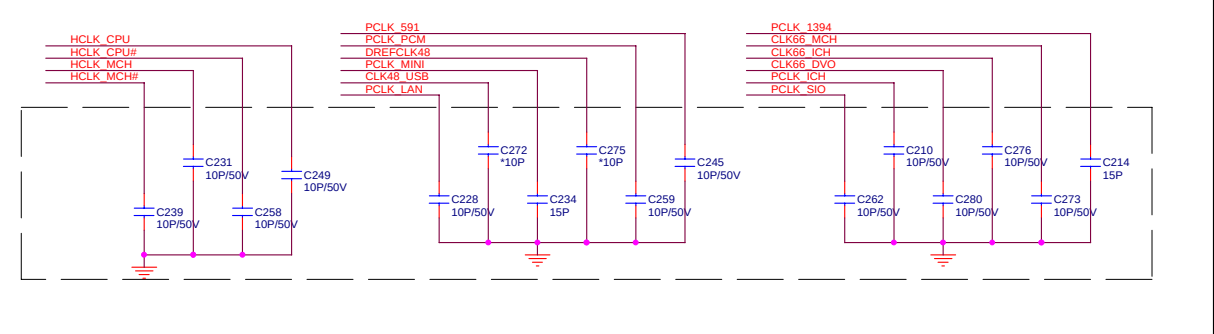
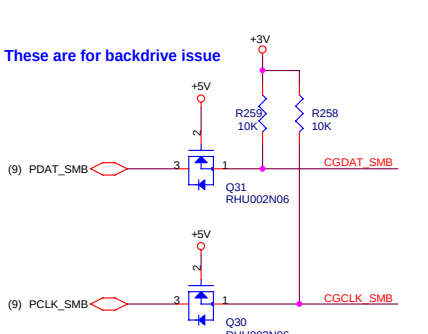
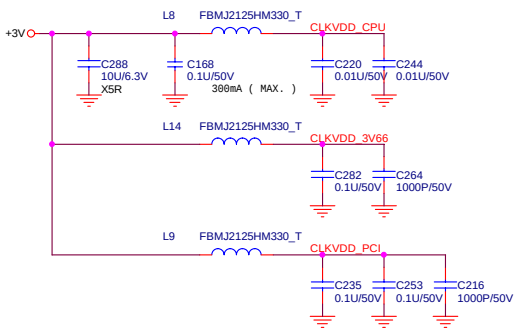
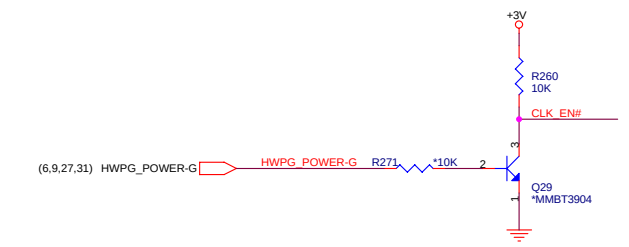
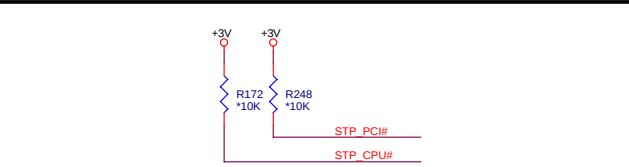


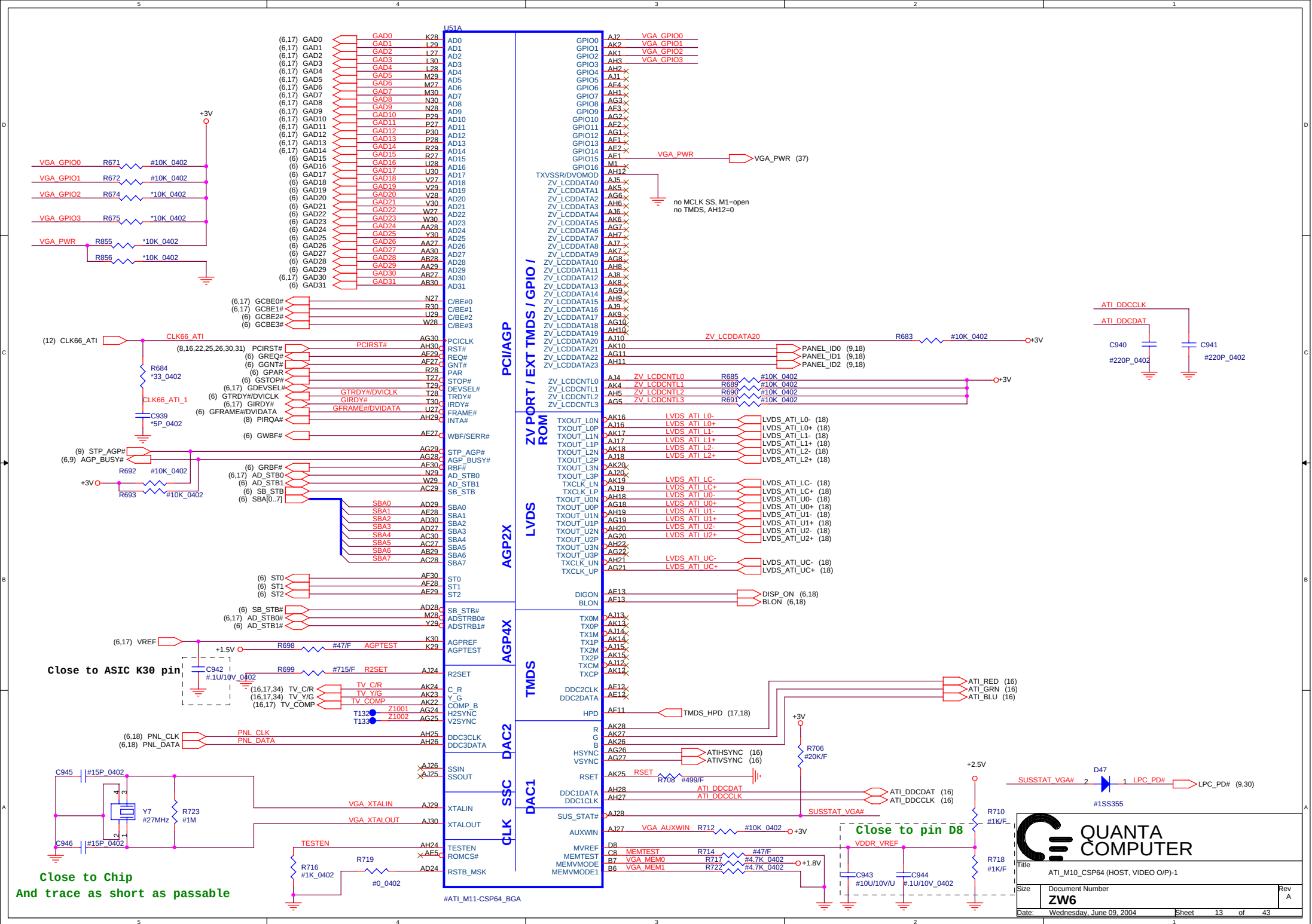


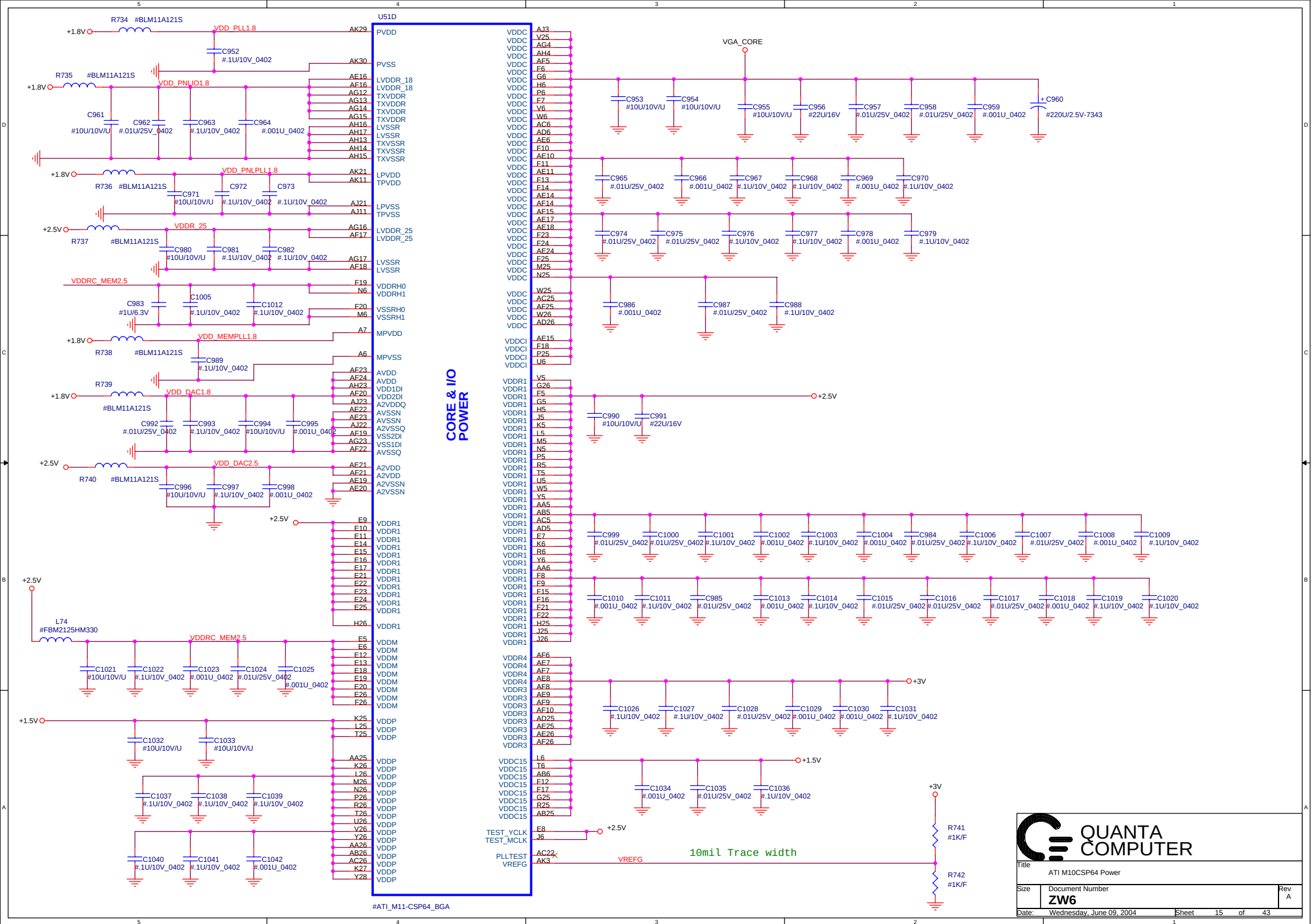


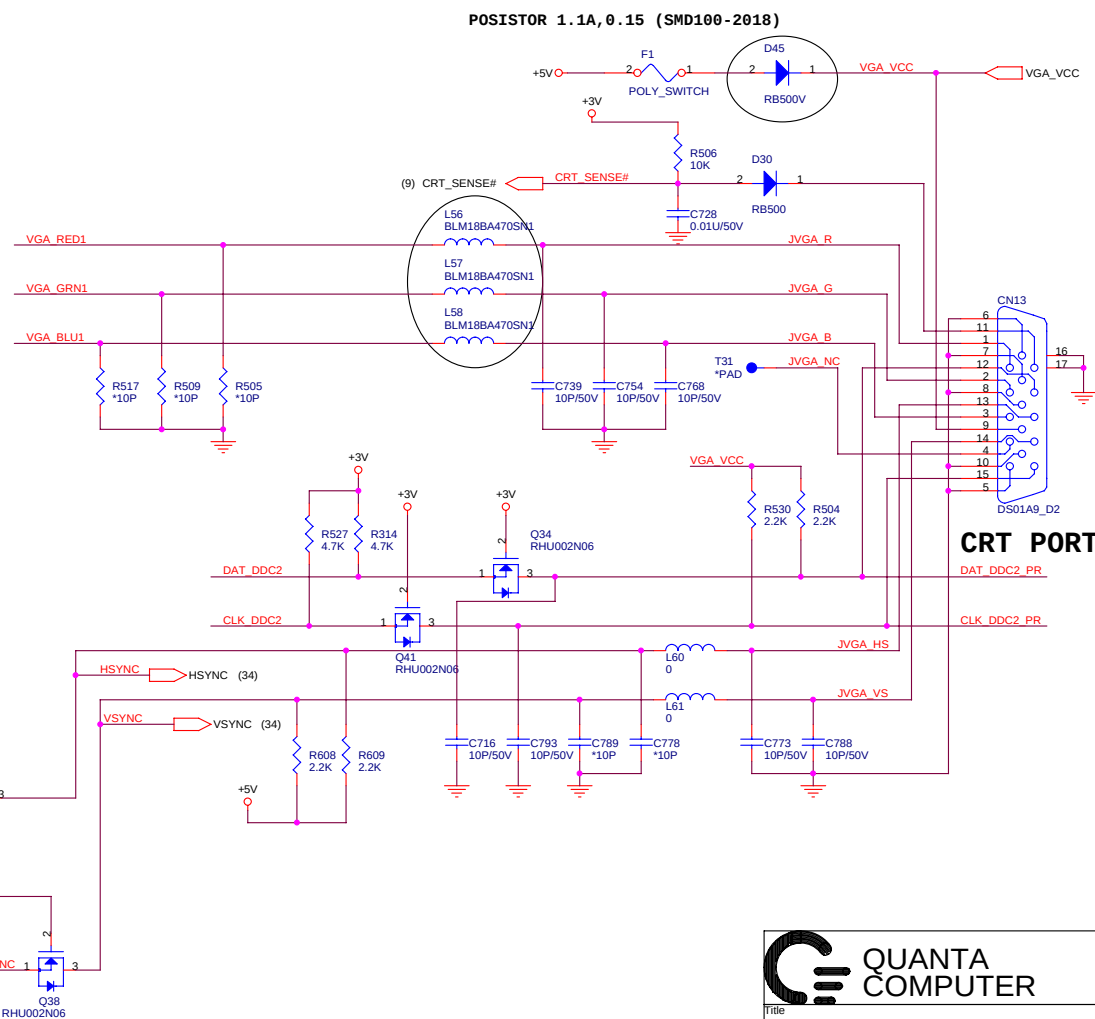
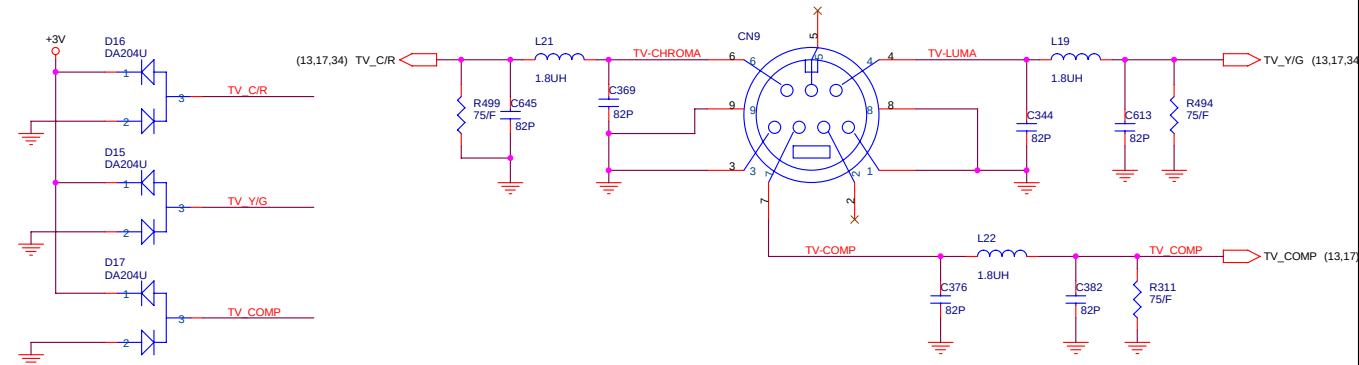
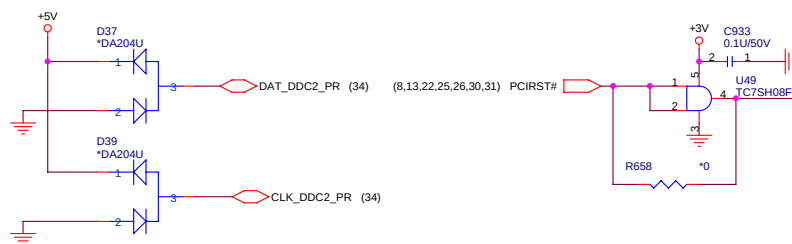


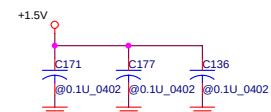
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input





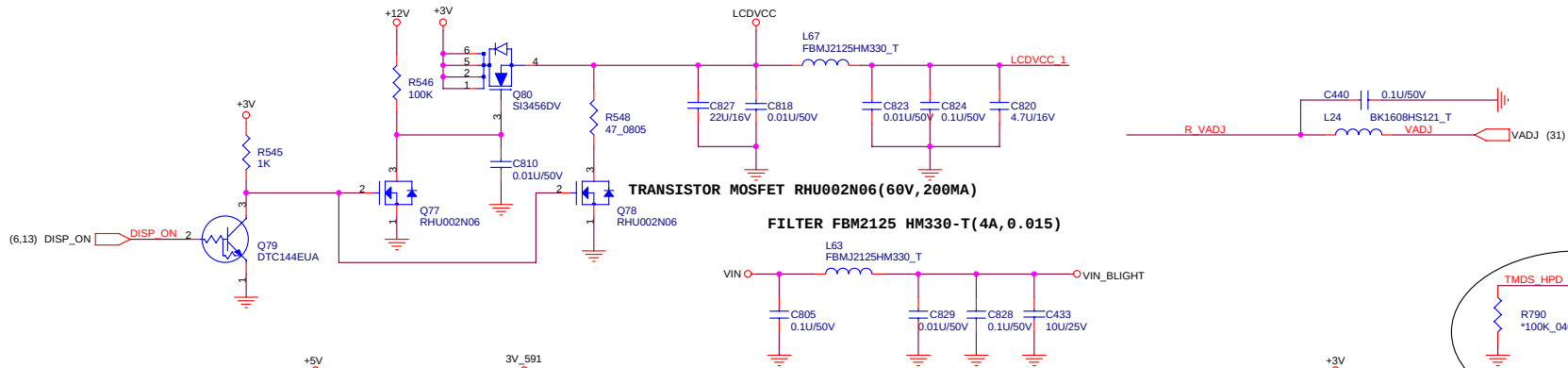




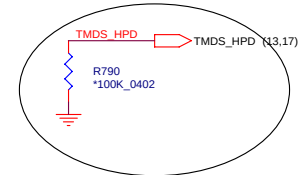
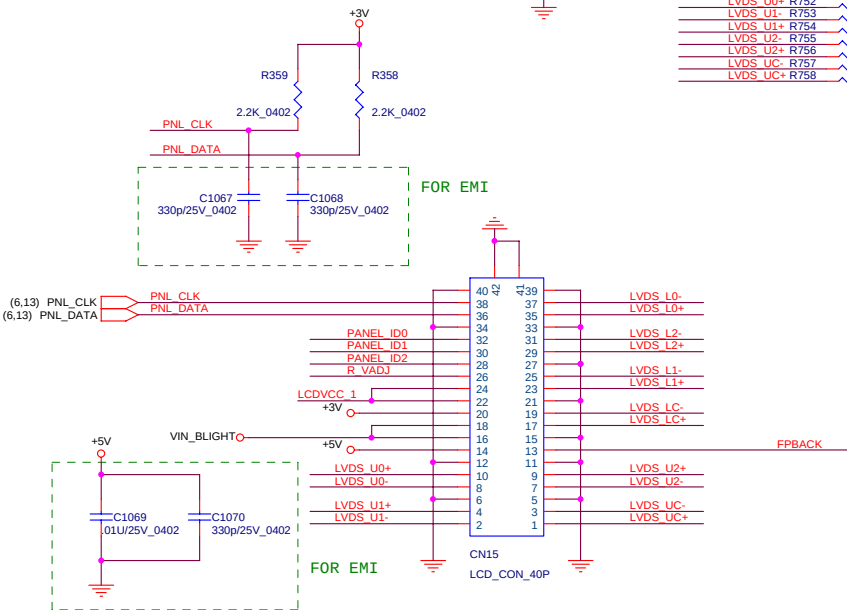
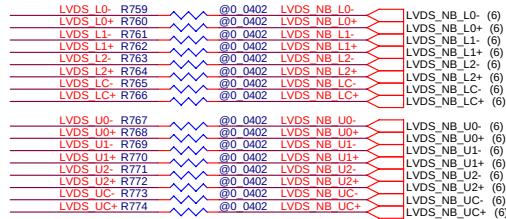
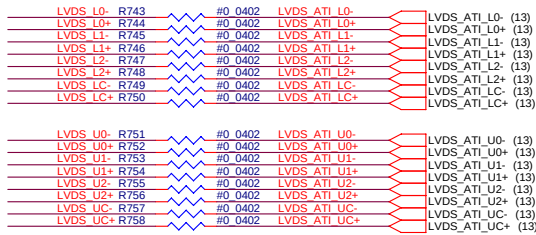
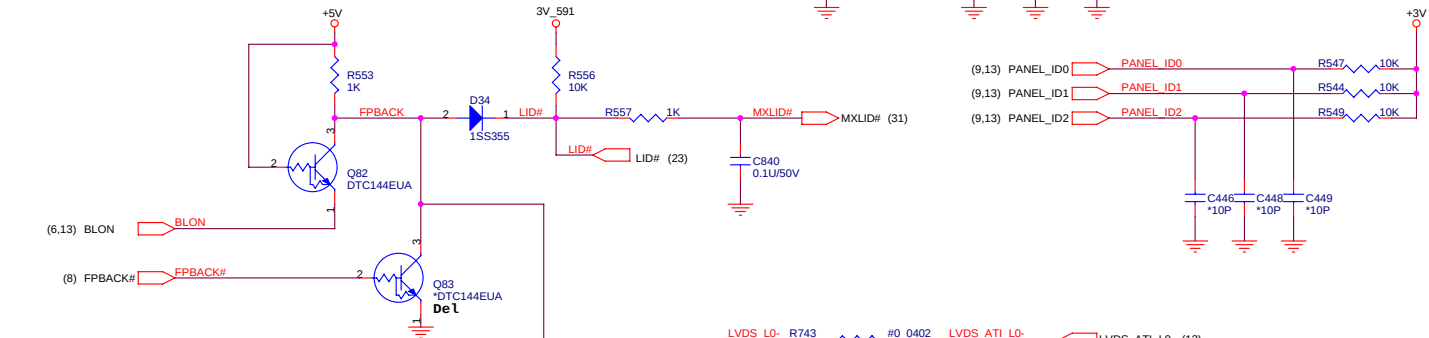


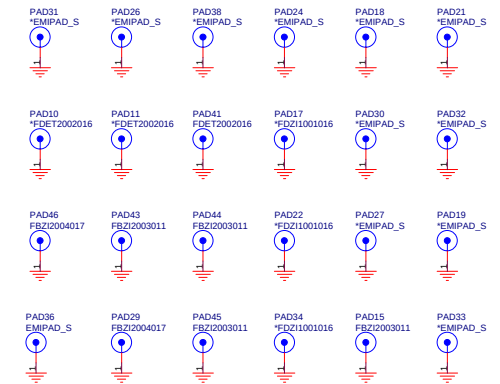
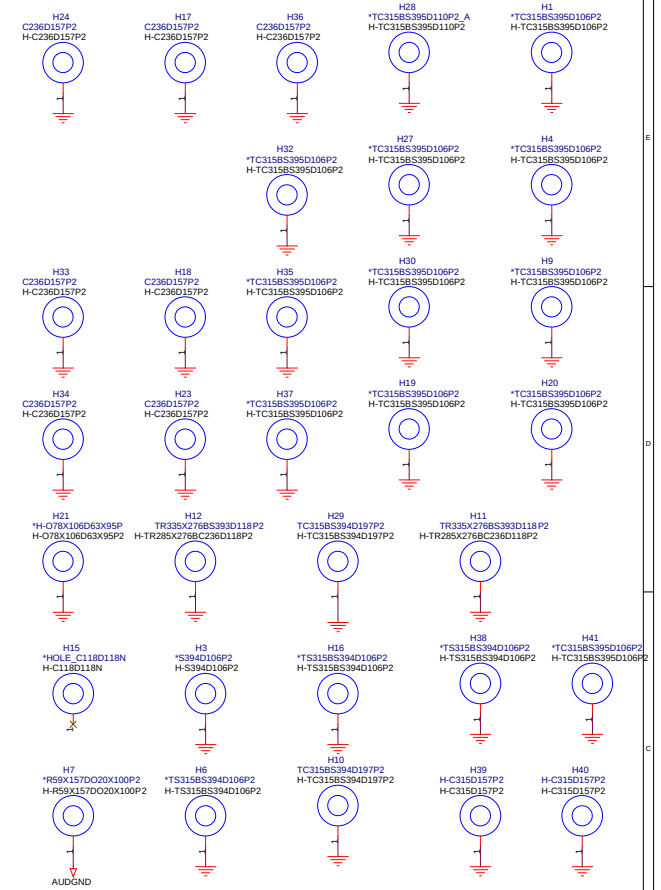
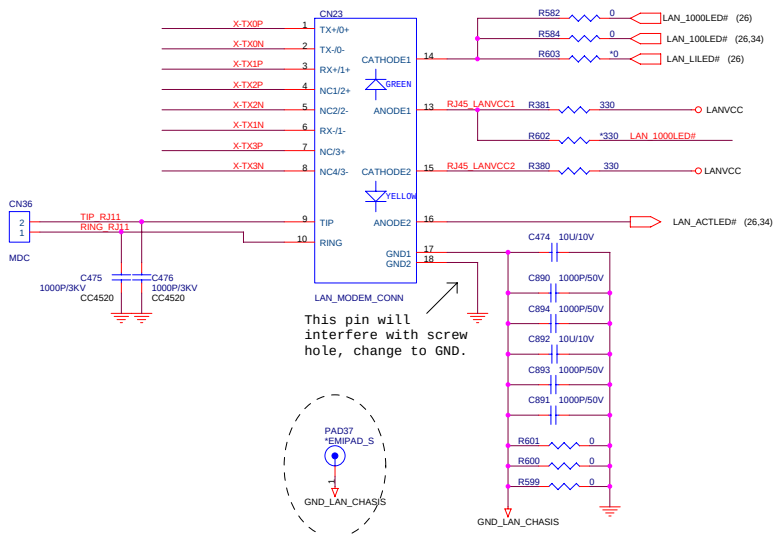
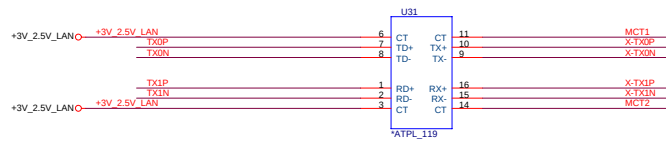
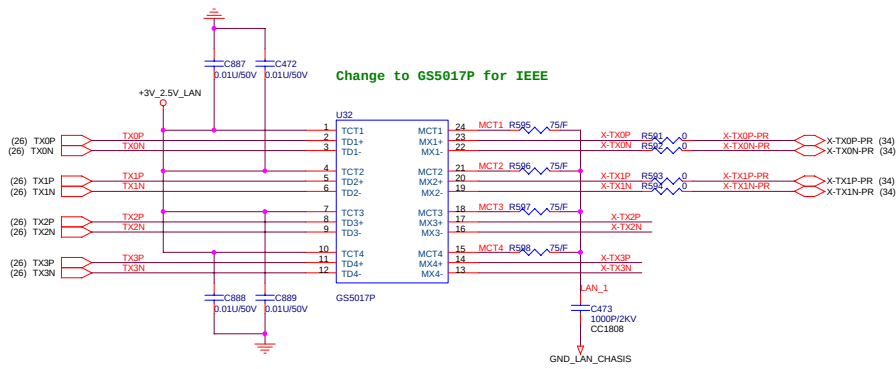
(6.13)	GAD11	GAD11	R827	@0	0402	DV0BD11
(6.13)	GAD12	GAD12	R828	@0	0402	DV0BD10
(6.13)	GAD9	GAD9	R829	@0	0402	DV0BD9
(6.13)	GAD10	SAD10	R830	@0	0402	DV0BD8
			R831	@0	0402	DV0BD7
(6.13)	GCBE0F	GAD8	R832	@0	0402	DV0BD6
(6.13)	GAD8	GAD6	R833	@0	0402	DV0BD5
(6.13)	GAD6					
(6.13)	GAD7	GAD7	R834	@0	0402	DV0BD4
(6.13)	GAD4	SAD4	R835	@0	0402	DV0BD3
(6.13)	GAD5	GAD5	R836	@0	0402	DV0BD2
(6.13)	GAD5	GAD2	R837	@0	0402	DV0BD1
(6.13)	GAD2	GAD3	R838	@0	0402	DV0BD0
(6.13)	GAD3					
(6.13)	AD_STB0F	AD_STB0F	R839	@0	0402	DV0BCLK#
(6.13)	AD_STB0	AD_STB0	R840	@0	0402	DV0BCLK
(6.13)	AD_STB0H	GCBE1F	R841	@0	0402	DV0BLANK#
(6.13)	GCBE1F	GAD13	R842	@0	0402	DV0BCLKINT
(6.13)	GAD13	GAD0	R843	@0	0402	DV0BHSYNC
(6.13)	GAD0	GAD1	R844	@0	0402	DV0BVSNC
(6.13)	GAD1					
(6.13)	GAD30	R861	@0	0402	DV0BINTTR#	
(6.13)	GOEVELLM	R862	@0	0402	I2CADATA	
(6.13)	GIRTY	R863	@0	0402	I2CCLK	

FILTER FBM2125 HM330-T(4A, 0.015)

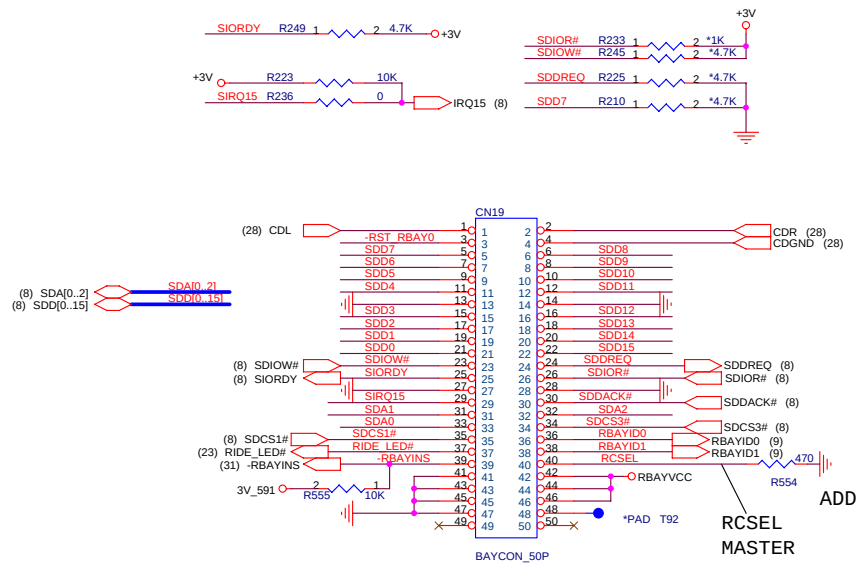


FILTER FBM2125 HM330-T(4A, 0.015)



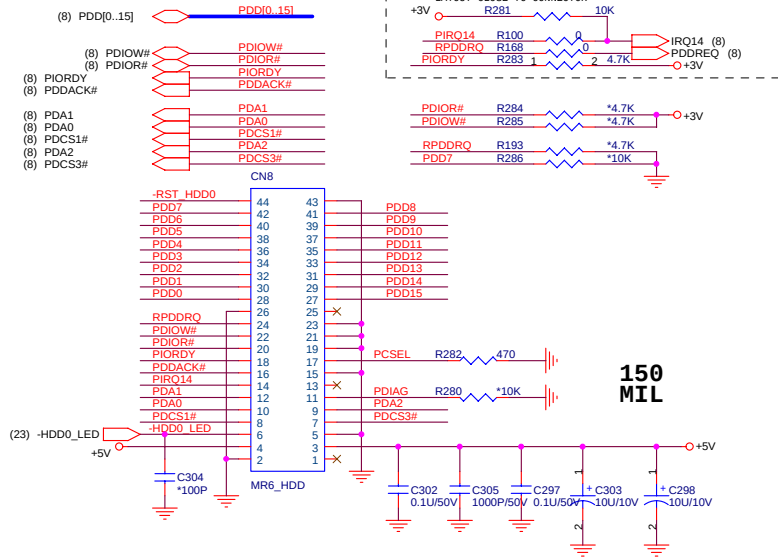


RJ11 & RJ45 & HOLES		
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BAY ID STATUS

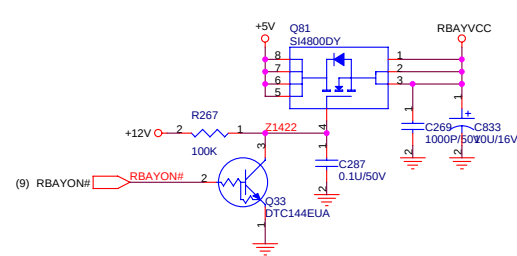
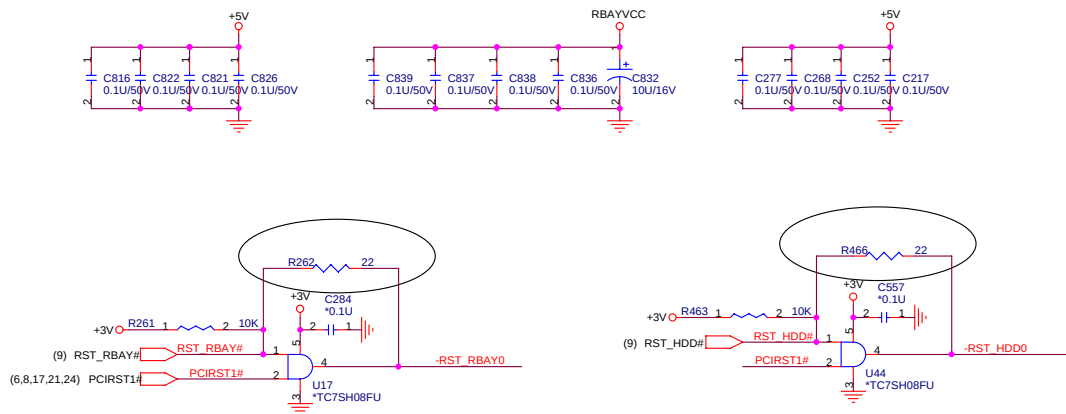
RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD



150
MIL

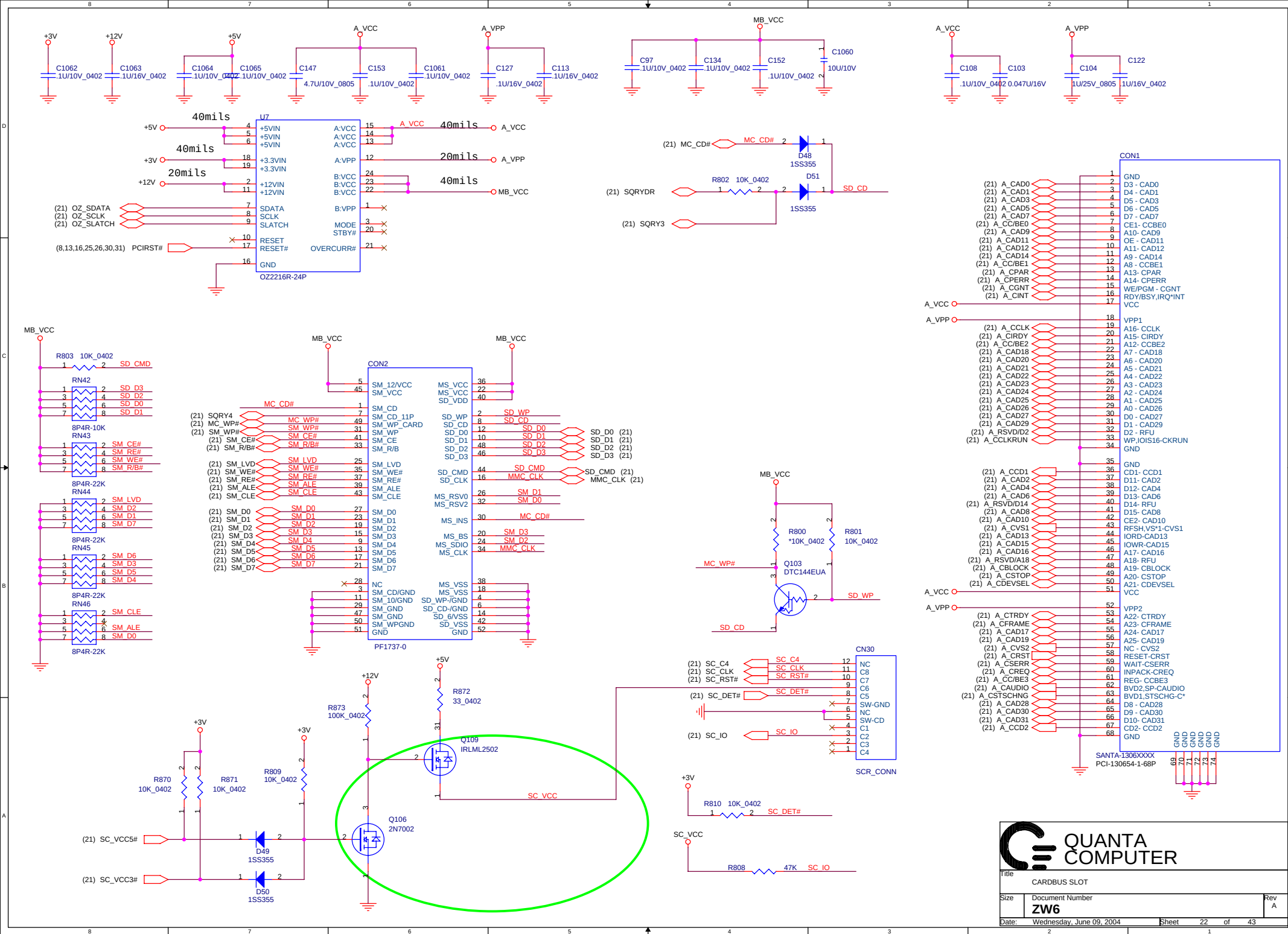
SWAP BAY: OPTICAL DRIVE, 2ND BATTERY, 2ND HDD

HDD CONNECTOR

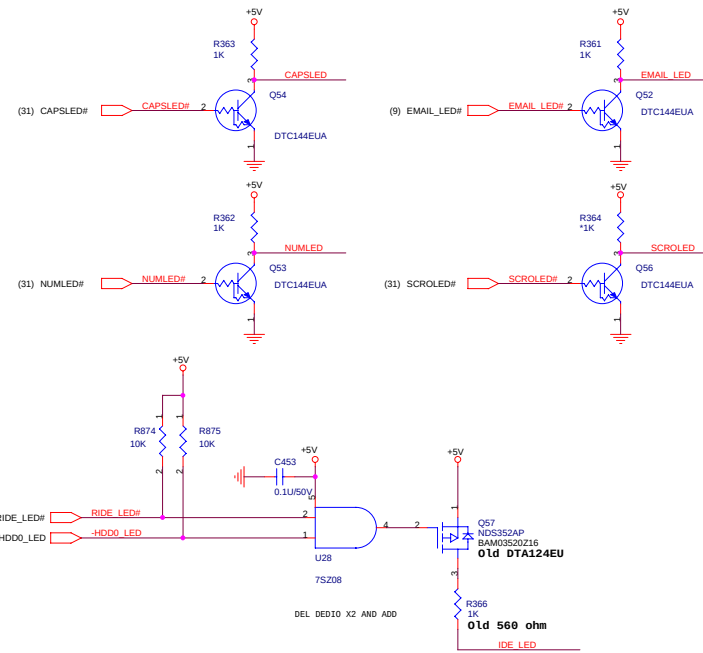
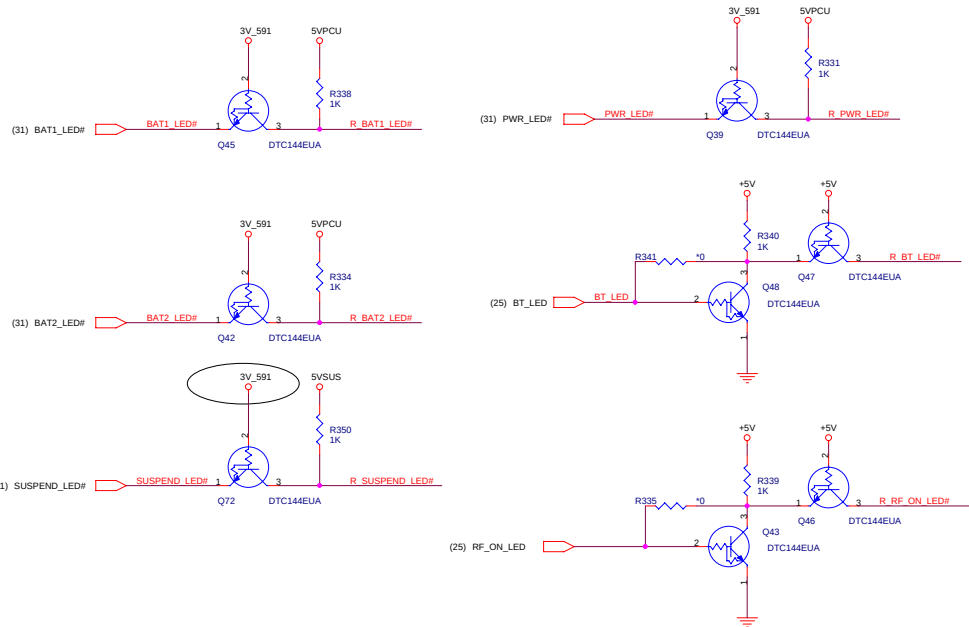
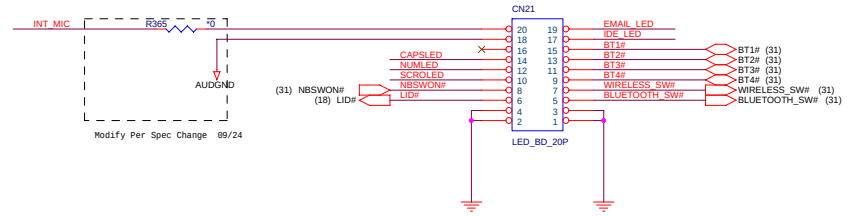
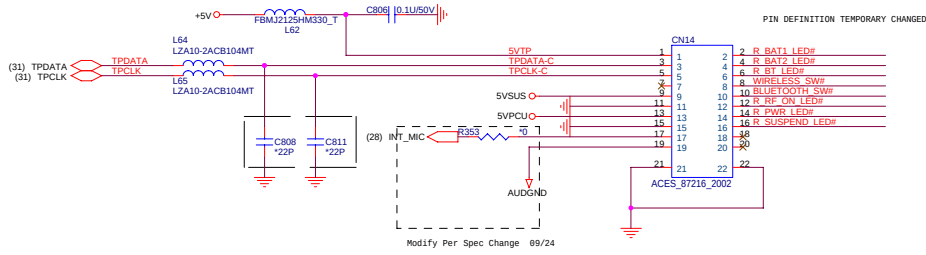


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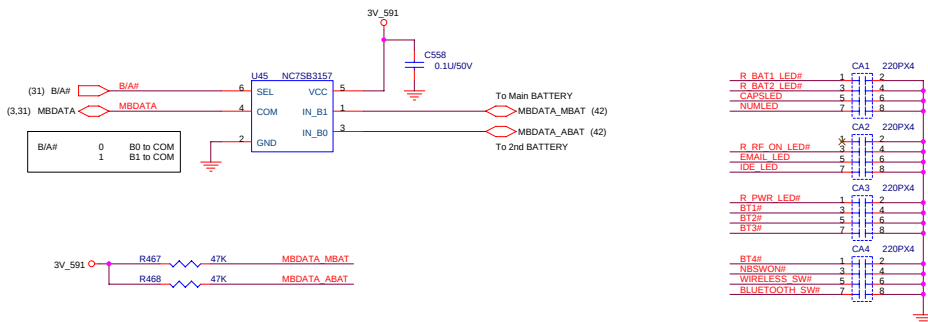
SWAP BAY POWER CONTROL & RESET



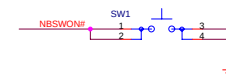
12 MIL



IDE LED CONTROL LOGIC



De1 SW2



File 4-IN-1 MEMORY CARD

Size Document Number

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Rev A

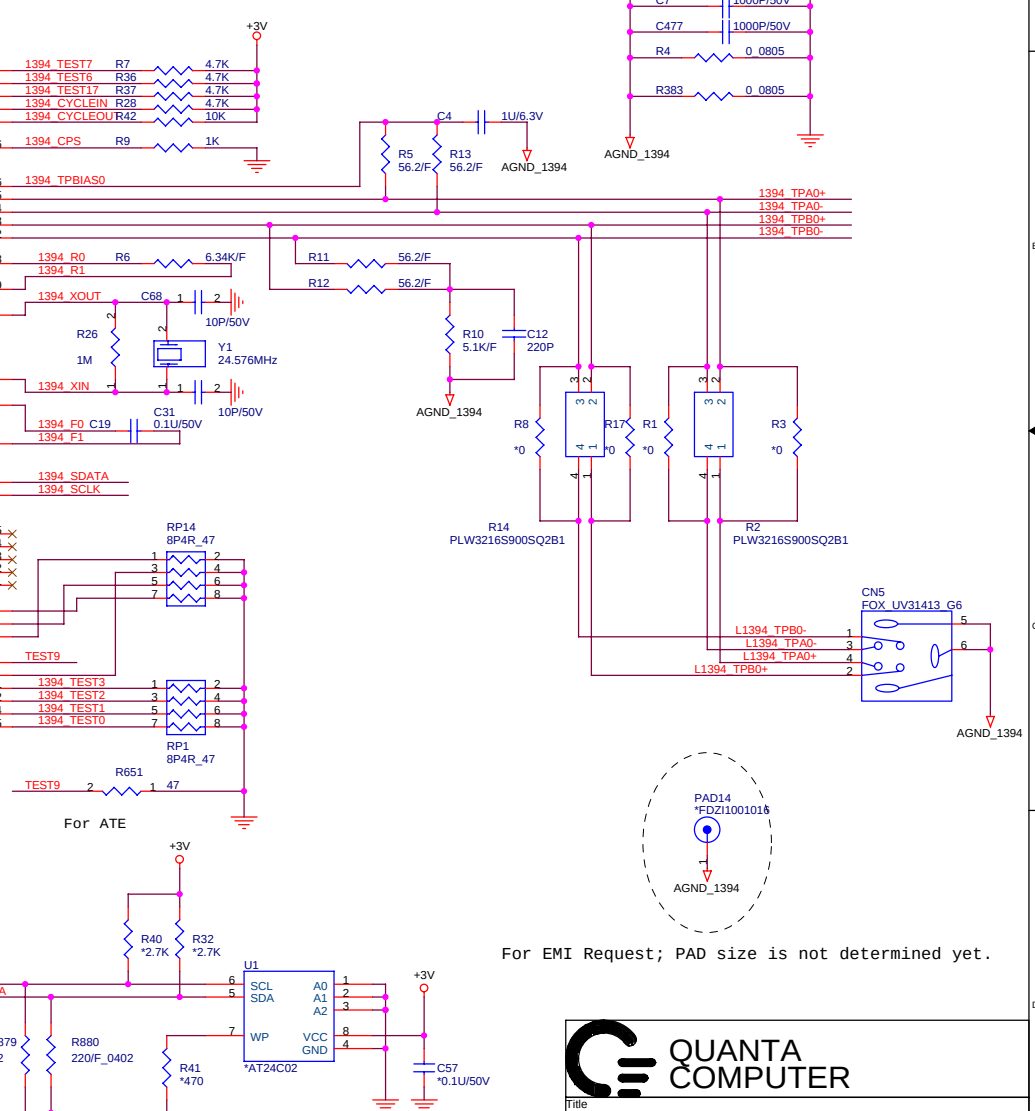
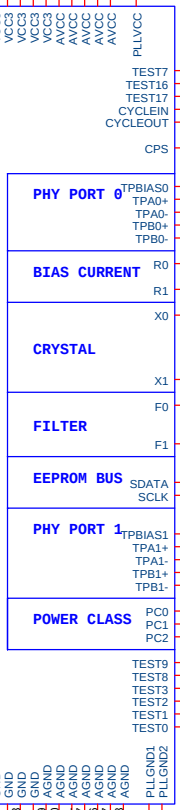
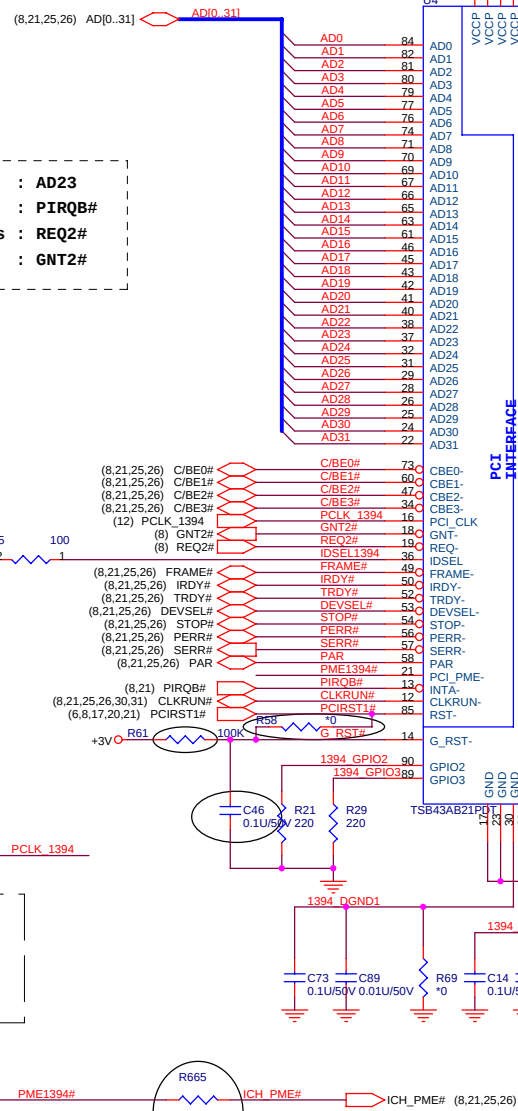
IEEE-1394

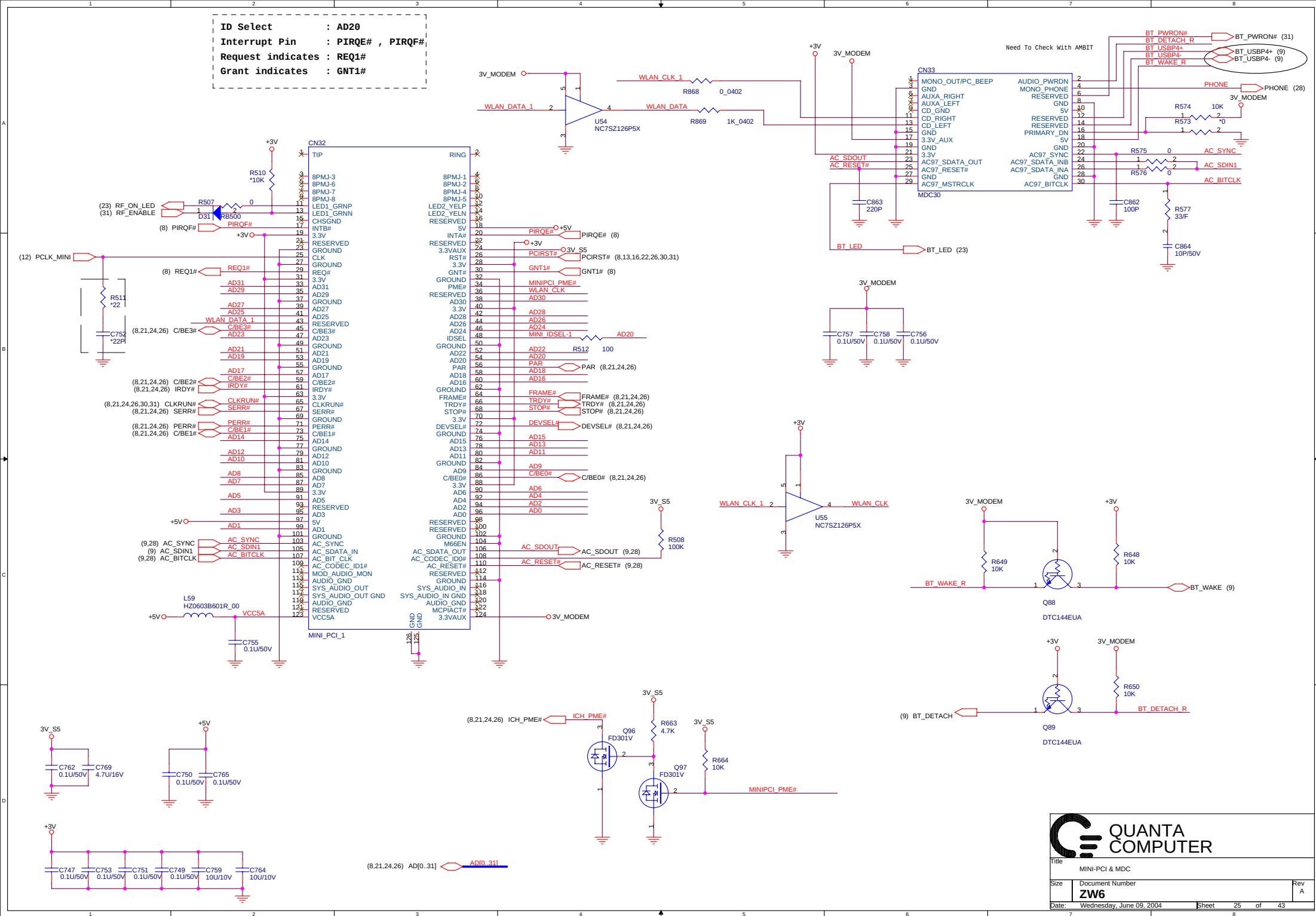
Used for vccp
(Pin20, 35, 48, 62, 78)

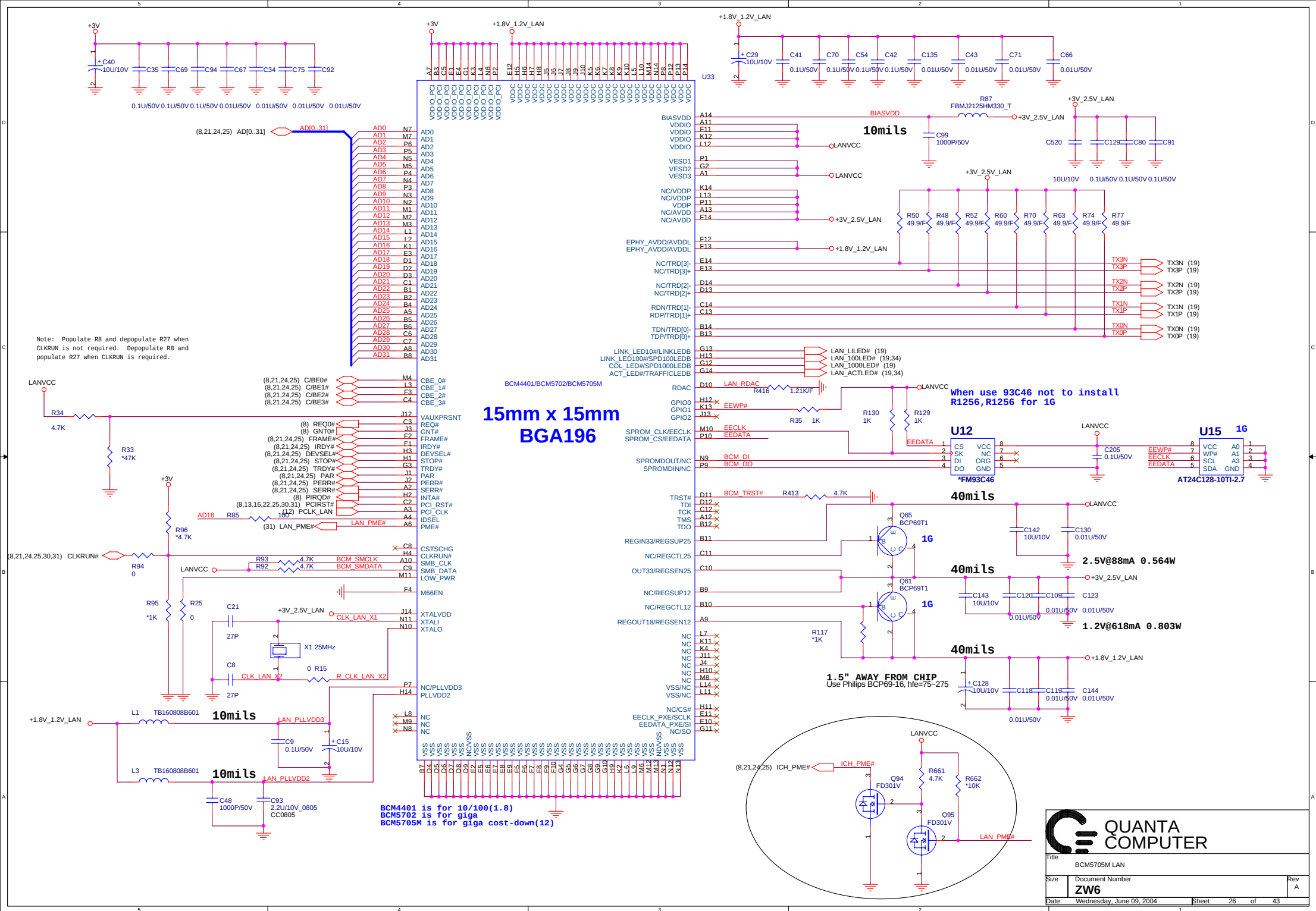
Used for vcc3
(Pin15, 27, 39, 51, 59, 72, 88, 100)

Used for AVCC
(Pin1, 2, 107, 108, 120)

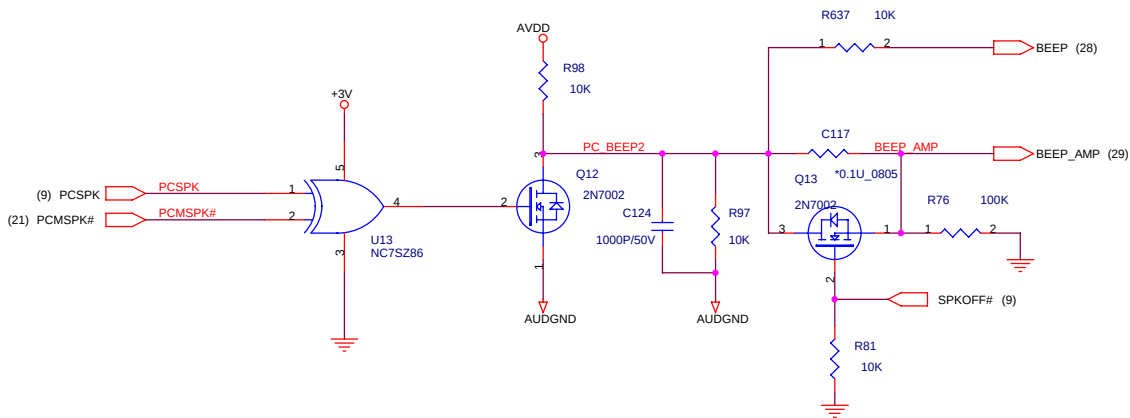
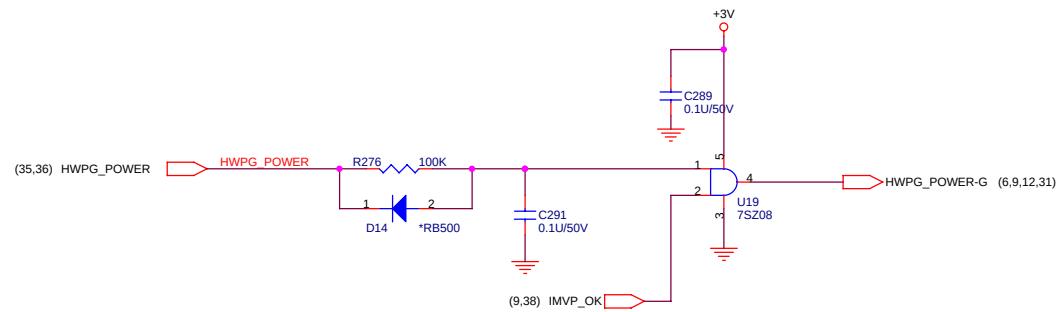
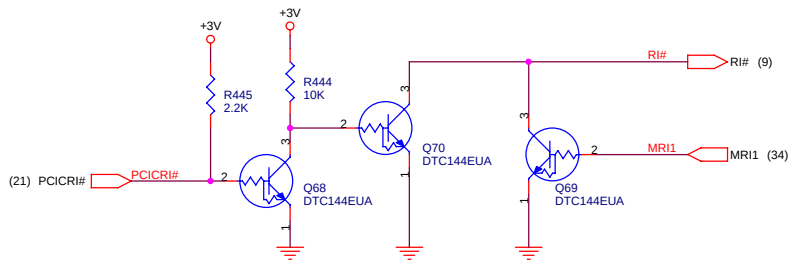
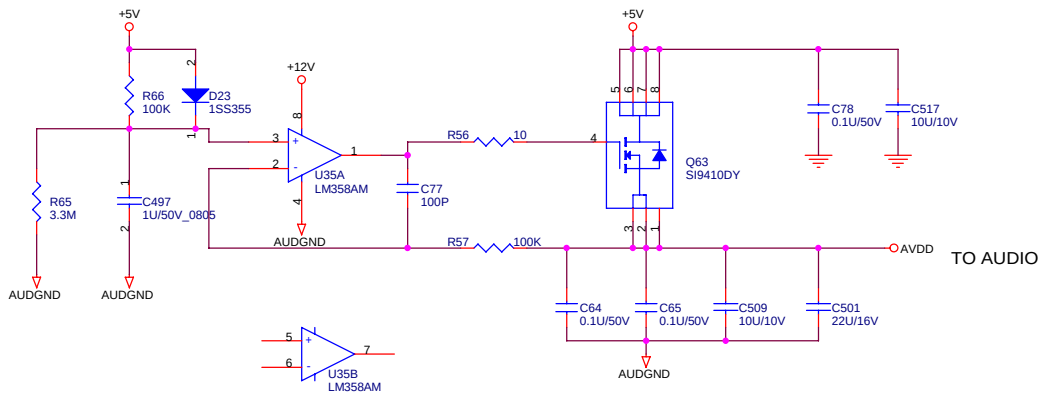
ID Select : AD23
Interrupt Pin : PIRQB#
Request indicates : REQ2#
Grant indicates : GNT2#







AUDIO POWER



Title	AUDIO POWER & BEEP & FDD SEL
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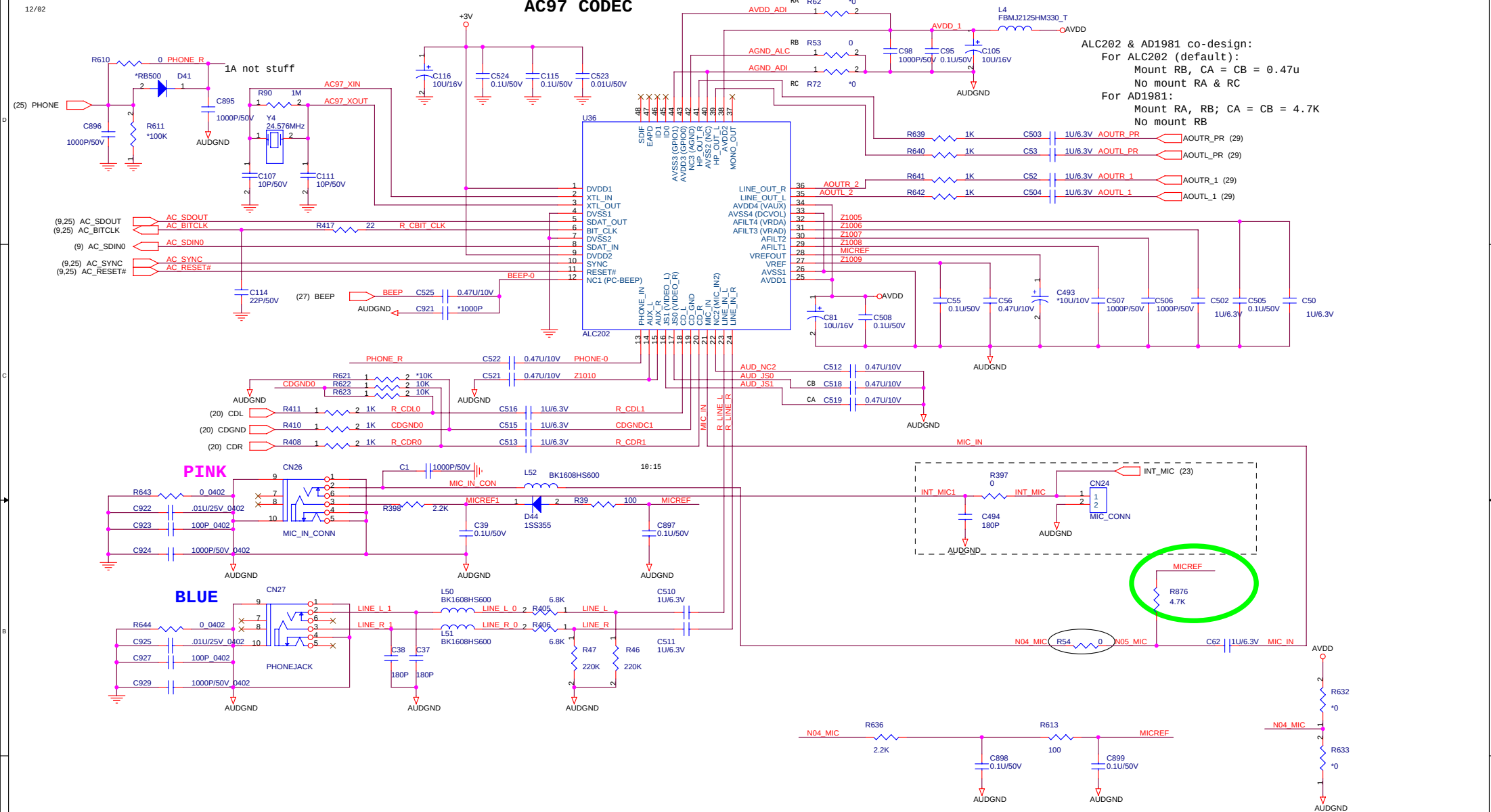
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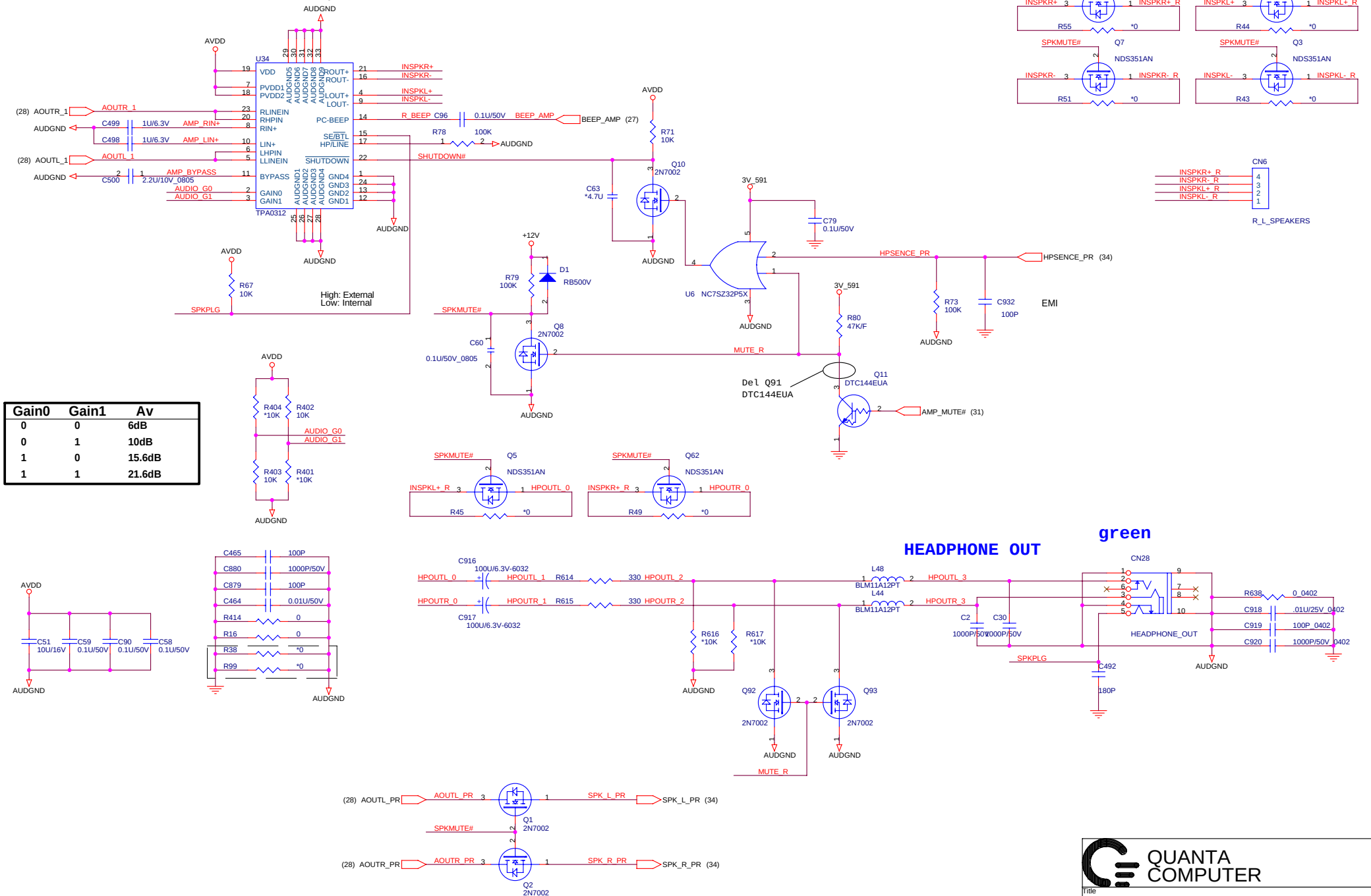
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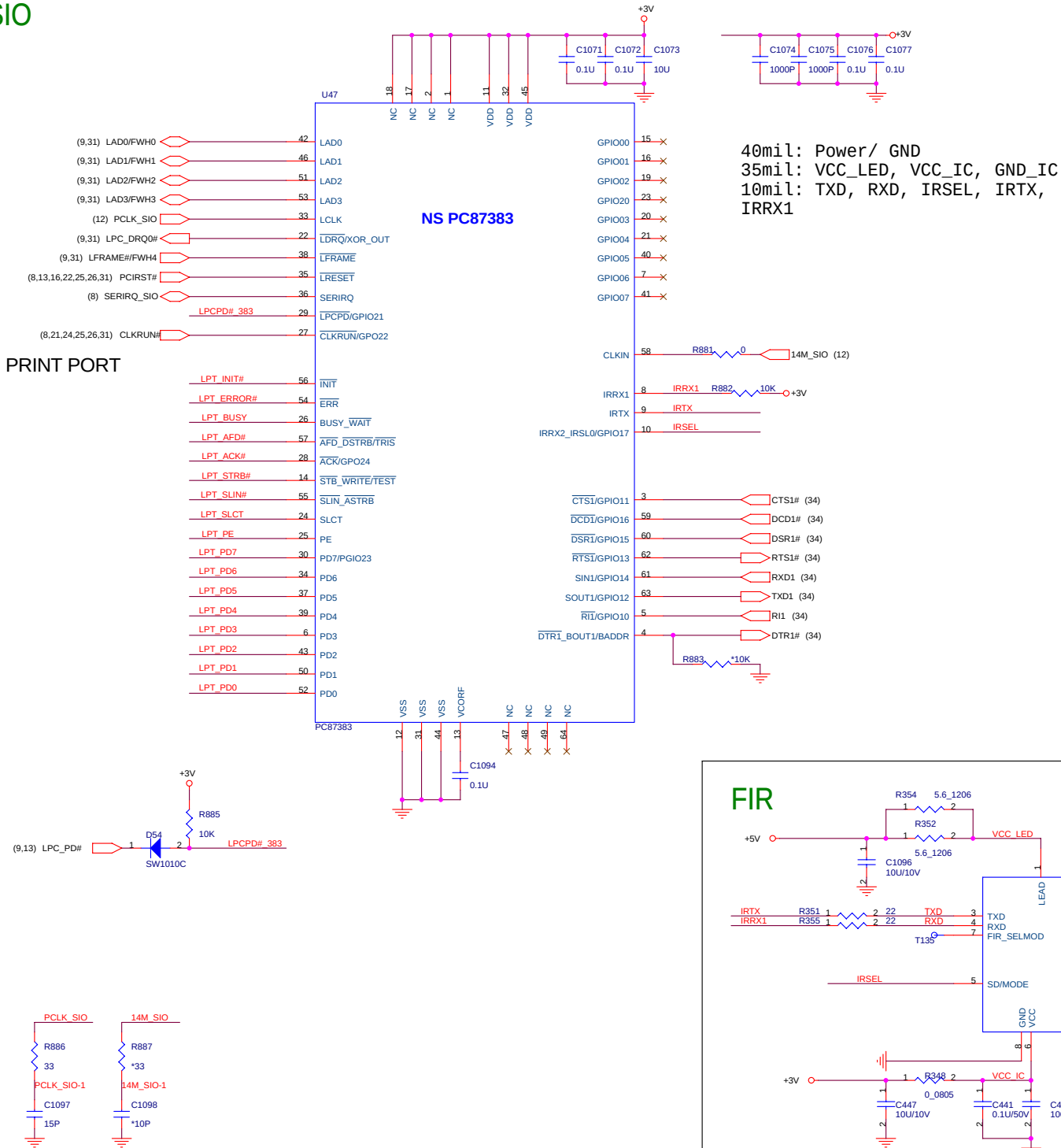
AC97 CODEC



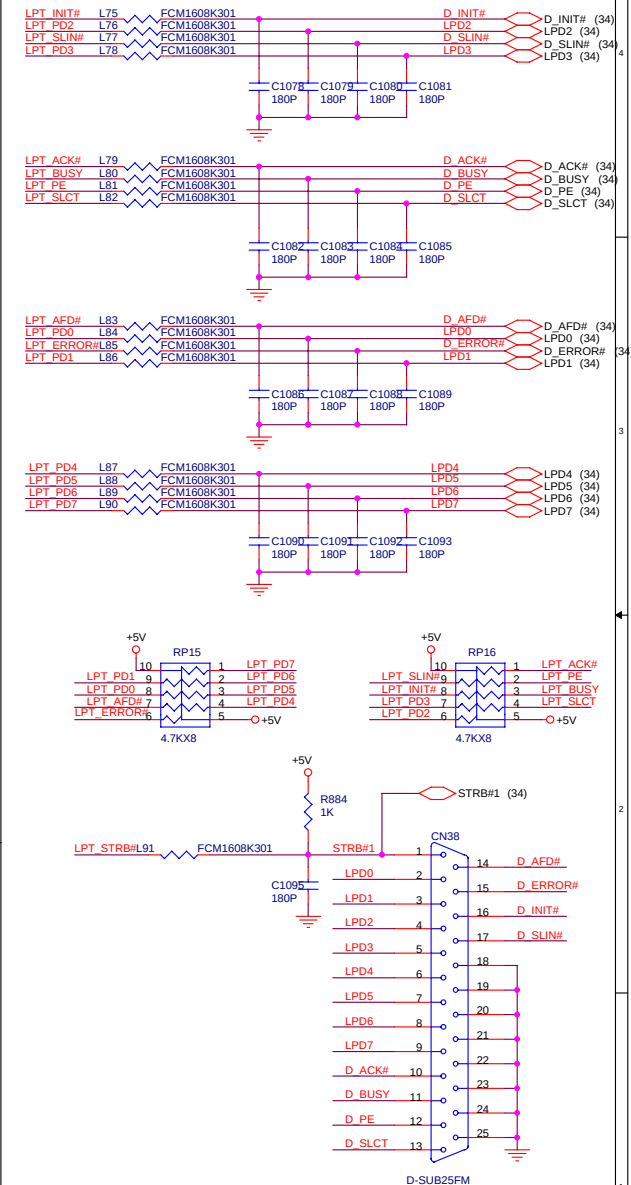
Audio Amplifier



SIO



PRINT PORT



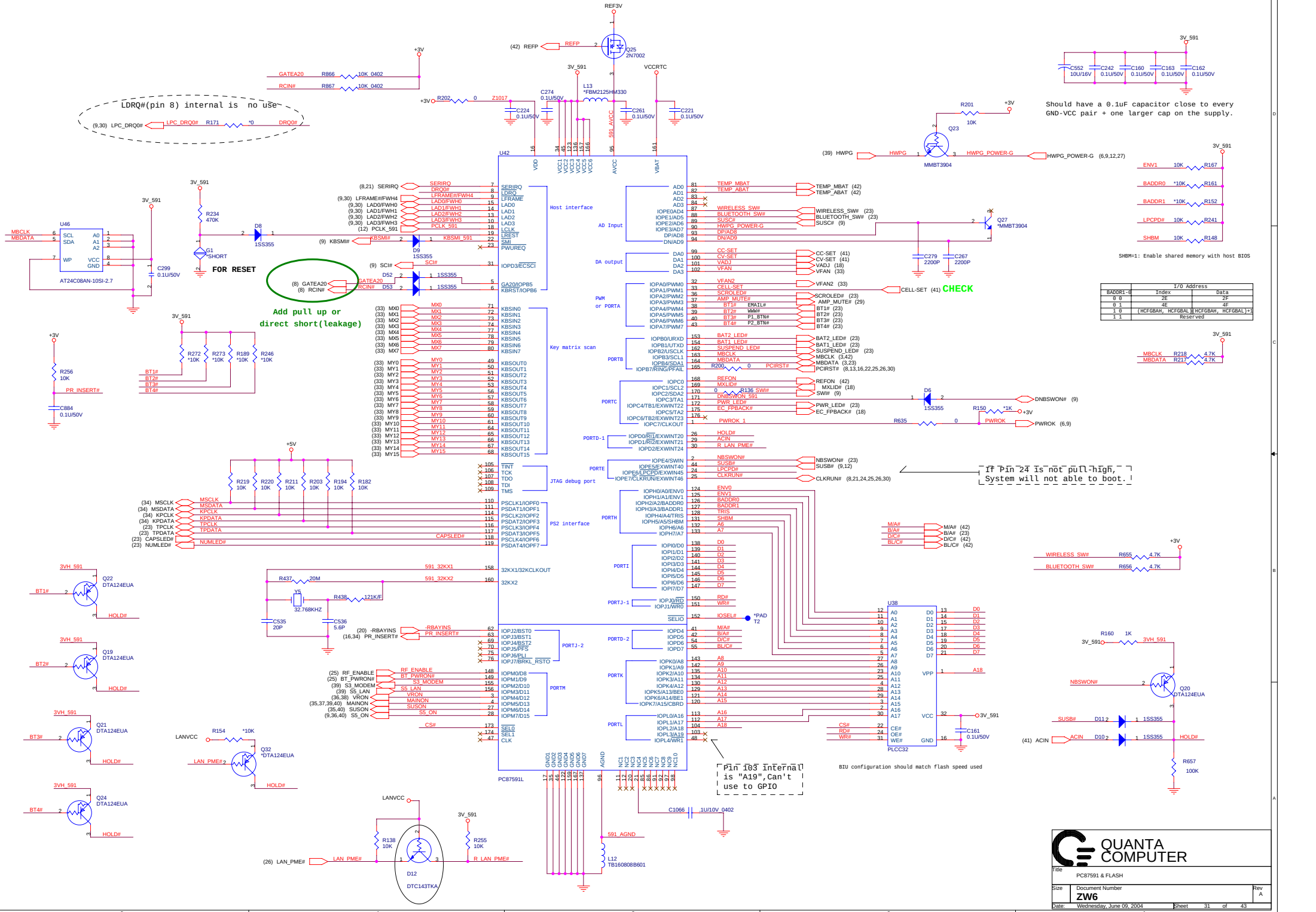
Title
SUPER I/O NS37383

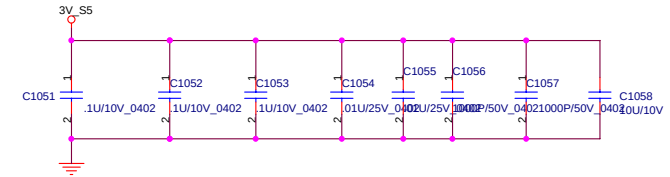
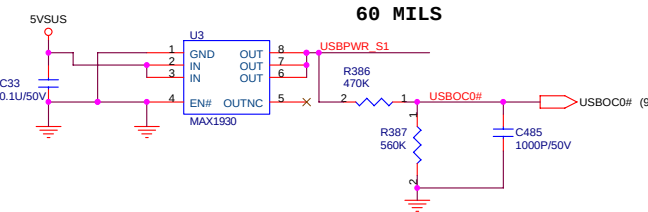
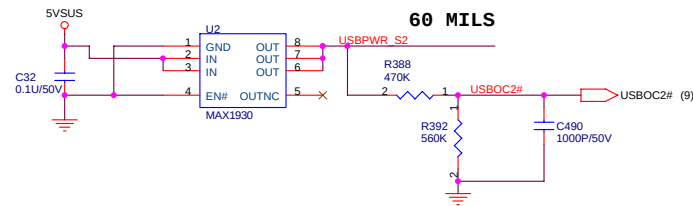
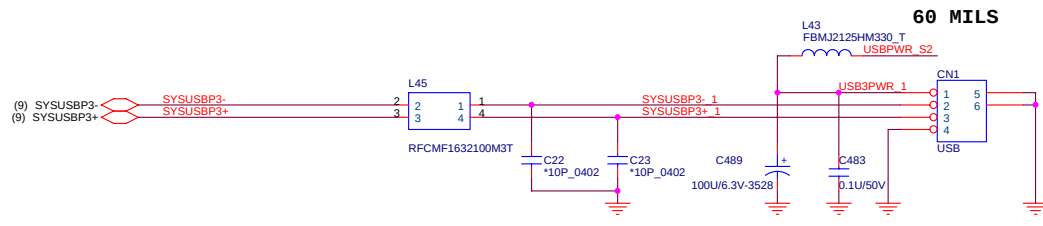
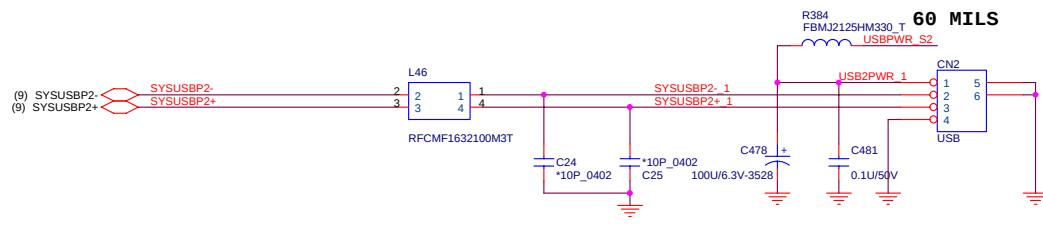
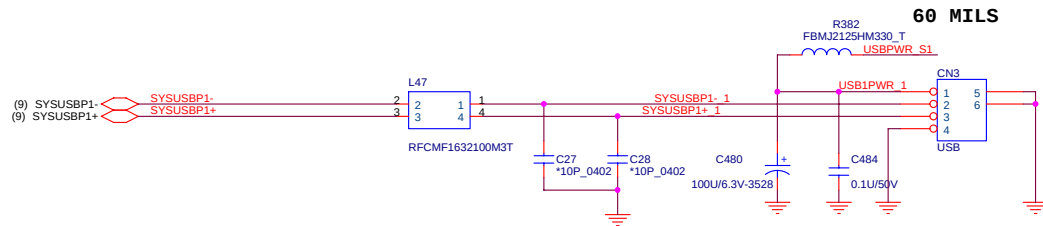
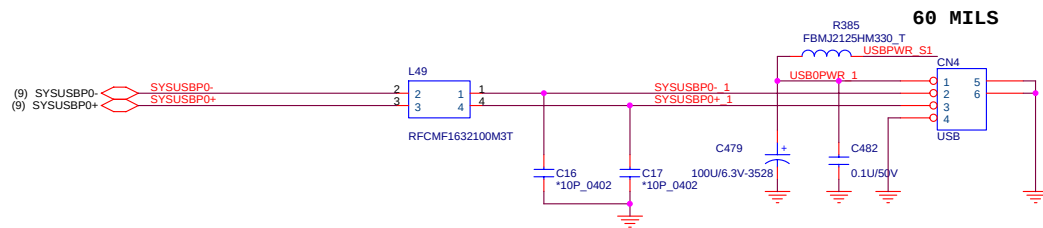
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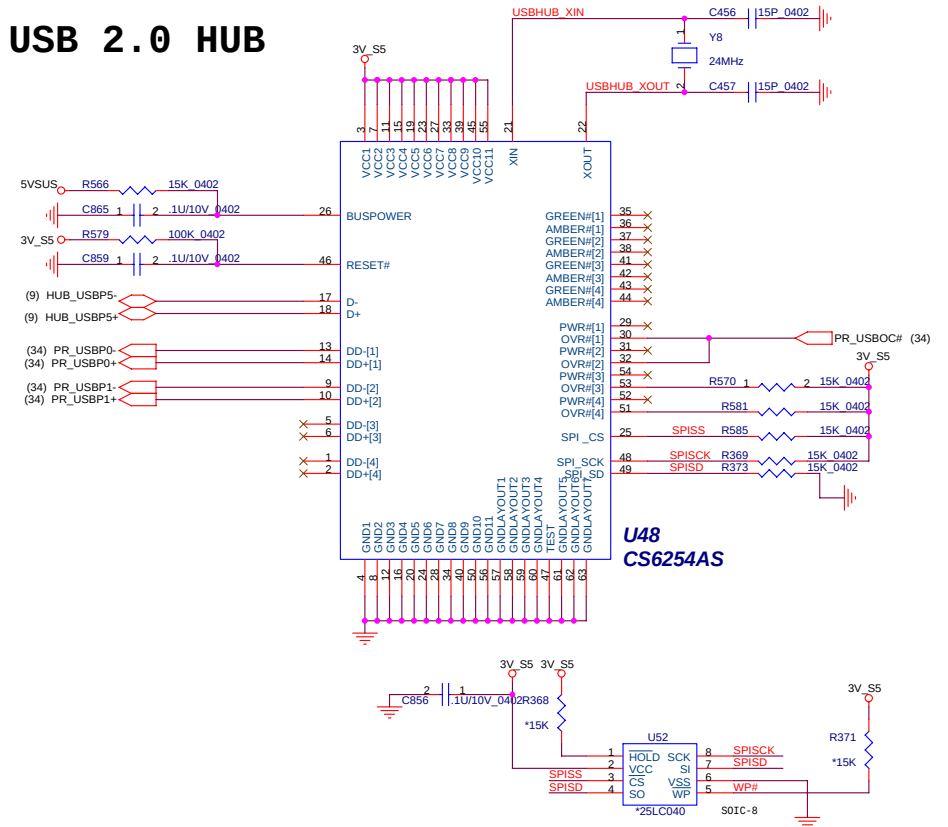
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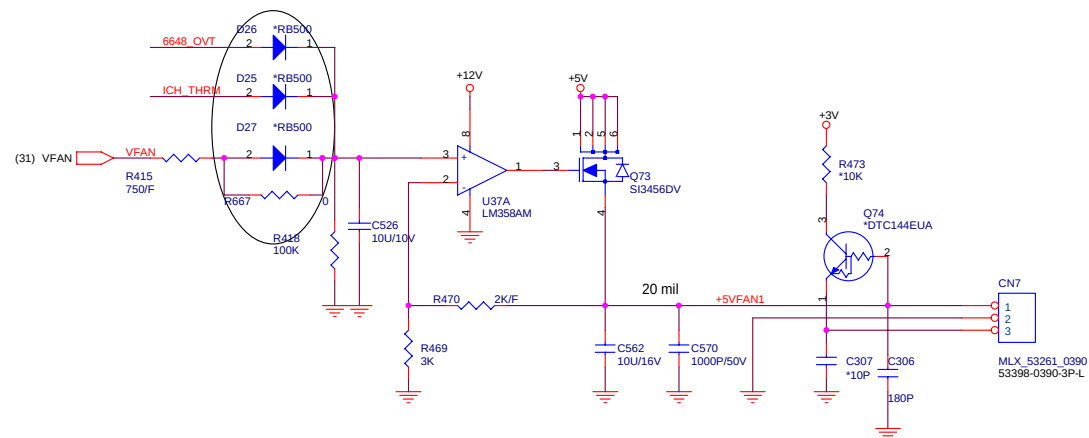




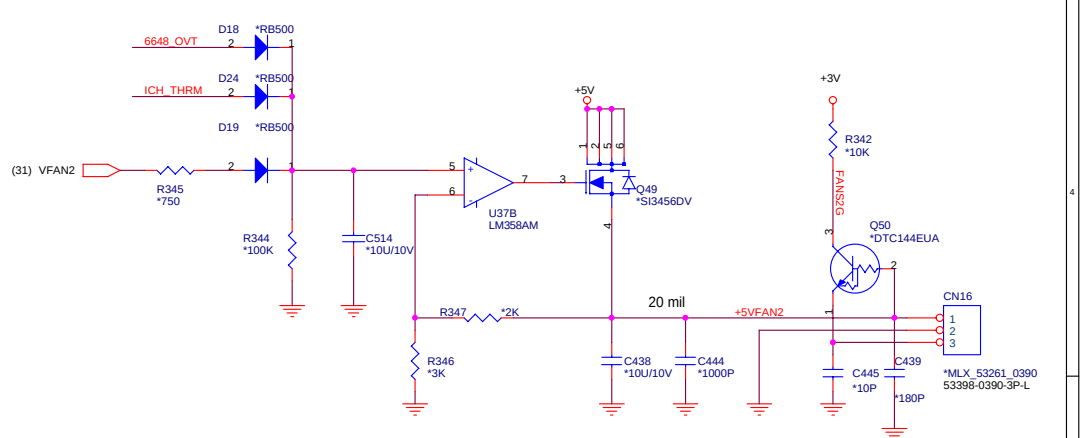
USB 2.0 HUB



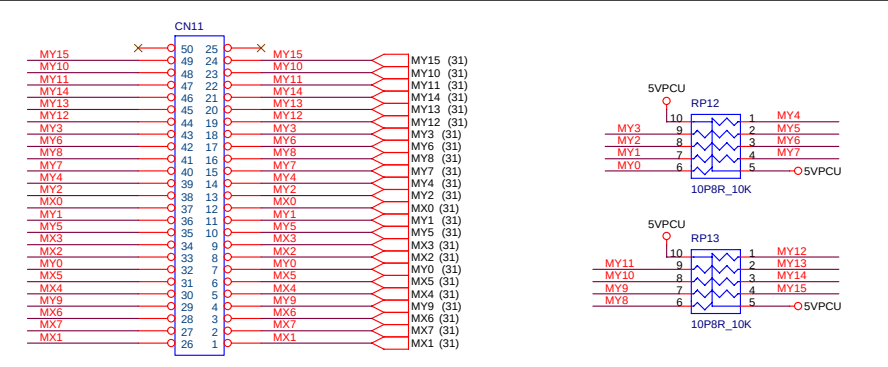
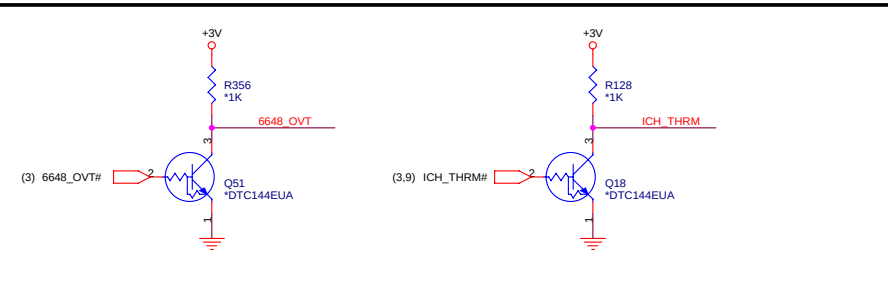
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Size	Document Number	Rev	
	ZW6	A	
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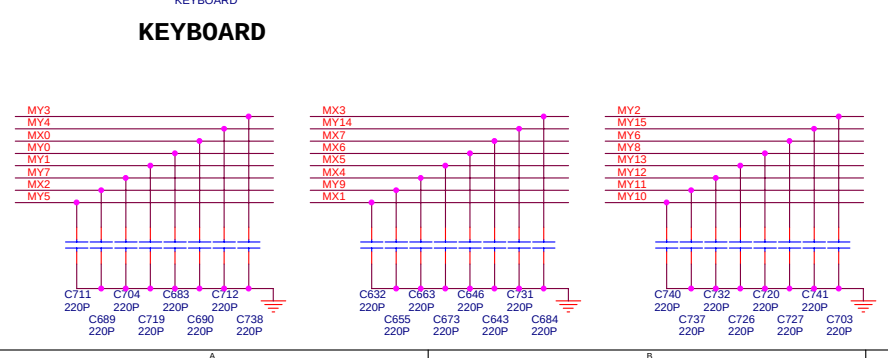
1st FAN OUT CONNECTOR



2nd FAN OUT CONNECTOR



KEYBOARD

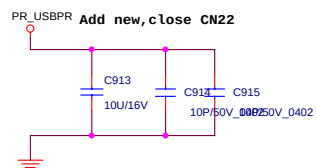
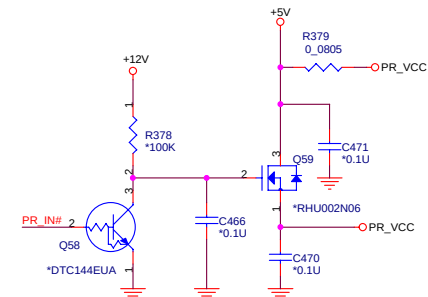


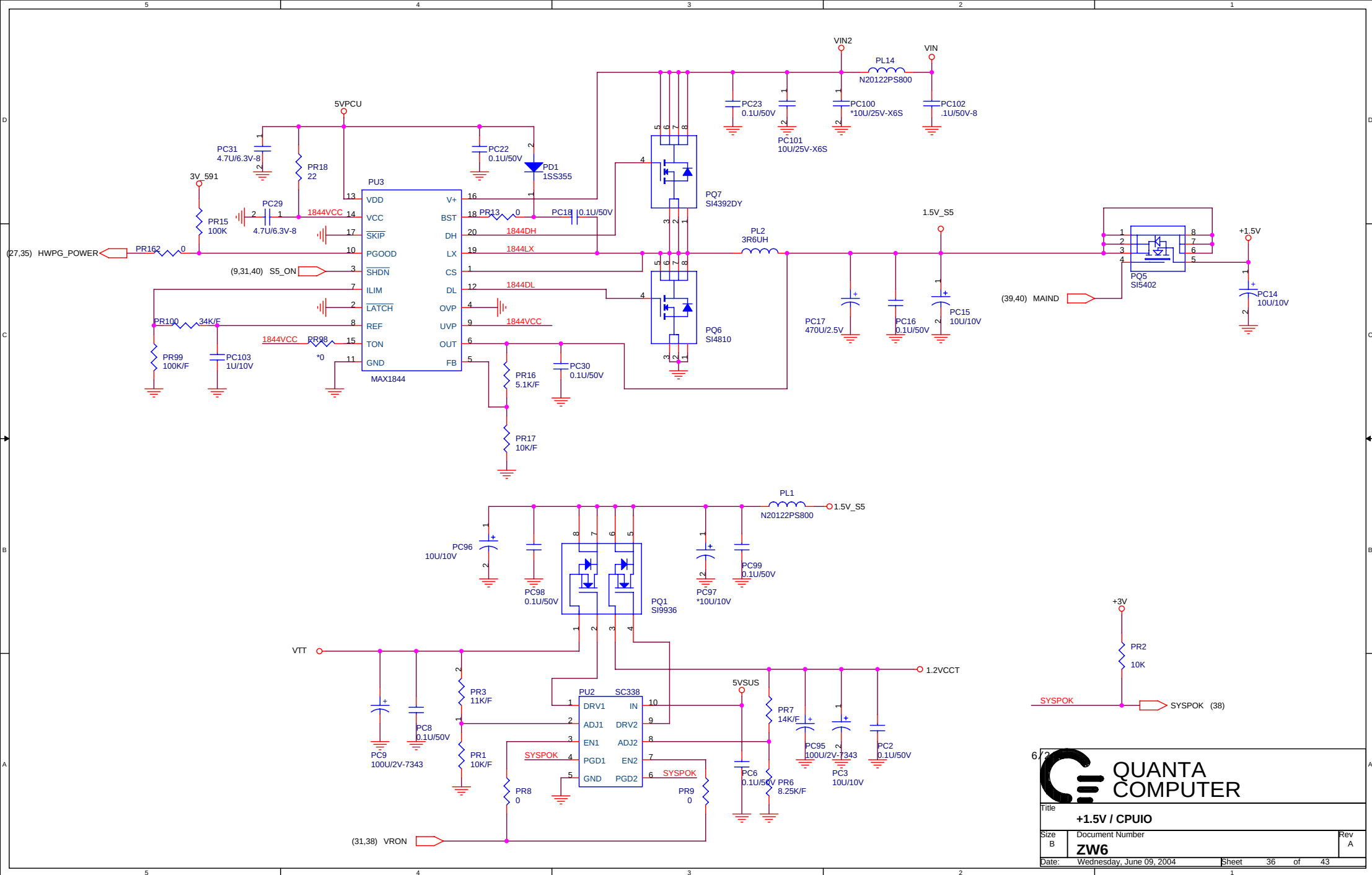
QUANTA
COMPUTER

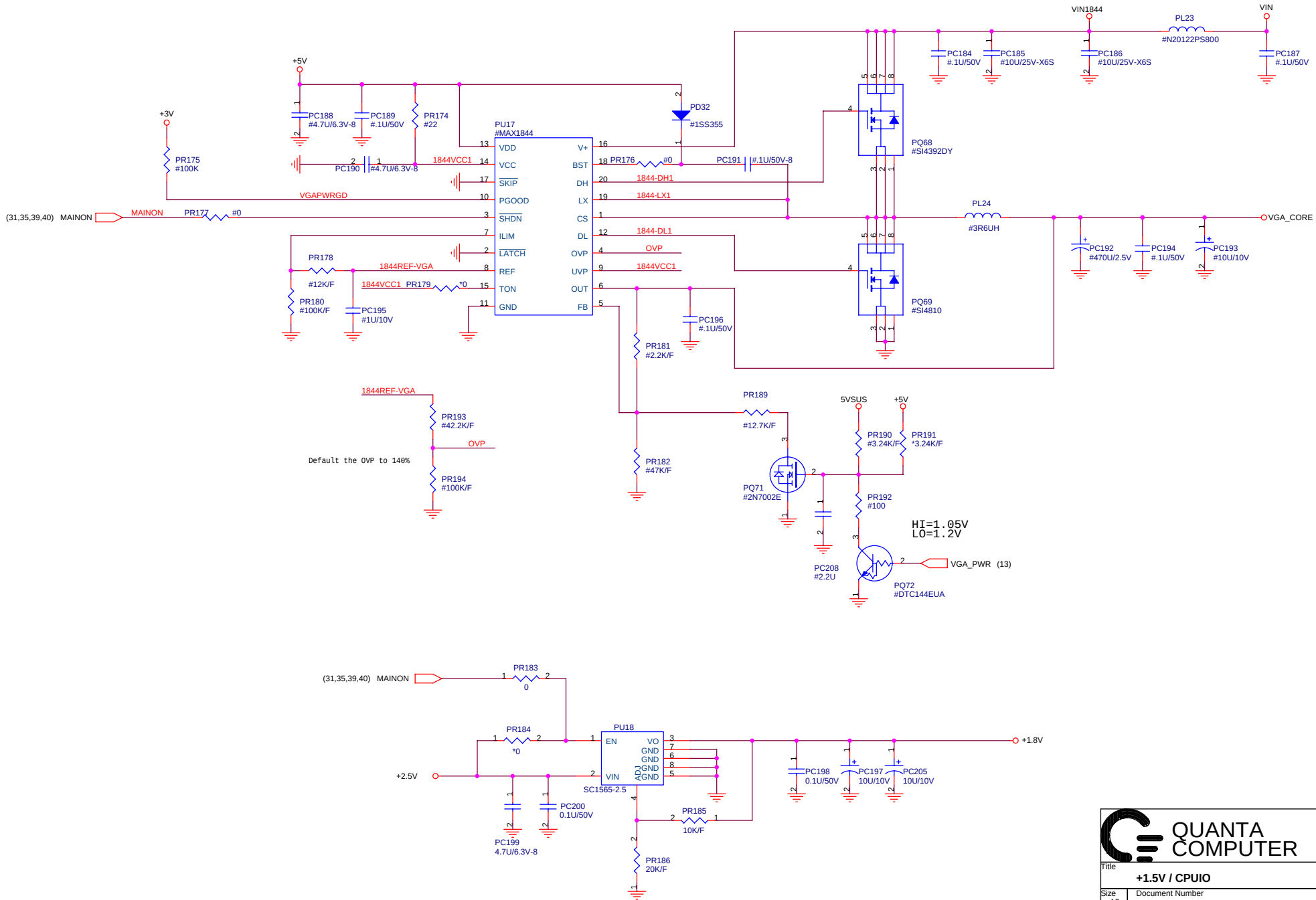
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Size: Document Number: **ZW6** Rev: A

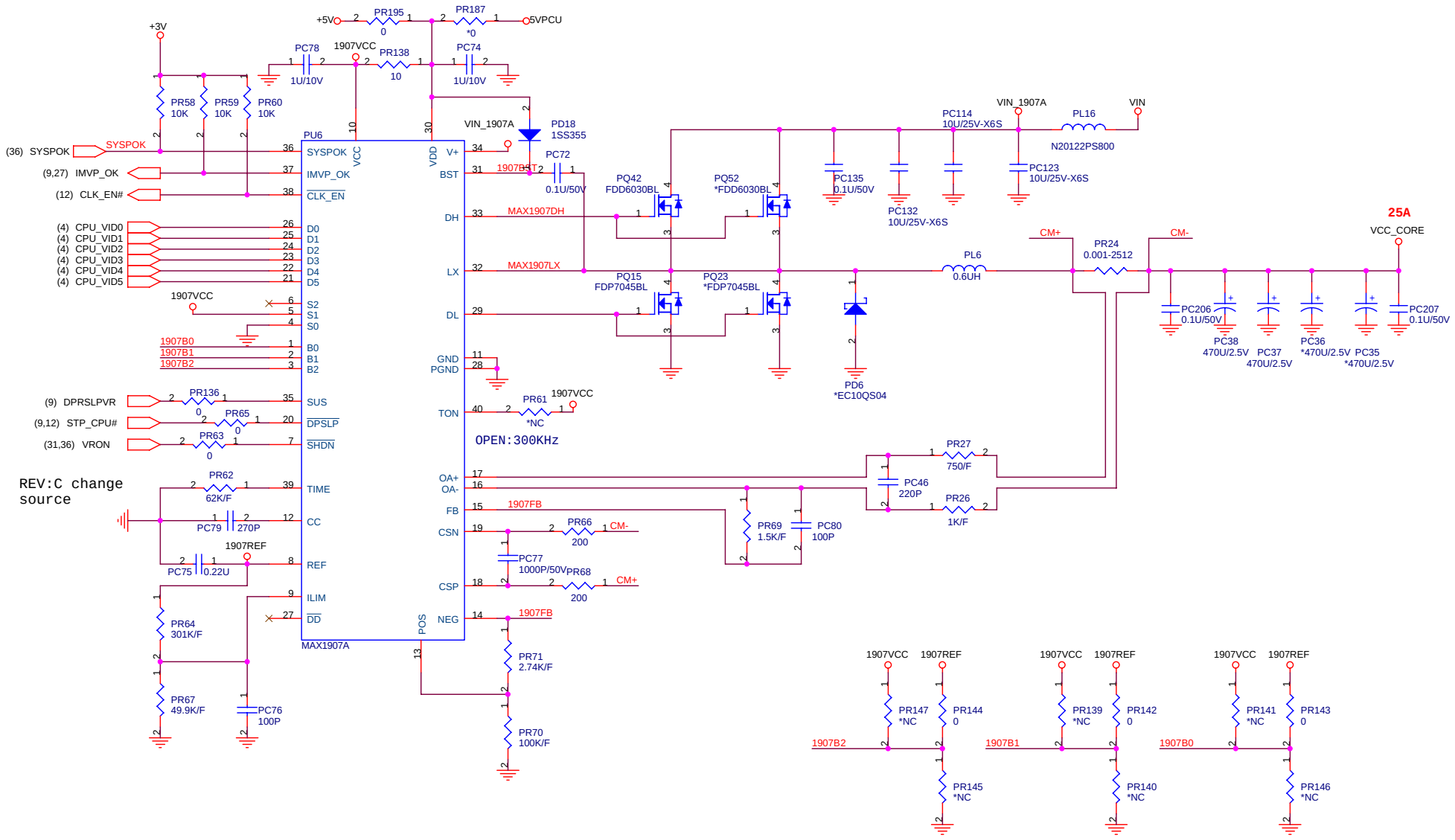
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Title			+1.5V / CPU10		
Size	Document Number				Rev
A3	ZW6				A
Date:	Wednesday, June 09, 2004		Sheet	37	of 43

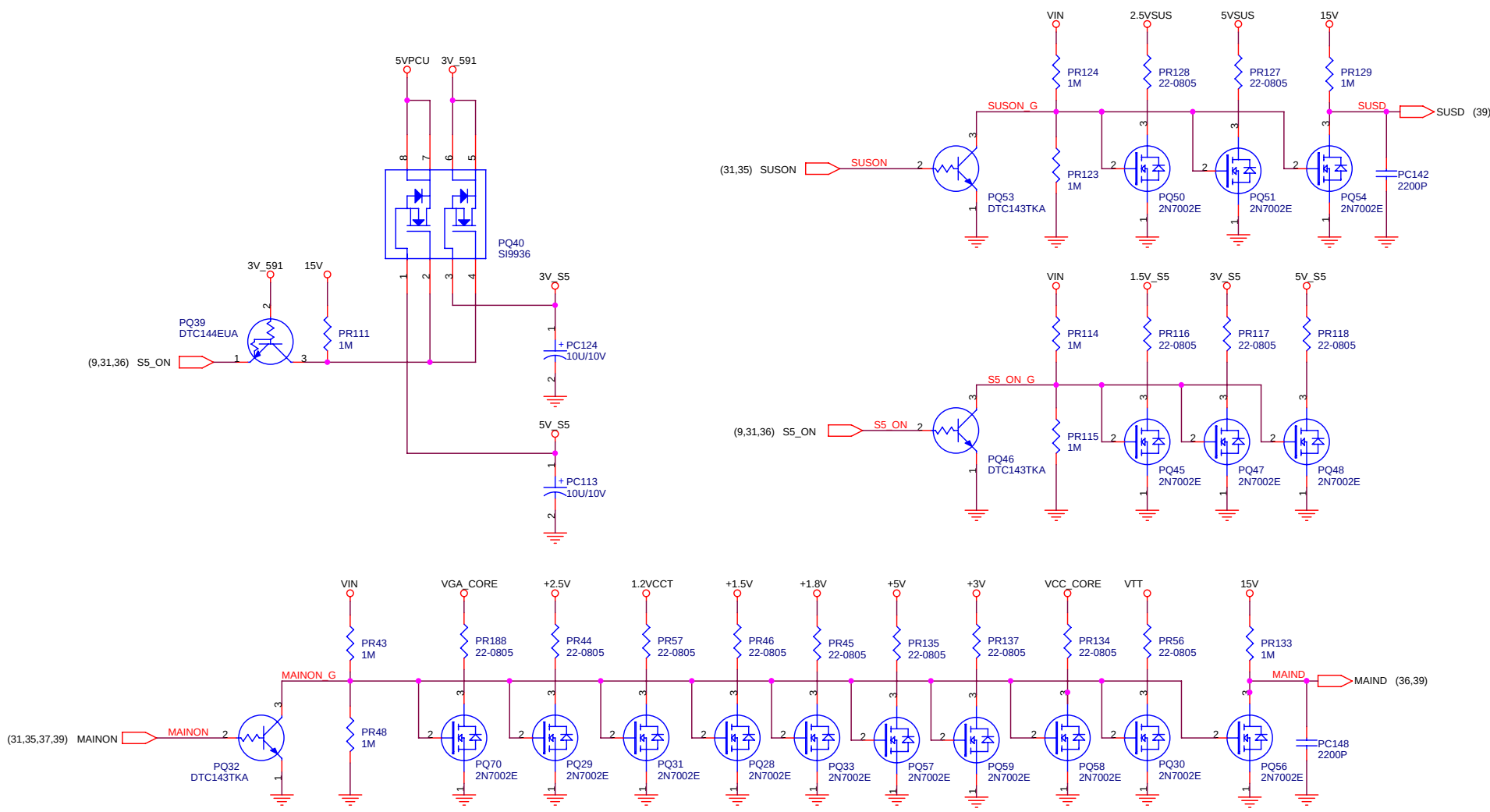


REV:C change source



QUANTA
COMPUTER

Title		
Cpu Power MAX1709		
Size	Document Number	Rev
B	ZW6	A
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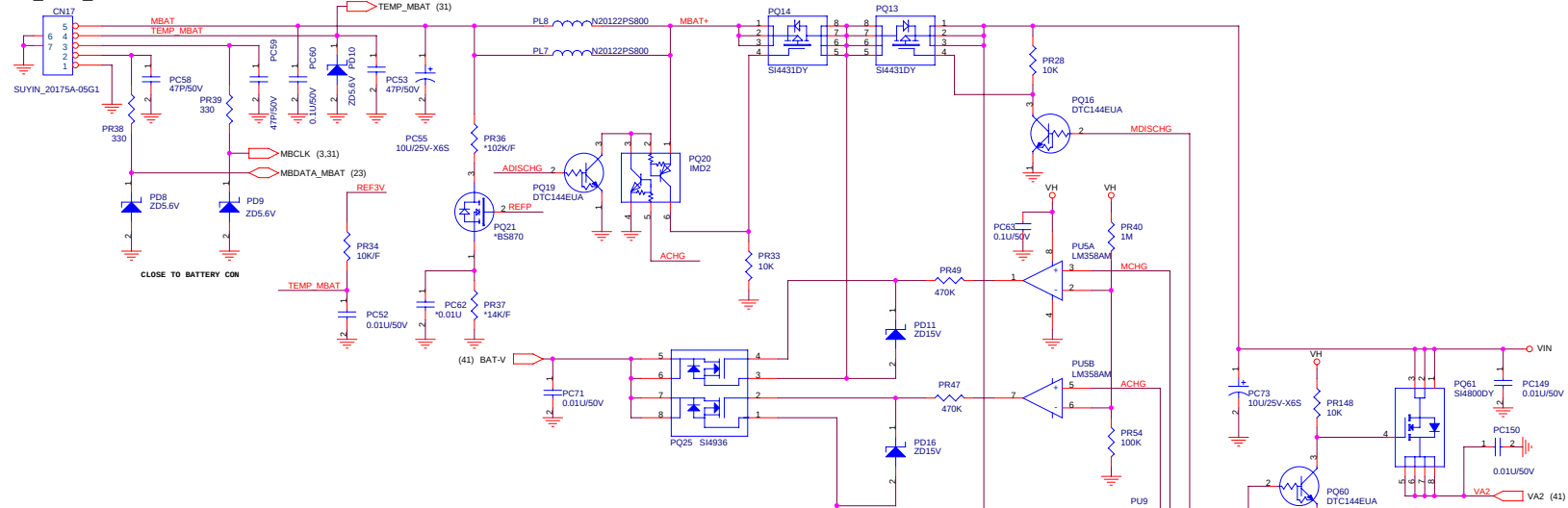




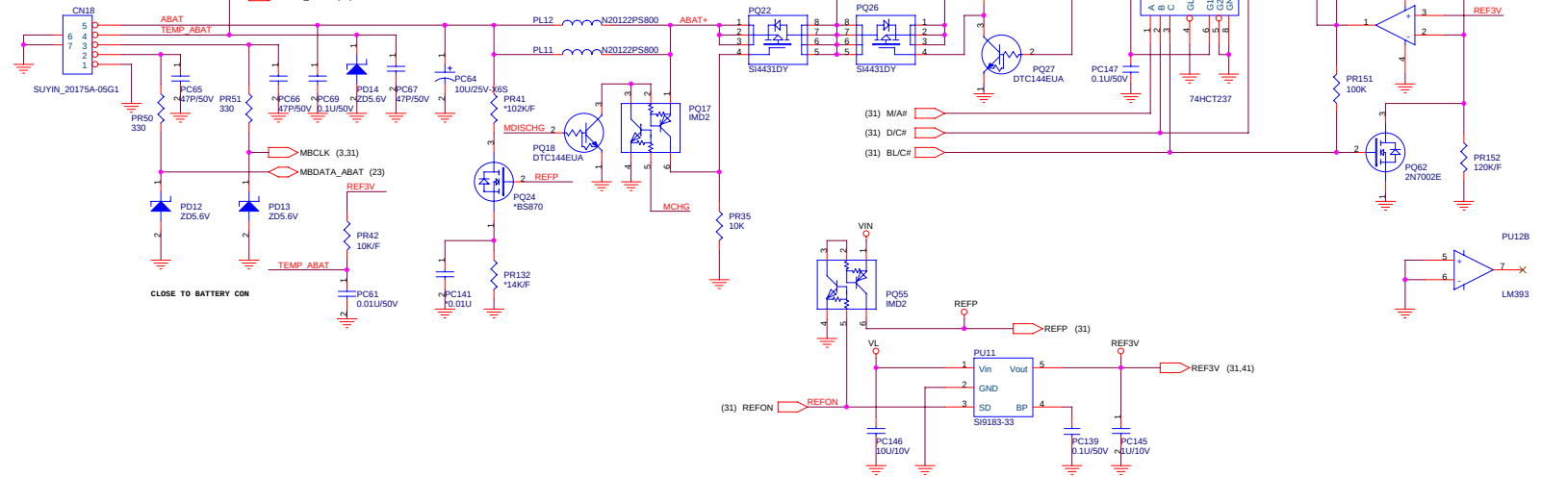
QUANTA
COMPUTER

Title		
Discharge Circuit		
Size	Document Number	Rev
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1ST_BATT_CONN



2ND_BATT_CONN



File	Battery Select		
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C	ZW6		A
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Change list

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	B.Ver#	
1	Z168 can't detect AGP device	CHANGE POWERJACK, PCMCIA, SIO DEL DVI ADD PRINTER PORT	A	6 7 18	DEL R775-R776 DEL R777-R784, R694-R697 DEL R777-R784, R694-R697	Rev B	

QUANTA
COMPUTER

Title

Battery Select

Size

Custom

Document Number

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A

D
C
B
A