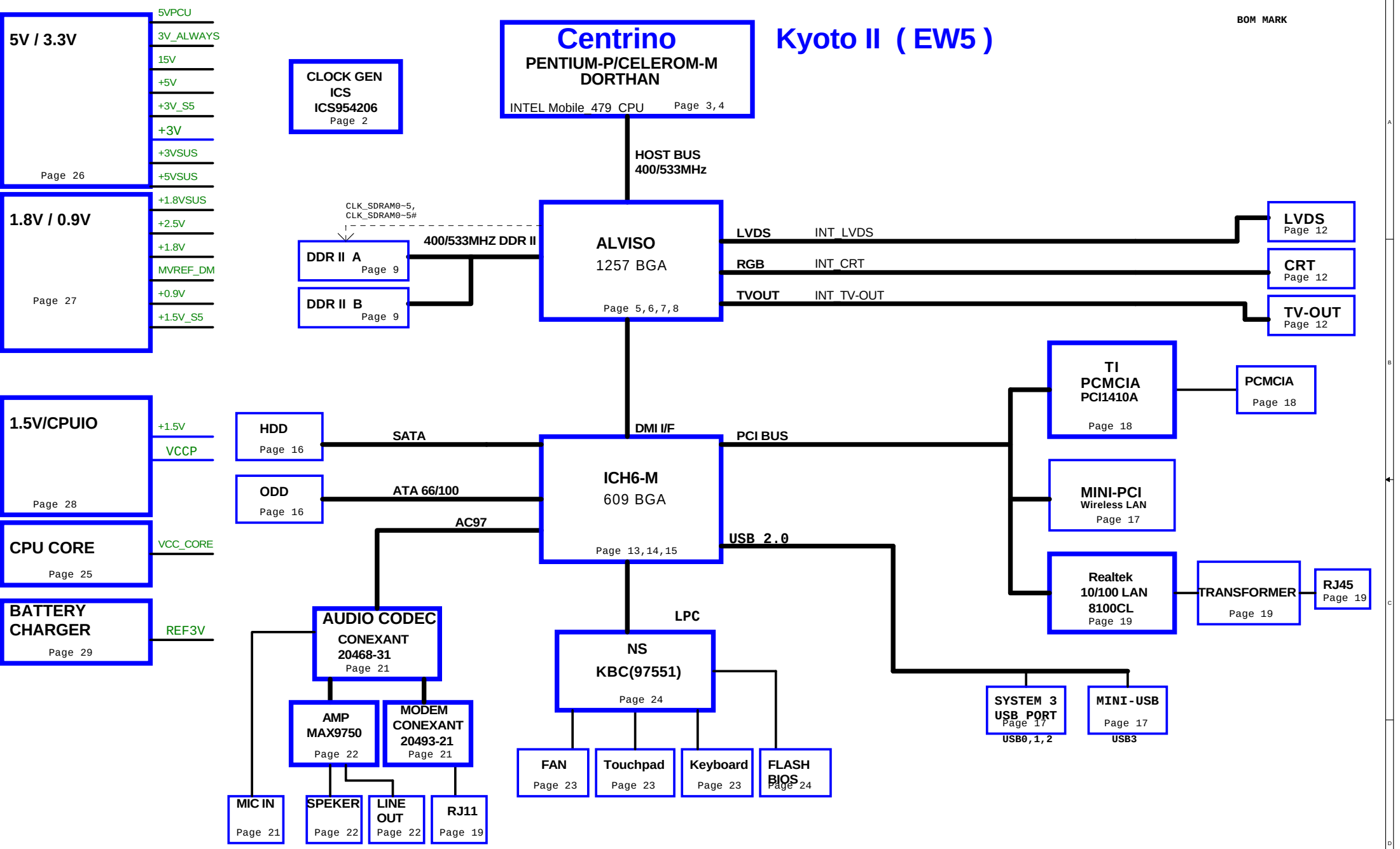




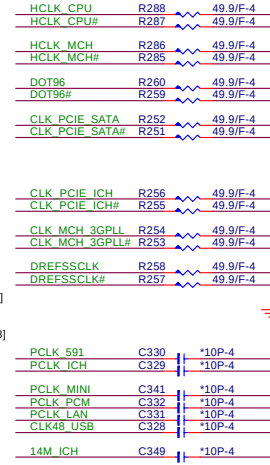
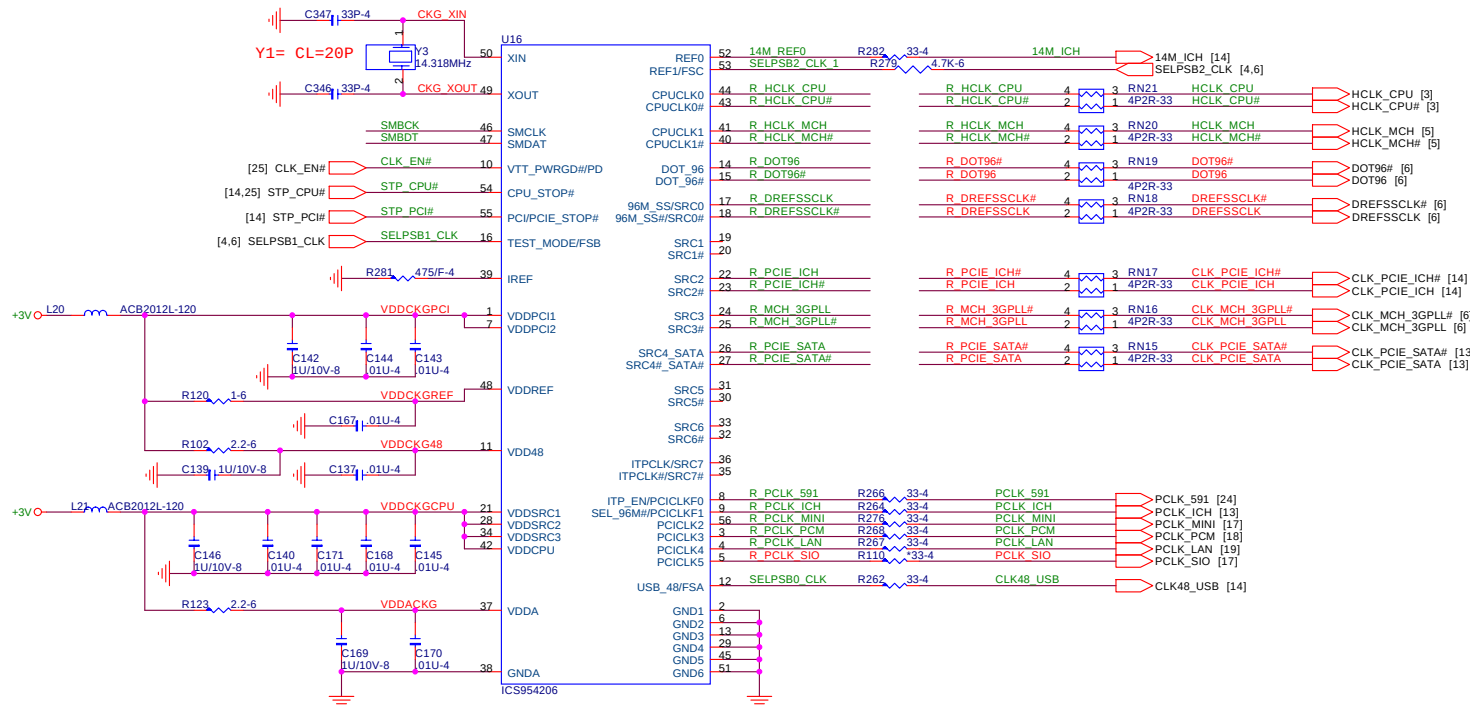
PCI ROUTING TABLE	IDSEL	INTERRUPT	DEVICE
REQ0# / GNT0#	AD18	INTD#	REALTEK LAN
REQ1# / GNT1#	AD20	INTB#, INTC#	MINI-PCI
REQ2# / GNT2#	AD17	INTE#	TI 1410A



QUANTA COMPUTER
HW Engineer :

Title BLOCK DIAGRAM		
Size : X	Document Number : EW5 --- Alviso & ICH6	Rev : 1A
Date : Saturday, July 09, 2005	Sheet 1	of 30

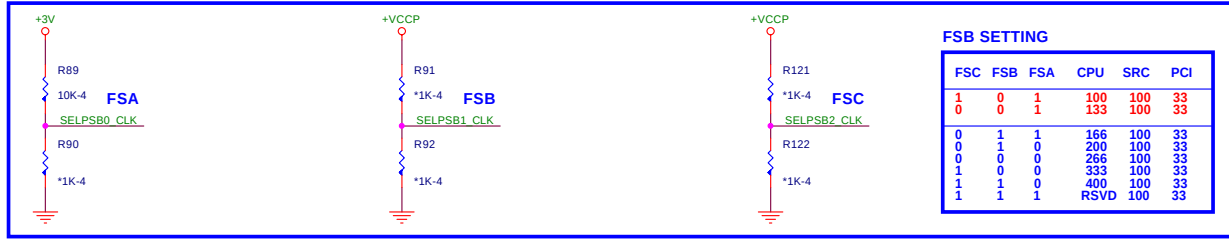
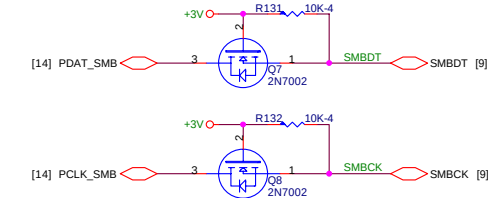
Add for DEBUG BOARD



96M/SRC0 SELECT
0: 96M 1: SRC0



ITP/SRC7 SELECT
0: SRC7 1: ITP



FSB SETTING

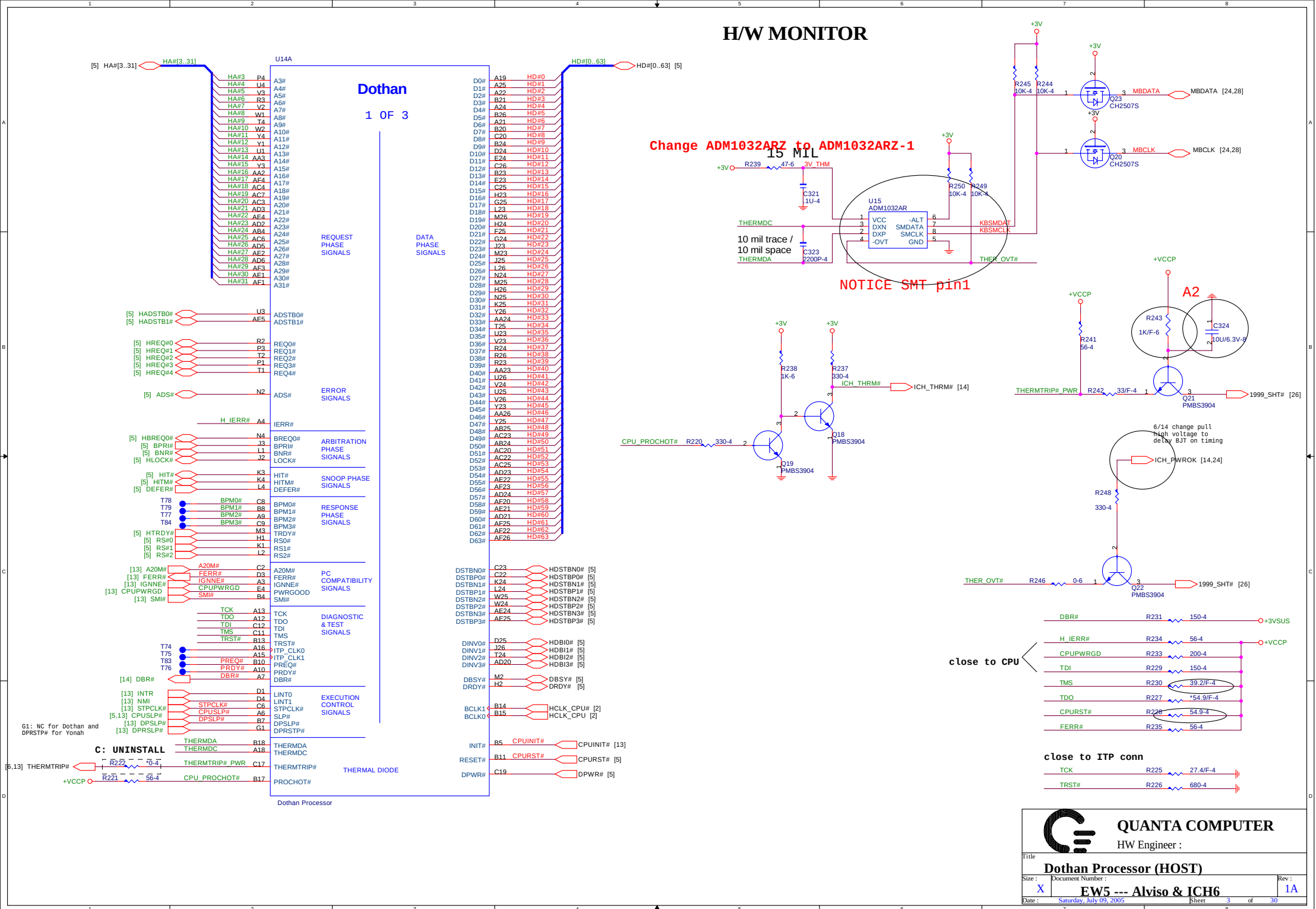
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

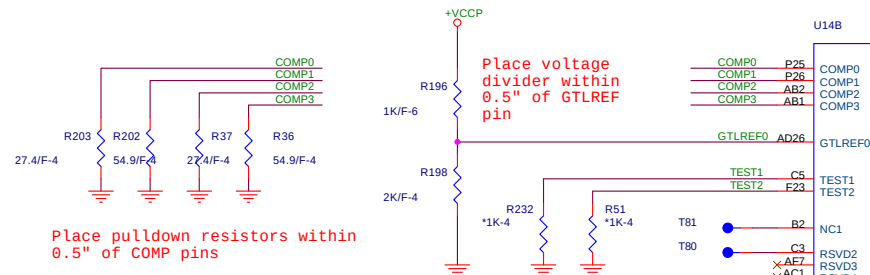


QUANTA COMPUTER
HW Engineer :

Title : **CLOCK GENERATOR**
 Size : **X** Document Number : **EW5 --- Alviso & ICH6** Rev : **1A**
 Date : **Saturday, July 09, 2005** Sheet **2** of **30**

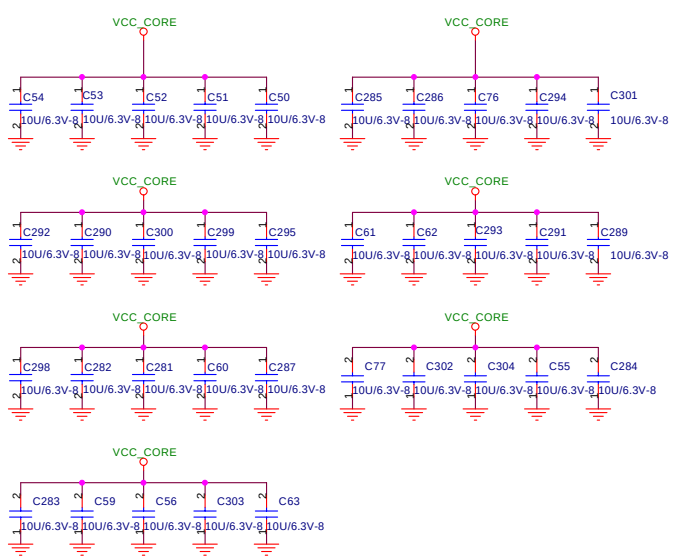
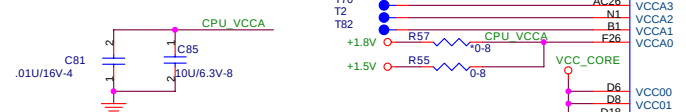
H/W MONITOR



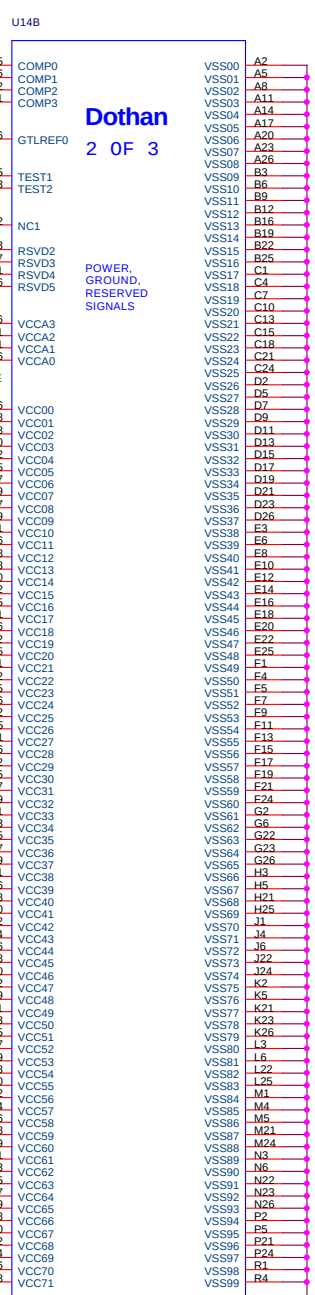
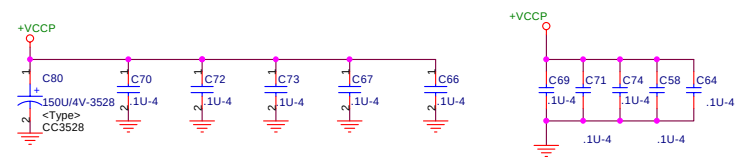


Dothan
2 OF 3

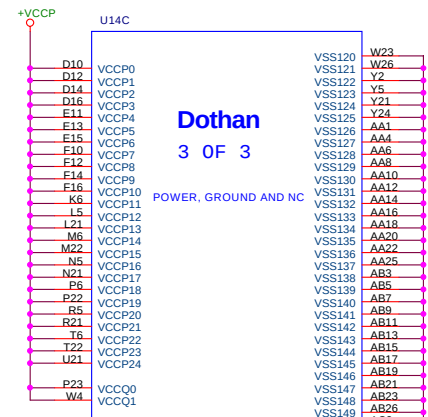
POWER,
GROUND, AND NC
RESERVED
SIGNALS



Total caps = 2633 uF
ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15

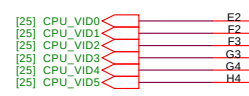


Dothan Processor

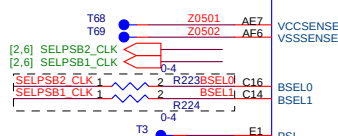


Dothan
3 OF 3

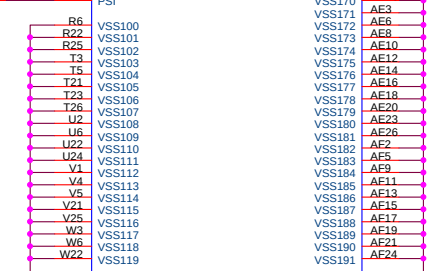
POWER, GROUND AND NC



VID



DOOTHAN-A NC
DOOTHAN-B
POP



Dothan Processor



QUANTA COMPUTER

HW Engineer :

Title

Dothan Processor (POWER)

Size

Document Number :

EW5 --- Alvise & ICH6

Rev :

1A

Date :

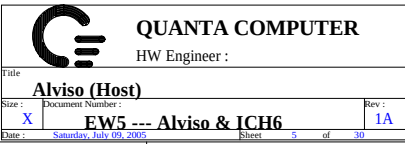
Saturday, July 09, 2005

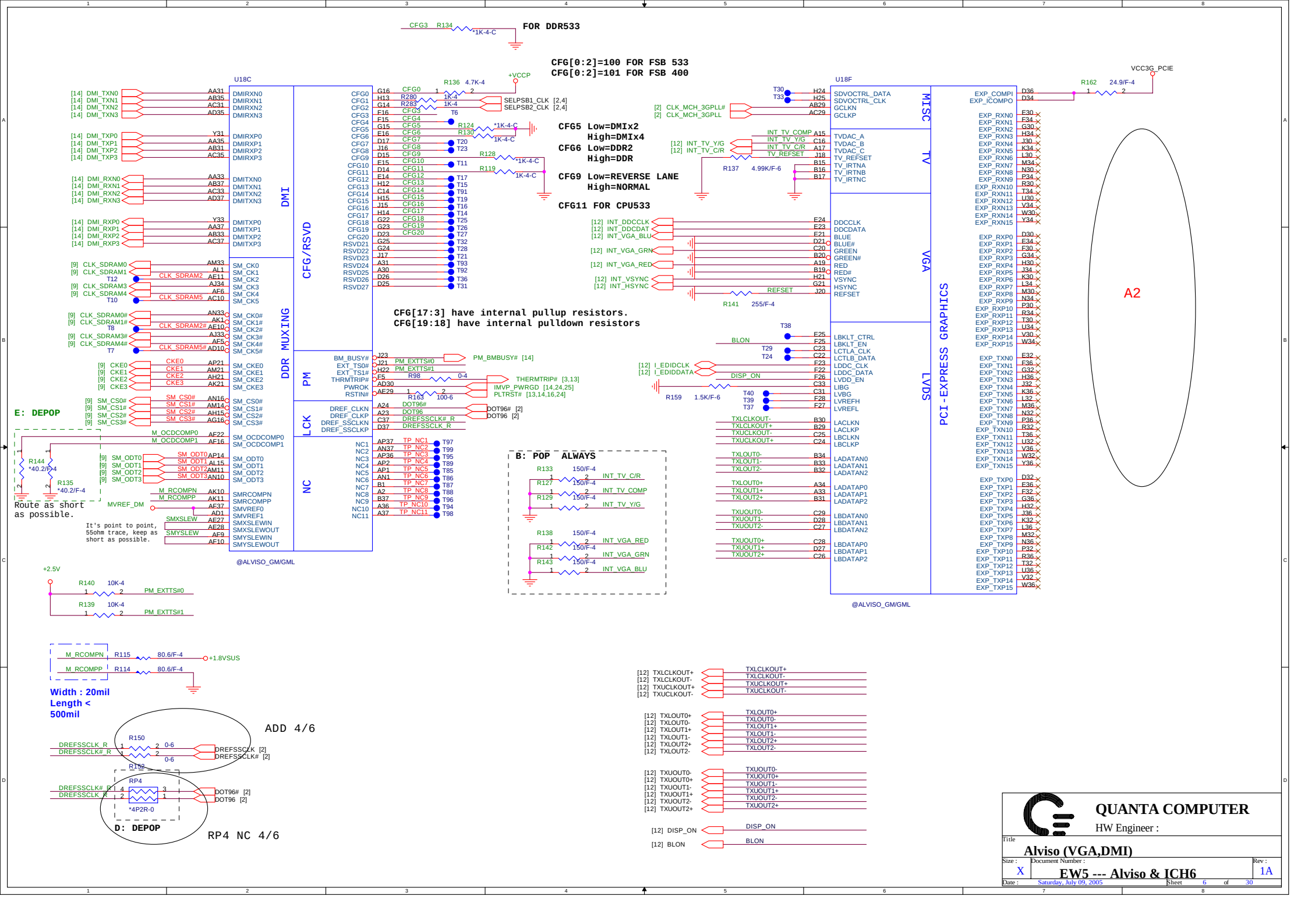
Sheet

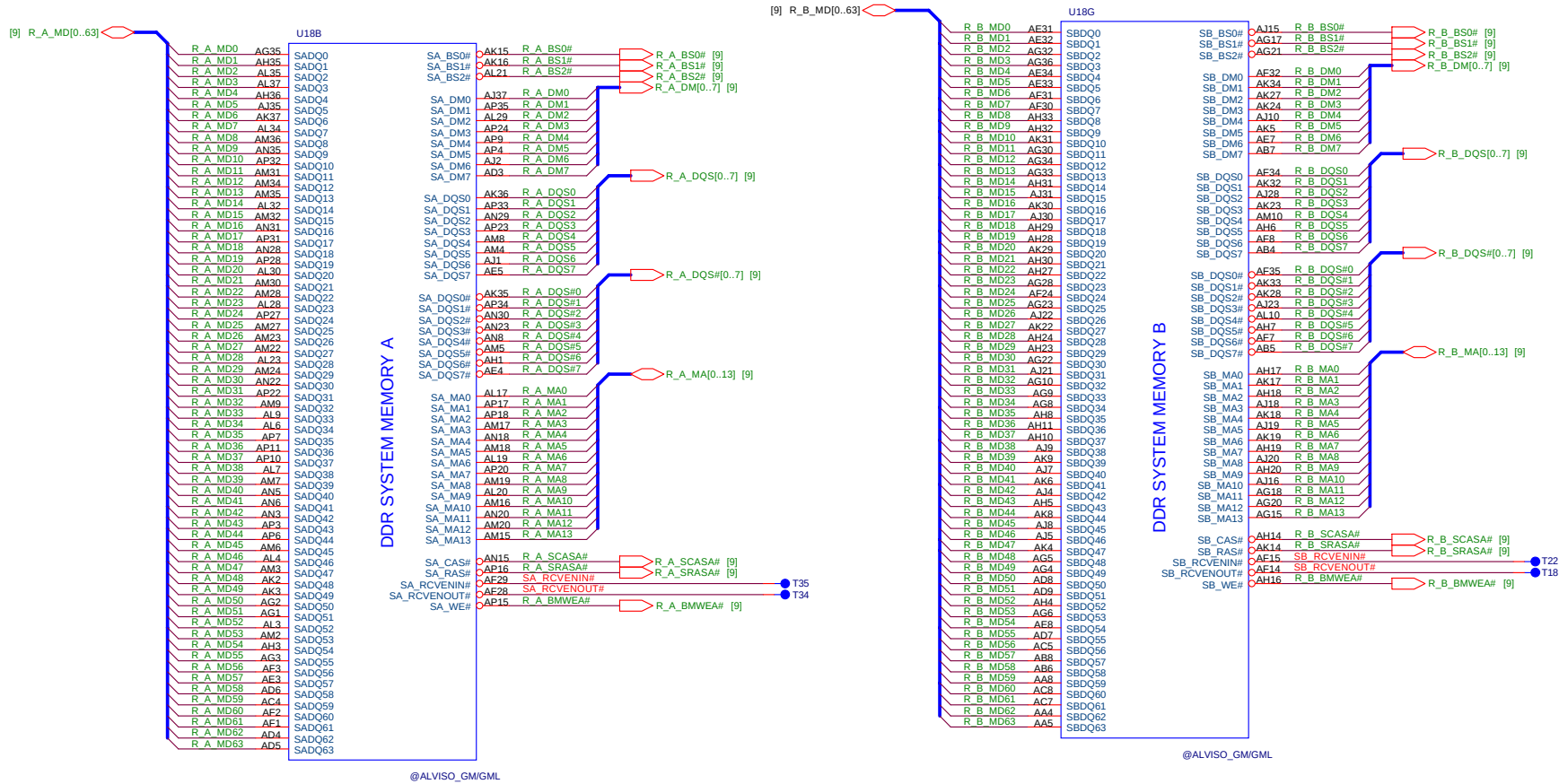
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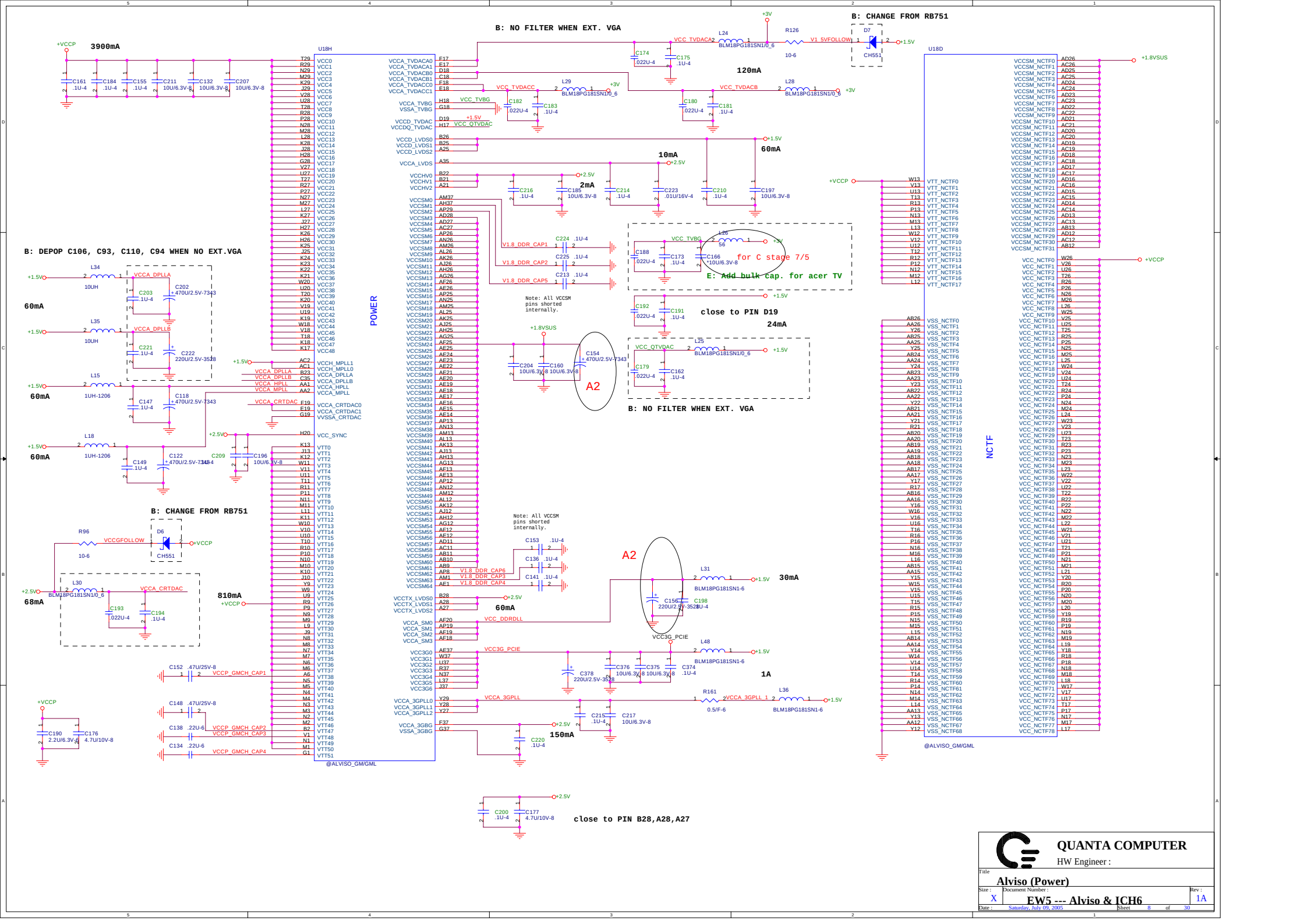
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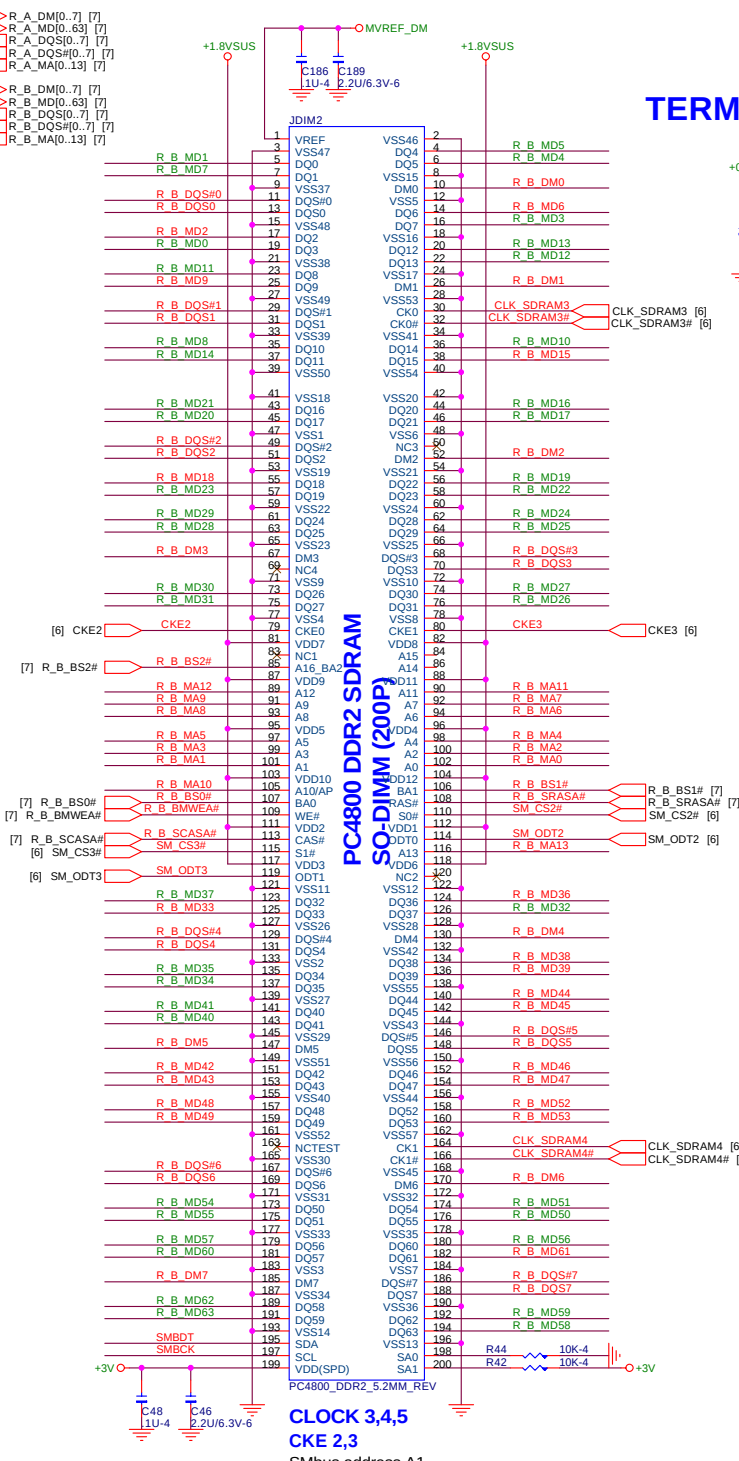
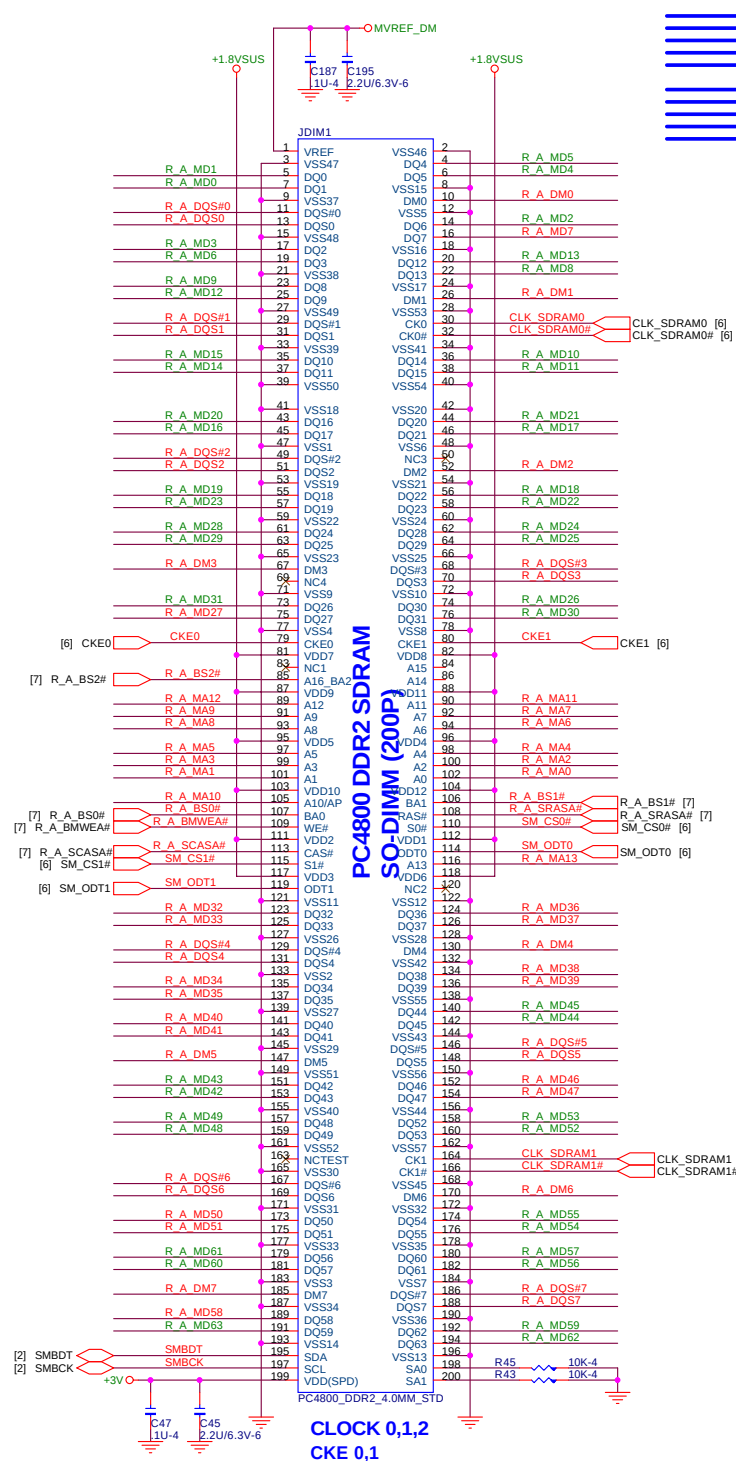
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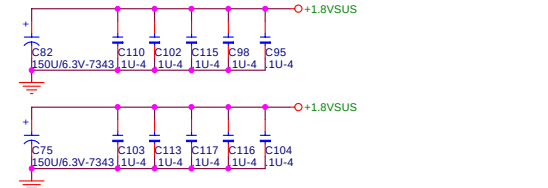
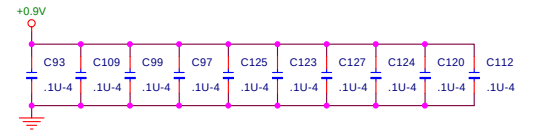
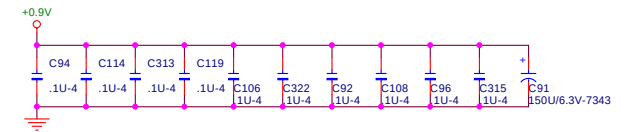




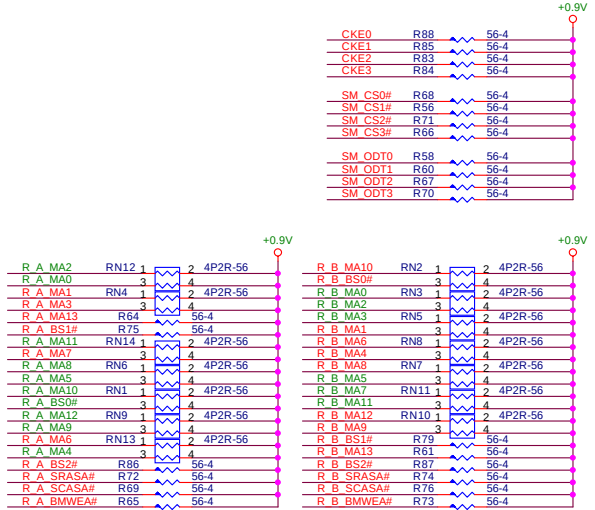




TERMINATOR DECOUPLING CAPACITOR



DDR2 TERMINATOR

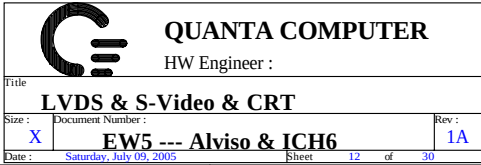


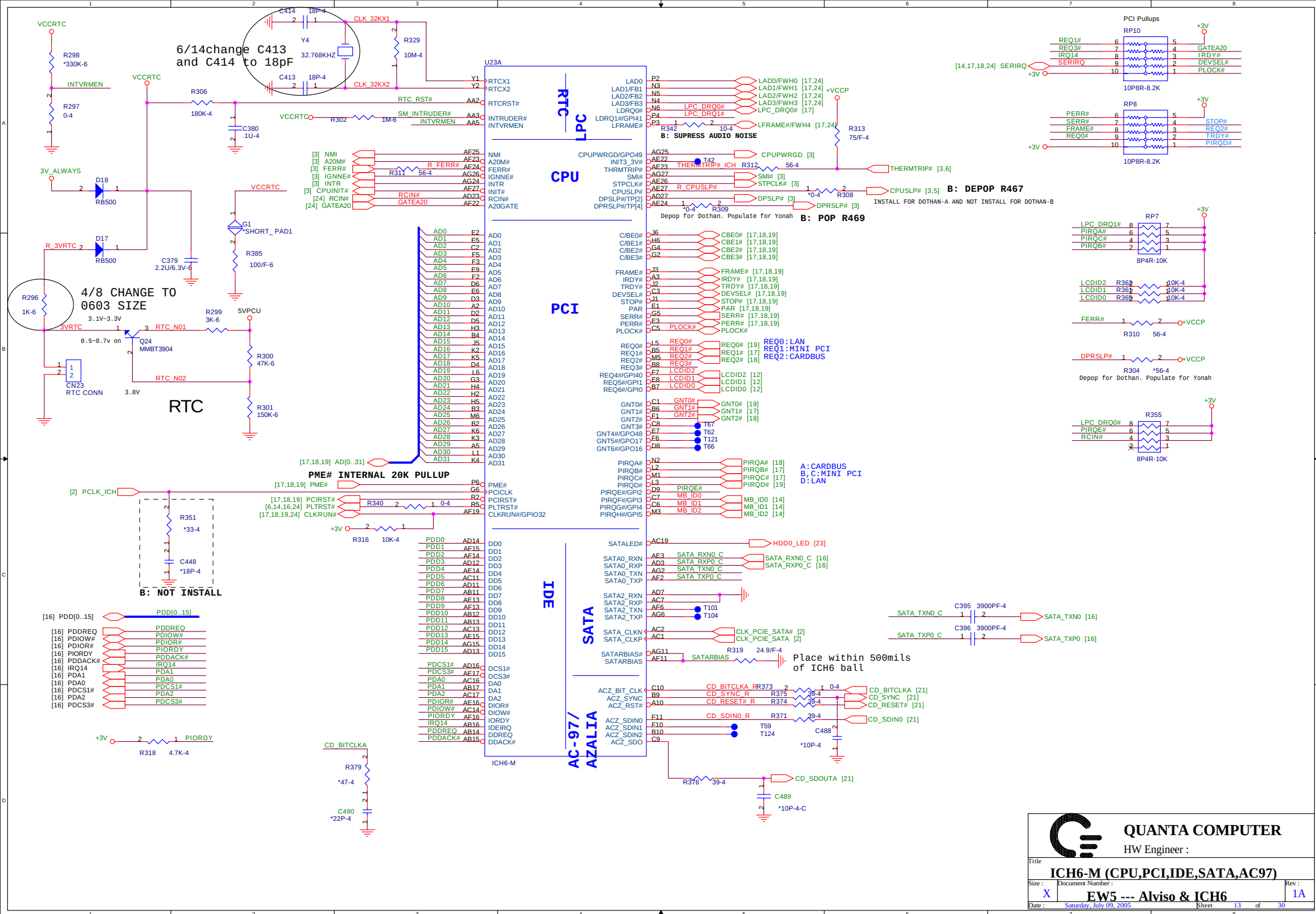
**QUANTA COMPUTER**
HW Engineer :
Title :
DDR2 SO-DIMM & TERMINATOR
Size : X Document Number :
EW5 --- Alviso & ICH6 Rev : 1A
Date : Saturday, July 09, 2005 Sheet 9 of 30

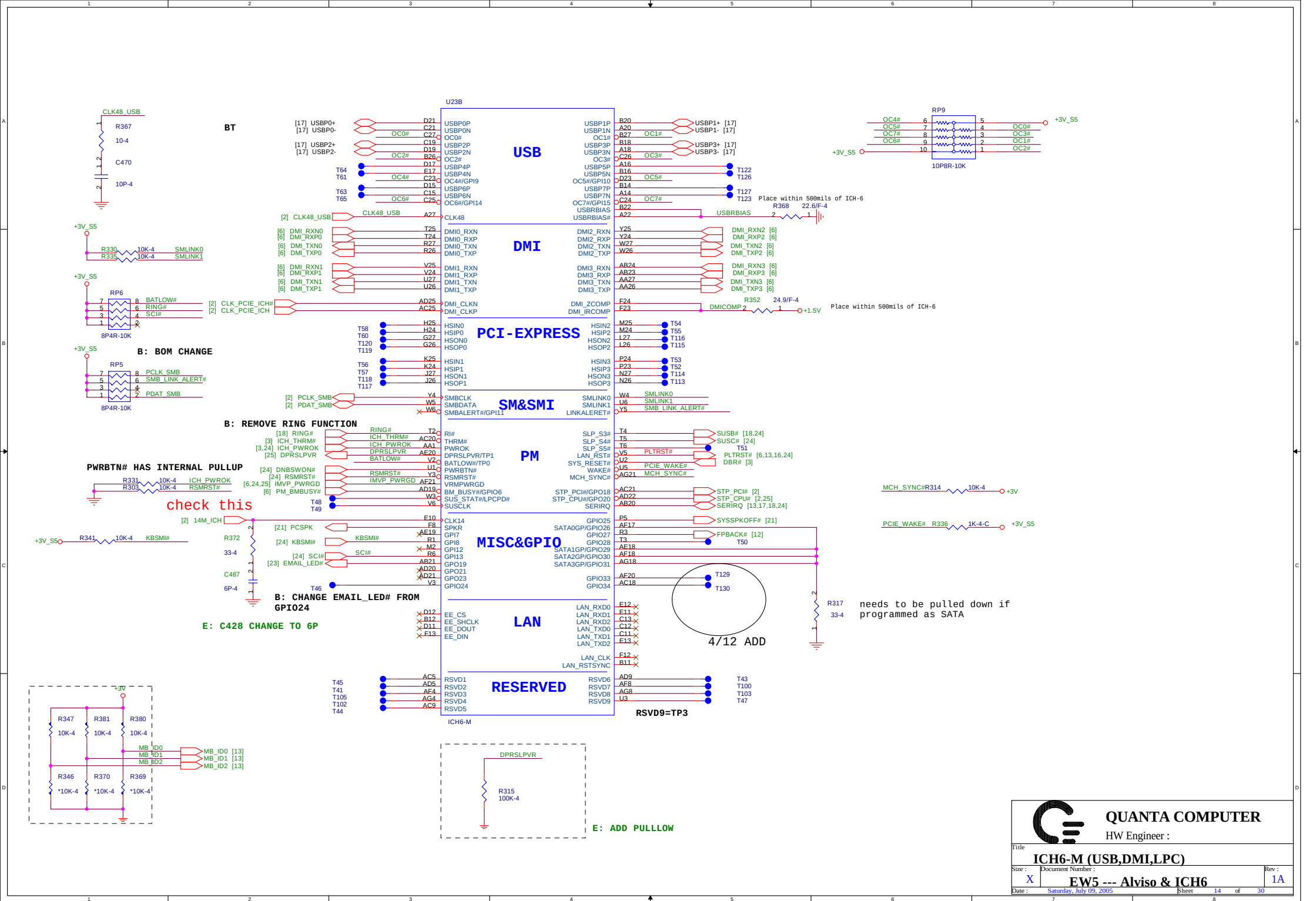
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		HW Engineer :	
Title			
AGP Bus Conn			
Size :	Document Number :		Rev :
X	EW5 --- Alviso & ICH6		1A
Date :	Saturday, July 09, 2005		Sheet 10 of 30

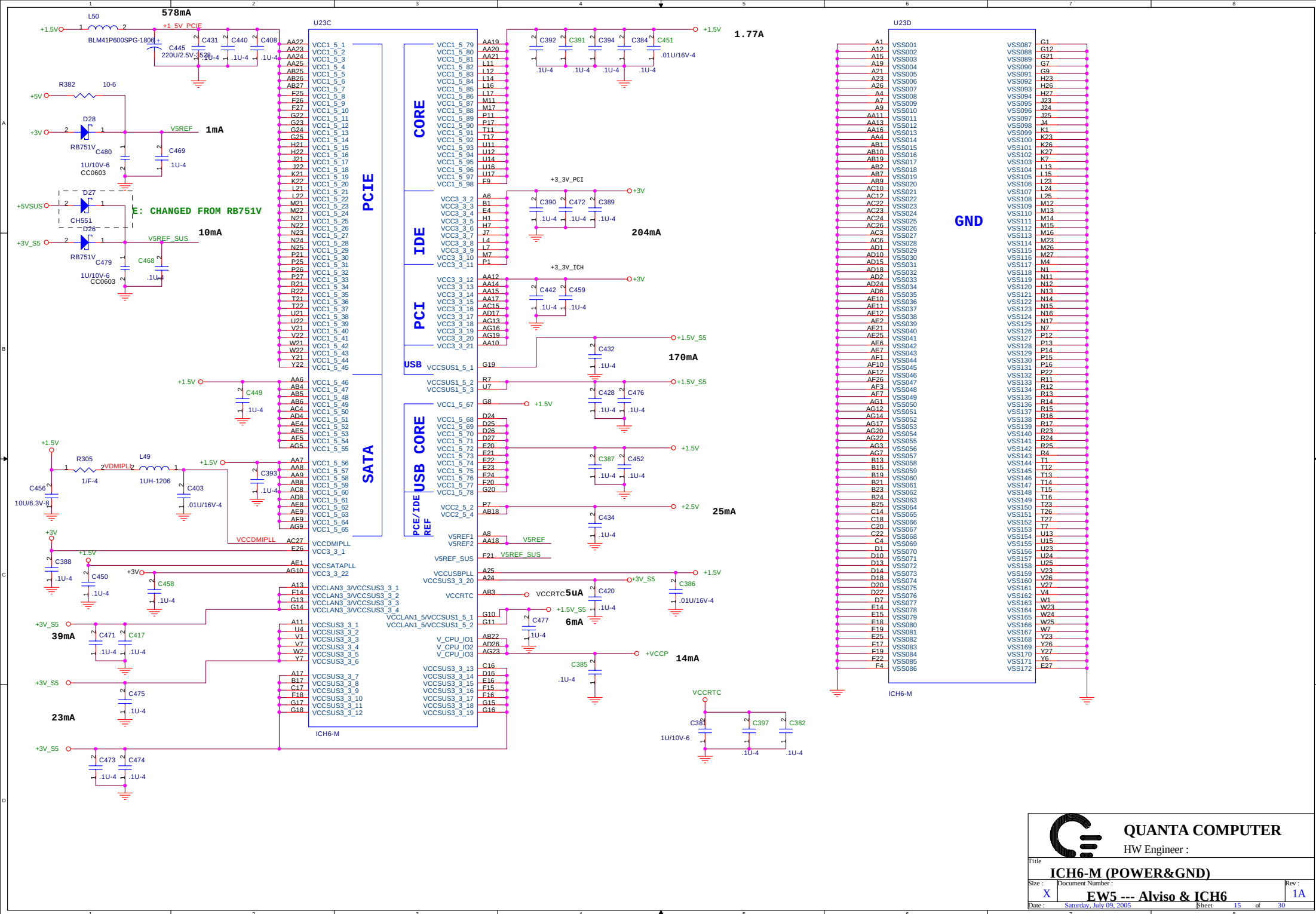


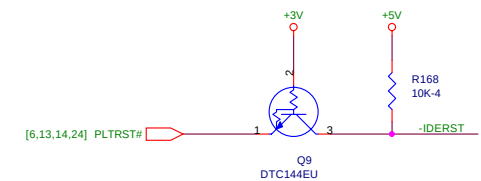
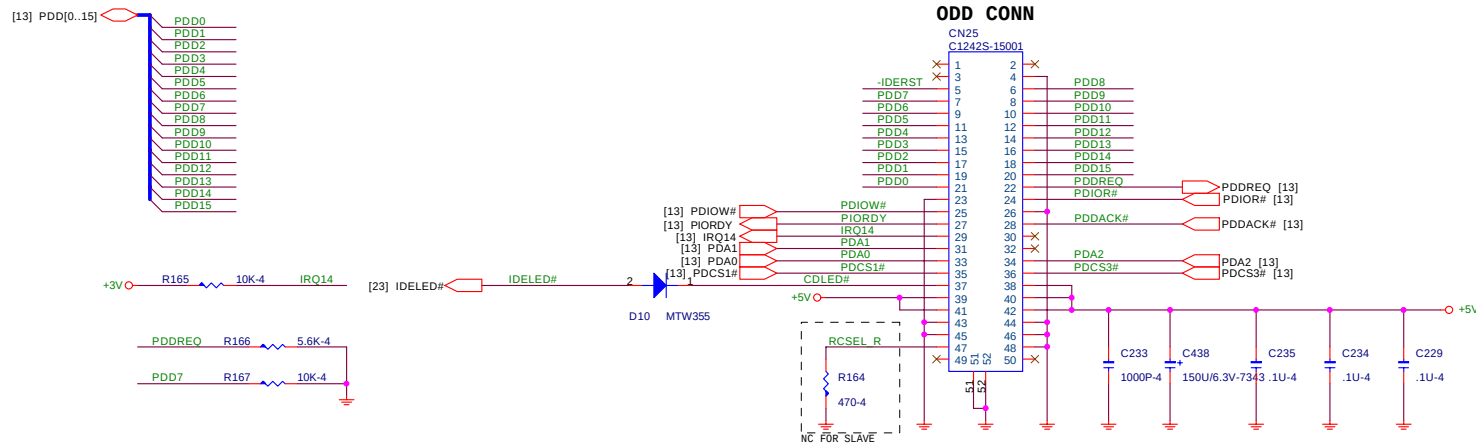
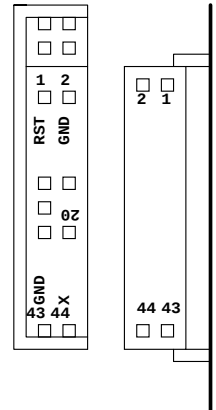
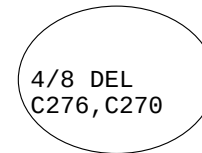
		QUANTA COMPUTER	
Title		NC	
Size : X		Document Number :	
Date : Saturday, July 09, 2005		Rev : 1A	
Date : Saturday, July 09, 2005		Sheet 11 of 30	







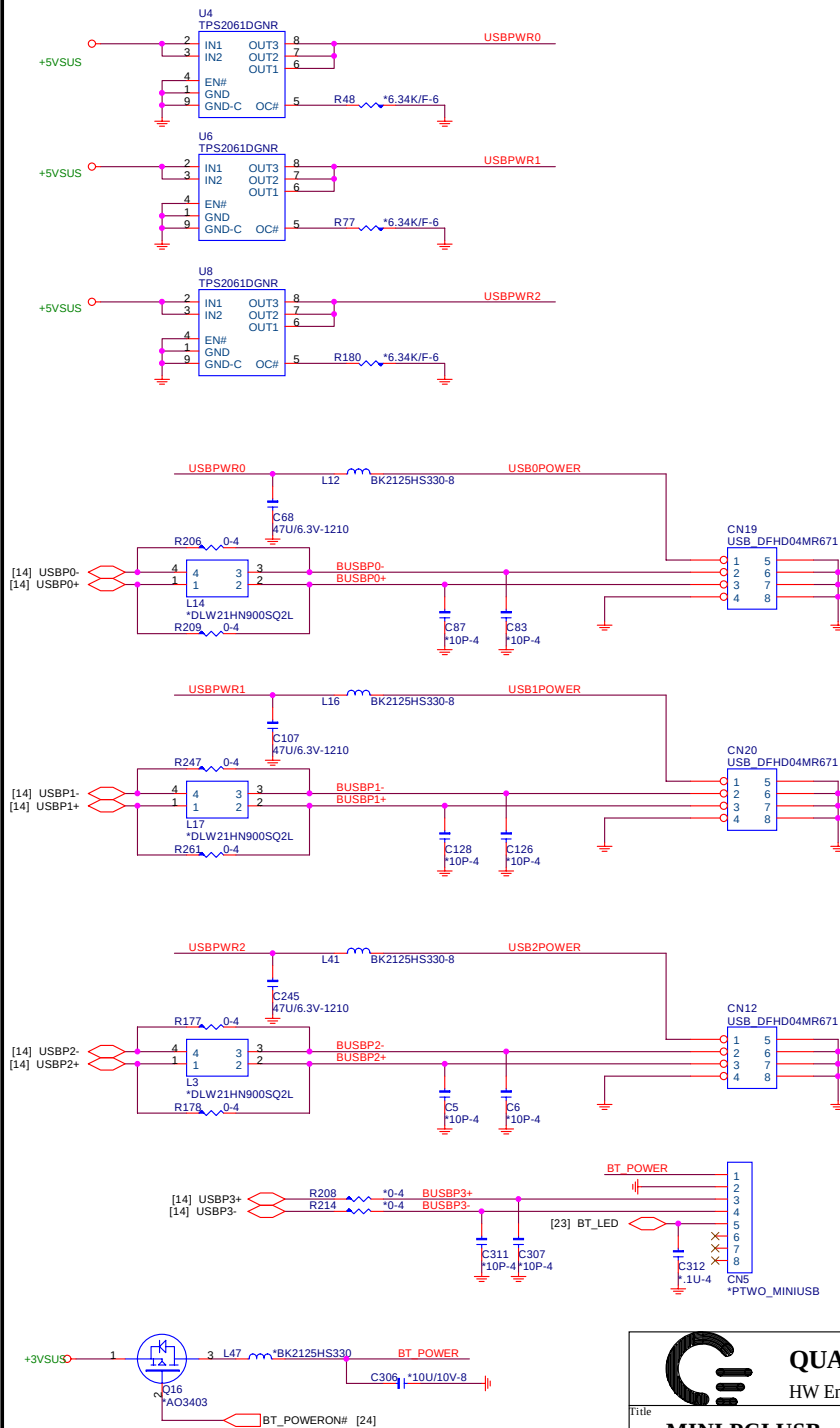
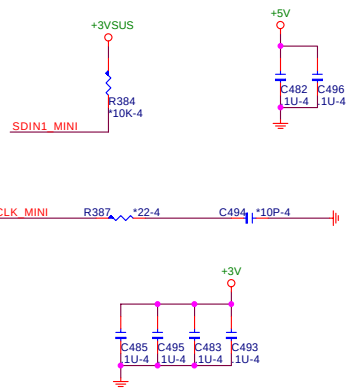
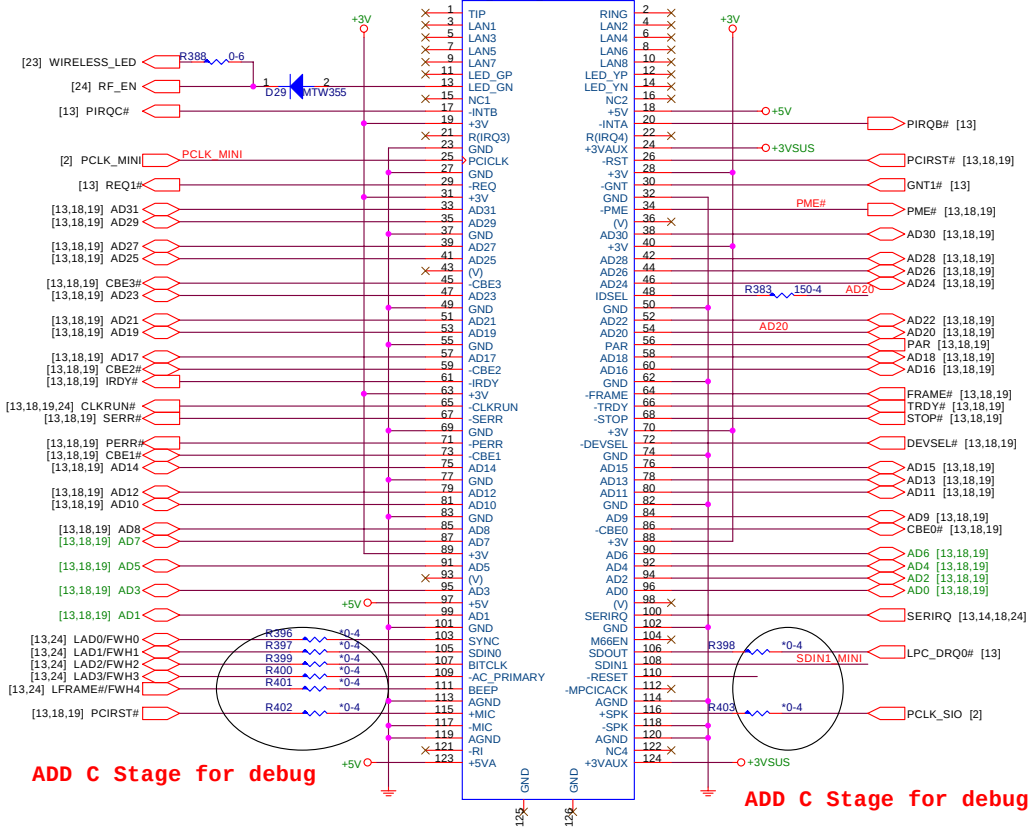




ID Select : AD20
Interrupt Pin : INTB# , INTC#
Request Indicate : REQ1#
Grant Indicate : GNT1#

MINI-PCI

CN26 QTC_MINIPCI_H7.95



QUANTA COMPUTER
HW Engineer :

Title : **MINI PCI,USB**

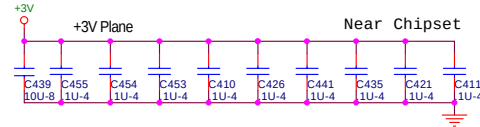
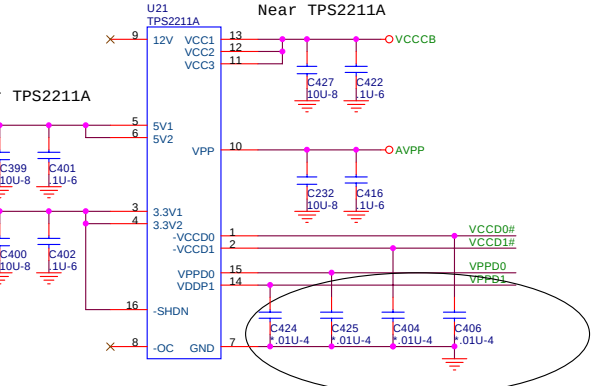
Size : X Document Number : **EW5 --- Alviso & ICH6** Rev : 1A

Date : Saturday, July 09, 2005 Sheet 17 of 30

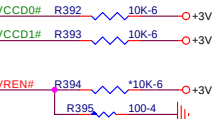
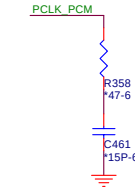
CARDBUS SLOT

ID Select : AD17
Interrupt Pin : INTA#
Request Indicate : REQ2#
Grant Indicate : GNT2#

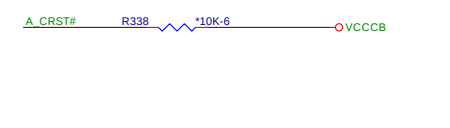
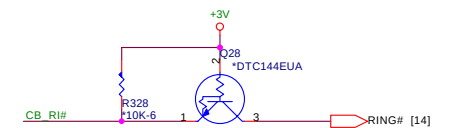
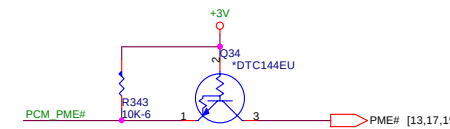
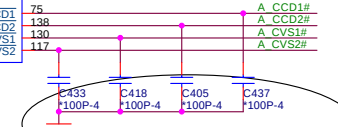
C/BE3#
C/BE2#
C/BE1#
C/BE0#
REQ2#
GNT2#
PCIRST#
PCLK PCM
FRAME#
TRDY#
IRDY#
DEVSEL#
STOP#
PERR#
SERR#
PAR
PCM PME#



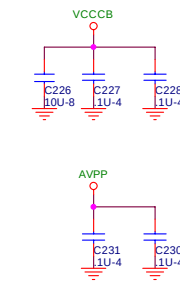
Near Chipset



CAD31/D10	144	A	CAD31
CAD30/D9	142	A	CAD30
CAD29/D8	141	A	CAD29
CAD28/D7	140	A	CAD28
CAD27/D6	139	A	CAD27
CAD26/D5	138	A	CAD26
CAD25/D4	137	A	CAD25
CAD24/D3	136	A	CAD24
CAD23/D2	135	A	CAD23
CAD22/D1	134	A	CAD22
CAD21/D0	133	A	CAD21
CAD20/A6	132	A	CAD20
CAD19/A5	131	A	CAD19
CAD18/A4	130	A	CAD18
CAD17/A3	129	A	CAD17
CAD16/A2	128	A	CAD16
CAD15/A1	127	A	CAD15
CAD14/A0	126	A	CAD14
CAD13/IORD	125	A	CAD13
CAD12/IOWR	124	A	CAD12
CAD11/CE2	123	A	CAD11
CAD10/CE1	122	A	CAD10
CAD9/D15	121	A	CAD9
CAD8/D14	120	A	CAD8
CAD7/D13	119	A	CAD7
CAD6/D12	118	A	CAD6
CAD5/D11	117	A	CAD5
CAD4/D10	116	A	CAD4
CAD3/D9	115	A	CAD3
CAD2/D8	114	A	CAD2
CAD1/D7	113	A	CAD1
CAD0/D6	112	A	CAD0
CC/BE3/REG	124	A	CC/BE3#
CC/BE2/A12	113	A	CC/BE2#
CC/BE1/A8	98	A	CC/BE1#
CC/BE0/CET	88	A	CC/BE0#
CPAR/A13	100	A	CPAR
CBLOCK/A19	101	A	CBLOCK#
CPERR/A14	102	A	CPERR#
CSTOP/A20	103	A	CSTOP#
CGNT/A	105	A	CGNT#
CDEVSEL/A21	106	A	CDEVSEL#
CCLK/A16	107	A	CCLK#
CTRDY/A22	108	A	CTRDY#
CIRDY/A15	110	A	CIRDY#
CFRAME/A23	111	A	CFRAME#
CRST/IRESET	119	A	CRST#
CREQ/INPACK	122	A	CREQ#
CINT/READ	131	A	CINT#
CSERR/WAIT	133	A	CSERR#
CAUDIO/BV2	134	A	CAUDIO
CSTSCHG/BV1	135	A	CSTSCHG
CCLKRUN/WP	136	A	CCLKRUN#
CRSVD/D14	84	A	RSVD/D14
CRSVD/A18	99	A	CRSVD/A18
CRSVD/D2	143	A	CRSVD/D2
CCD1/CD1	75	A	CCD1#
CCD2/GD2	138	A	CCD2#
CVS1/VS2	130	A	CVS1#
CVS2/VS2	117	A	CVS2#



Near Slot



A CAD0	1	GND
A CAD1	2	D3 - CAD0
A CAD2	3	D4 - CAD1
A CAD3	4	D5 - CAD2
A CAD4	5	D6 - CAD3
A CAD5	6	D7 - CAD4
A CAD6	7	CE1 - CCBE0
A CAD7	8	A10 - CAD9
A CC/BE0#	9	Q6 - CAD11
A CAD9	10	A11 - CAD12
A CAD11	11	A9 - CAD14
A CAD12	12	A8 - CCBE1
A CAD14	13	A14 - CPERR
A CPAR	14	WE/PGM - CGNT
A CPERR#	15	RDY/BSY, IRQ*INT
A CGNT#	16	VCC
A CINT#	17	
VPP1	18	A16 - CCLK
A CCLK1	19	A15 - CIRDY
A CIRDY#	20	A12 - CCBE2
A CC/BE2#	21	A7 - CAD18
A CAD18	22	A6 - CAD20
A CAD20	23	A5 - CAD21
A CAD21	24	A4 - CAD22
A CAD22	25	A3 - CAD23
A CAD23	26	A2 - CAD24
A CAD24	27	A1 - CAD25
A CAD25	28	A0 - CAD26
A CAD26	29	D0 - CAD27
A CAD27	30	D1 - CAD28
A CAD28	31	D2 - RFU
A CRSVD/D2	32	WP, IOIS16 - CKRUN
A CCLKRUN#	33	GND
	34	
	35	GND
A CCD1#	36	CD1 - CCD1
A CAD2	37	D11 - CAD2
A CAD4	38	D12 - CAD4
A CAD6	39	D13 - CAD6
A RSVD/D14	40	D14 - RFU
A CAD8	41	D15 - CAD8
A CAD10	42	CE2 - CAD10
A CVS1#	43	RFU, VS1 - CVS1
A CAD13	44	IORD - CAD13
A CAD15	45	A18 - RFU
A CAD16	46	A19 - CBLOCK
A CRSVD/A18	47	A20 - CSTOP
A CBLOCK#	48	A21 - CDEVSEL
A CSTOP#	49	VCC
A CDEVSEL#	50	
	51	
VPP2	52	A22 - CTRDY
A CTRDY#	53	A23 - CFRAME
A CFRAME#	54	A24 - CAD17
A CAD17	55	A25 - CAD19
A CAD19	56	NC - CVS2
A CVS2#	57	RESET - CRST
A CRST#	58	WAIT - CSERR
A CSERR#	59	INPACK - CREQ
A CREQ#	60	REG - CCBE3
A CC/BE3#	61	BVD2, SP - CAUDIO
A CAUDIO	62	BVD1, STSCHG - C*
A CSTSCHG	63	D8 - CAD28
A CAD28	64	D9 - CAD30
A CAD30	65	D10 - CAD31
A CAD31	66	CD2 - CCD2
A CCD2#	67	GND
	68	

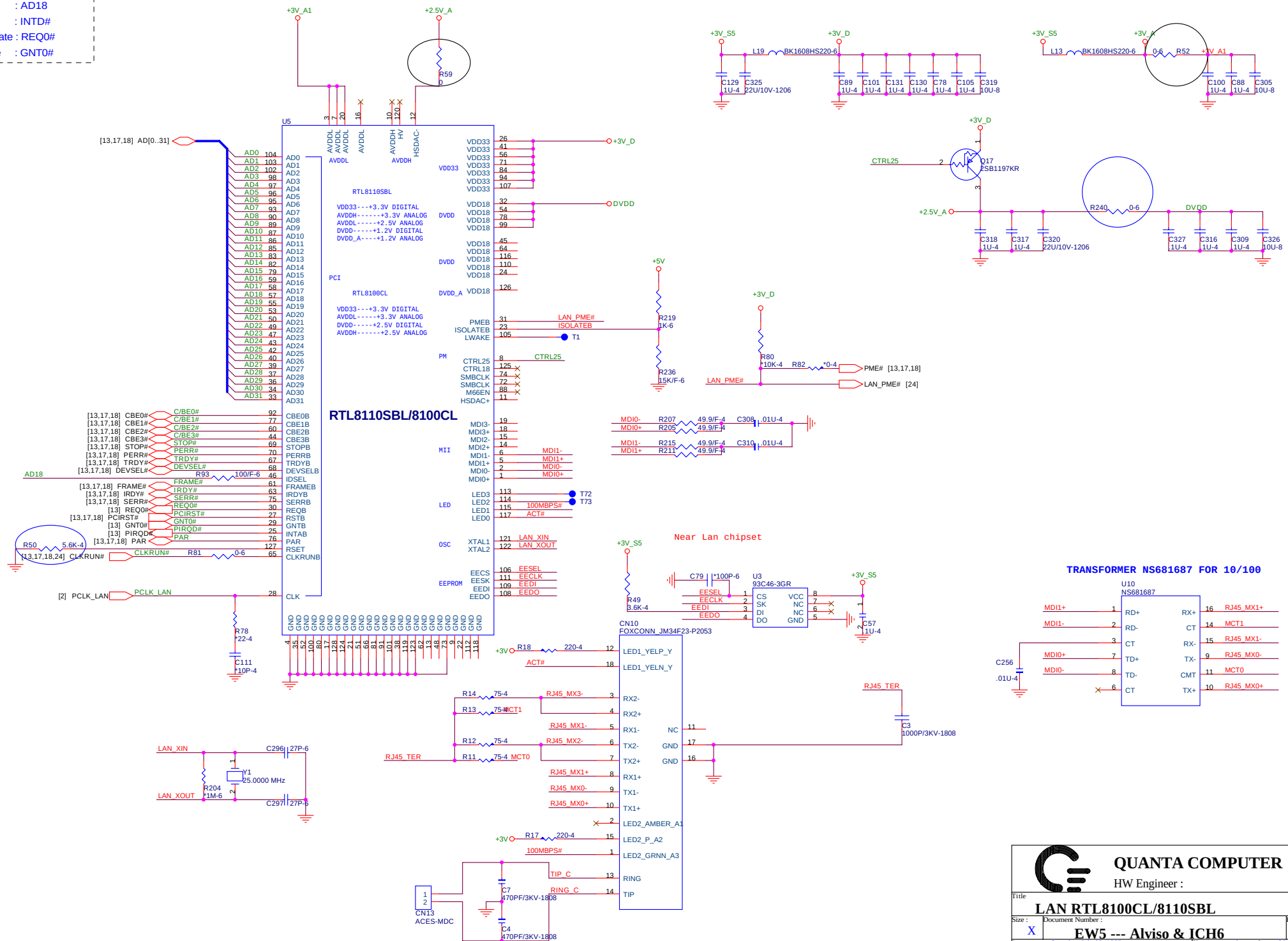
QUANTA COMPUTER
HW Engineer :

Title : **PCMCIA CONTROLLER**

Size : **X** Document Number : **EW5 --- Alviso & ICH6** Rev : **1A**

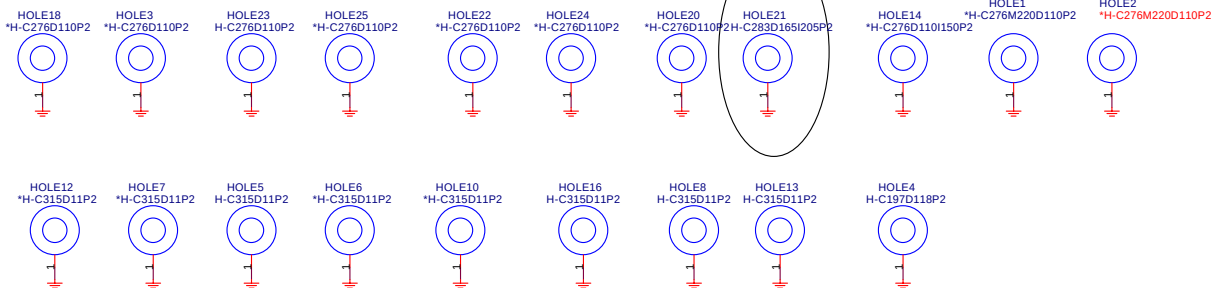
Date : **Saturday, July 09, 2005** Sheet **18** of **30**

ID Select : AD18
Interrupt Pin : INTD#
Request Indicate : REQ0#
Grant Indicate : GNT0#



4/ 8 CHANGE PCB Footprint

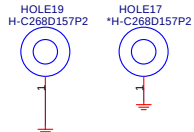
FOR M/B HOLE



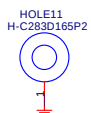
CHECK
THIS

CHECK
THIS

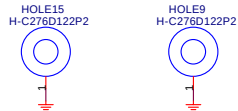
FOR MODEM MODULE



FOR CPU HEATSINK



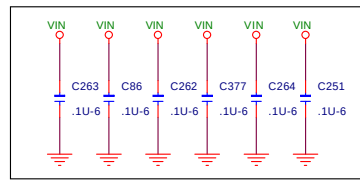
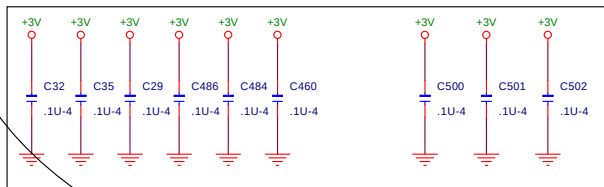
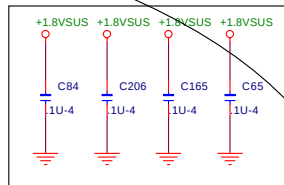
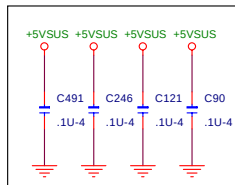
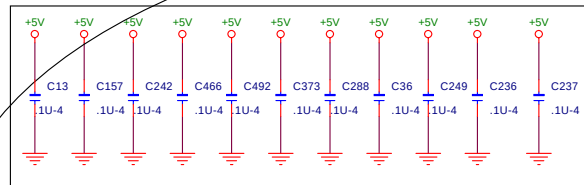
NEAR NORTHBRIDGE



08/23

4/8 ALL EMI C ADD

3/15 EMI ADD



3/15 EMI ADD

4/6 ADD EMI 3 pcs

4/8 CHANGE TO 0603 SIZE 25V



QUANTA COMPUTER

HW Engineer :

Title

HOLES

Size :

Document Number :

Date :

Saturday, July 09, 2005

Sheet

20

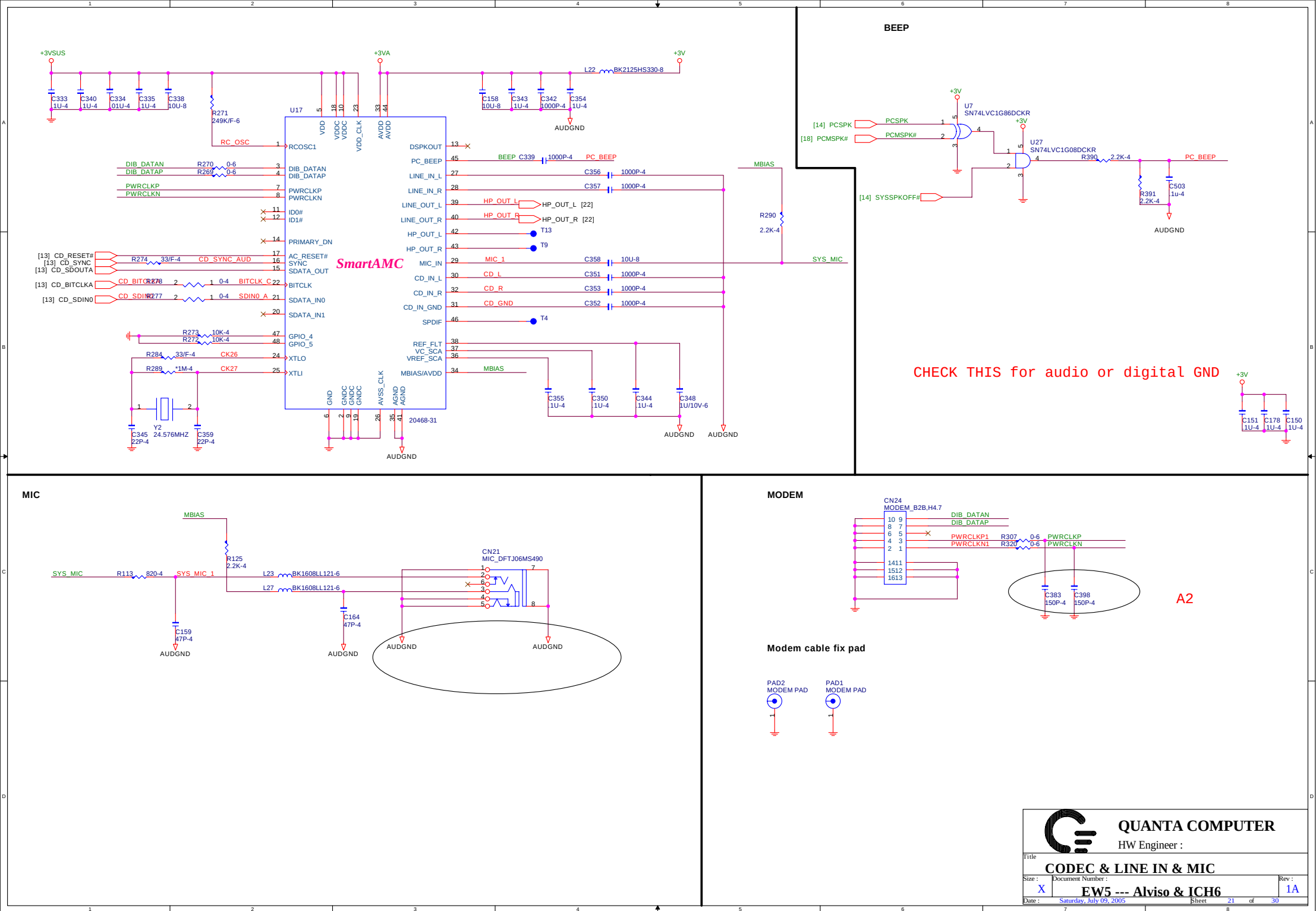
of

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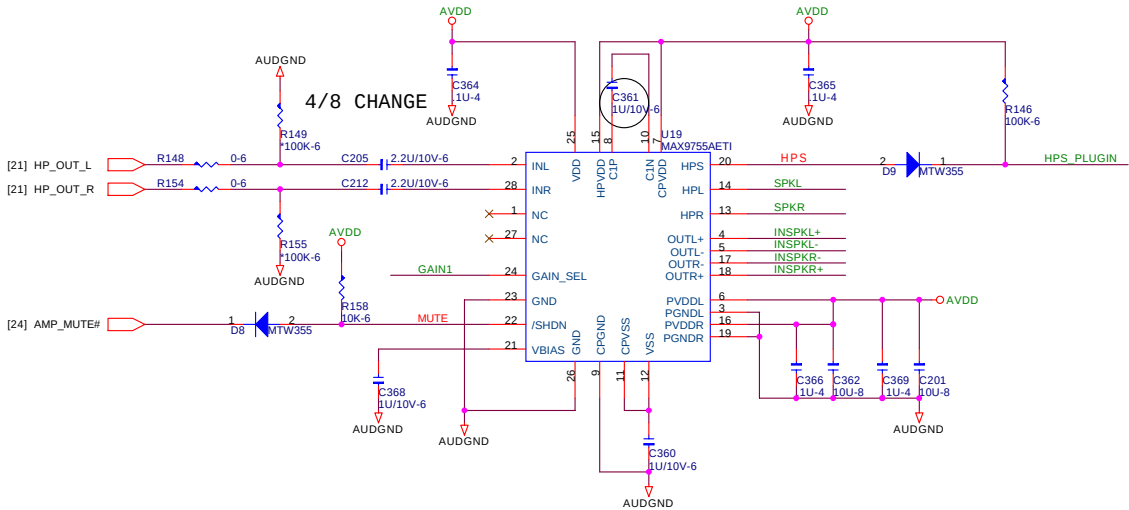
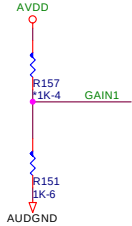
Rev :

1A

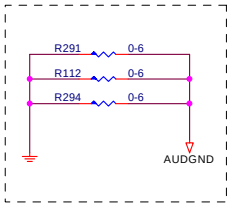
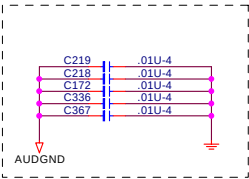
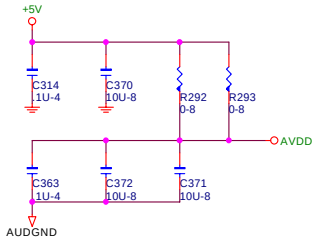
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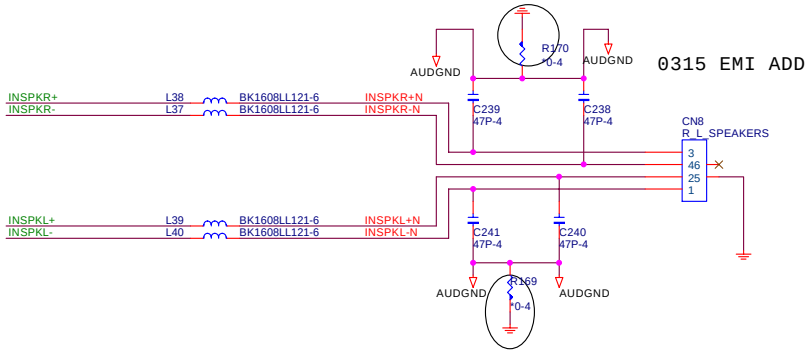
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



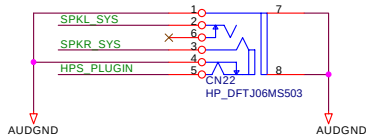
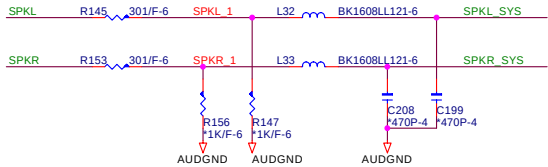
AMP POWER



SPEAKER CON.



LINE-OUT





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AUDIO AMP

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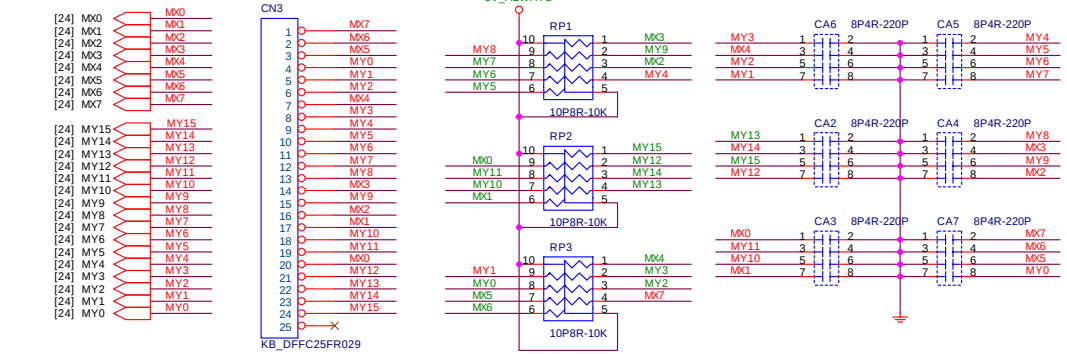
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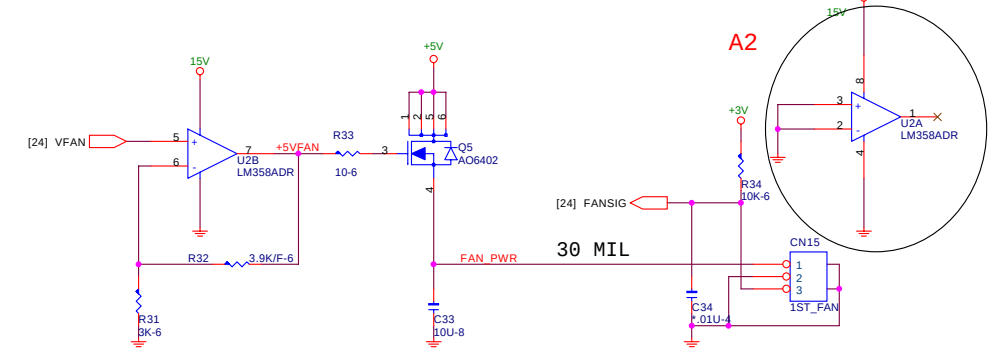
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INT K/B

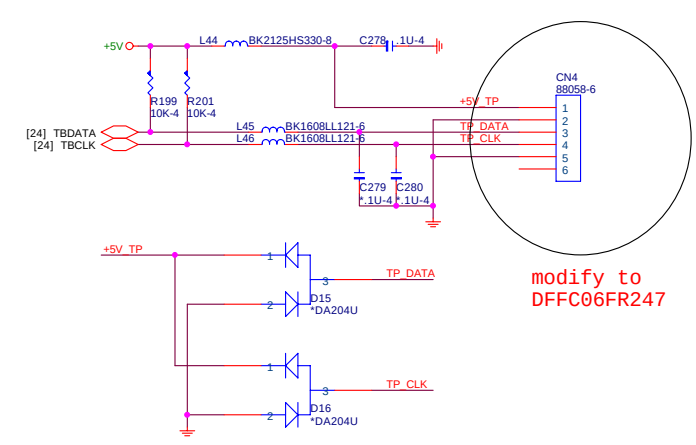


FAN CONTROL

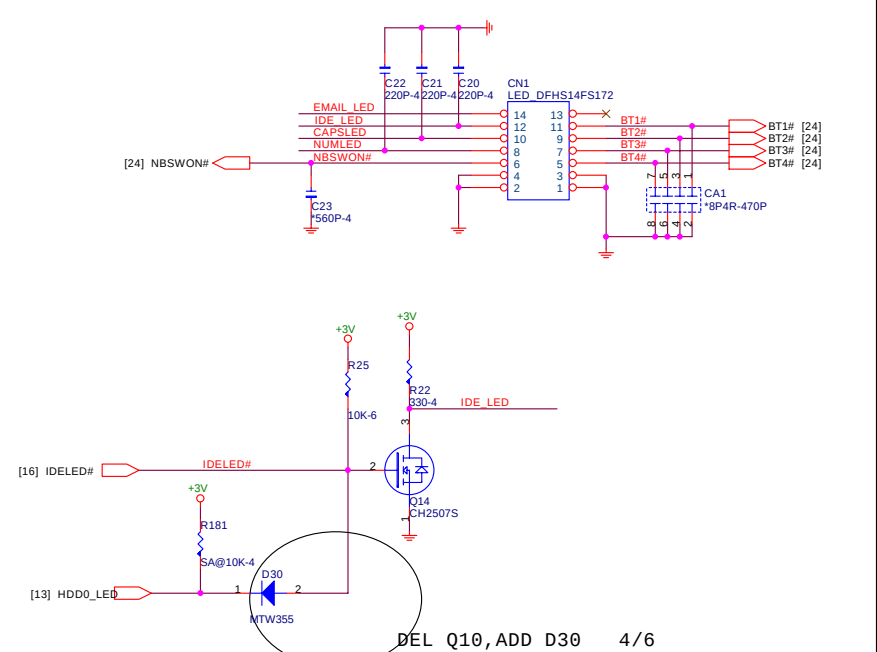
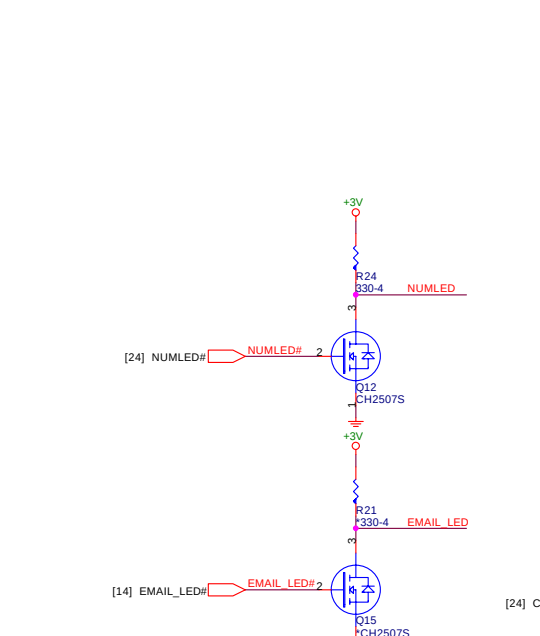
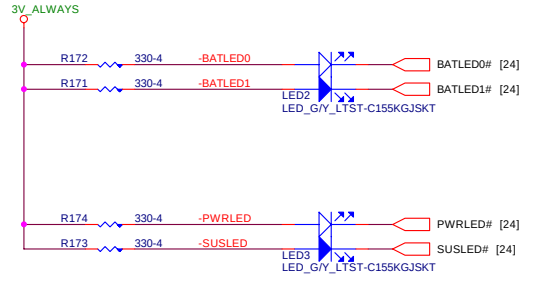
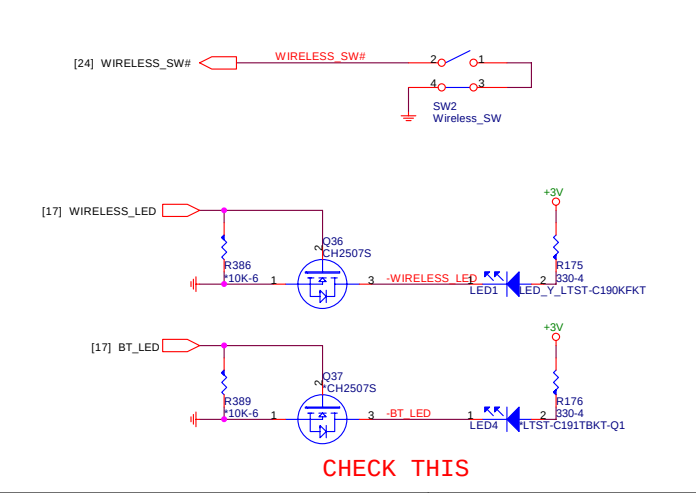


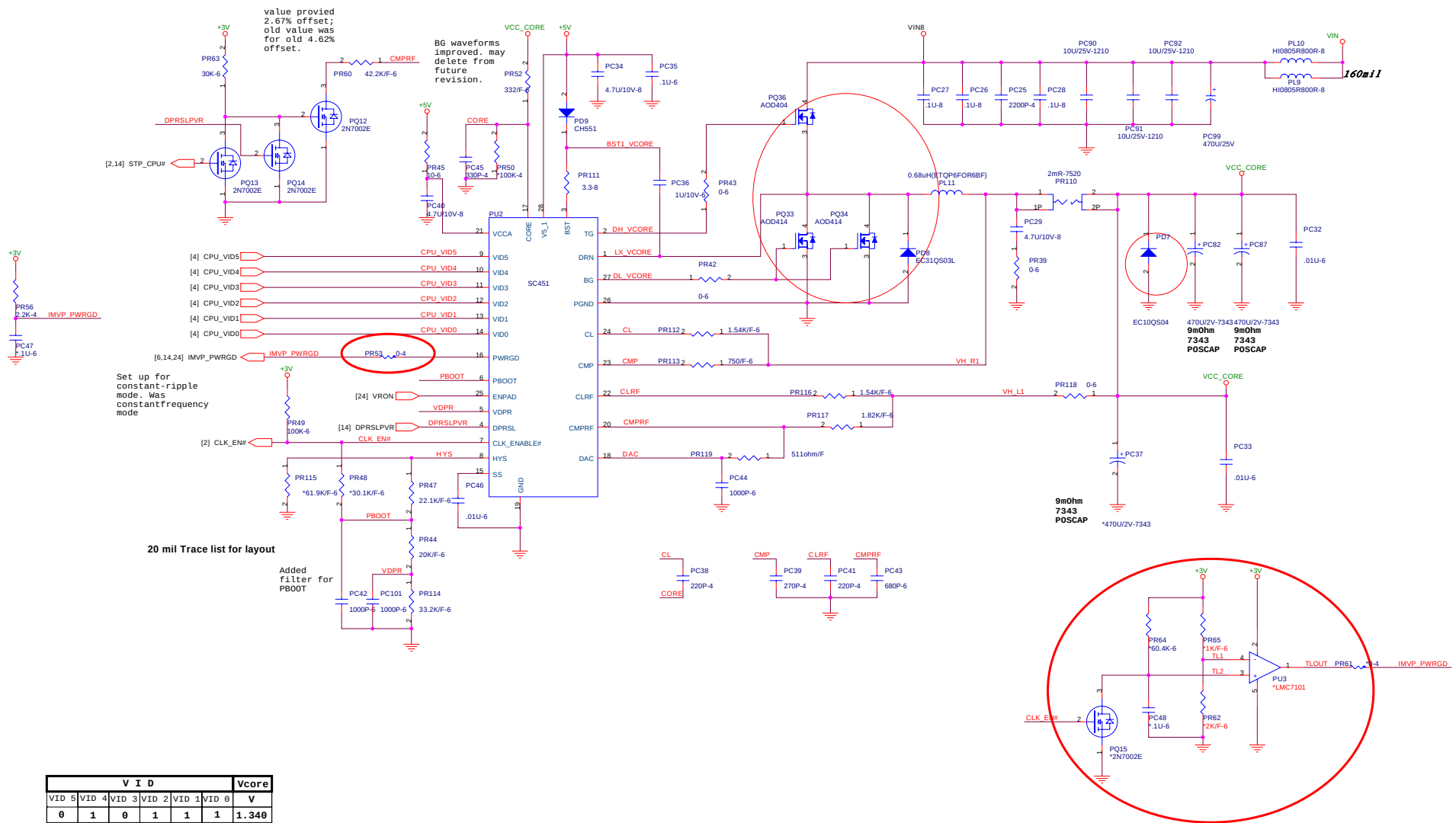
TOUCH PAD

20 MIL



WIRELESS SWITCH

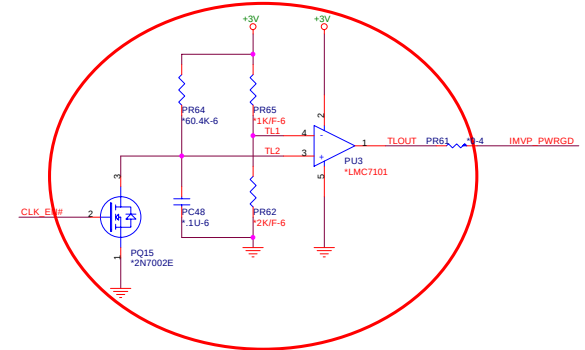
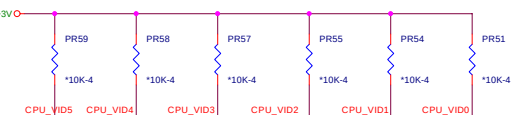


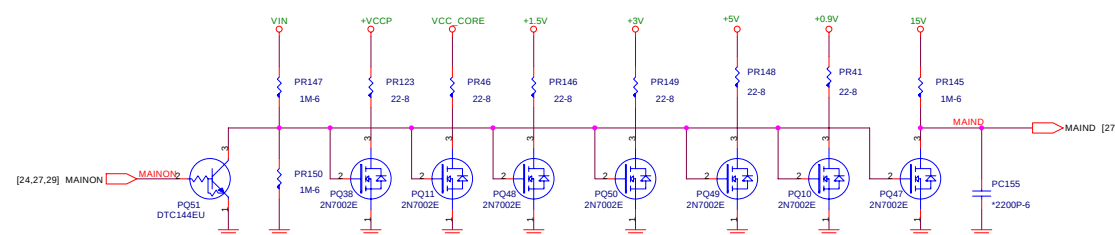
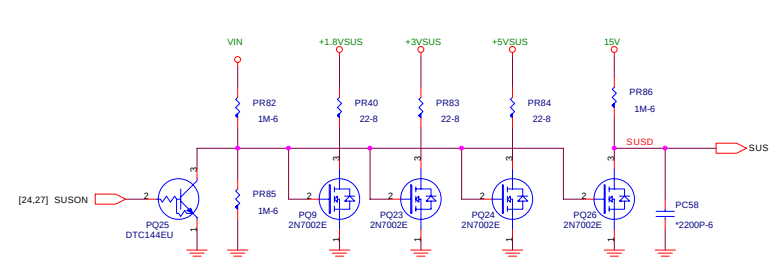
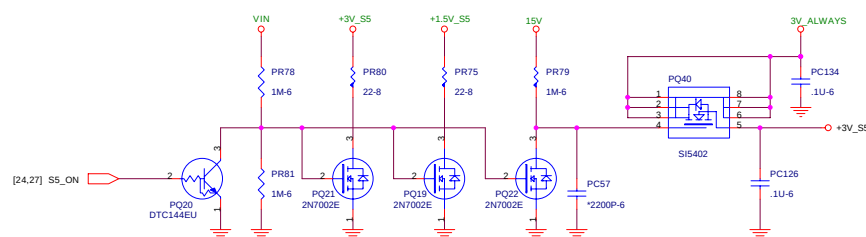
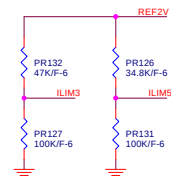


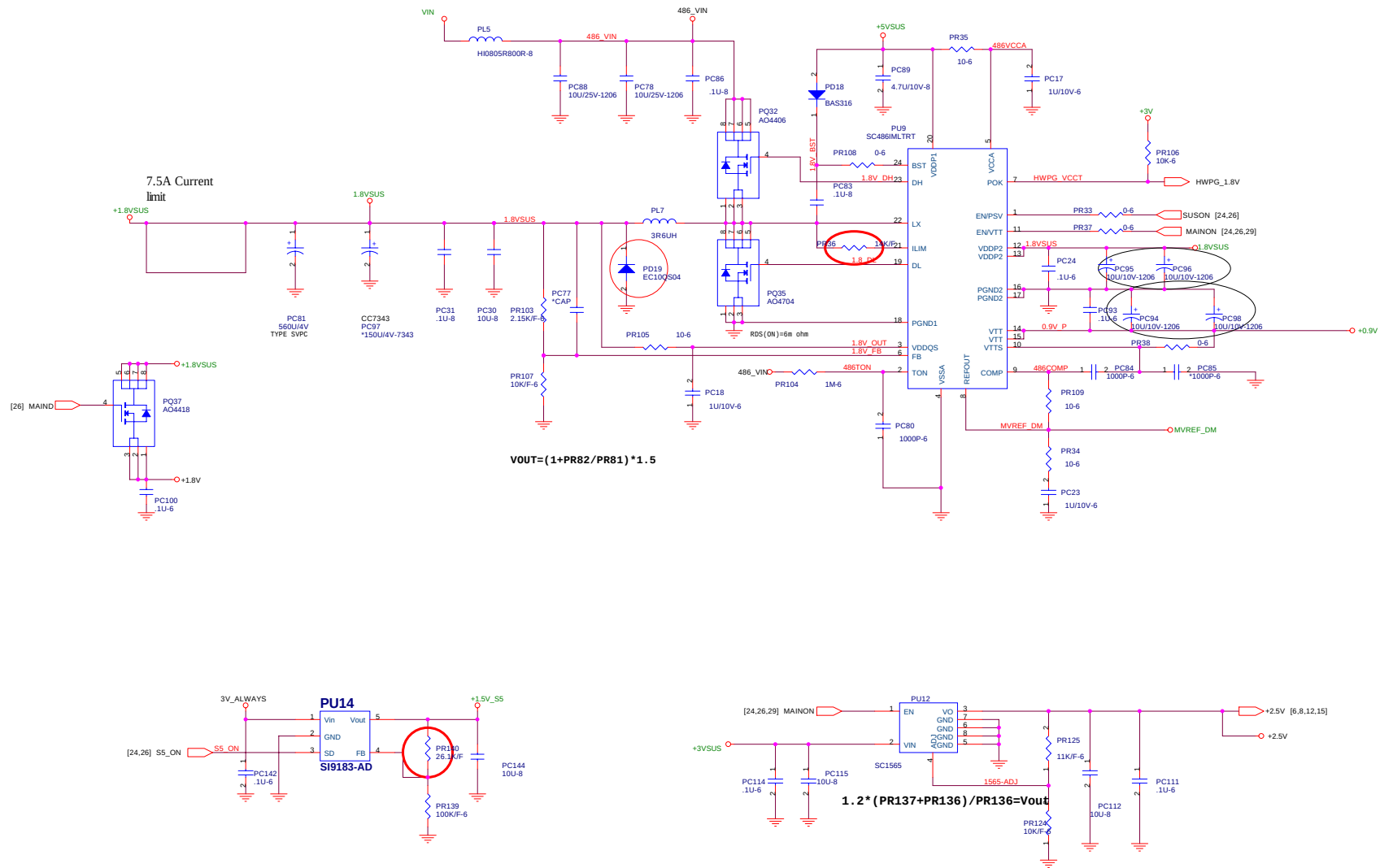
V I D								Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0			V
0	1	0	1	1	1	1	1	1.340
0	1	1	0	0	0	0	0	1.324
0	1	1	0	1	0	0	0	1.292
0	1	1	1	0	0	0	0	1.260
0	1	1	1	0	1	1	1	1.244
0	1	1	1	1	1	1	1	1.212
1	0	0	0	0	0	1	1	1.180
1	0	0	0	0	1	1	1	1.148
1	0	0	1	1	0	1	0	1.100
1	0	1	0	0	1	0	1	1.052
1	0	1	0	1	1	1	1	1.020
1	0	1	1	1	0	0	0	0.972
1	1	0	0	0	0	0	0	0.940

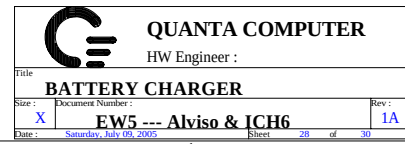
DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

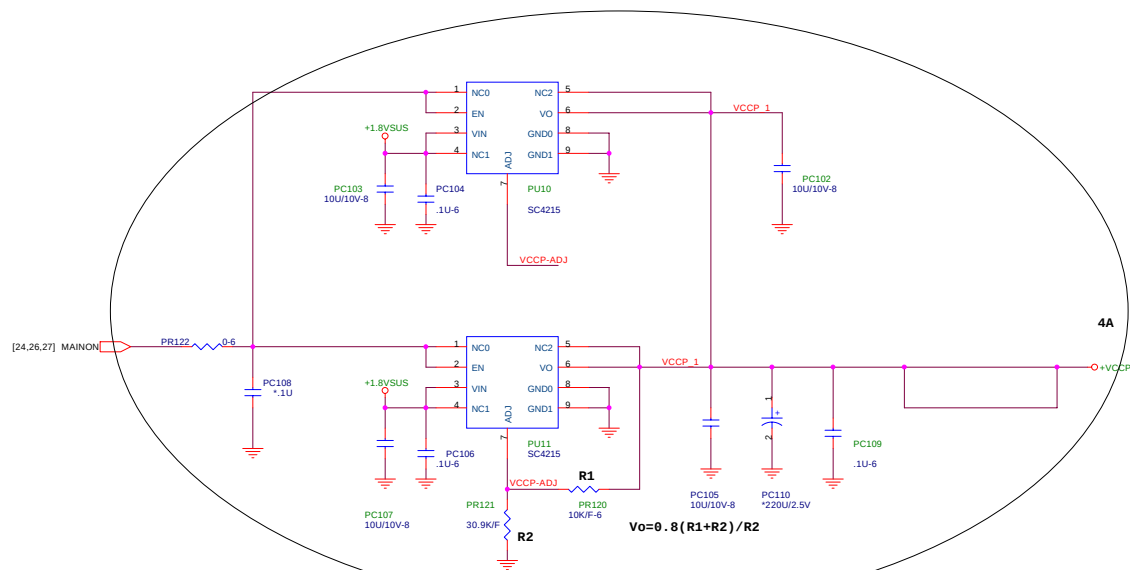
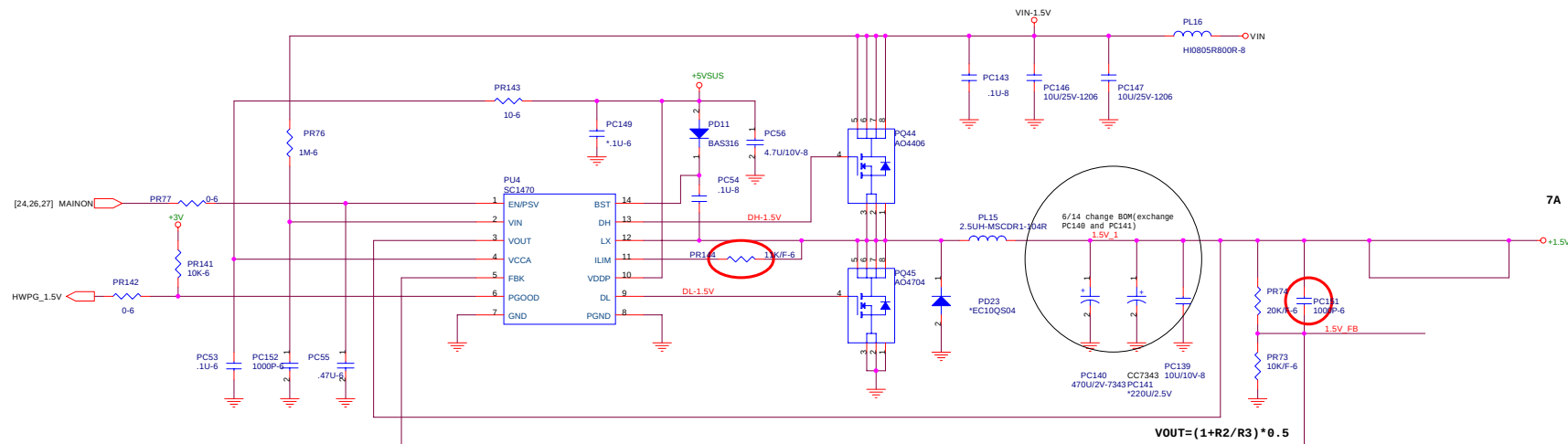
10 mil Trace list for layout
SC1476
pin 4 pin
5 pin 7
pin 25
pin 30











QUANTA COMPUTER

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+1.5V / CPU10

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DATE_VER.

MODIFIED BY

CHANGE LIST

2005/3/7_REVA2

First Version

1. change C201 470U/2.5V-7343,C205 220U/2.5V-3528 location and values
2. Change C541, C542 to 150p to fixed modem can't dial question
3. Change C533,C534 from 1U to 2.2U to fixed Audio measurement fail issued
4. ADD C570 10U to fixed system hard to power on, and change R252 to 1K
5. Remove R241, R243 , and Add R85
6. Add RP11 for internal LVDS
7. Change DDR2 connector to PI
8. Remove Q27, Q23, VGA PWRGD
9. Change battery charger and VCCP schematic.

LAYOUT Notice

1. modem must keep the safety region
2. move under CPU side CAP, to meet the ME request.
3. remove C201,C205 to meet the ME request
4. routing USB signals to avoid EMI risk

2005/6/16_REVC

- 1.Add debug card schematic at MINI PCI for C stgae
Add R396,R397,R398,R399,R400,401,R402,R403 location, but don't SMT
- 2.Change Q22 pull high voltage to ICH_POROK to avoid system can't power on risk , and Change thermal IC to ADM1032ARZ-1
- 3.Change 3V_ALWAYS PC123 to 560U, EC side add C506 150U
- 4.For RTC testing fail , to change C413,C414 to 18P
- 5.For ME request change BOM PC141 to PC140.
- 6.For LID wakeup function , add C505 .1U to GND
- 7.For EC X'TAL can't work fine issued, change R364 values to 68k
- 8.move PAD1, PAD2 to right location.
- 9.Change LCD connector footprint (asked paggy to offer footprint)
- 10.Touch PAD click nosie too large, change switch to 100g pressure.
11. Add 3V_ALWAYS trace width to 120mil.
- 12.SM BUS signals trace width to 10 mil.
- 13.remove R185
- 14.Change Q2,Q3,Q4,Q11,to BSS138LT1



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