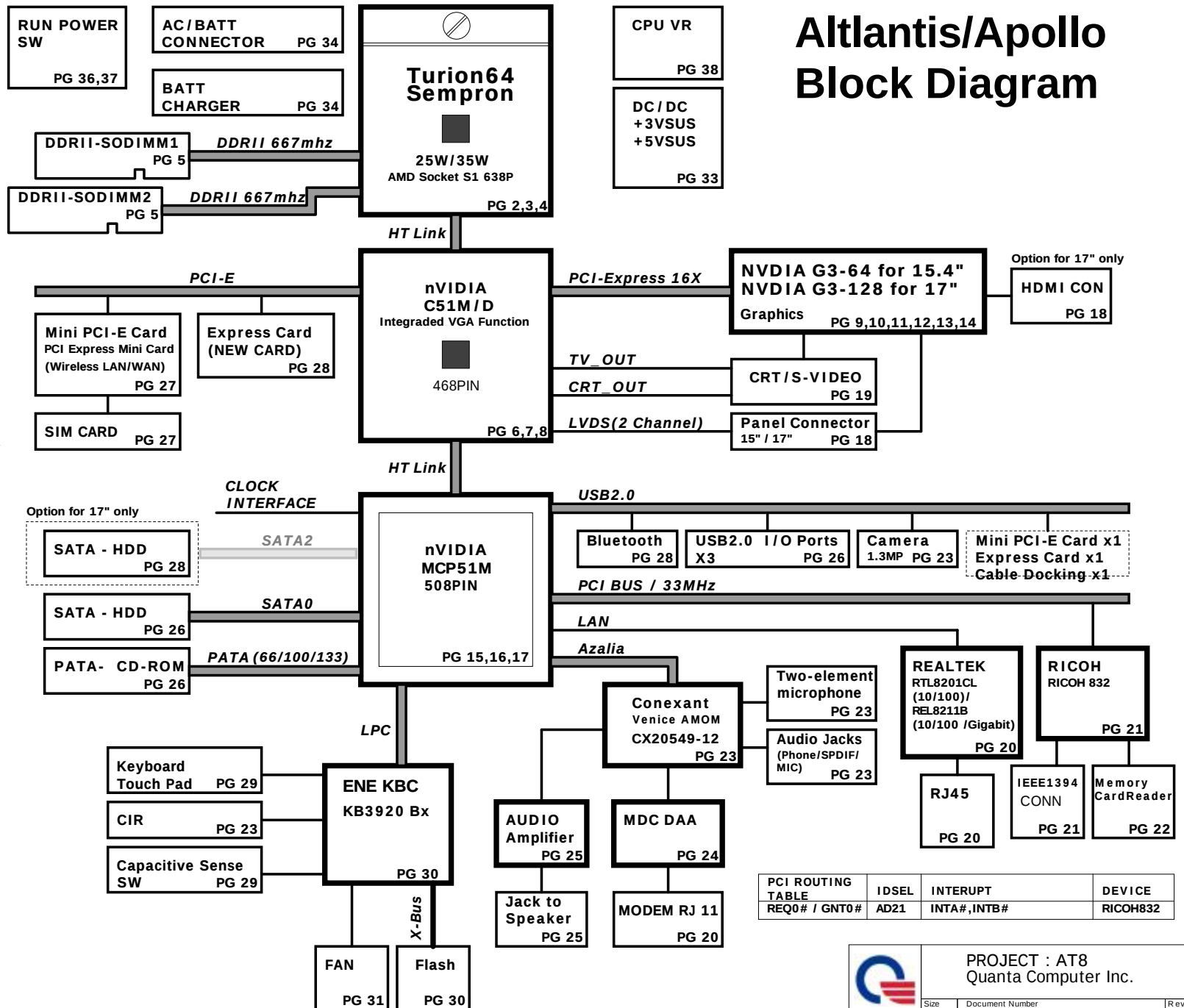


PCB STACK UP

LAYER 1 : TOP
LAYER 2 : SGND1
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : IN3
LAYER 7 : SGND2
LAYER 8 : BOT

Cable Docking
TV_OUT
VGA
RJ-45
CIR/Pwr btn
SPDIF Out
Stereo MIC
Headphone Jack
USB Port
VOL Cntr
PG 31

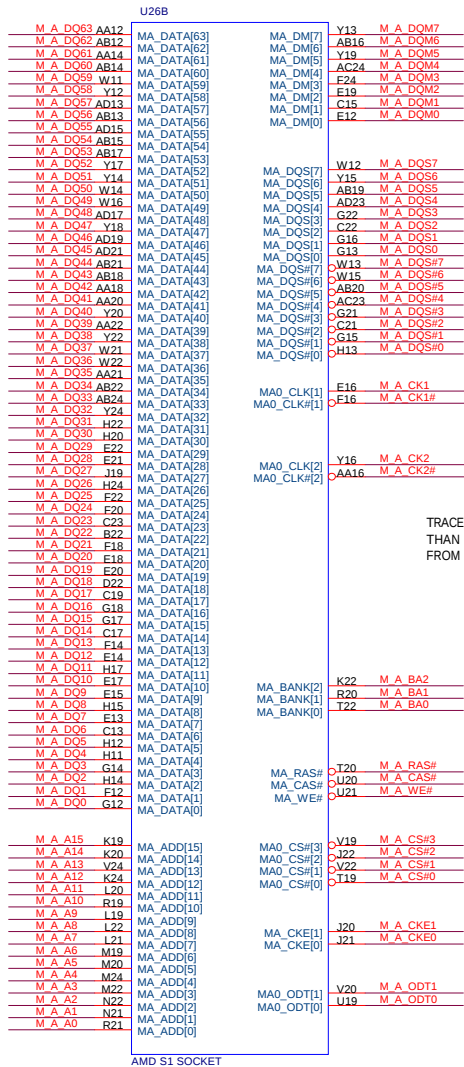
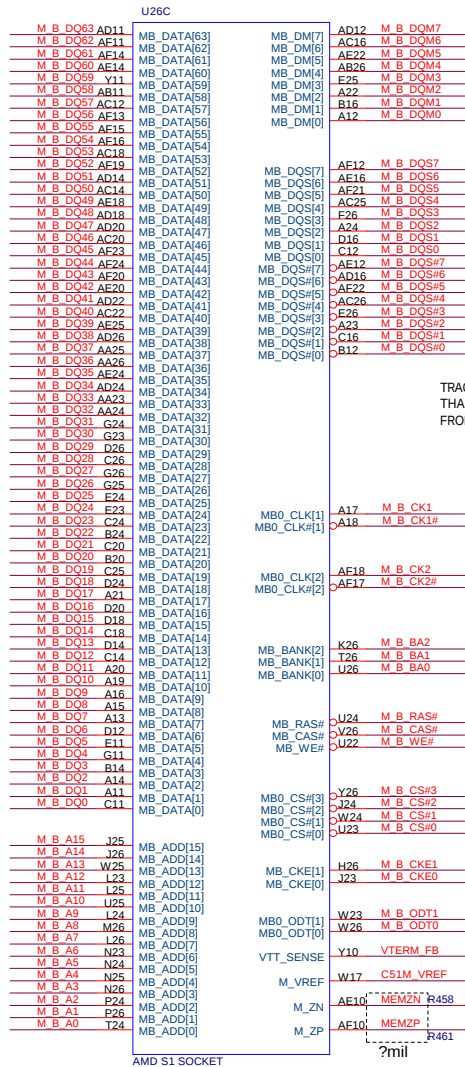


VAULE DEFINE
A=0603,B=0805,C=1206,F=1%,
OTHER IS 0402

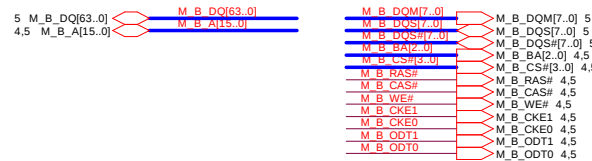
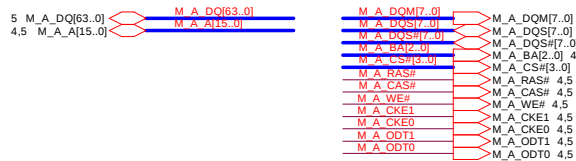
EXAMPLE
10R=10ohm(0402)
10A=10ohm(0603)
10B=10ohm(0805)
10C=10ohm(1206)
10/F=10ohm(0402 and 1%)

PCI ROUTING TABLE REQ0# / GNT0#	IDSEL AD21	INTERUPT INTA#,INTB#	DEVICE RICOH832
---------------------------------------	---------------	-------------------------	--------------------

 NB5/RD2/HW1	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number Block Diagram	Rev 1A
Date: Thursday, December 29, 2005 Sheet 1 of 38			

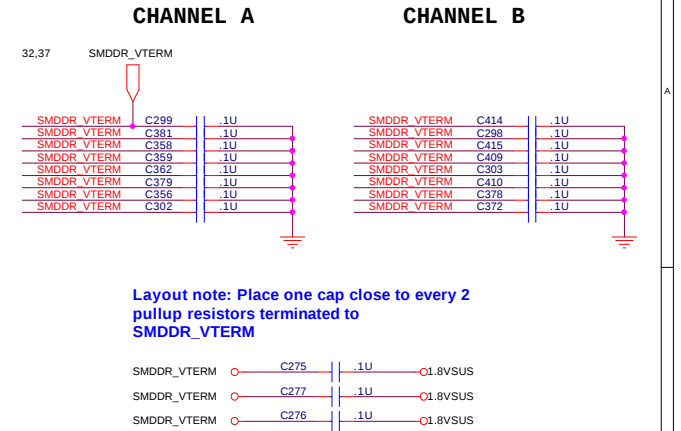
Tolerance is
+/-10%TRACE FROM CAP TO CPU MUST BE LESS
THAN 1200MILS MAX NECKDOWN TO &
FROM CAPS IS 500MILSTRACE FROM CAP TO CPU MUST BE LESS
THAN 1200MILS MAX NECKDOWN TO &
FROM CAPS IS 500MILSTolerance is
+/-10%

C51MVREF : W = 20MIL AND SPACE = 20MIL

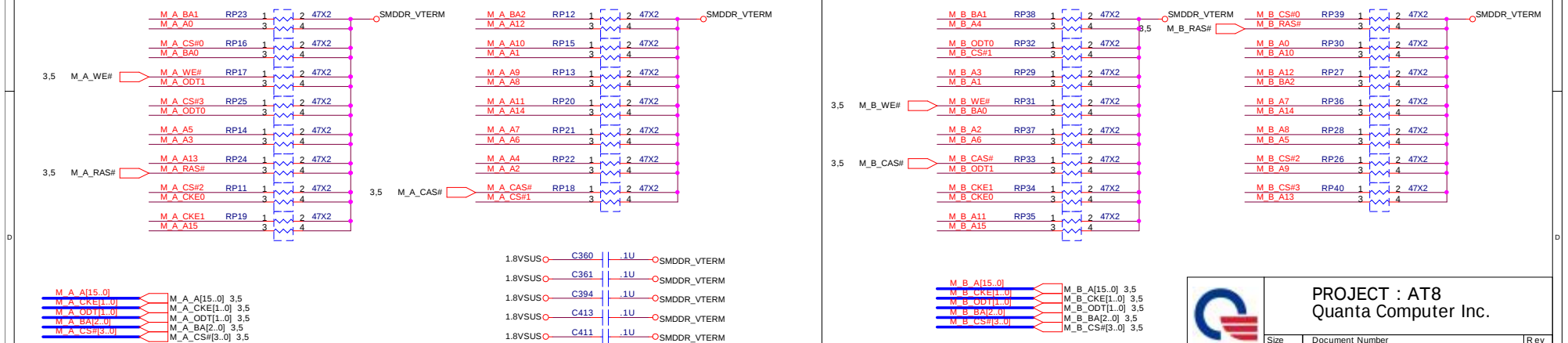
PROJECT : AT8
Quanta Computer Inc.

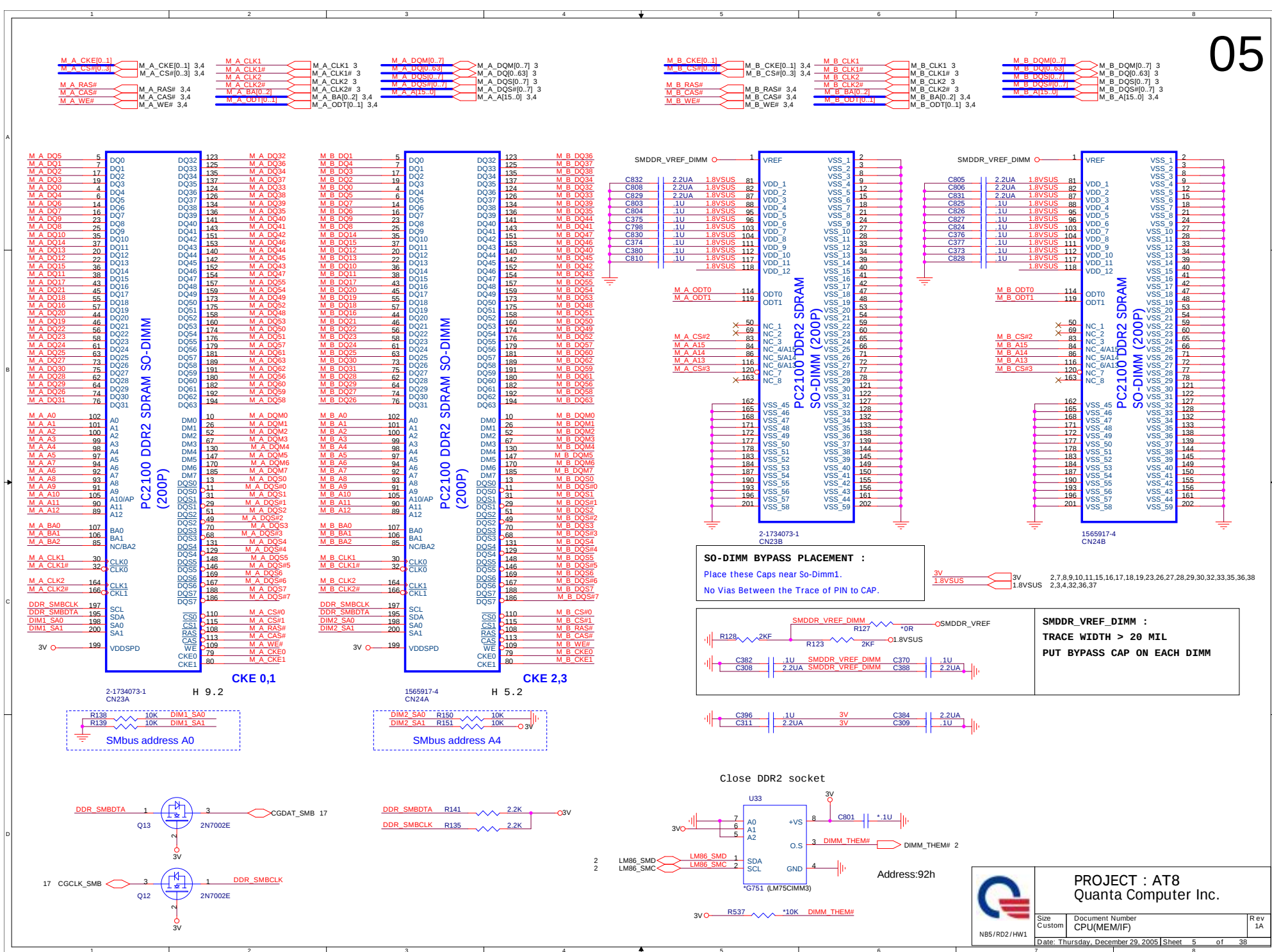
Size Custom	Document Number CPU(MEM)/F	Rev 1A
Date: Thursday, December 29, 2005	Sheet 3 of 38	

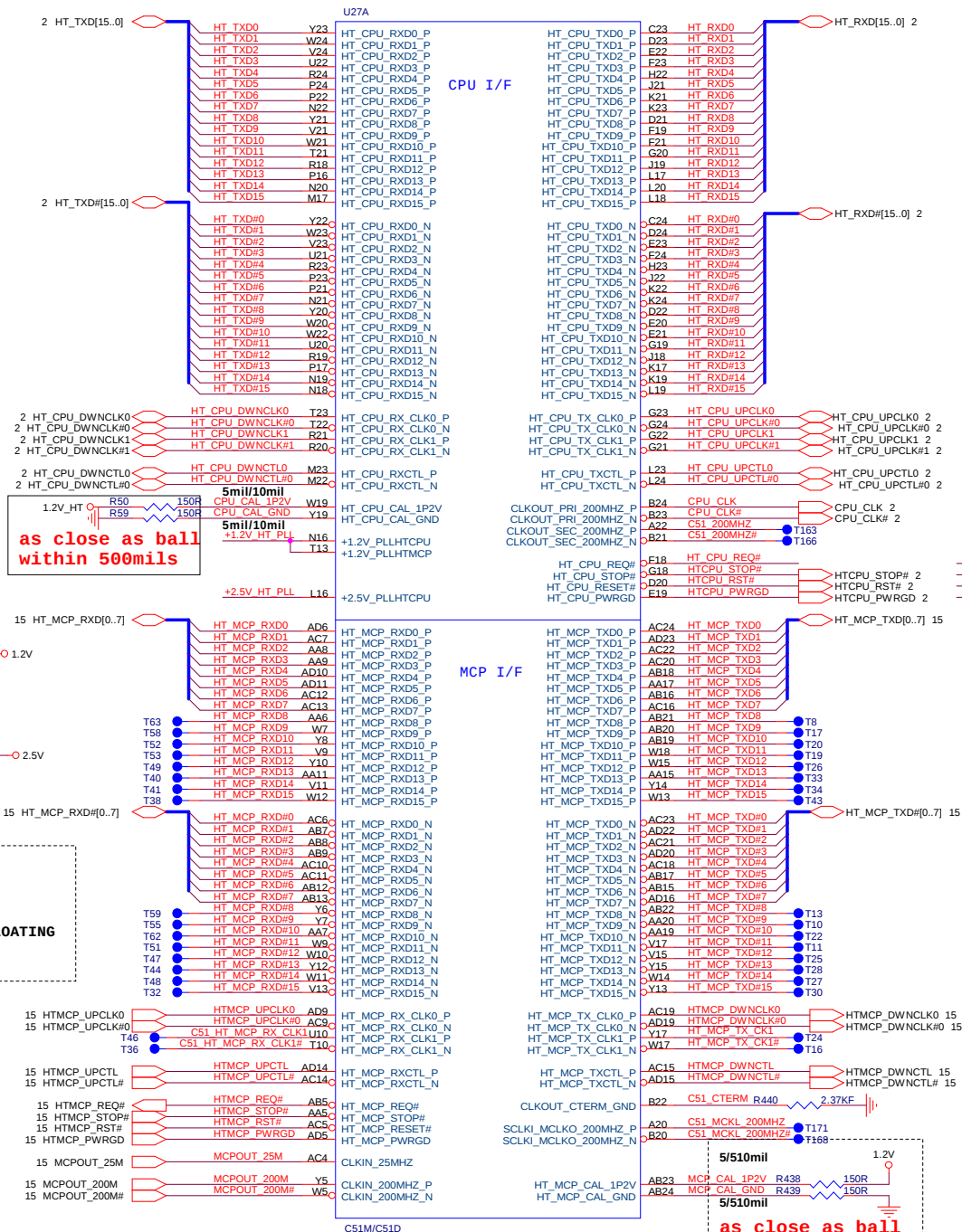
04



DDRII CHANNEL B TERMINATION







```
#note from nv design
guide
```

Pull-Hi for rise time

	CINC
--	------

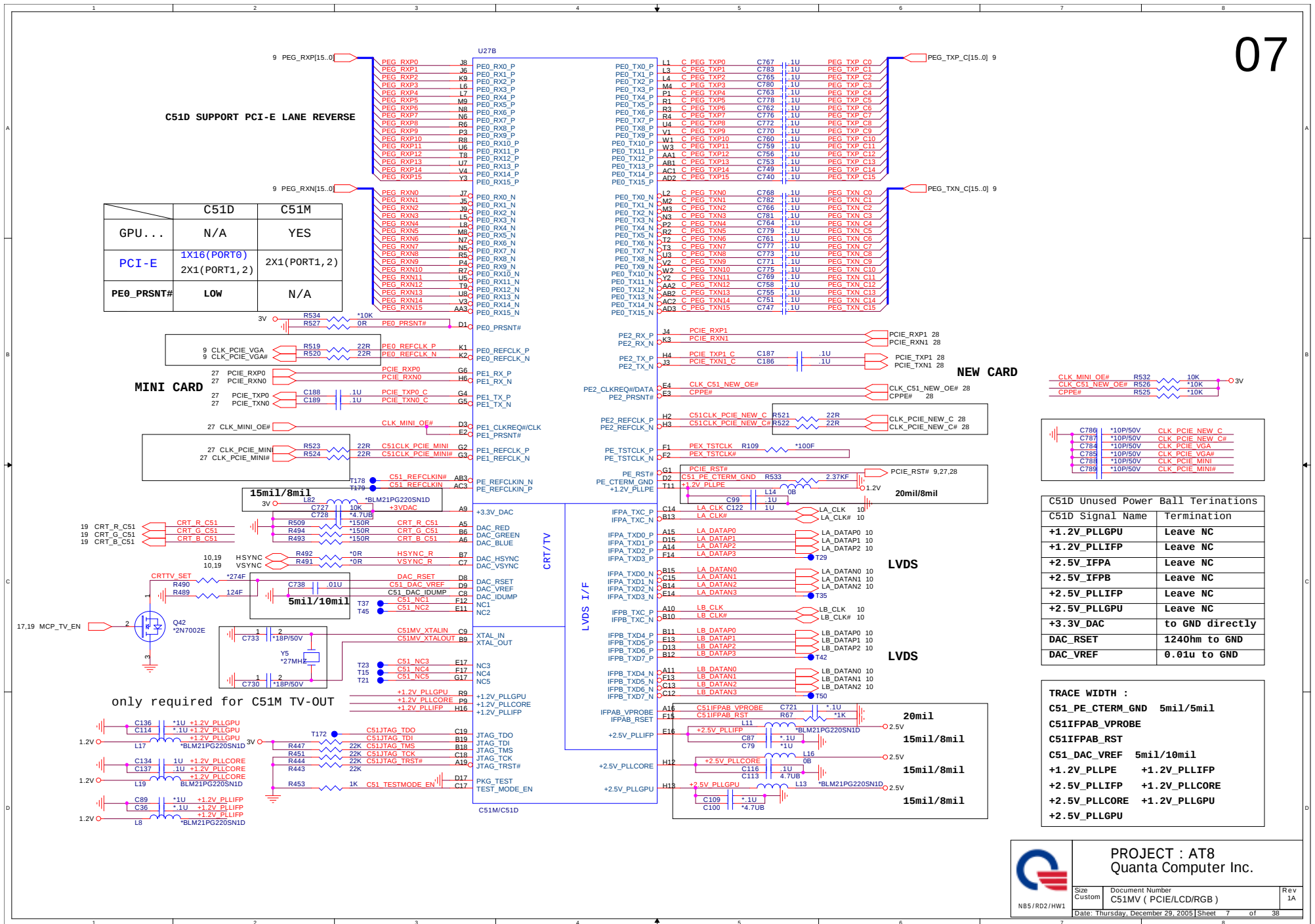
STUFF : 4X4 HT LINK/UNUSED PIN FLOATING
NON-STUFF : 8X8 HT LINK(DEFAULT)

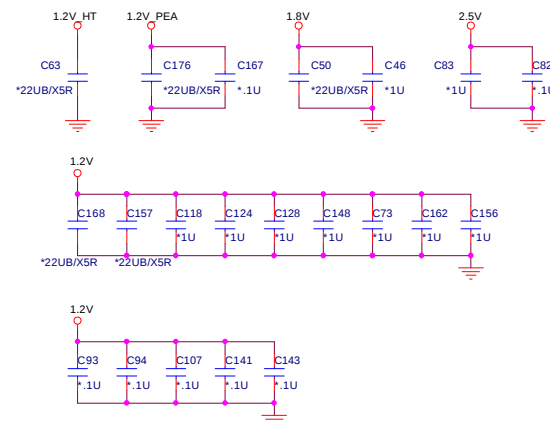
as close as ball
within 500mils



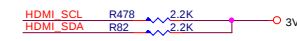
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number C51MV (HT LINK)	Re 1
Date: Thursday, December 29, 2005	Sheet 6 of 38	



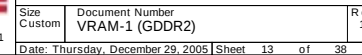


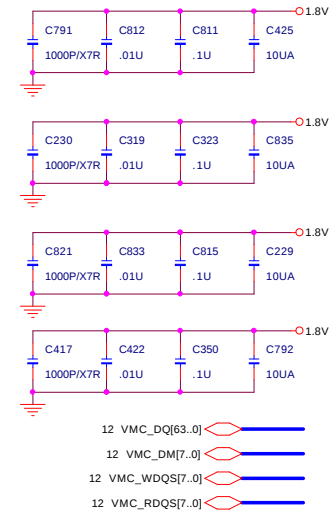
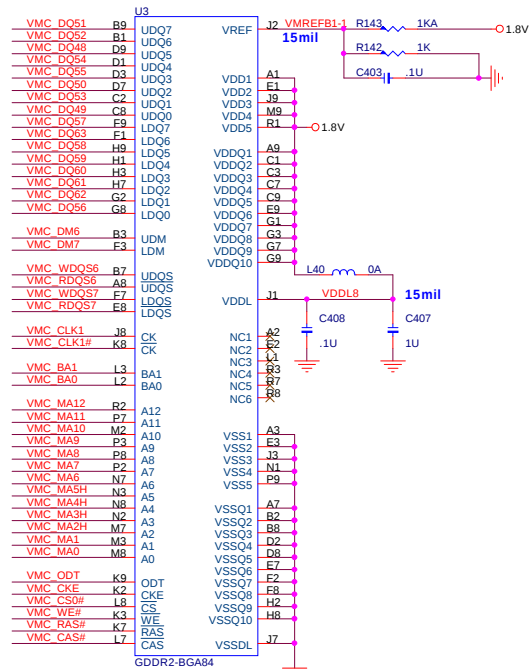
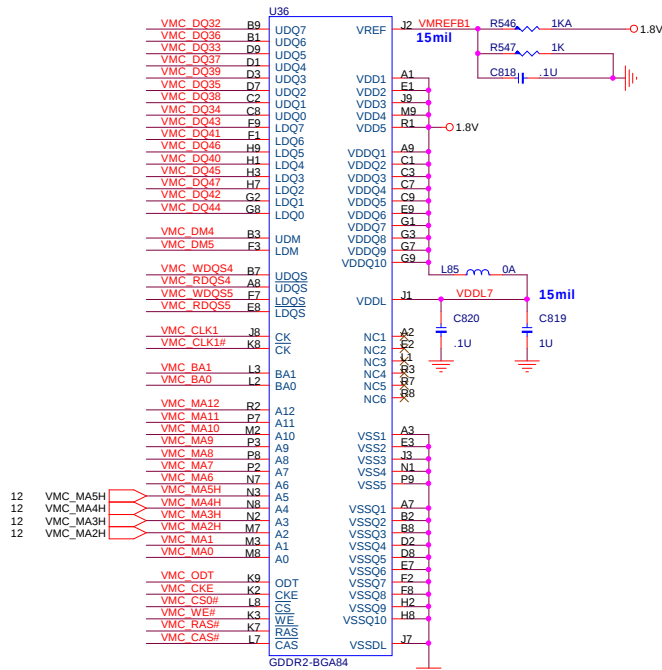
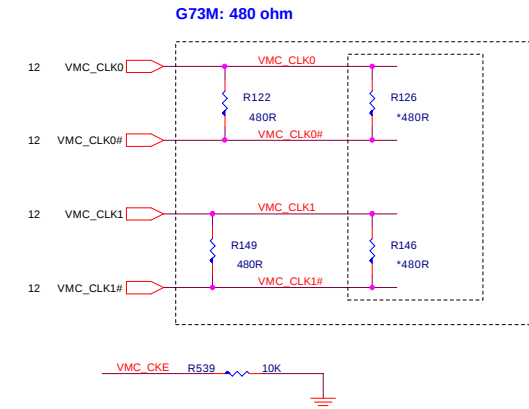
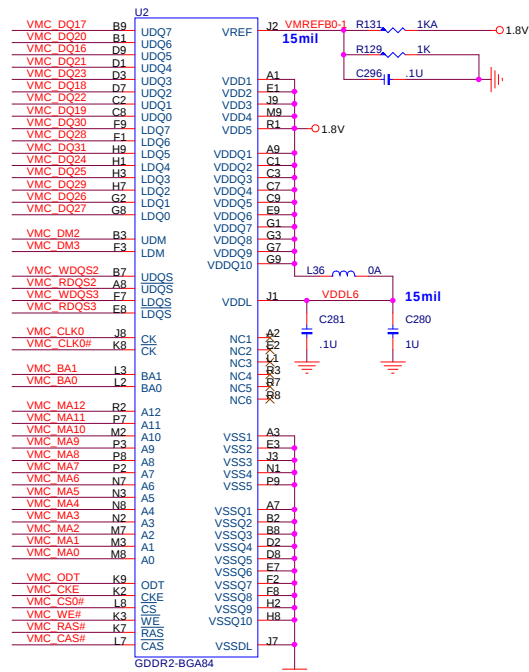
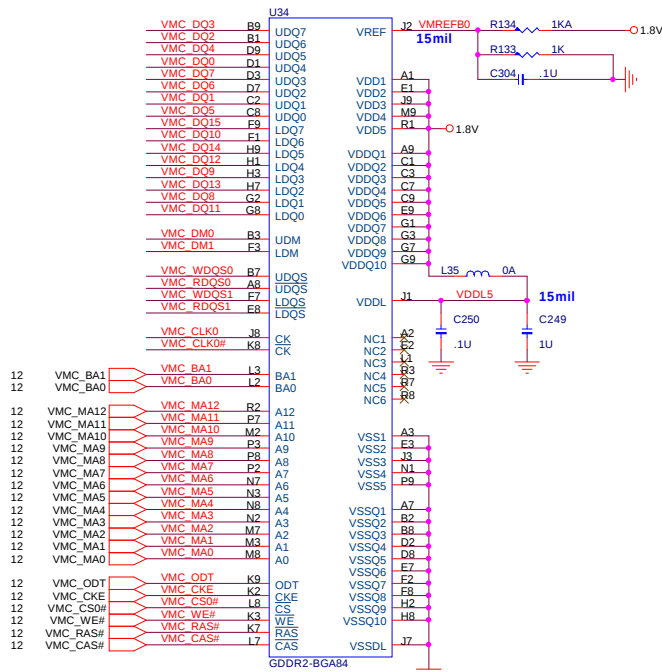










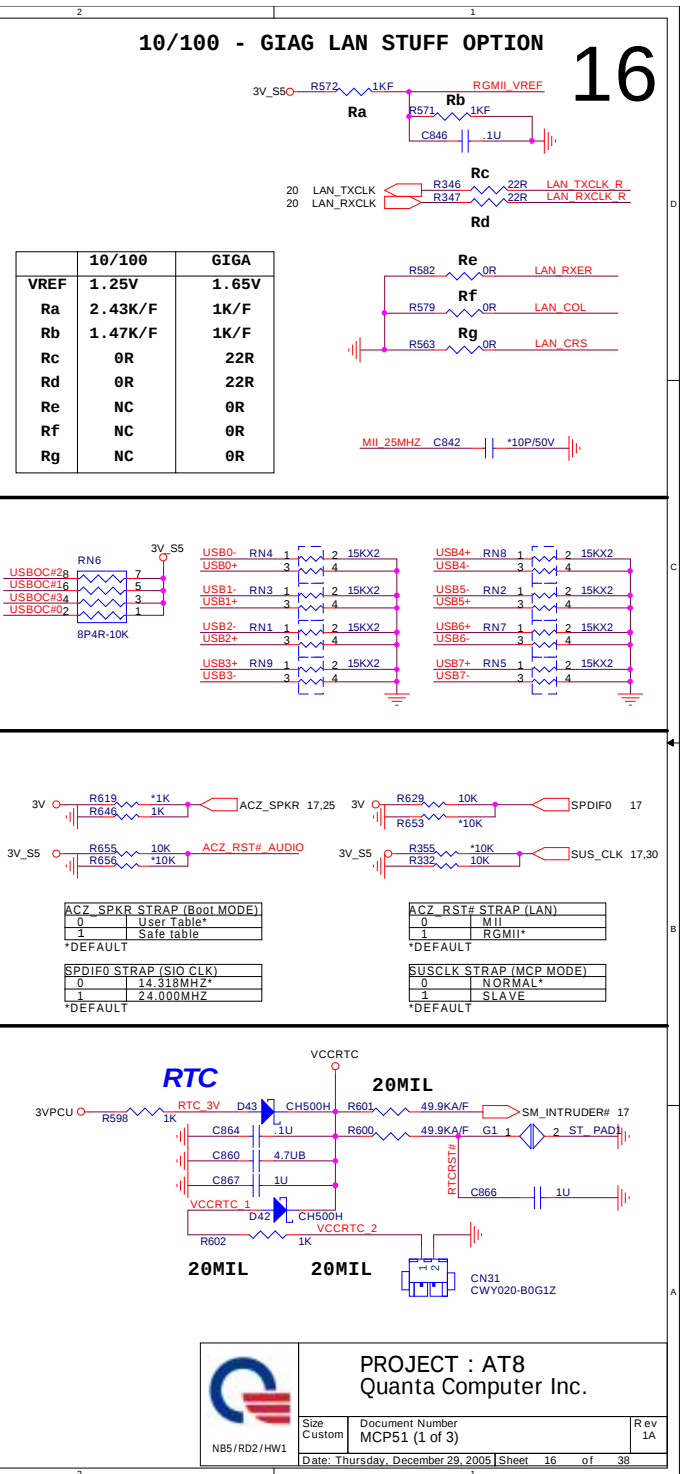
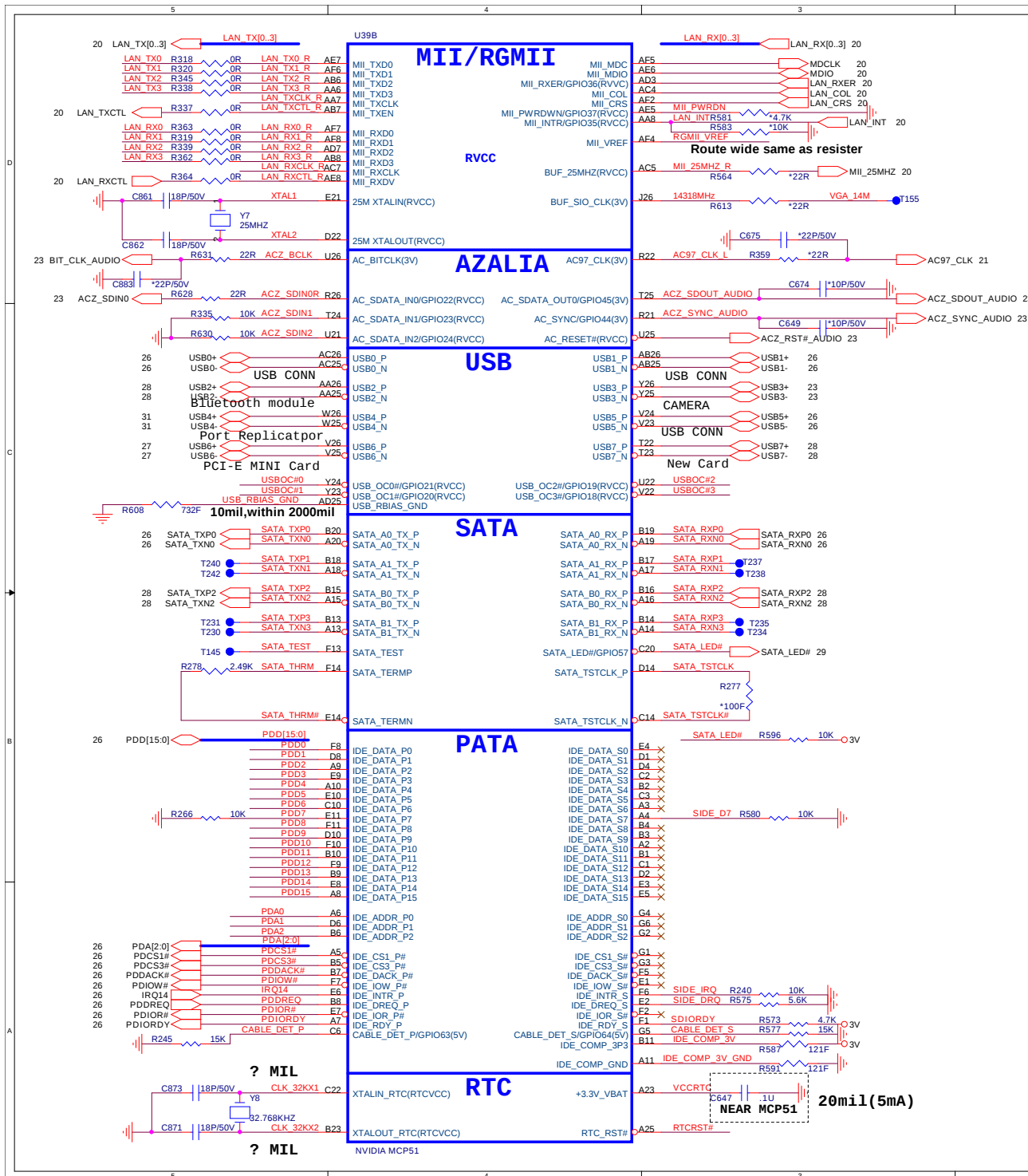


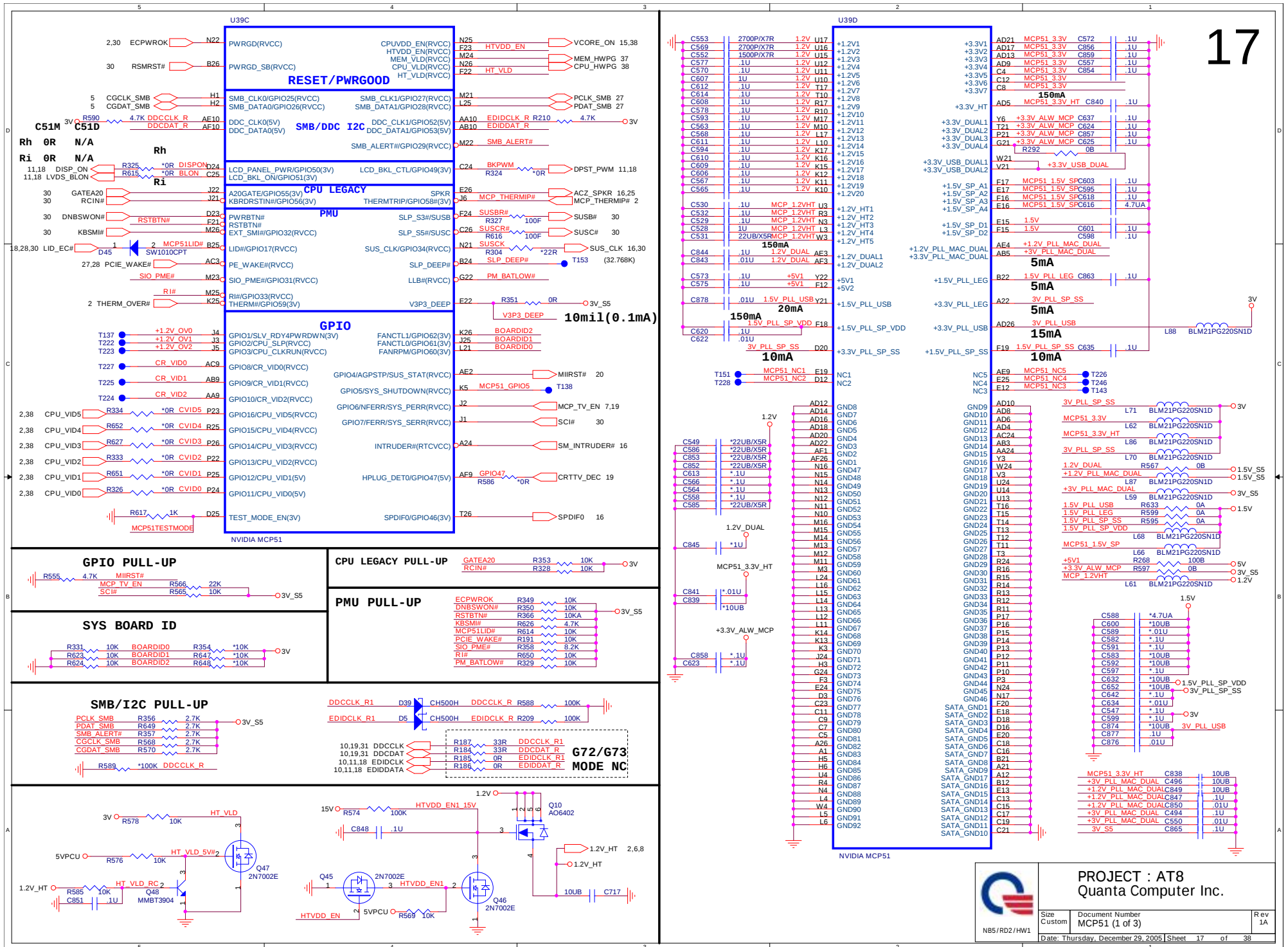
256Mb : AKD5JGAT*05
512Mb : AKD59G-T*01



PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number VRAM-2 (GDDR2)	Rev 1A
Date: Thursday, December 29, 2005	Sheet 14 of 38	



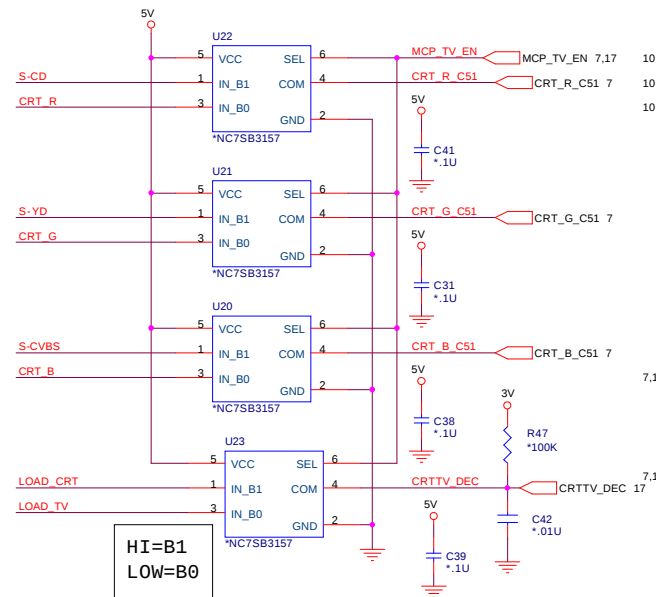




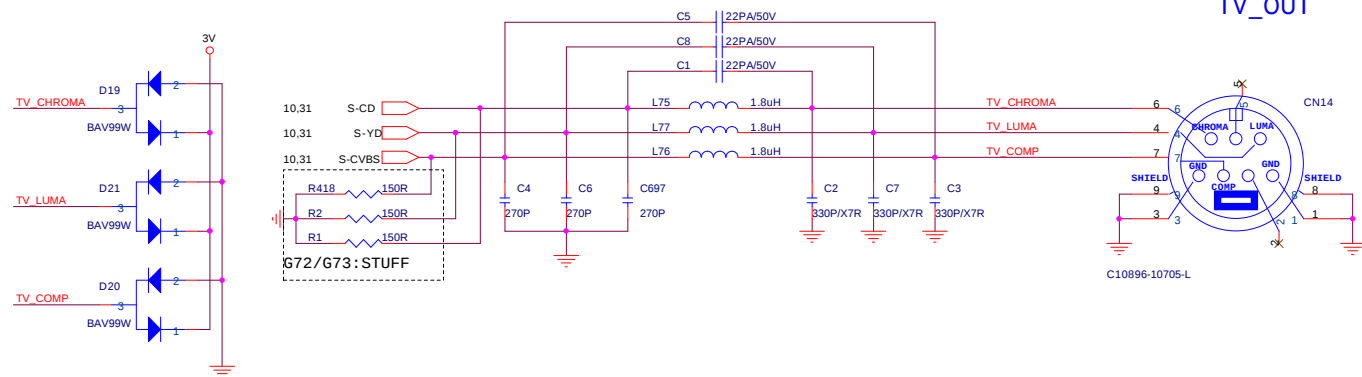
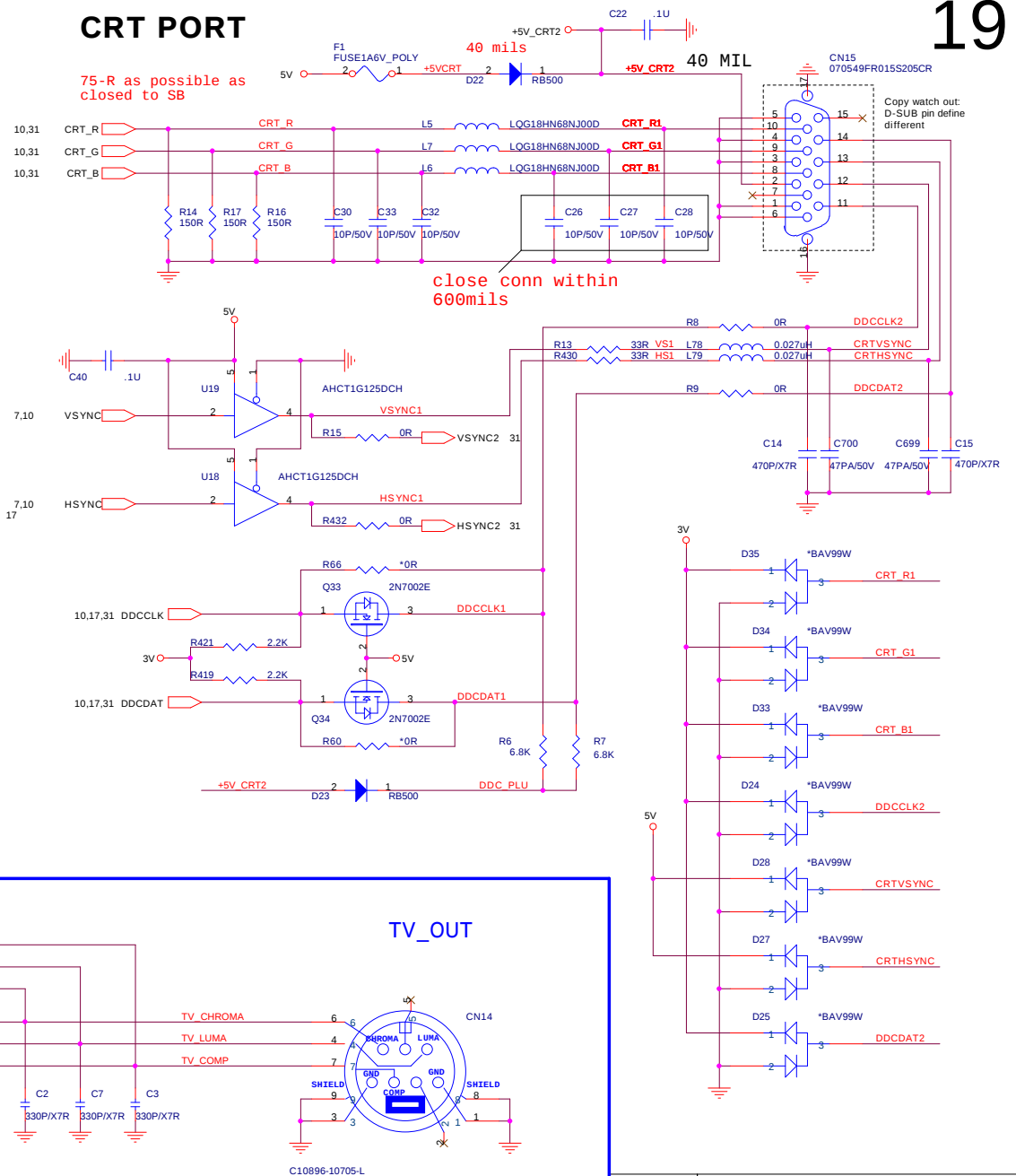
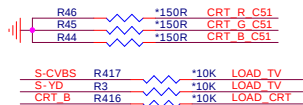
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number LCD CONN,HDMI CONN	Re 1
Date: Thursday, December 29, 2005		Sheet 18 of 38

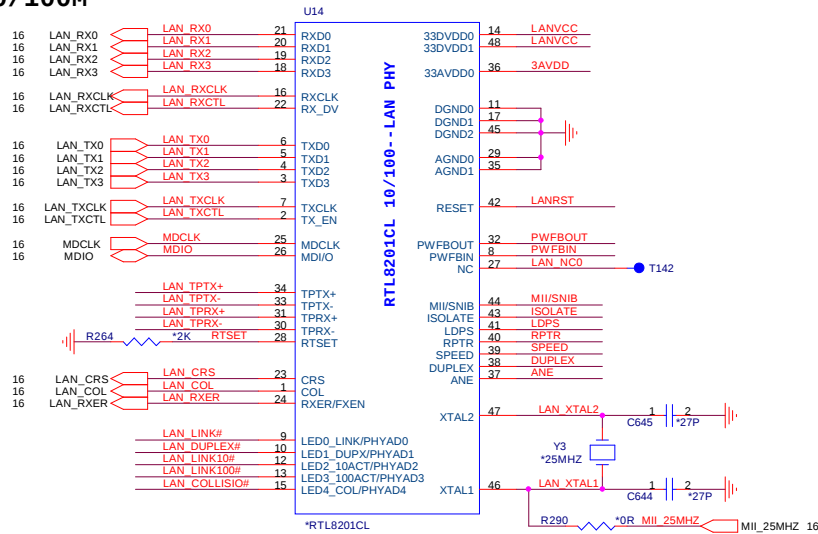
CRT PORT



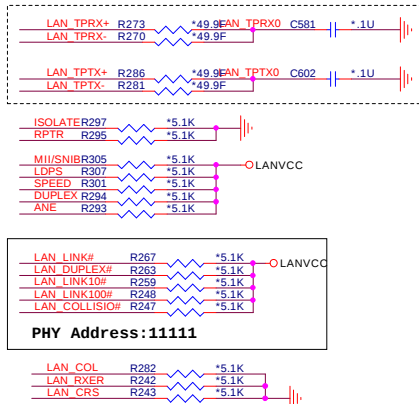
close within
600mils



10/100M



Close To RTL8201CL

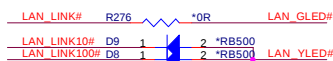
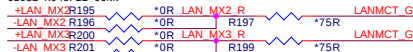


LAN_COL	1		RTL8201BL LED
	0	Default	RTL8201CL LED
LAN_RXER_R	1		Fiber Mode
	0	Default	UTP Mode
LAN_CRS	0	Default	Ensure operating at normal mode

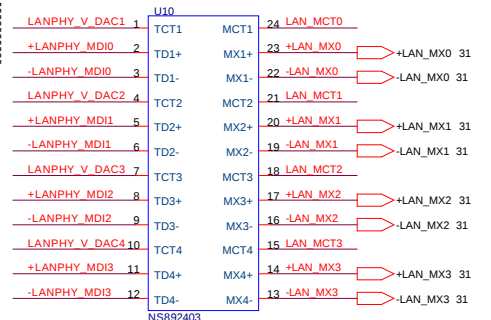
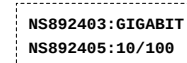
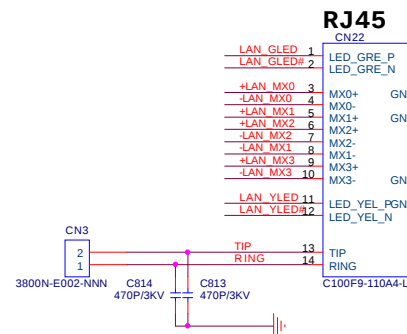
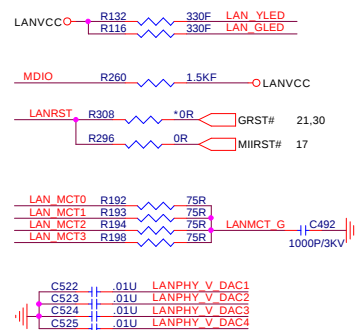
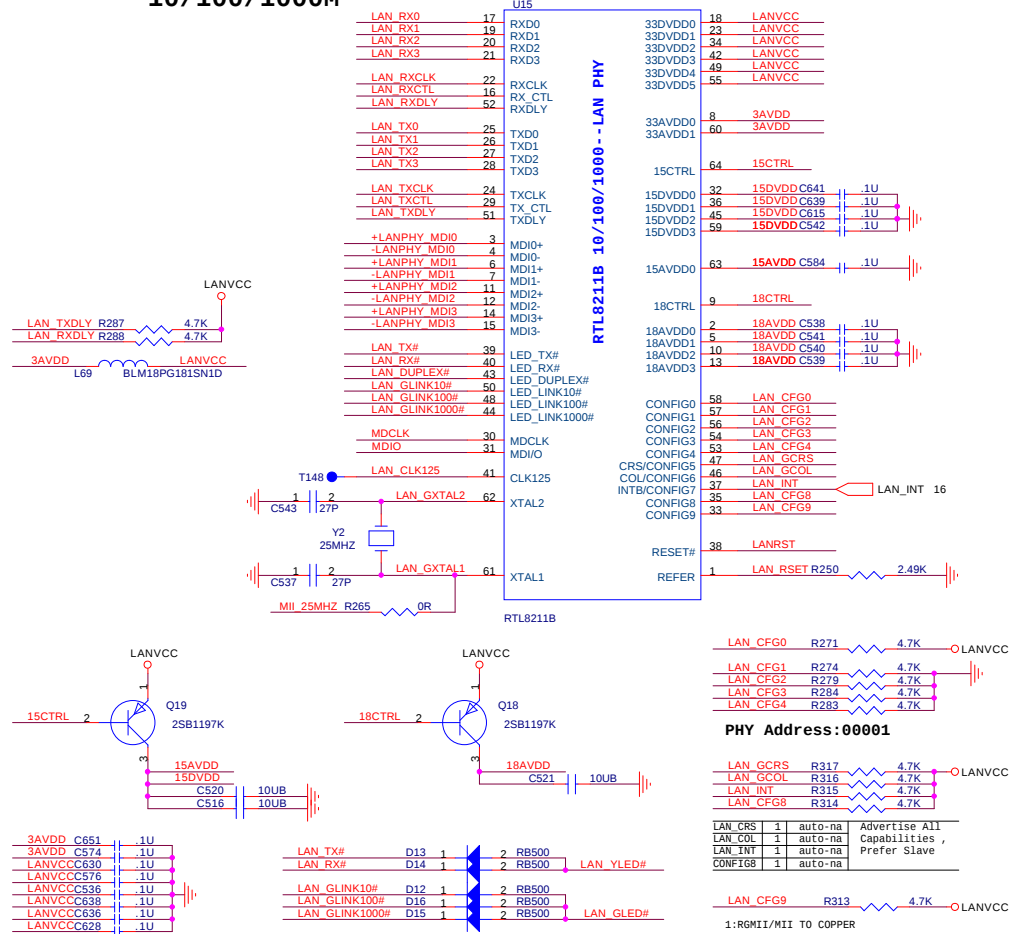
CLOSE TRANSFORMER



CLOSE RJ45/11 CONN

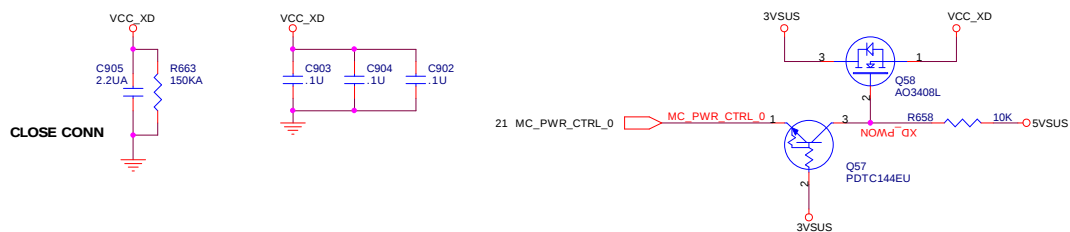
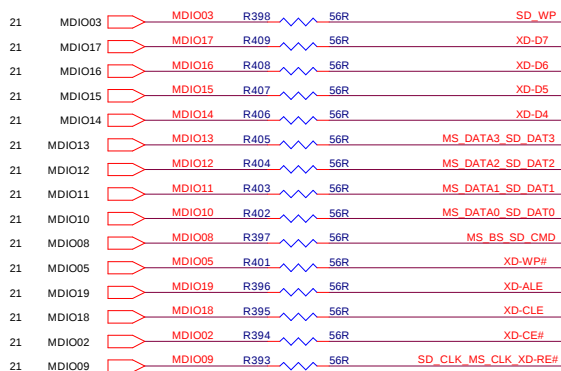


10/100/1000M



PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number LAN PHY RTL8211B/8201CL,RJ45	Rev 1A
Date: Tuesday, February 07, 2006		Sheet 20 of 38

[illegible]

HOLE1 H-C315D10P2	HOLE5 H-C256D15.7P2	HOLE3 H-C315D10P2	HOLE5 H-C315D10P2
HOLE8 H-C315D11P2	HOLE5 H-C256D15.7P2	HOLE4 H-C315D10N1.25P2	
HOLE15 H-D13K118D17X11N	HOLE8 H-C256D15.7P2	HOLE1 H-C315D10P2	
HOLE12 H-C278BC135D10P2	HOLE13 H-C256D15.7P2	HOLE2 H-C315D10P2	
HOLE30 H-C244D181P2	HOLE11 H-C256D15.7P2	HOLE33 H-C315D10P2	
HOLE10 H-C244D181P2	HOLE5 H-C256D15.7P2	HOLE32 H-C315D10P2	
HOLE17 H-C244D181P2	HOLE1 H-T0X76B08X0D2_7X3_P2	HOLE3 H-C315D10P2	
HOLE18 H-C244D181P2	HOLE4 H-T2727BC135D11P2	HOLE10 H-C256D15.7P2	
		HOLE21 H-C315D10P2	

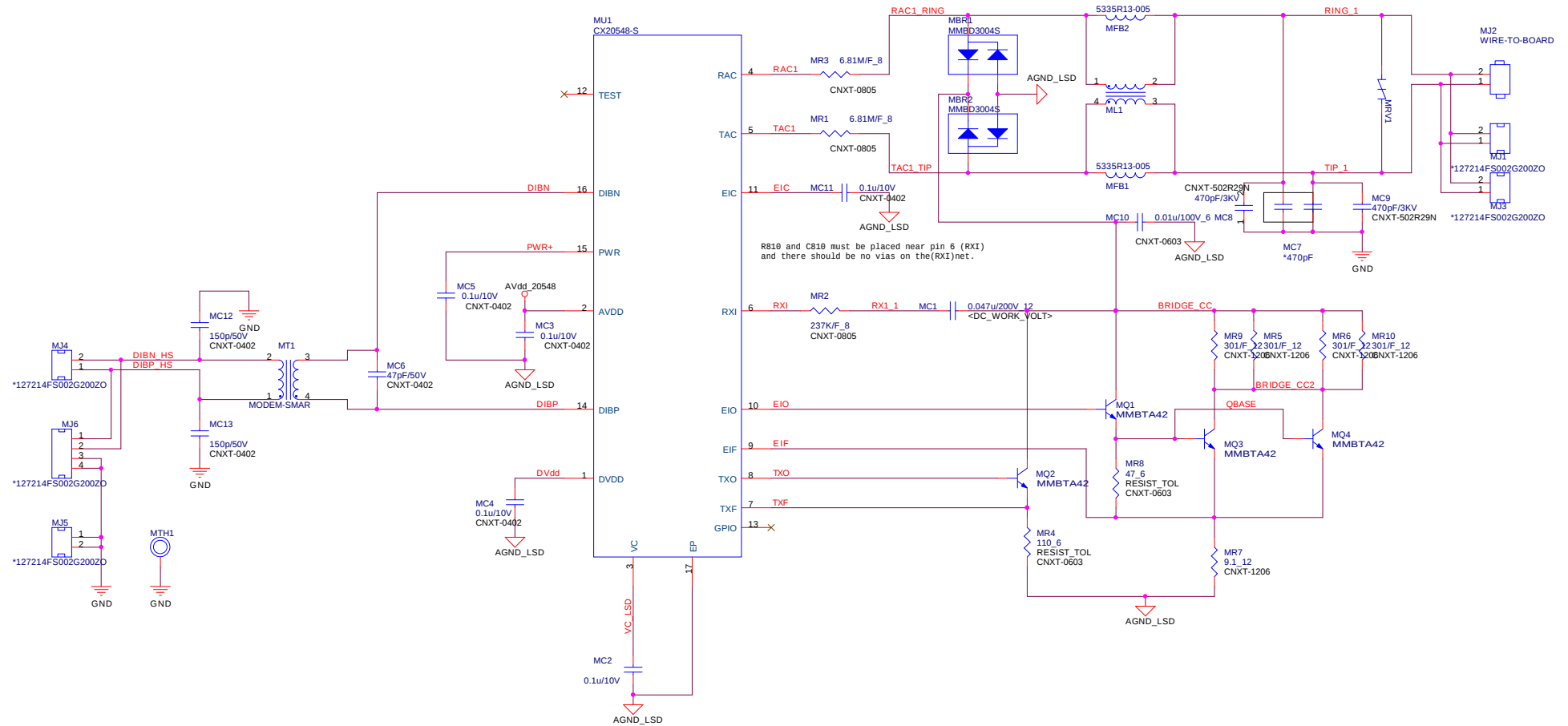


Size Custom	Document Number CARD READER CONN	Rev 1A
Date: Thursday, December 29, 2005		Sheet 22 of 38

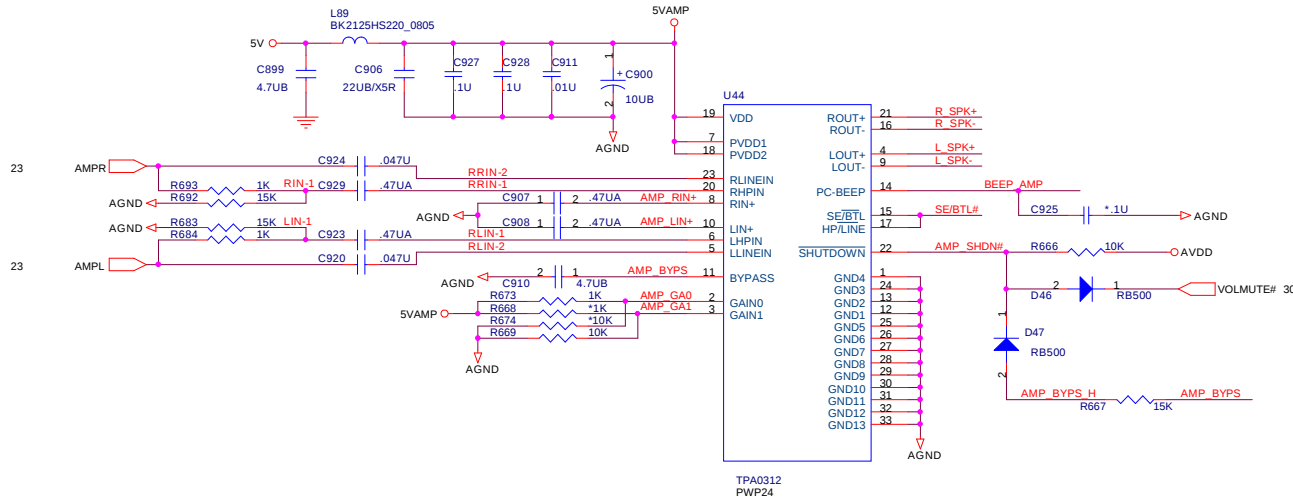
Revision History

REV	Description	Date
0	Initial Release	April 26, 2005

23 DIBN_HS
23 DIBP_HS

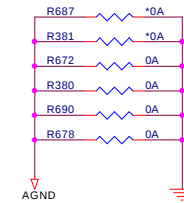


 NB5/RD2/HW1	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number MODEM (DAA)	Rev 1A
	Date: Thursday, December 29, 2005 Sheet 24 of 38		

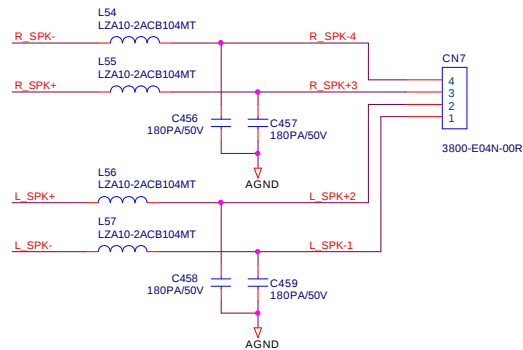


0312 Gain Table

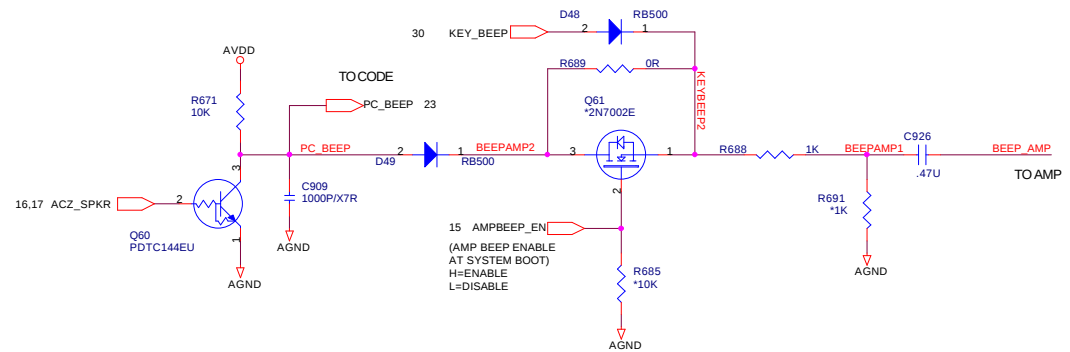
GAIN0	GAIN1	SE/BTL	AV(INV)
0	0	0	6dB
0	1	0	10dB
1	0	0	15.6dB
1	1	0	21.6dB
x	x	1	4.1dB



INT. SPEAKER



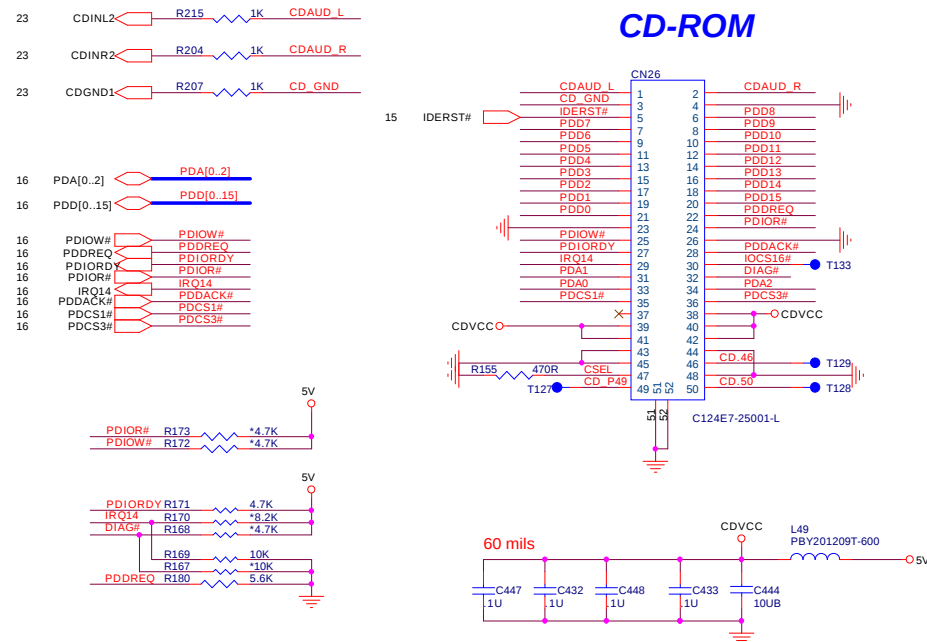
PCSPK BEEP



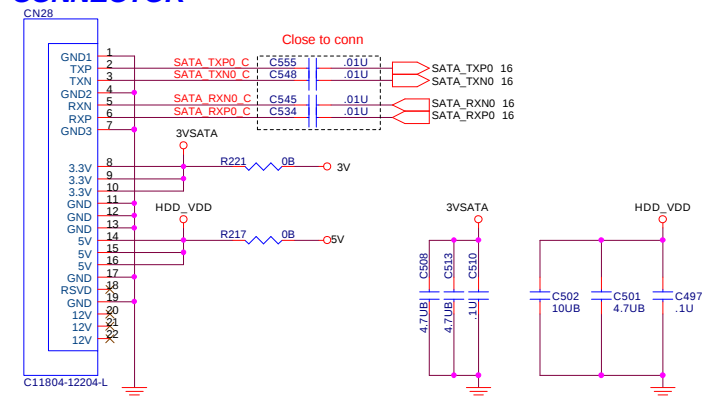
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number JACK, AMP_TPA0312	Rev 1A
Date: Thursday, December 29, 2005	Sheet 25 of 38	

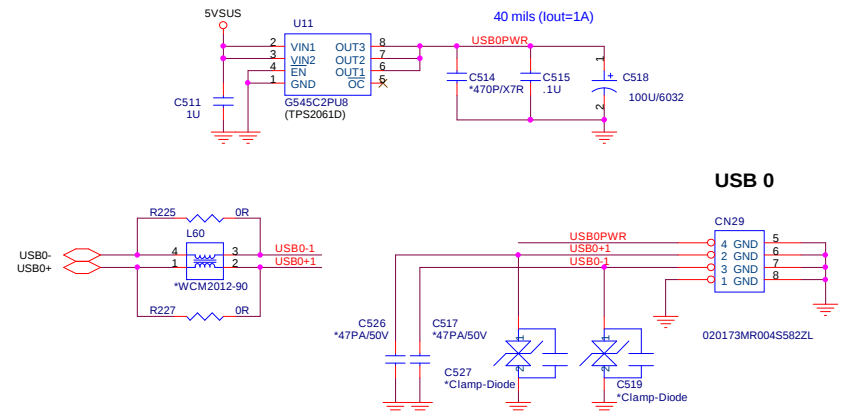
USBX2



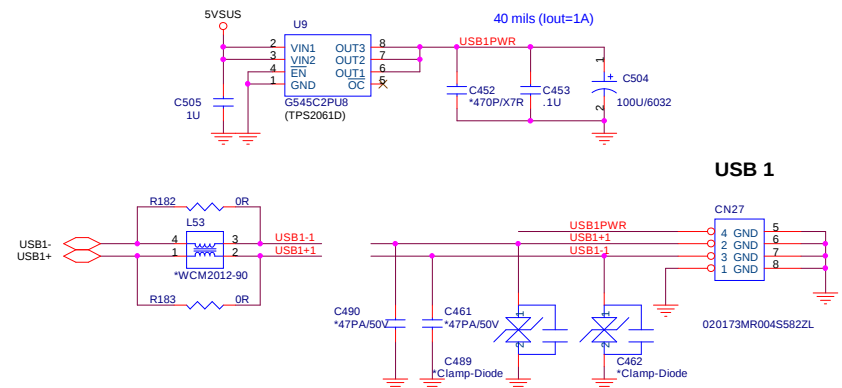
**SATA_1
CONNECTOR**



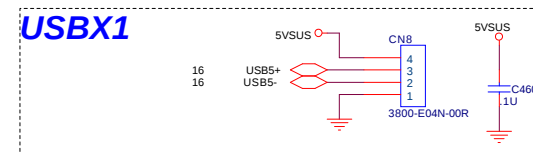
USB 0



USB 1



USBX1



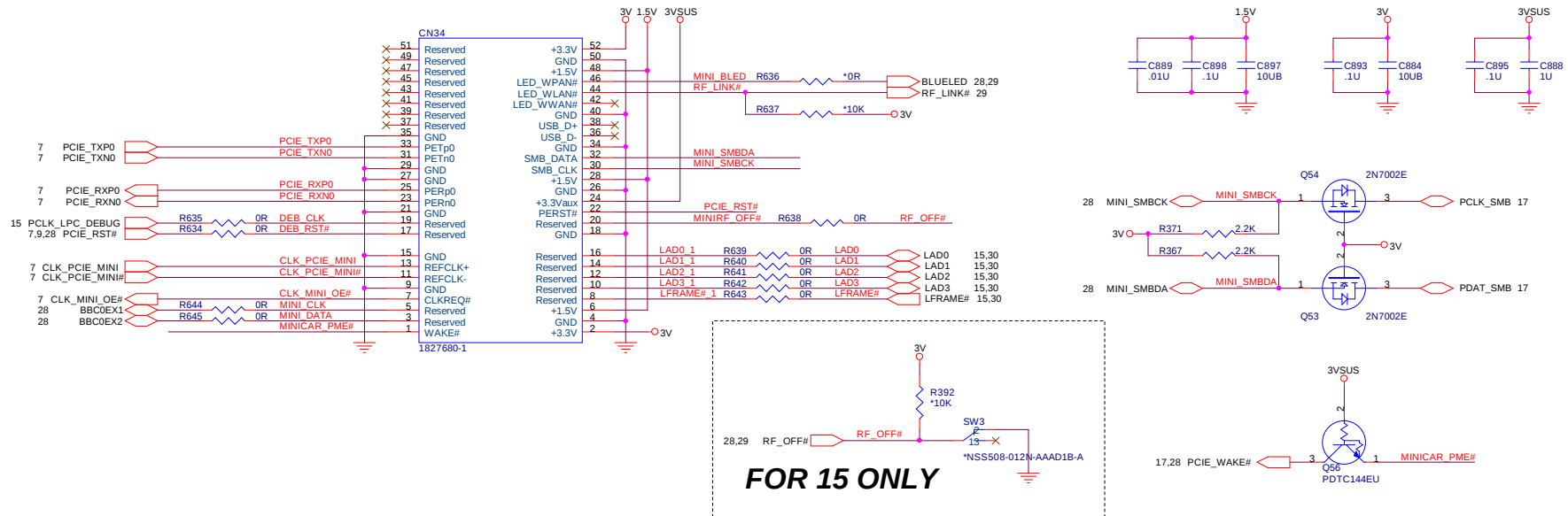
TO AD/B



PROJECT : AT8
Quanta Computer Inc.

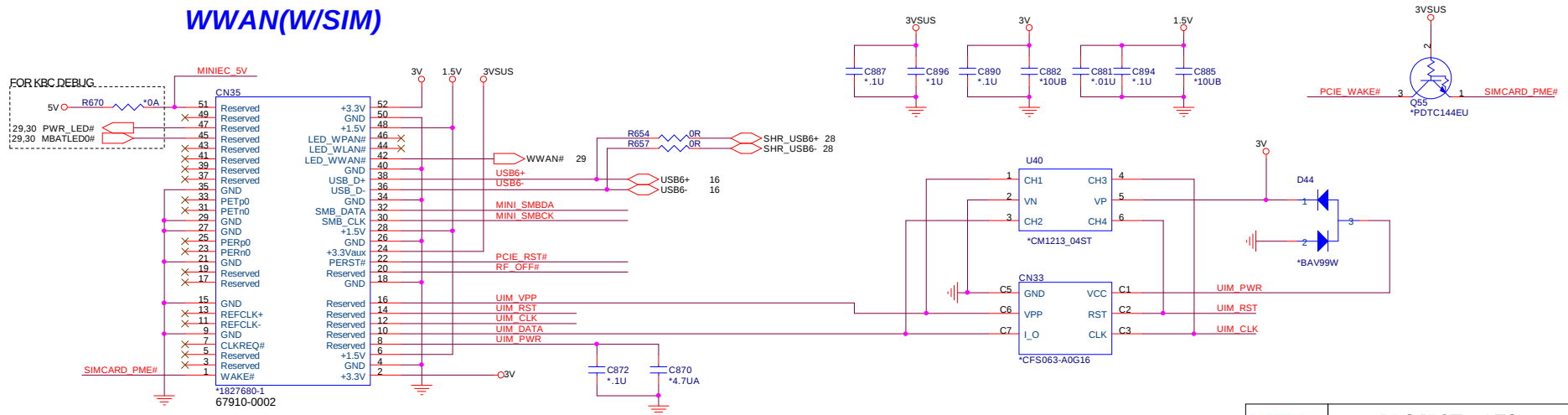
Size Custom	Document Number SATA HDD, CD-ROM, USBX2	Rev 1A
Date: Thursday, December 29, 2005 Sheet 26 of 38		

Mini PCI-E Card 1 WLAN



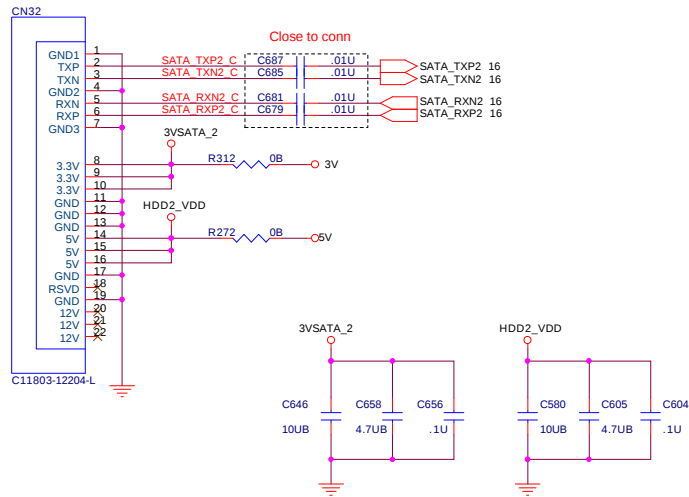
Mini PCI-E Card 2 WWAN(W/SIM)

FOR 15 ONLY

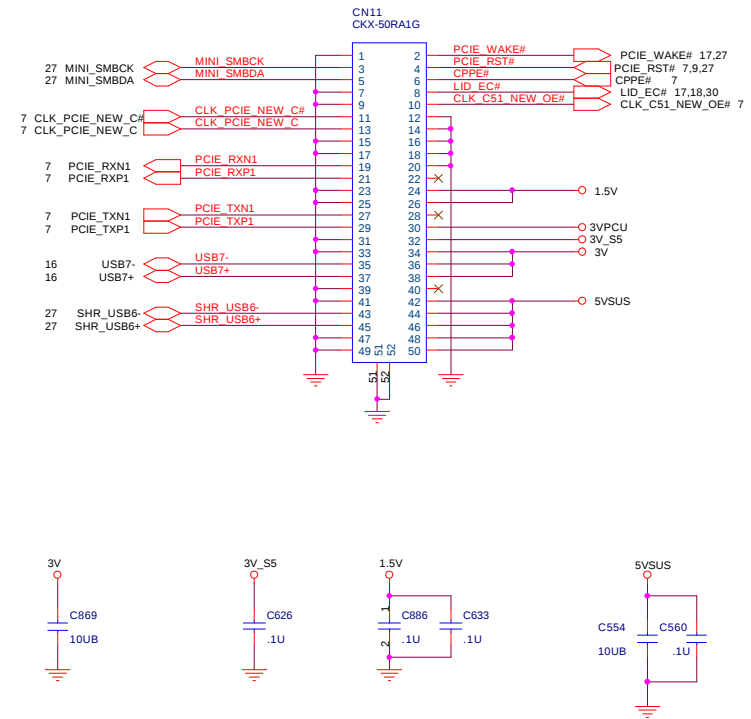


	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number MINI CARD	Rev 1A
Date: Thursday, December 29, 2005 Sheet 27 of 38			

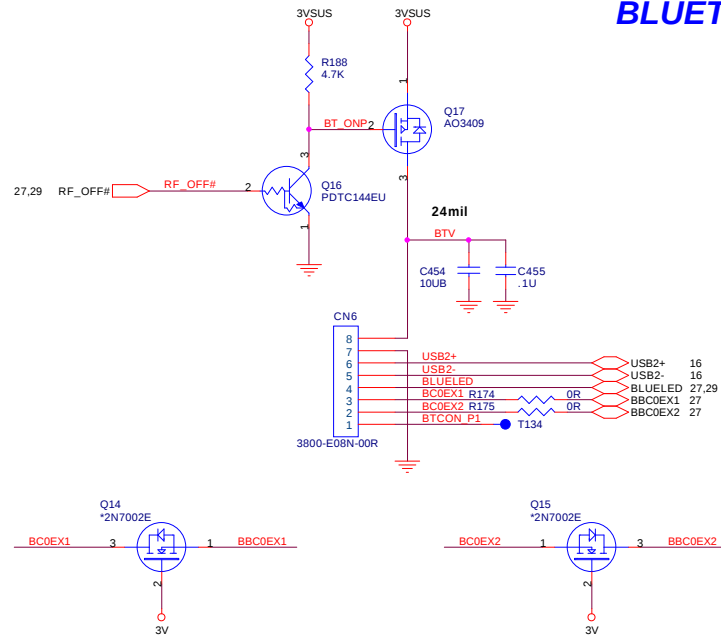
SATA_2 CONNECTOR For 17"W Second HDD



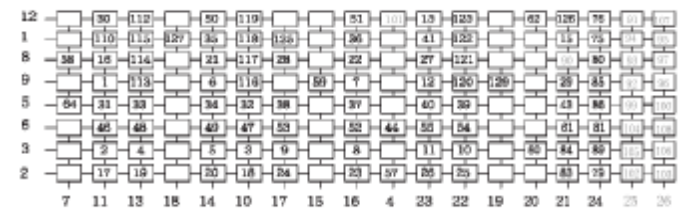
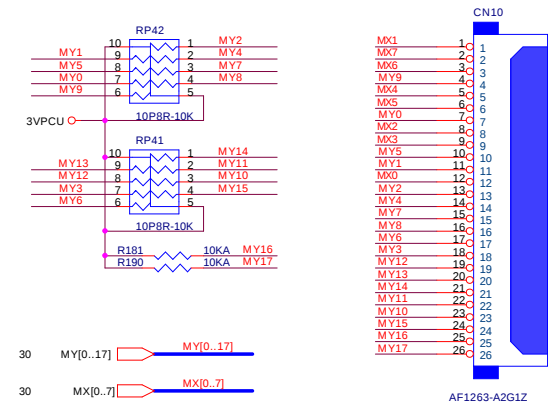
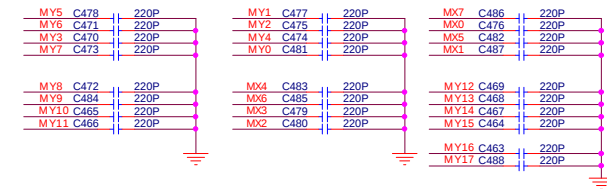
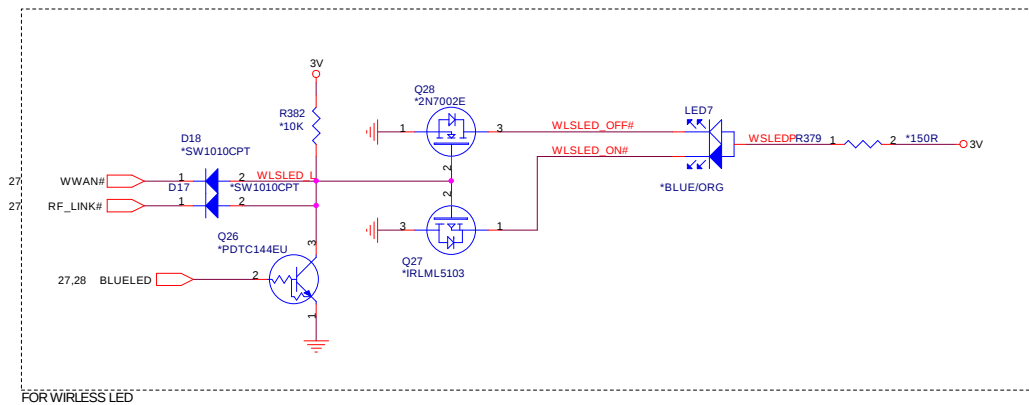
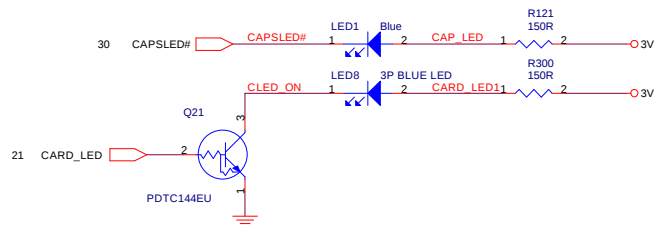
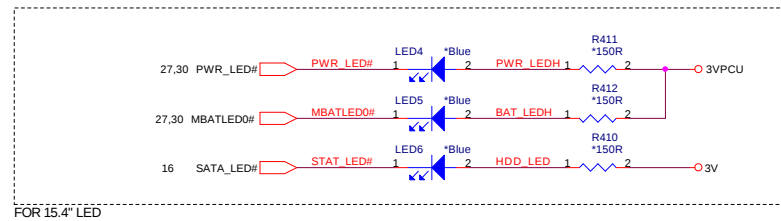
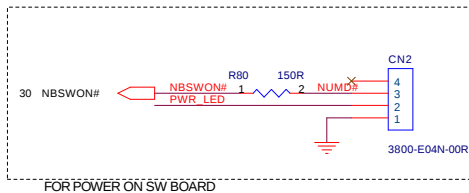
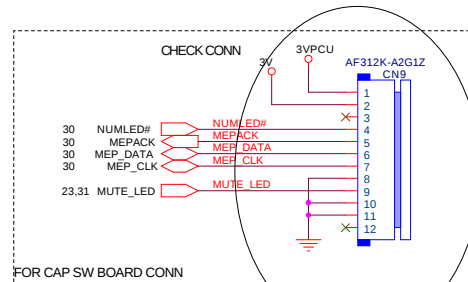
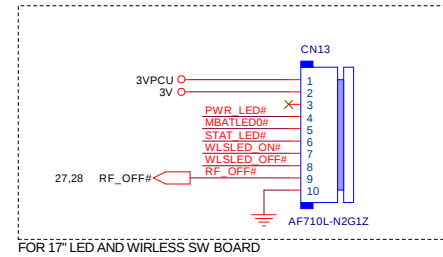
NEWCARD



BLUETOOTH

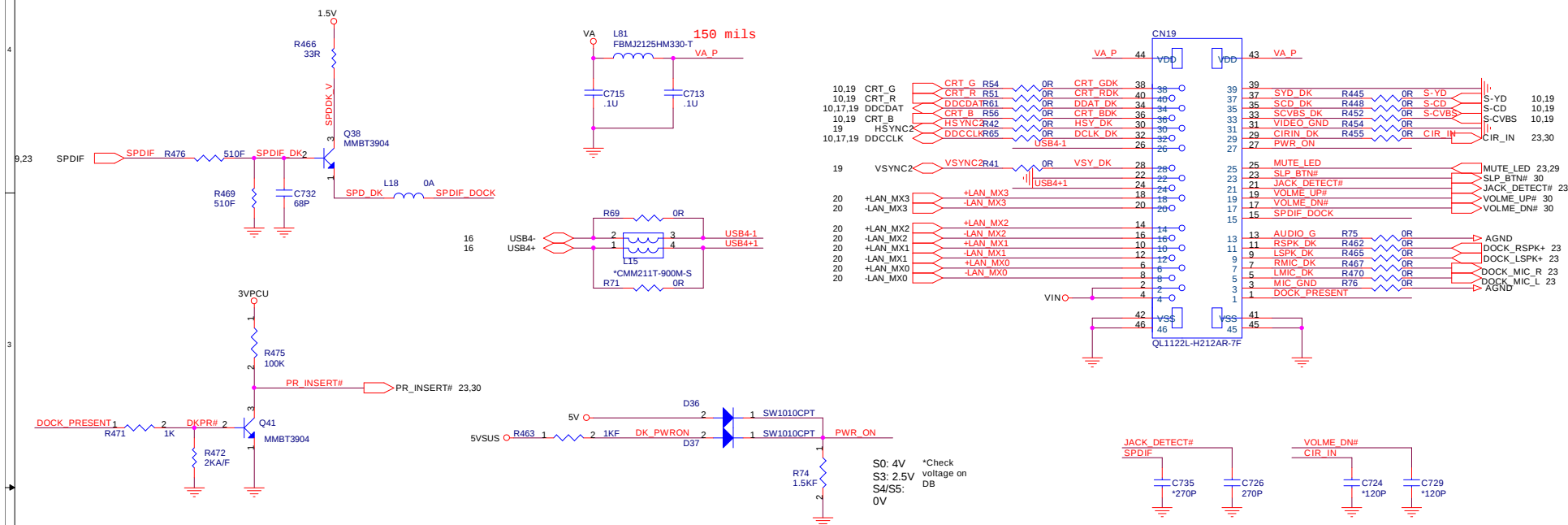


	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number NEW CARD/BT	Rev 1A
	Date: Thursday, December 29, 2005 Sheet 28 of 38		

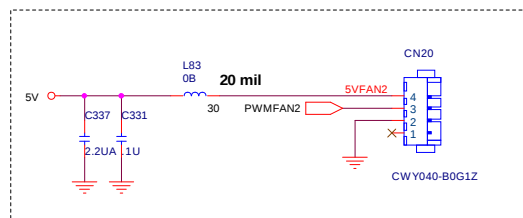


	PROJECT : AT8 Quanta Computer Inc.		
	Size Custom	Document Number LED/KEYBOARD/TP	Rev 1A
	Date: Thursday, December 29, 2005 Sheet 29 of 38		

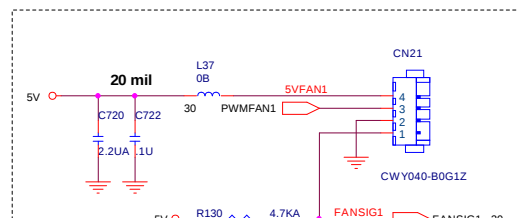
CABLE DOCK



FAN



FAN2 PWM CONNECTOR



FAN1 PWM CONNECTOR

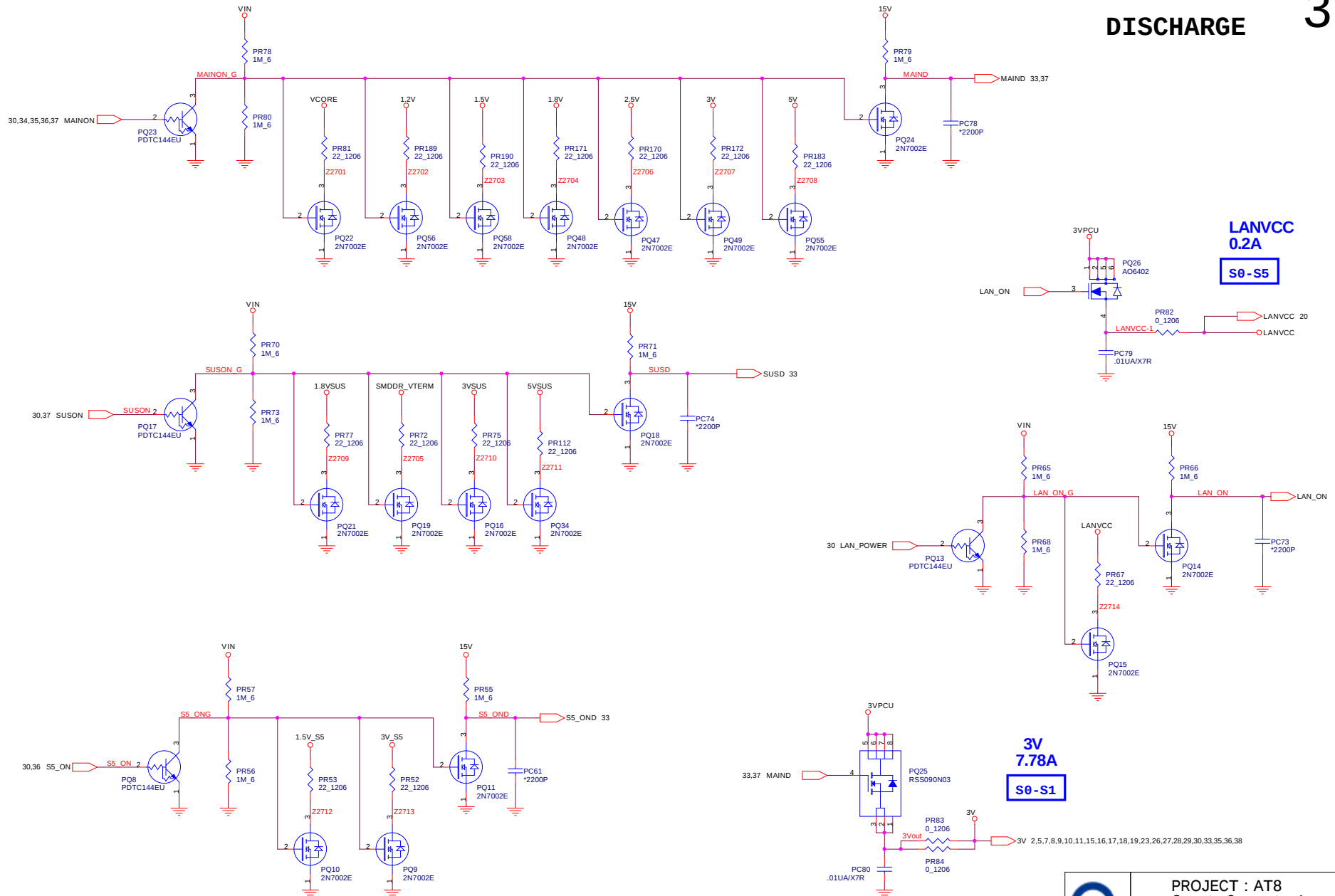


NB5/RD2/HW

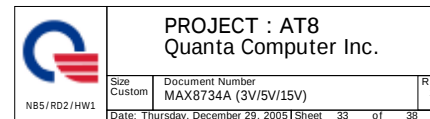
PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number CABLE DOCKING	Rev 1A
Date: Thursday, December 29, 2005		Sheet 31 of 38

DISCHARGE



PROJECT : AT8 Quanta Computer Inc.		
Size Custom	Document Number DISCHARGE	Rev 1A
Date: Thursday, December 29, 2005 Sheet 32 of 38		



Adapter
19V/3.4A/65W
4.75A/90W

MAX8724A

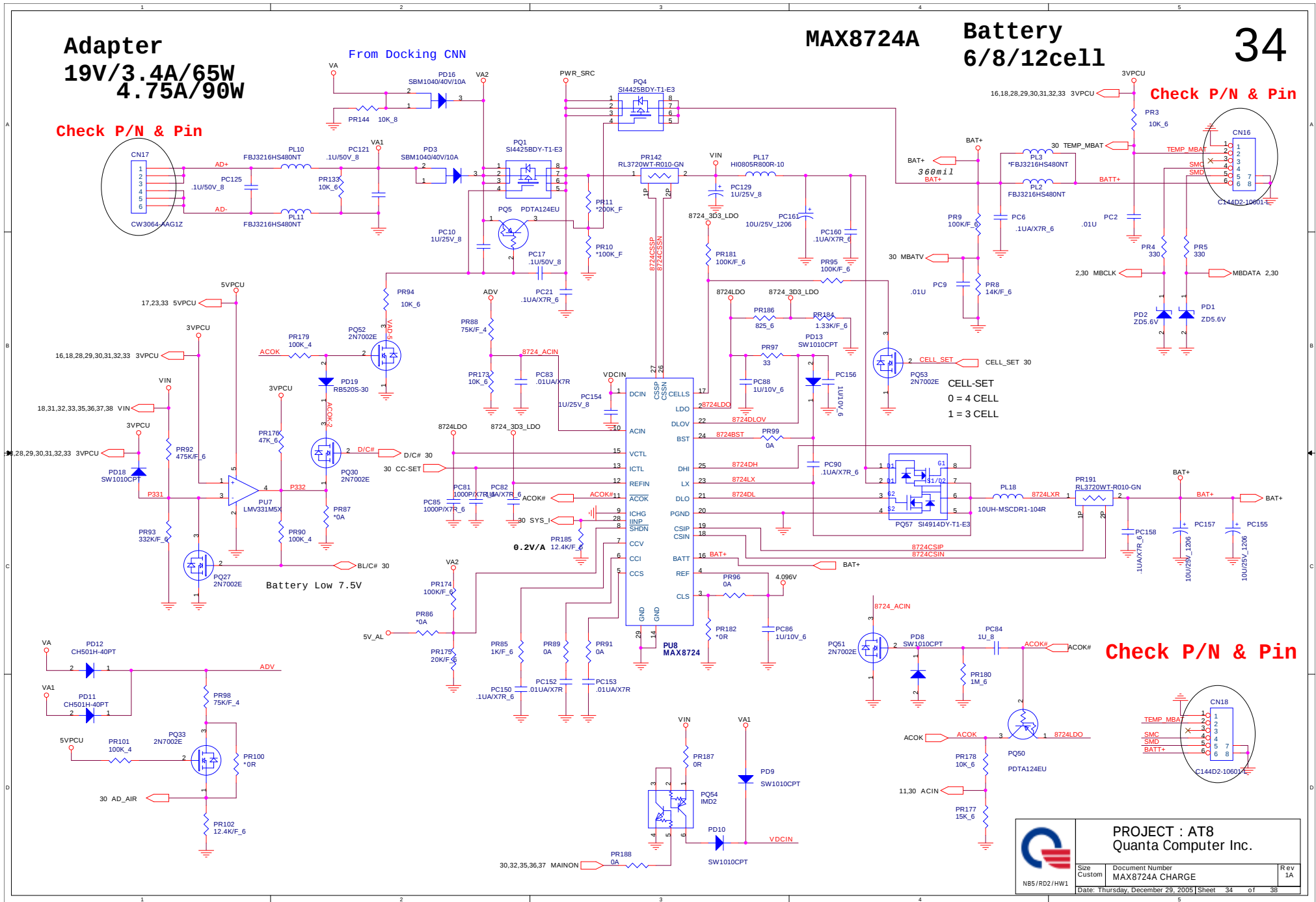
Battery
6/8/12cell

34

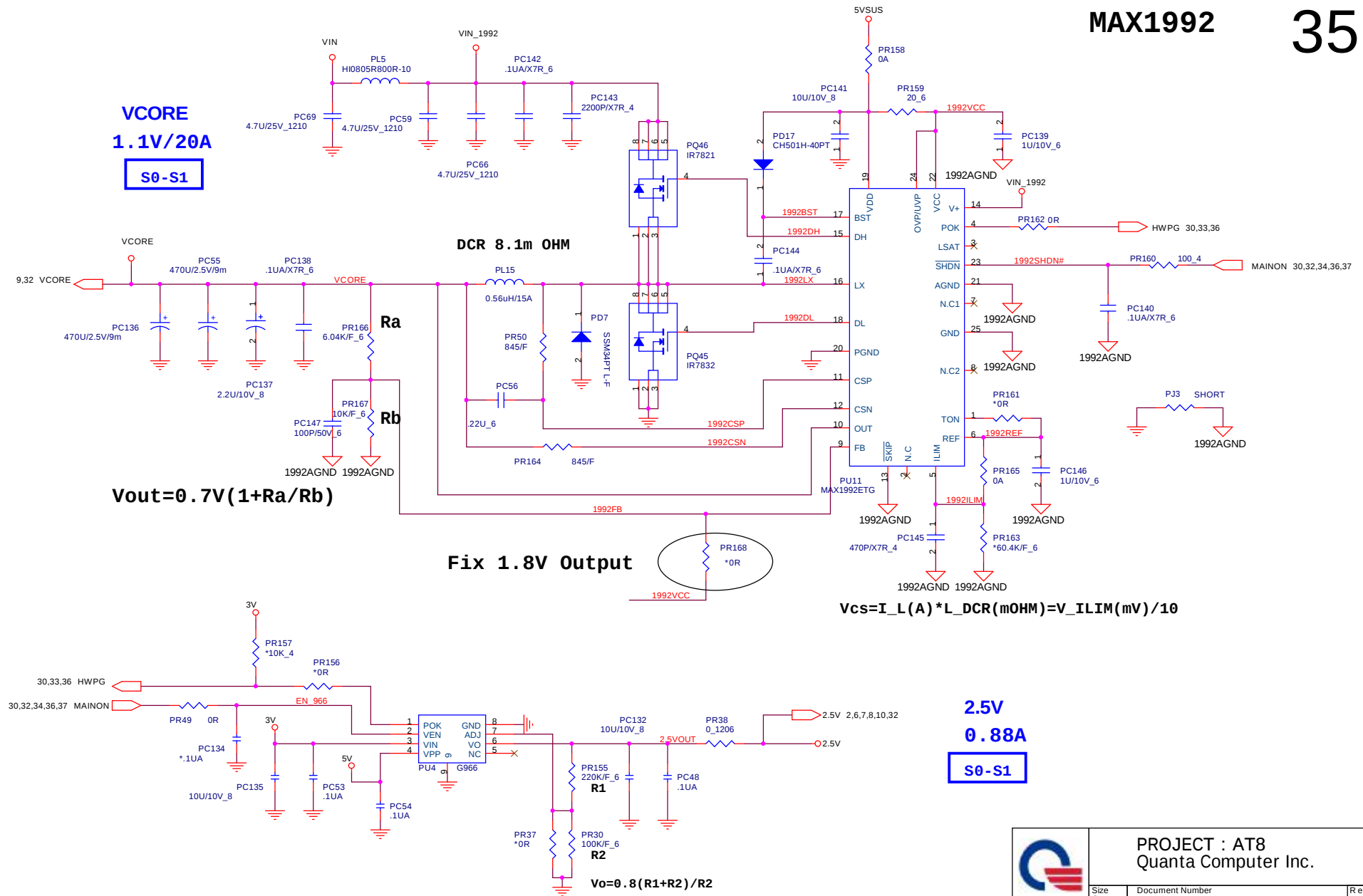
Check P/N & Pin

Check P/N & Pin

Check P/N & Pin



 NBS / RD2 / HW1		PROJECT : AT8 Quanta Computer Inc.	
Size Custom	Document Number MAX8724A CHARGE	Rev 1A	
Date: Thursday, December 29, 2005 Sheet 34 of 38			



PROJECT : AT8
Quanta Computer Inc.

Size Custom	Document Number MAX1992 1.1V/2.5V	Rev 1A
Date: Thursday, January 12, 2006	Sheet 35 of 38	



