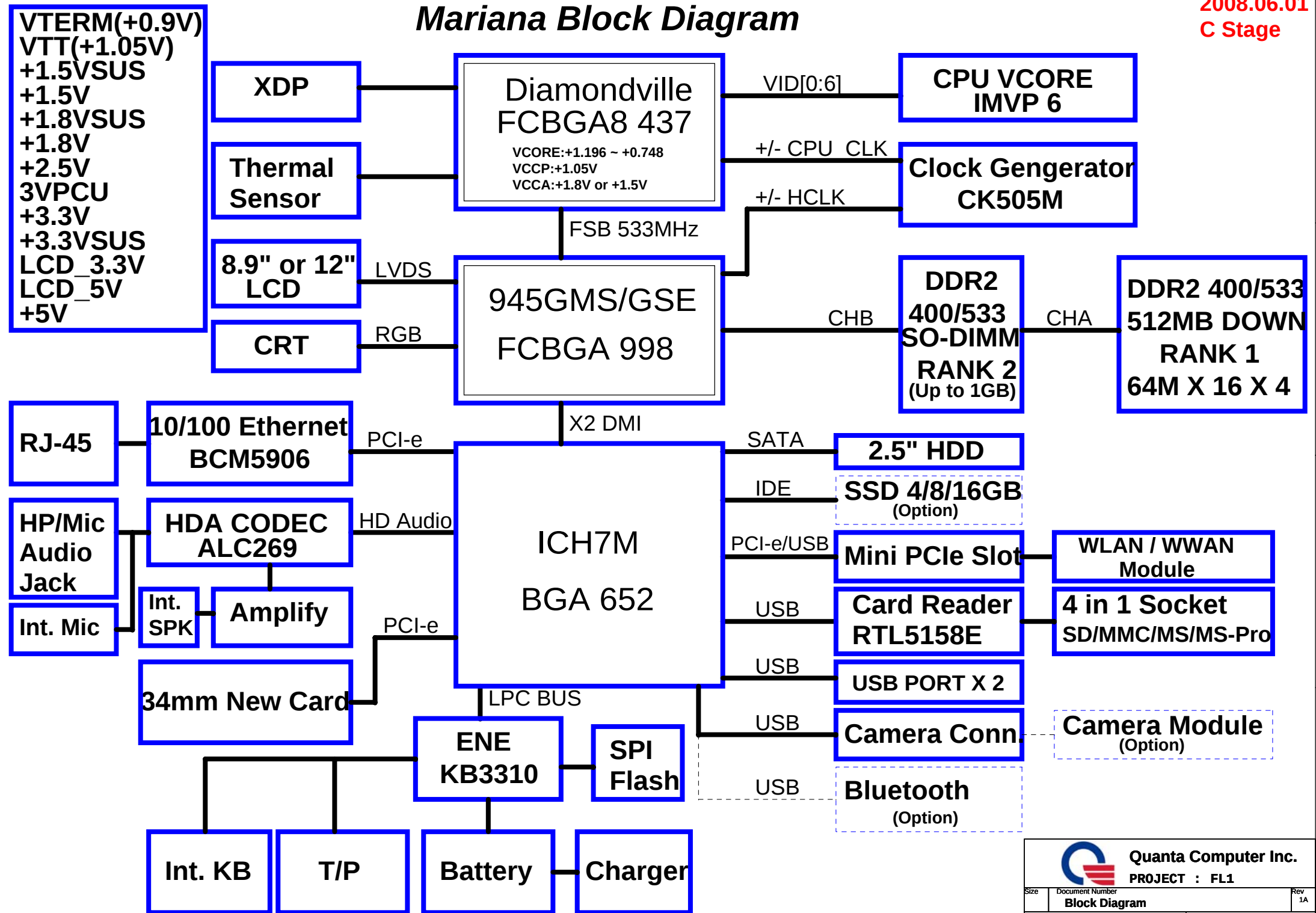
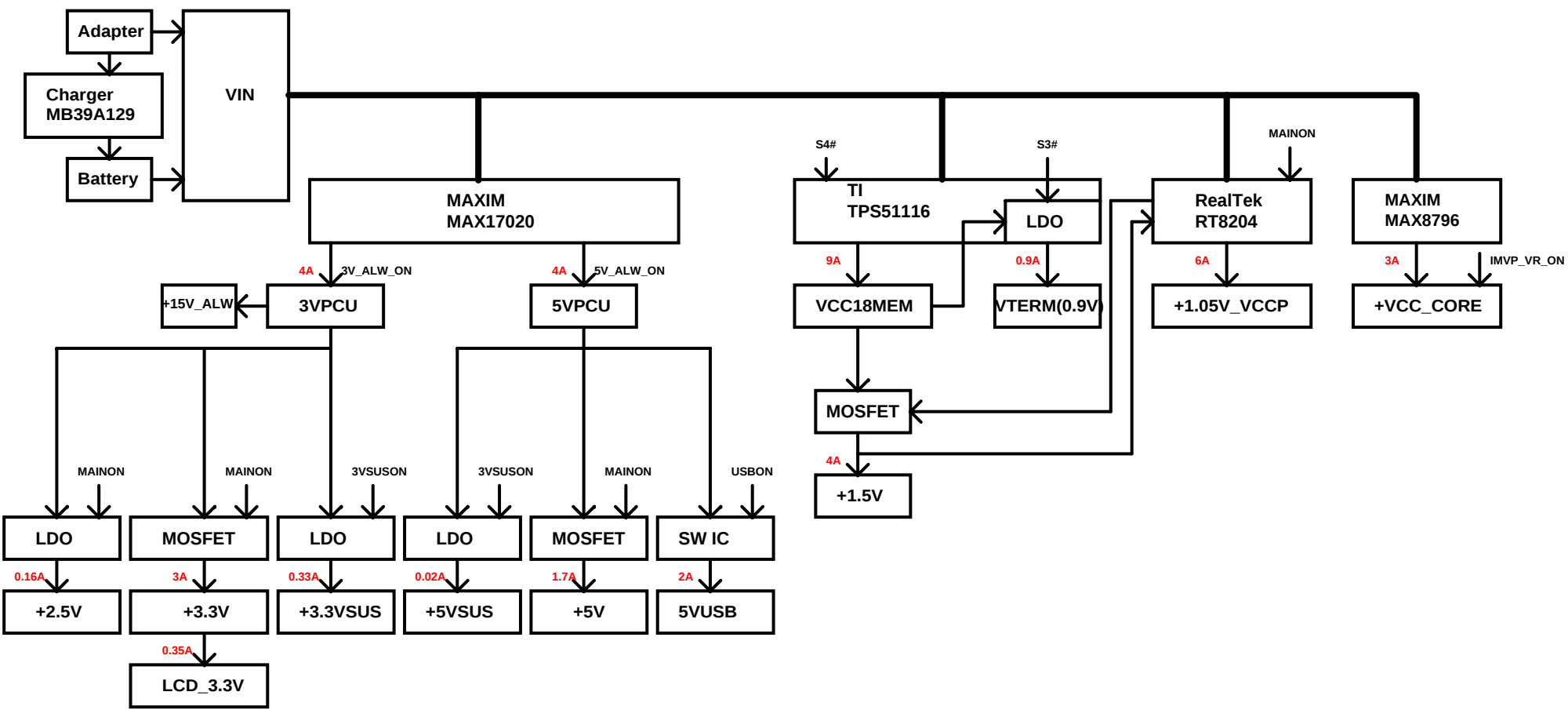
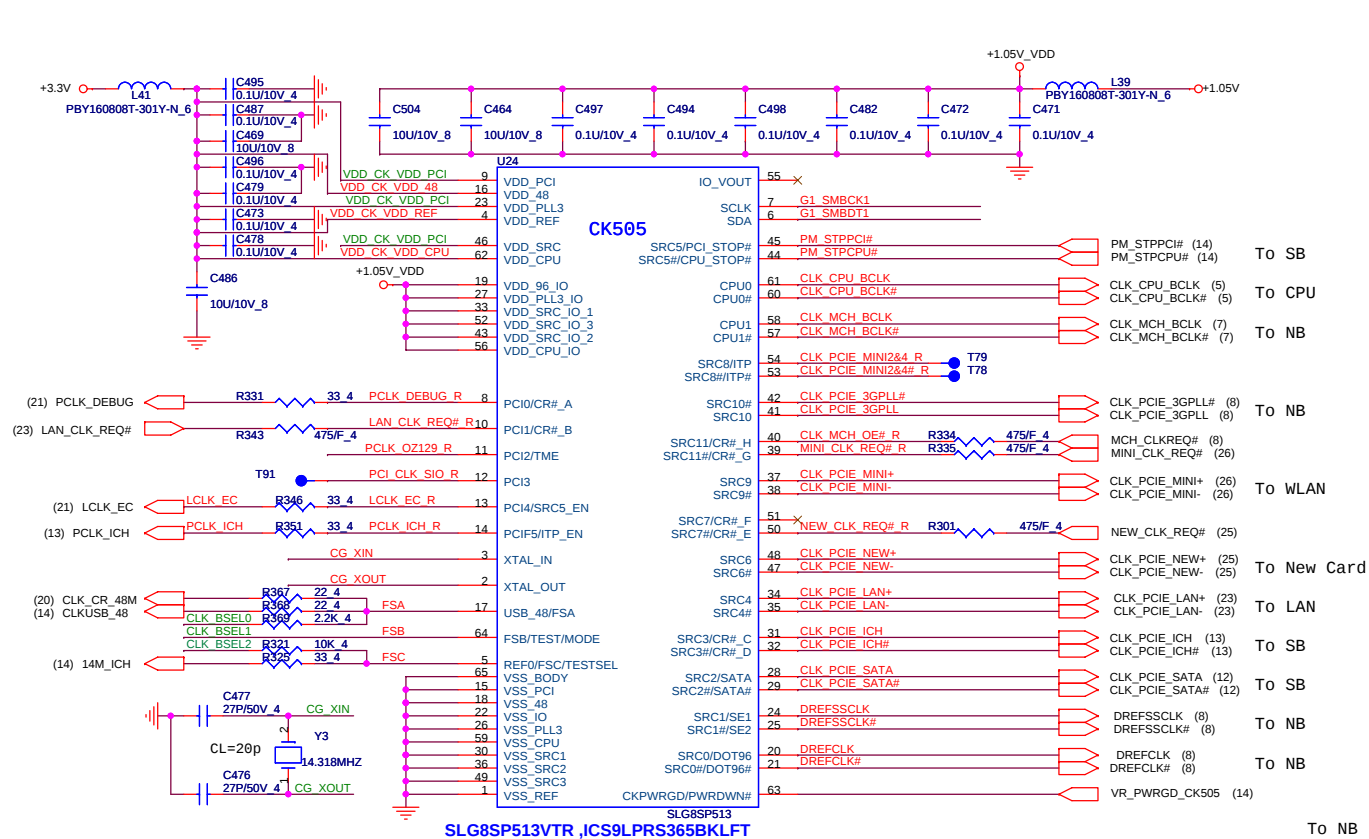


Mariana Block Diagram



[illegible][illegible][illegible]





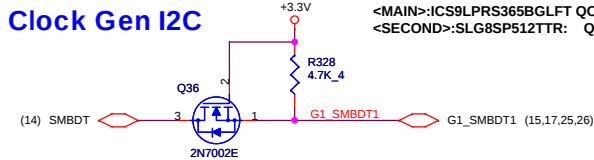
(21) PCLK_DEBUG
(23) LAN_CLK_REQ#

(21) LCLK_EC
(13) PCLK_ICH

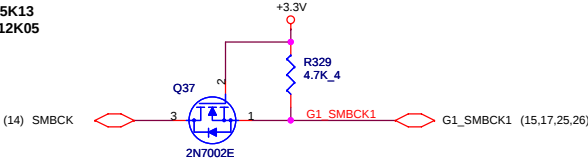
(20) CLK_CR_48M
(14) CLKUSB_48
(14) 14M_ICH

	ICS9LPRS365 (ALPRS365K13)	RYM8751-606 (AL000875K06)	PULL HIGH	PULL DOWN
Pin 11	PCI2/TME	PCI2/TME Internal PD	NO OVERCLOCKING (default)	NORMAL RUN
Pin 12	PCI-3/SRC5_EN	PCI-3/SRC5_EN Internal PD	PIN37/38 IS SRC5	PIN37/38 IS PCI_STOP/CPU_STOP (default)
Pin 13	PCI-4/27M_SEL	PCI-4/27M_SEL Internal PD	PIN 17/18 IS 27MHz	PIN 17/18 IS SRC/DOT (default)
Pin 14	PCIF-5/ITP_EN	PCIF-5/ITP_EN Internal PD	PIN 46/47 IS CPUITP	PIN 46/47 IS SRC8 (default)

Clock Gen I2C

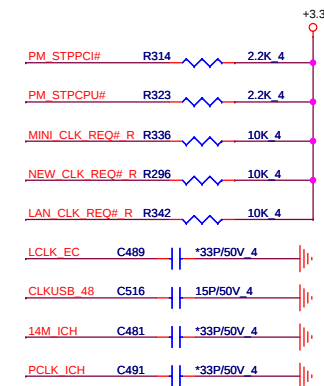


<MAIN>: ICS9LPRS365BGLFT QCI: ALPRS365K13
<SECOND>: SLG8SP512TTR: QCI: AL8SP512K05



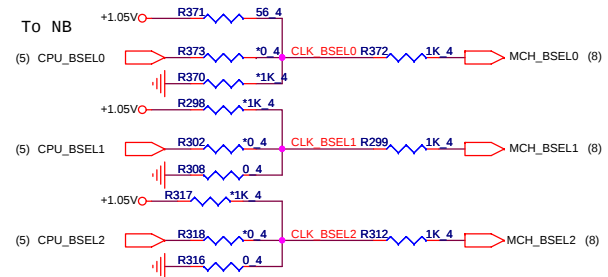
SM_BUS_Group1

1. Clock Gen
2. Memory Rom
3. Mini Card
4. New Card
5. CPU Thermal Monitor



Frequency select

FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	Reserved		



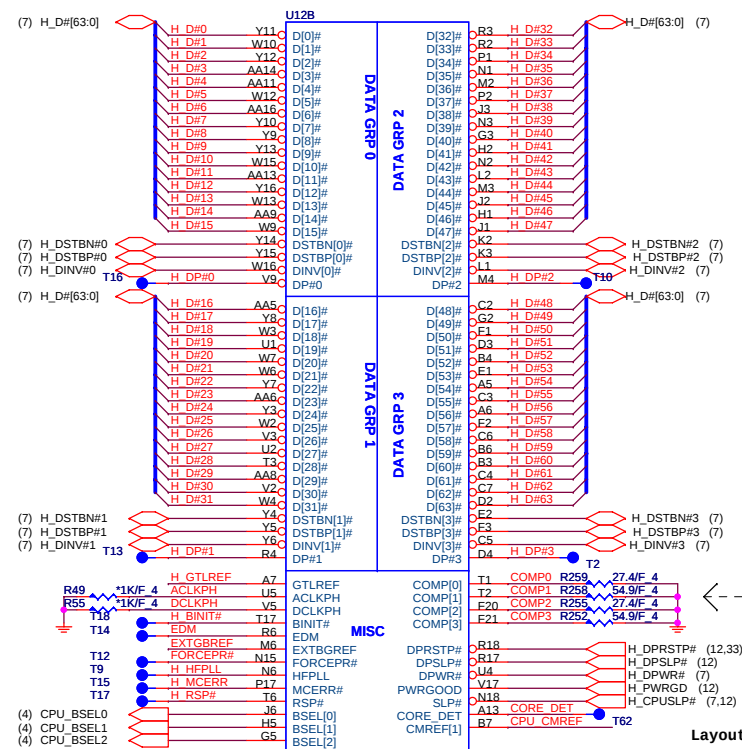
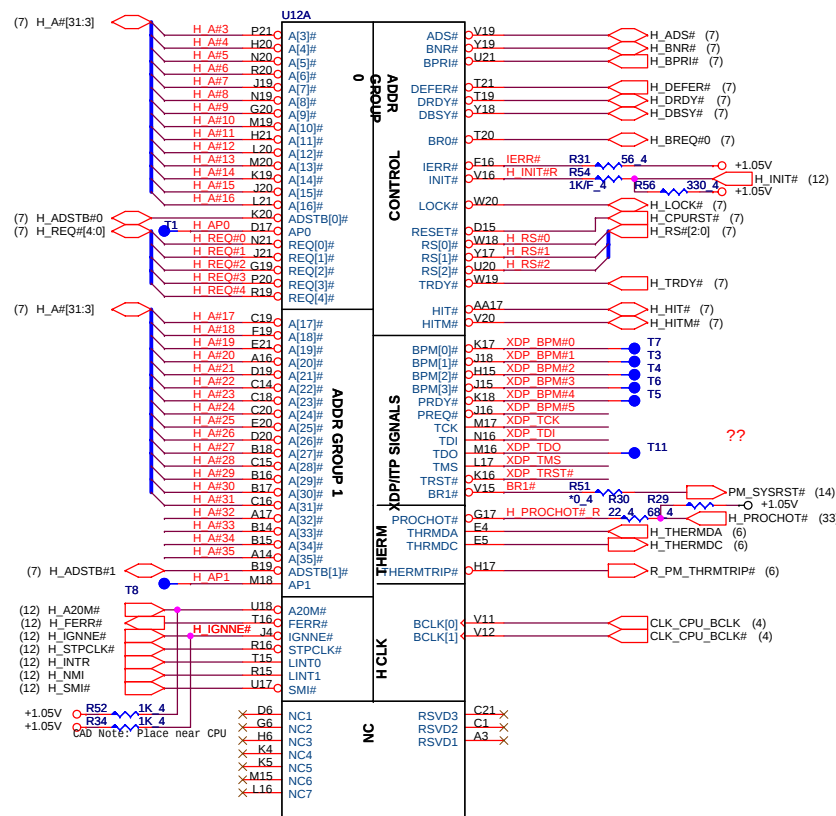
Quanta Computer Inc.
PROJECT : FL1

Size Document Number

CLOCK GENERATOR

Date: Saturday, June 07, 2008 Sheet 4 of 34

Rev 1A



R53 NO-ASM --0424

For defensive design reservation only in this initial release

Layout note:
Zo=55ohm, 0.5" max
for GTLREF

Layout note:
Zo=55ohm, 0.5" max
for EXTGBREF

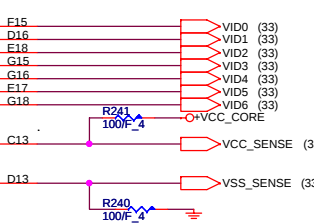
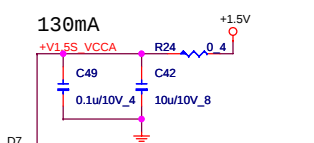
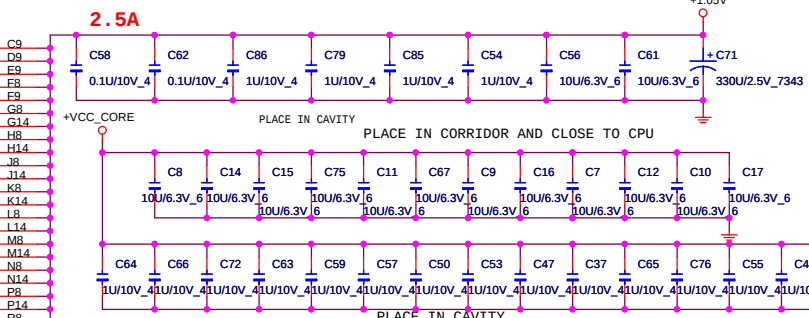
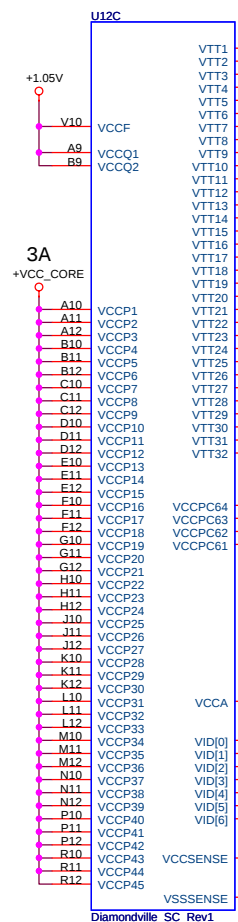
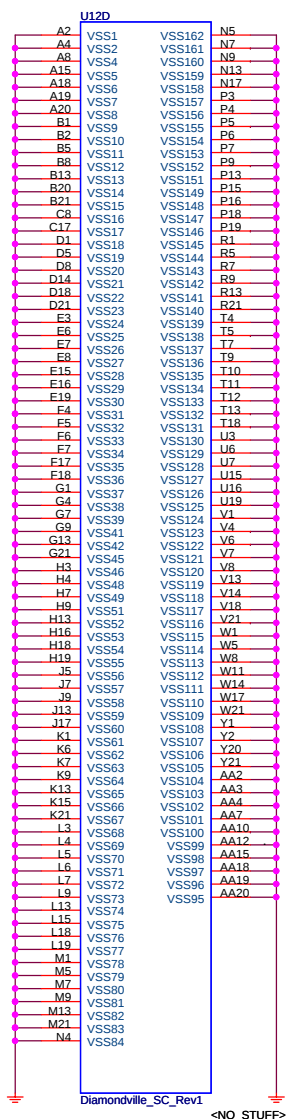
Layout note:
Zo=55ohm, 0.5" max
for GTLREF

Layout note:
Comp0,2 connect with Zo=27.4ohm, make trace length shorter than 0.5"
Comp1,3 connect with Zo=55ohm, make trace length shorter than 0.5"

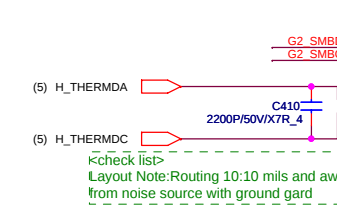


Quanta Computer Inc.
PROJECT : FL1

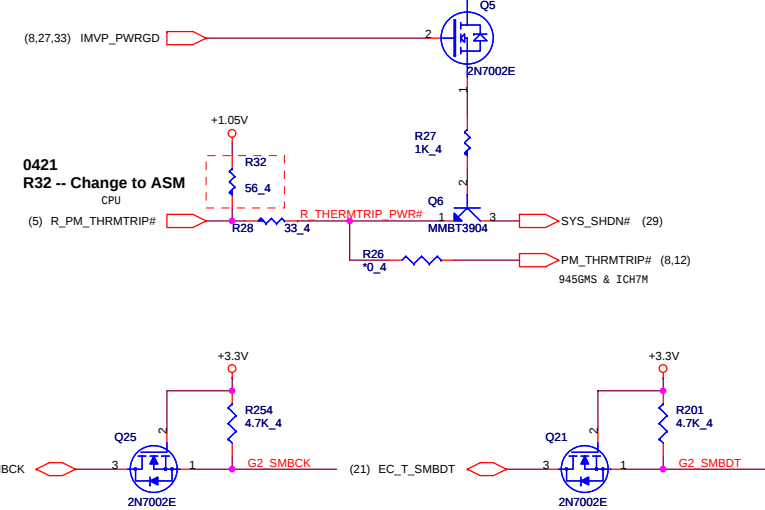
Size	Document Number	Rev 1A
Date:	Saturday, June 07, 2008	Sheet 5 of 34



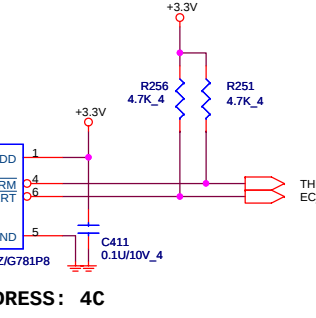
CPU Thermal monitor

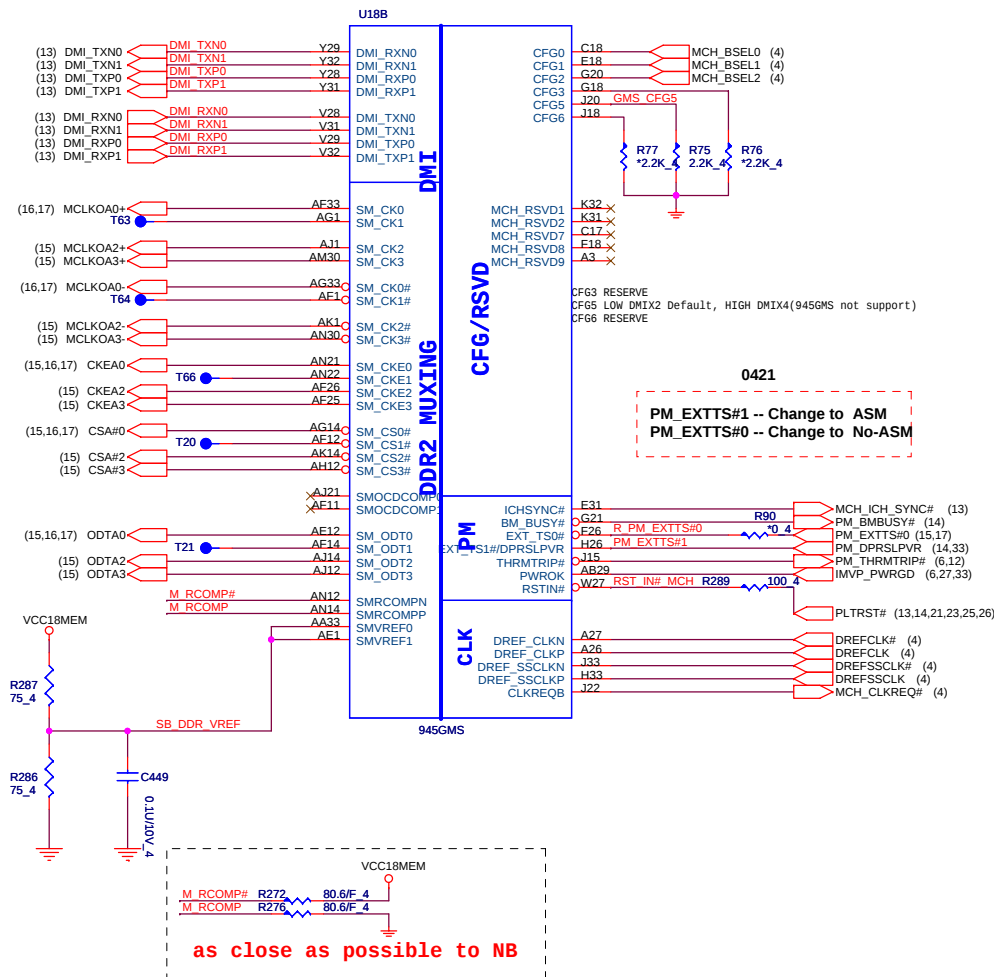


125 Degree Protection

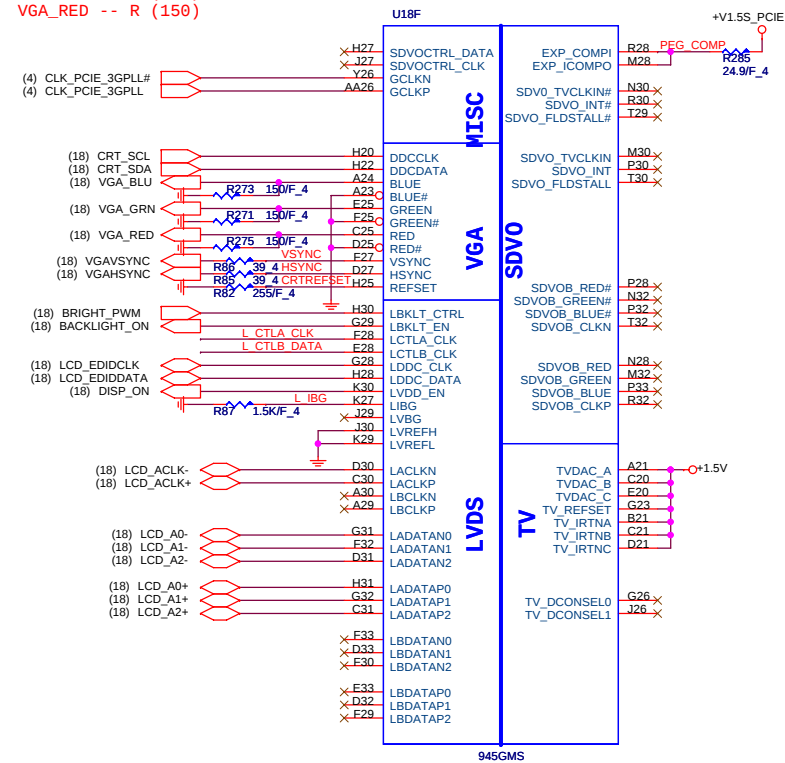


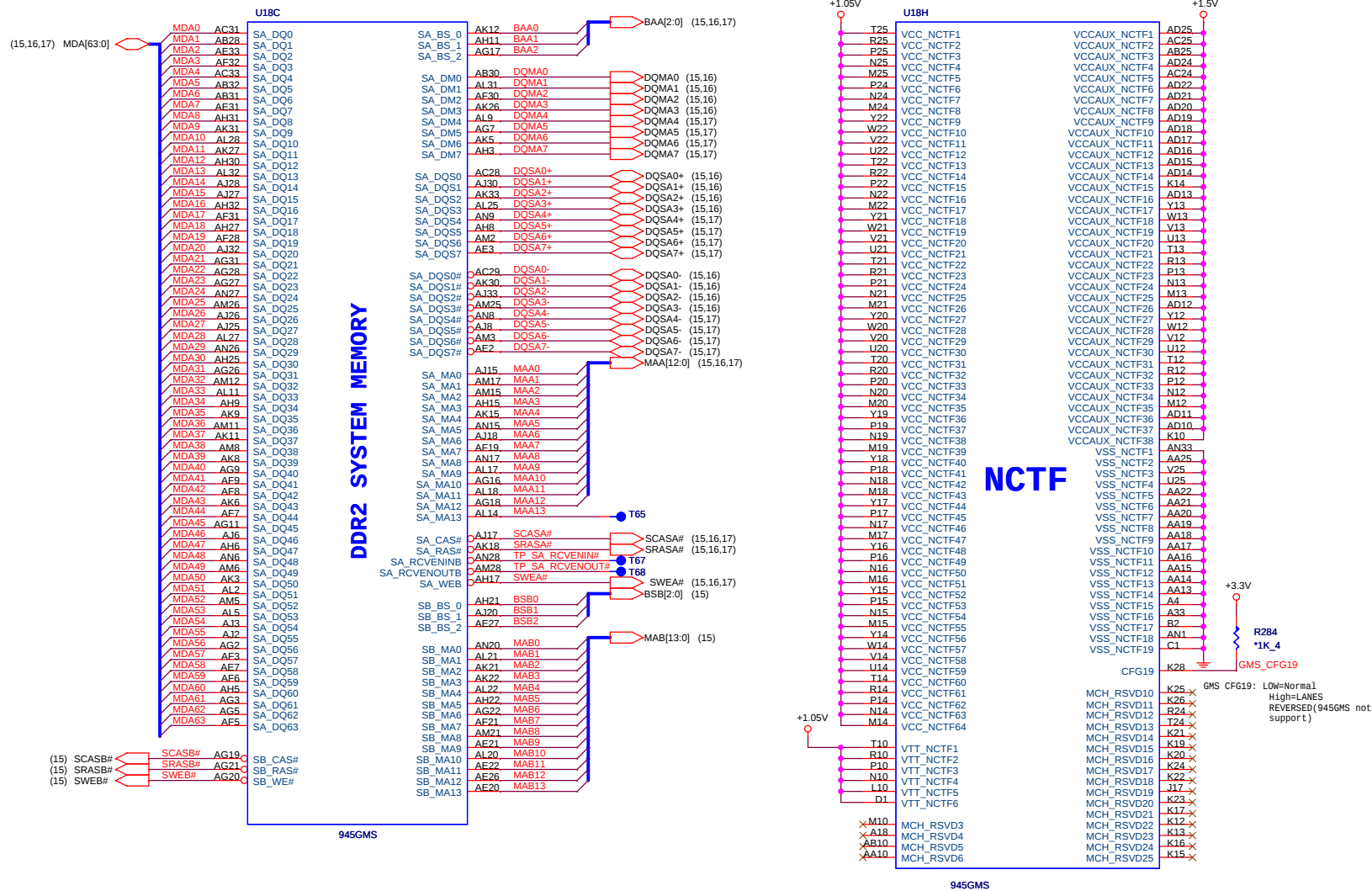
SMBus ADDRESS: 4C

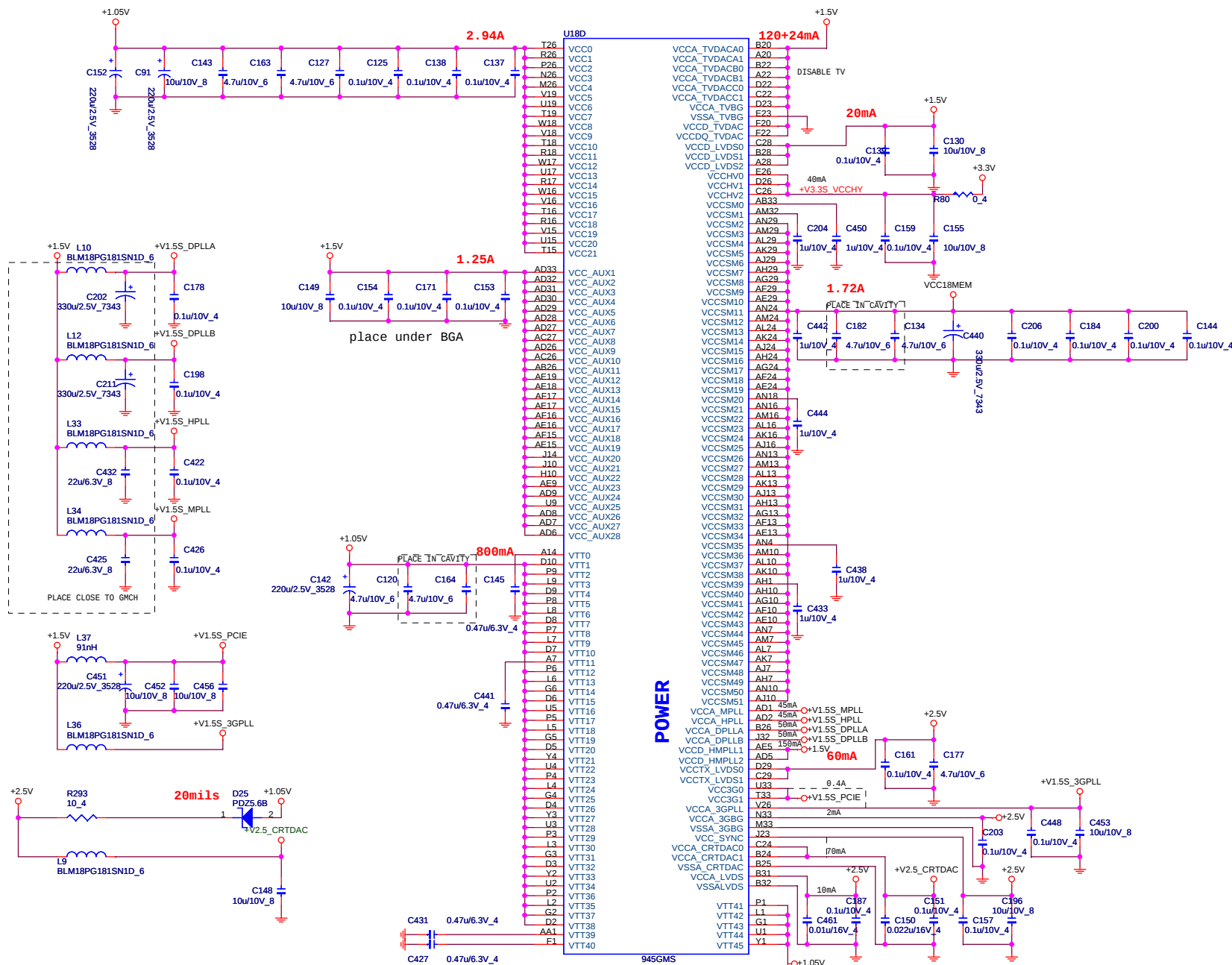


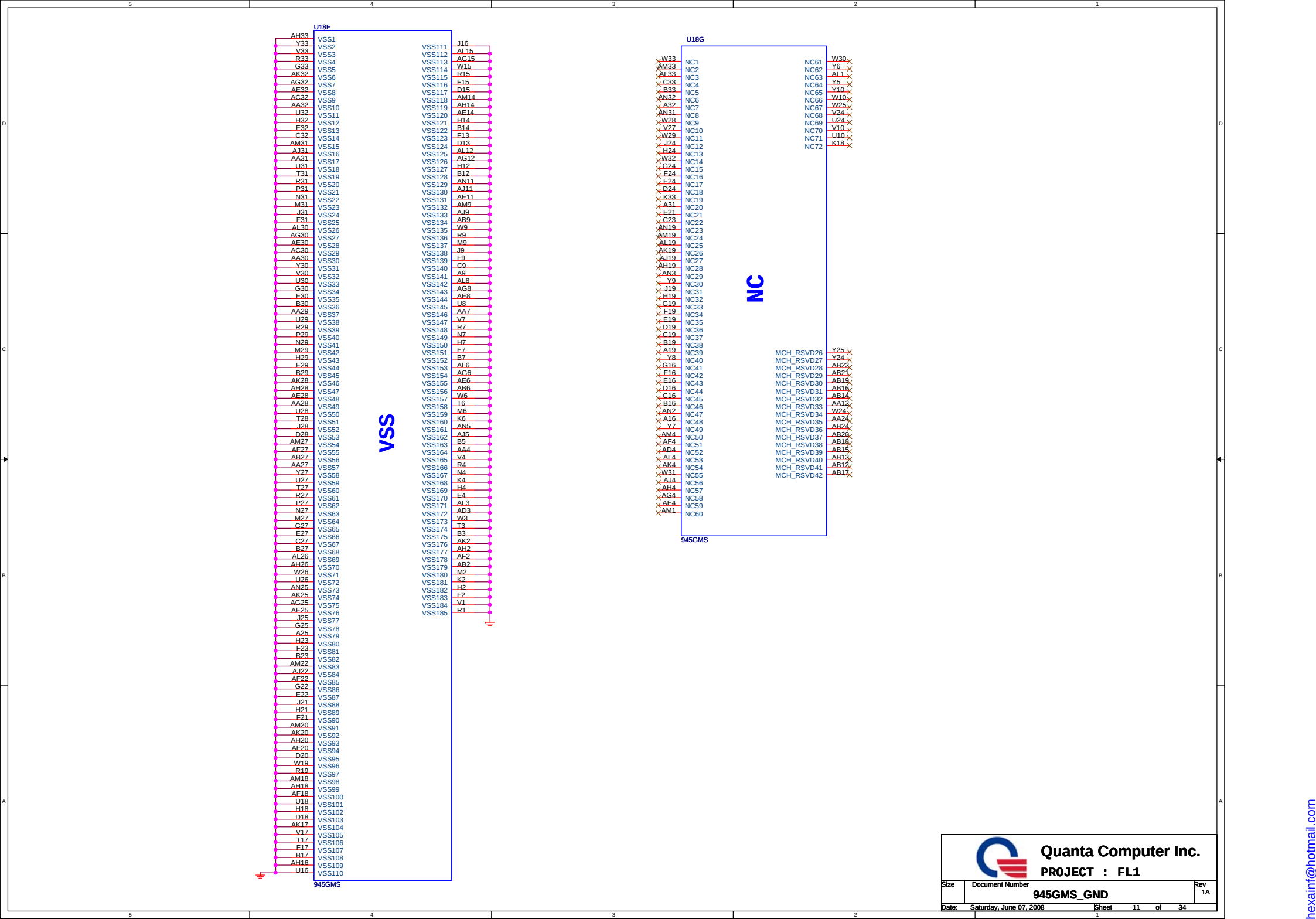


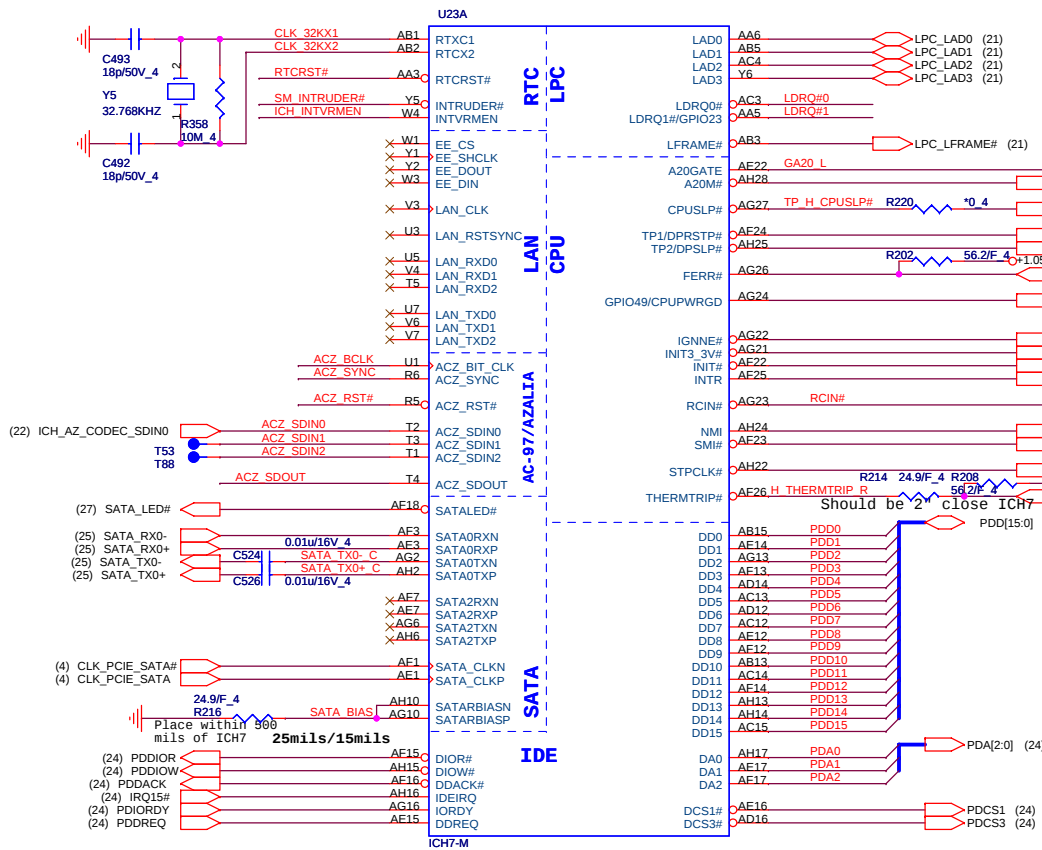
as close as possible to NB
VGA_BLU -- R (150)
VGA_GRN -- R (150)
VGA_RED -- R (150)



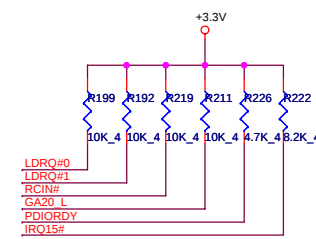




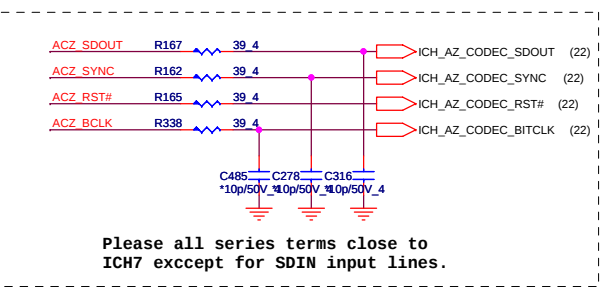
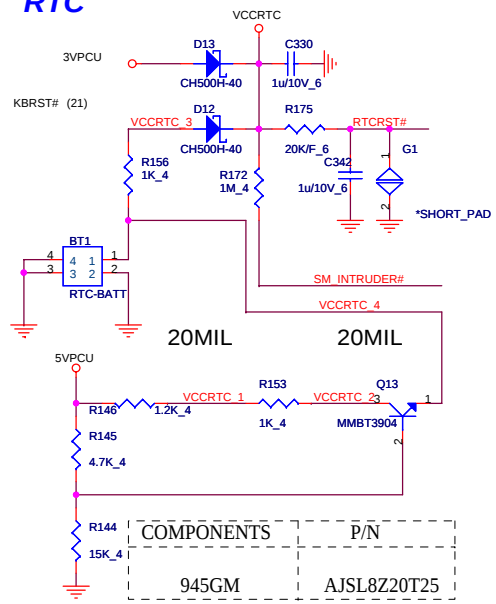




Pull-UP

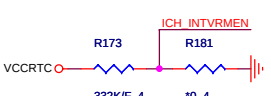


RTC

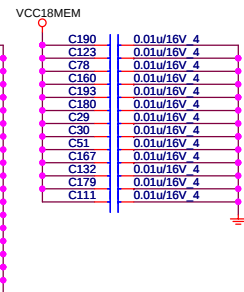
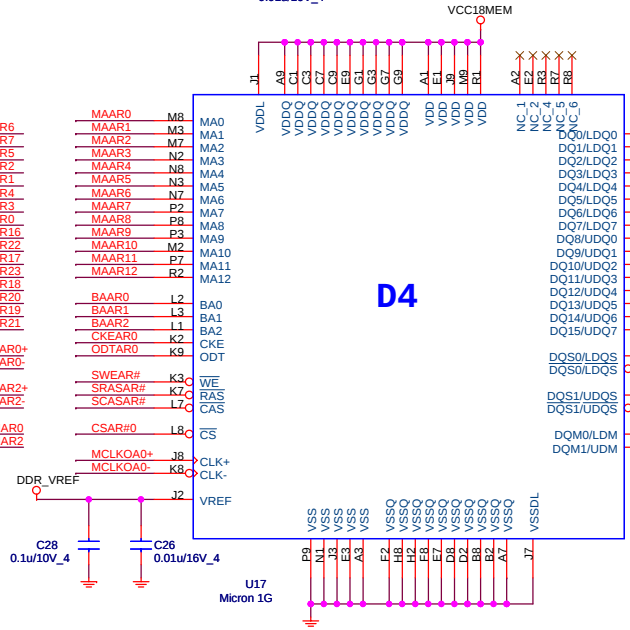
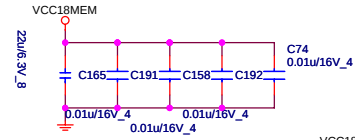
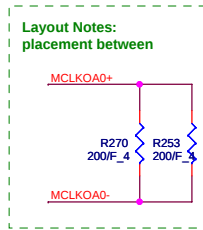
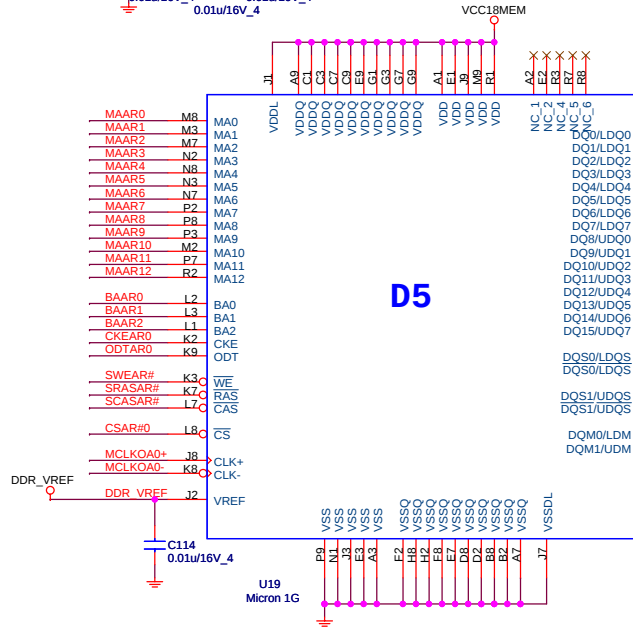
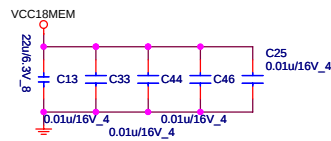
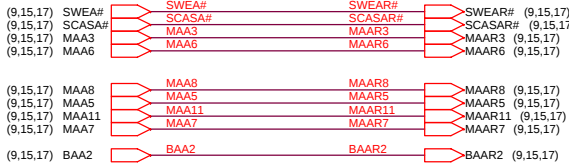
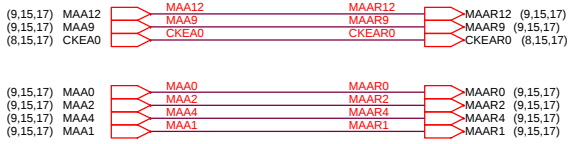
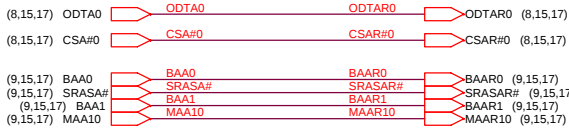
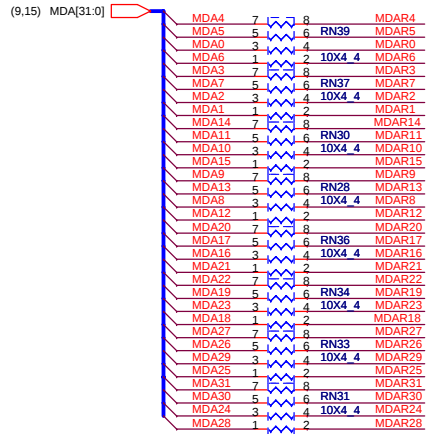
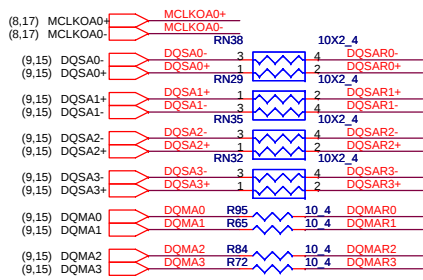


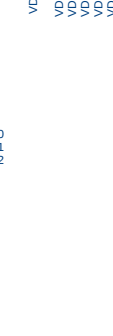
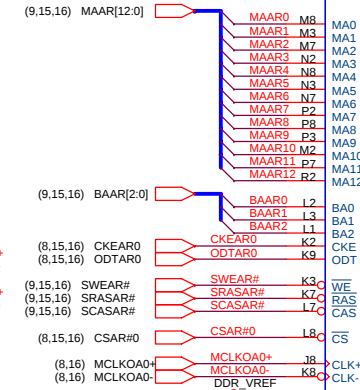
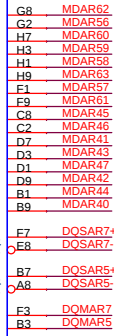
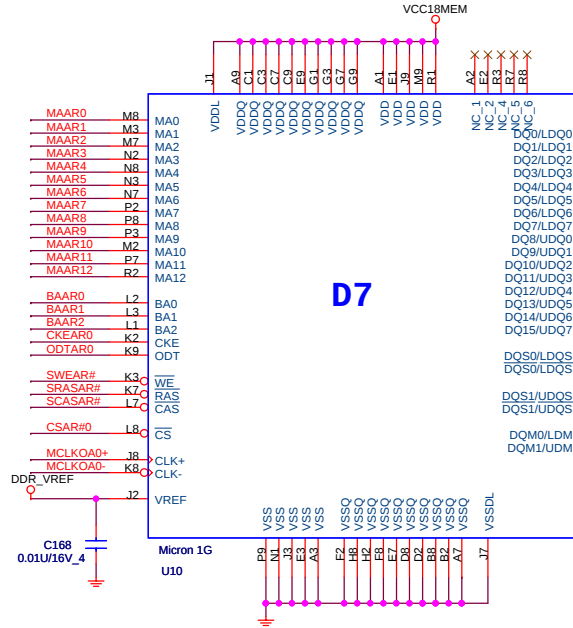
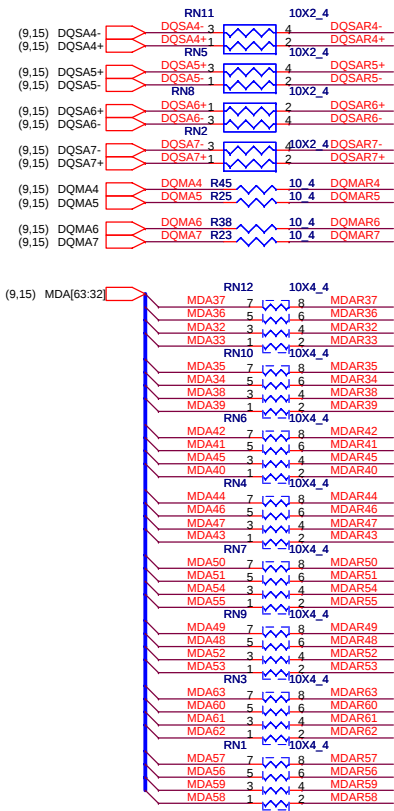
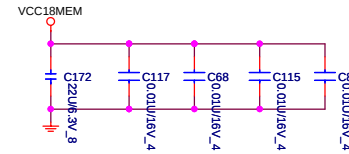
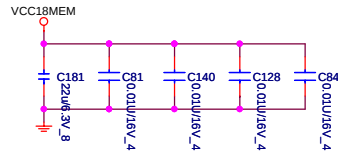
	INTVRMEN
Enable (default)	1
Disable	0

ICH7 internal VR enable strap

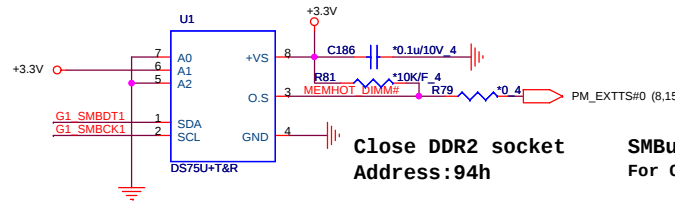




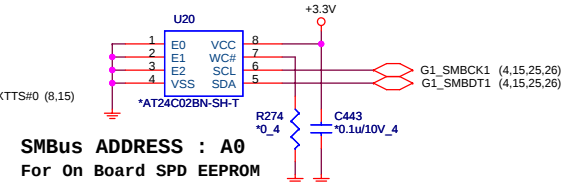




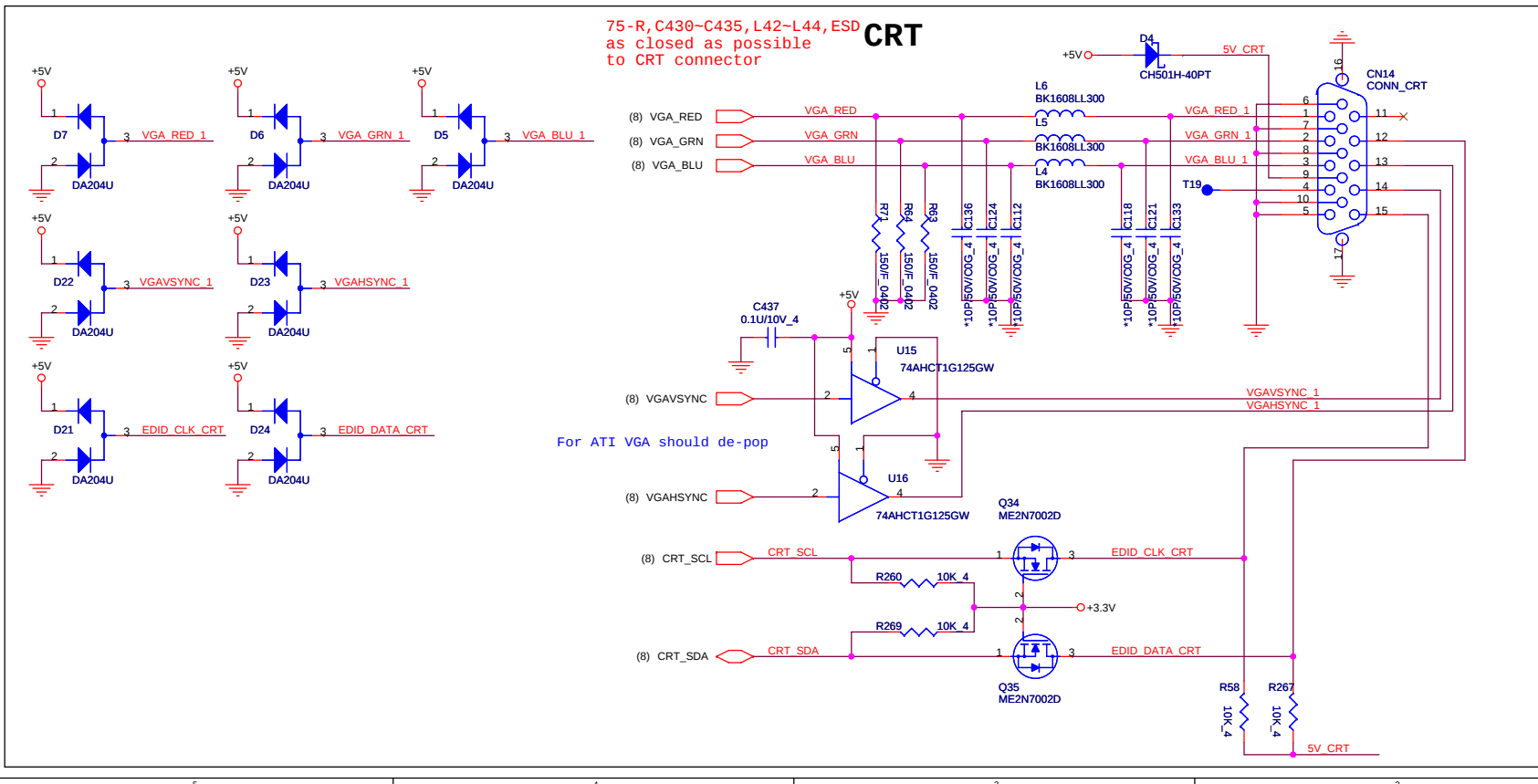
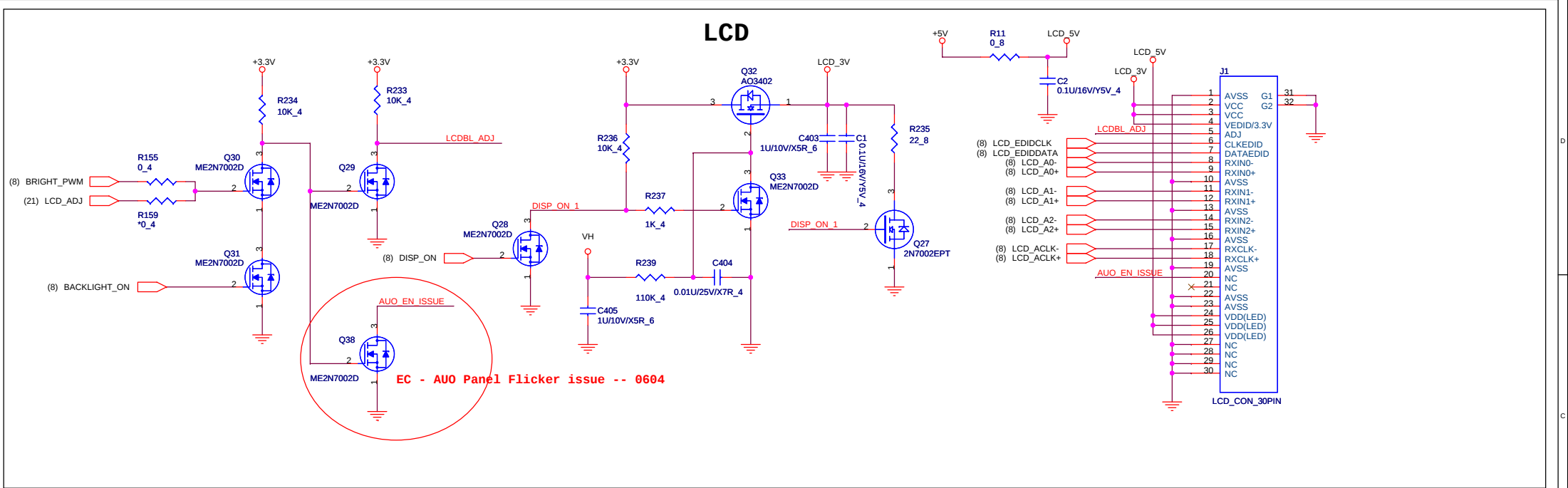
Reserve for On-Board RAM



Close DDR2 socket
Address:94h

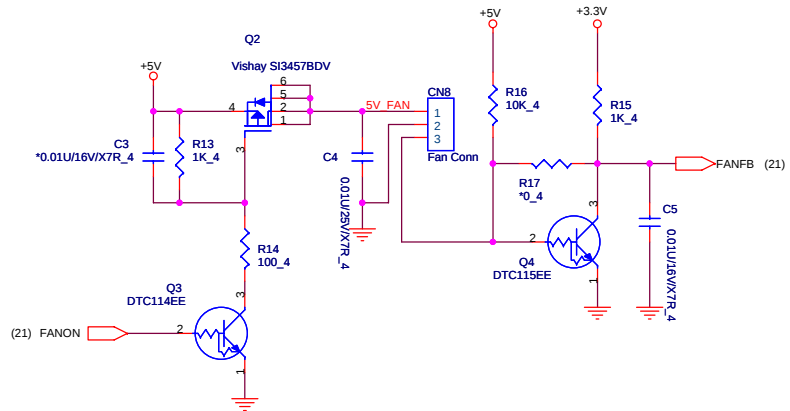


SMBus ADDRESS : A0
For On Board SPD EEPROM

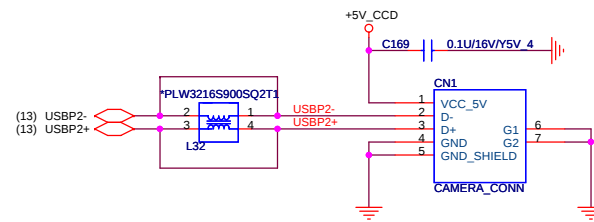
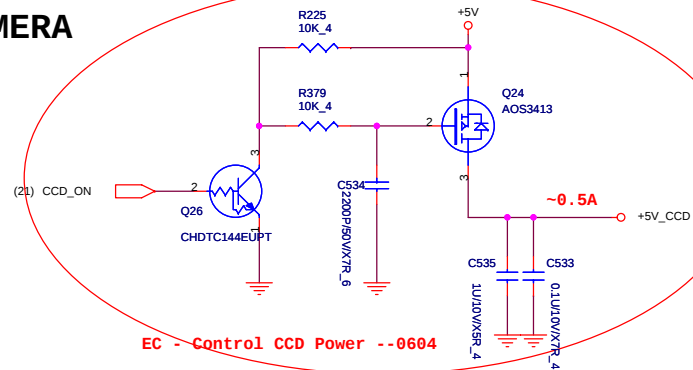


hexainf@hotmail.com

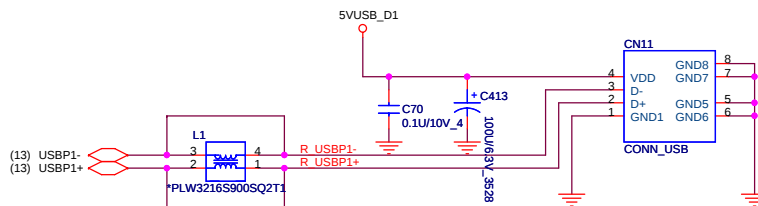
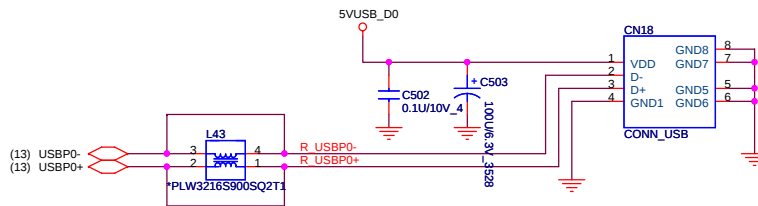
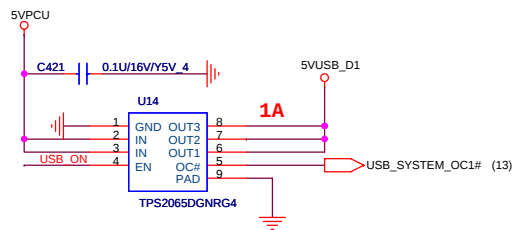
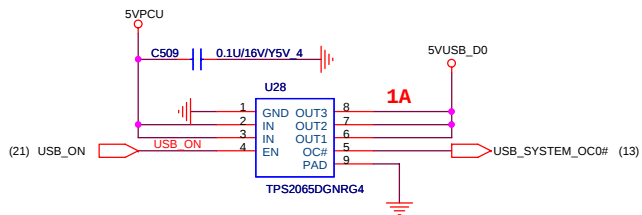
FAN



CAMERA



USB X 2 PORTS



Quanta Computer Inc.
PROJECT : FL1

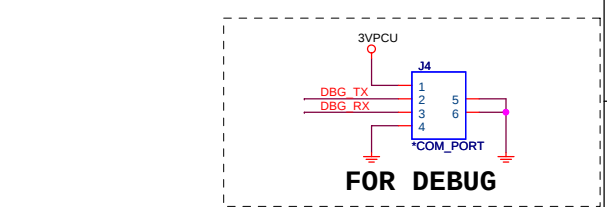
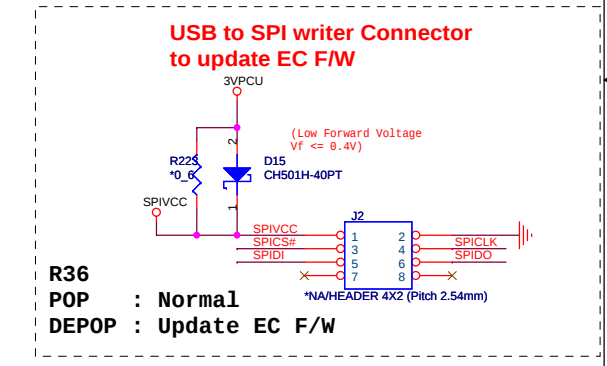
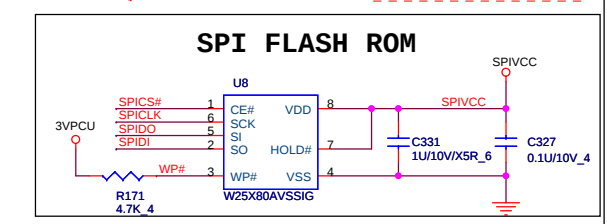
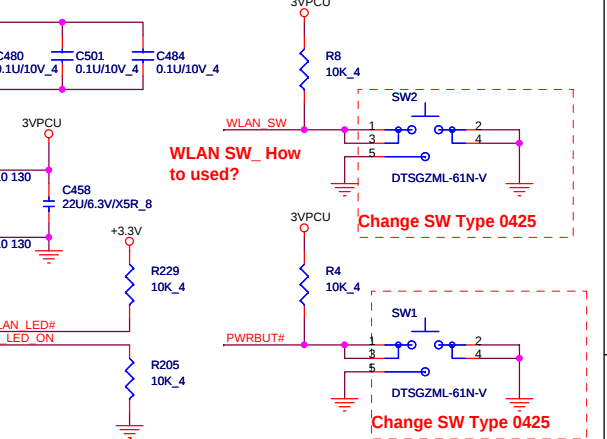
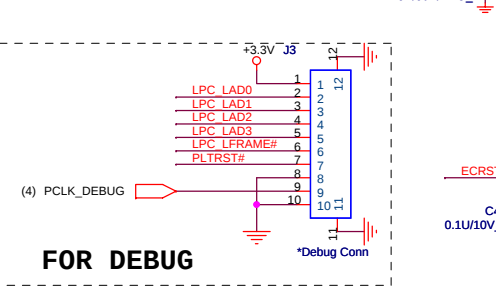
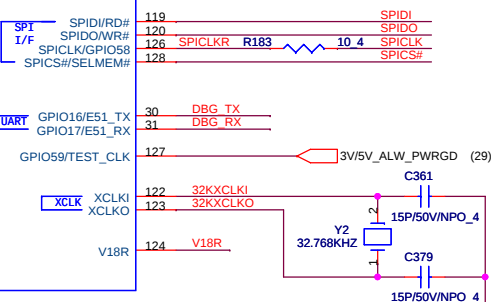
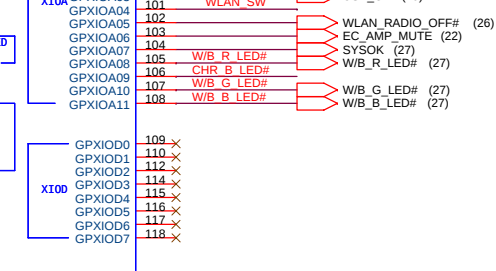
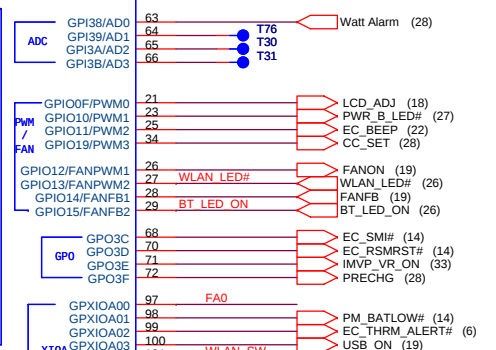
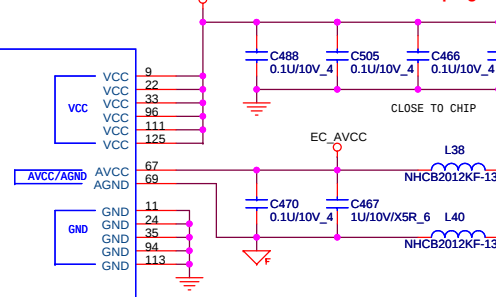
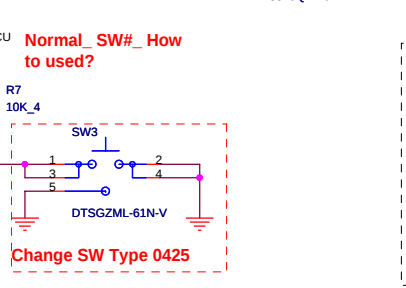
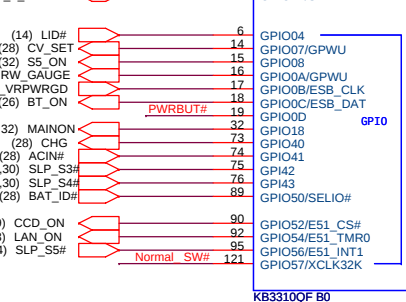
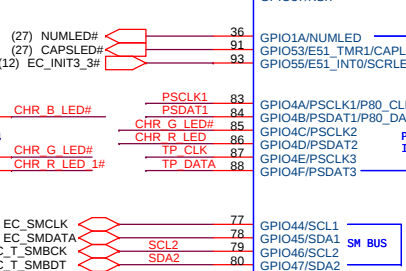
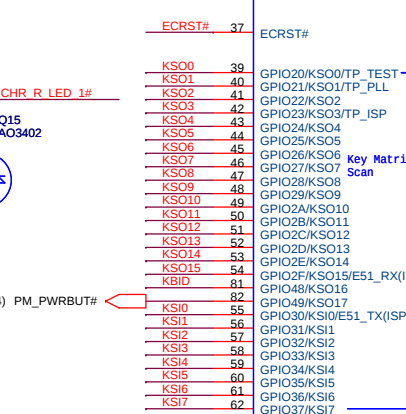
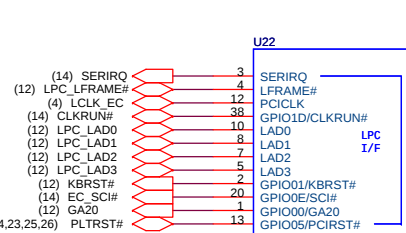
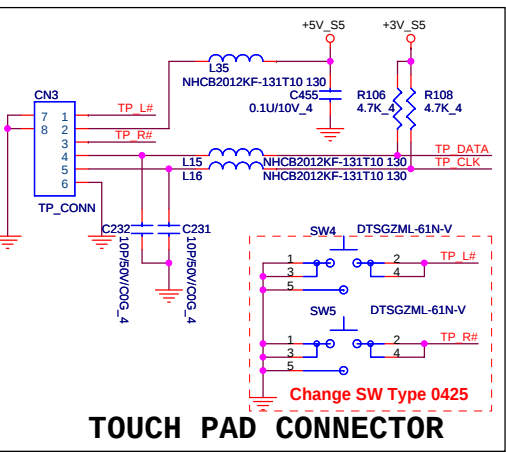
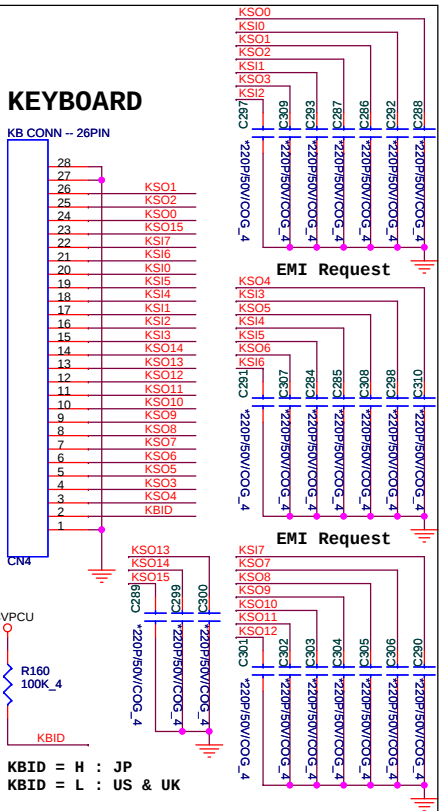
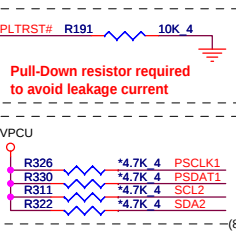
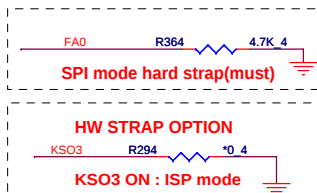
USB & FAN & CAMERA

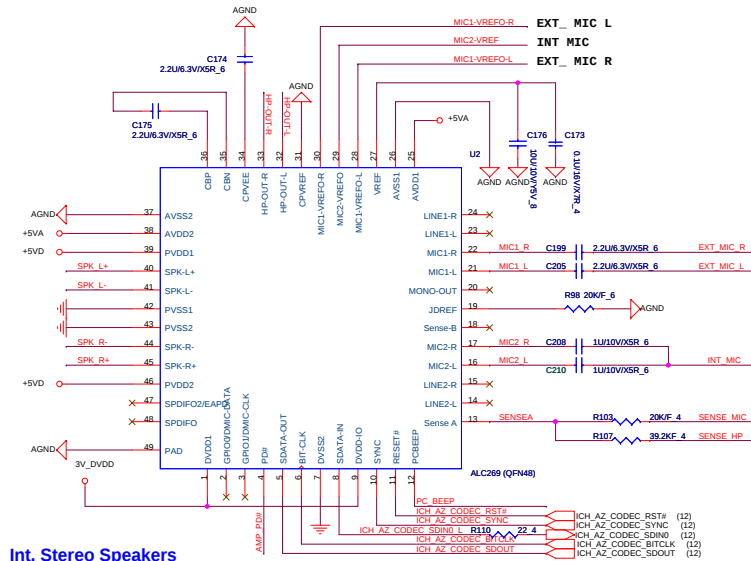
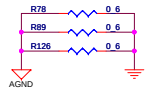
Rev
1A

Date: Saturday, June 07, 2008

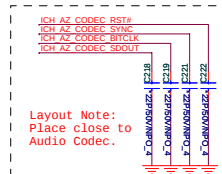
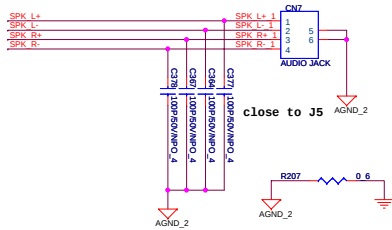
Sheet 19 of 34

nexainf@hotmail.com



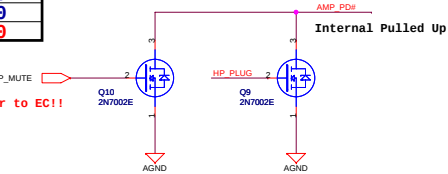


Int. Stereo Speakers
5V / 4 Ohm / 1.5W

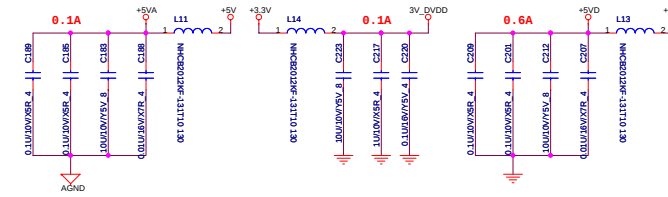
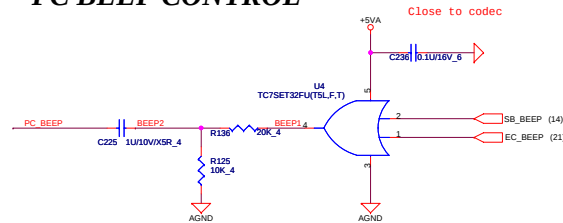


EC_AMP_MUTE	HP_PLUG	AMP_SHDN#
0	0	1
0	1	0
1	0	0
1	1	0

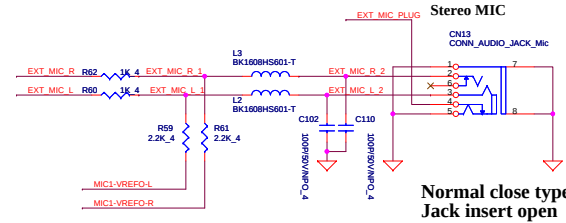
Need to connector to EC!!



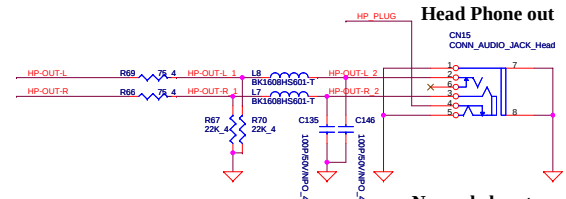
PC BEEP CONTROL



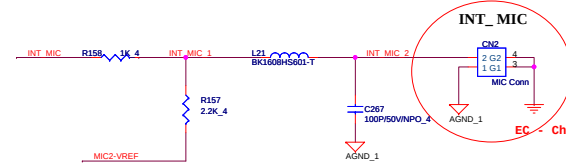
External Microphone Jack



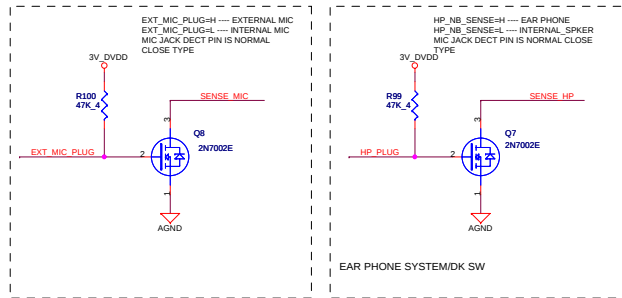
Normal close type Jack insert open

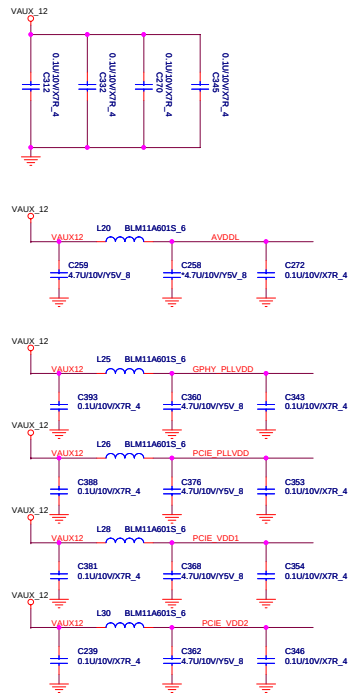


Normal close type Jack insert open

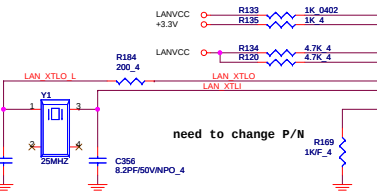


EC - Change MIC Type with wire-- 0604

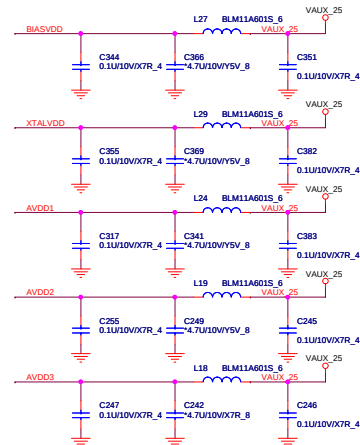
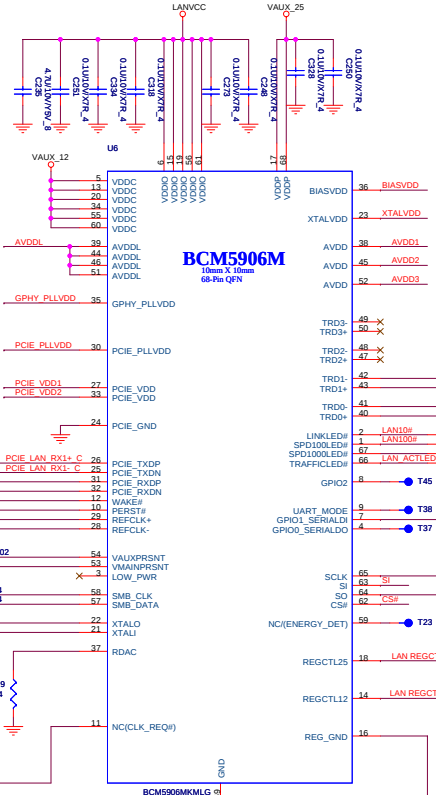
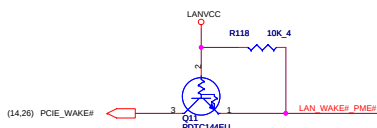




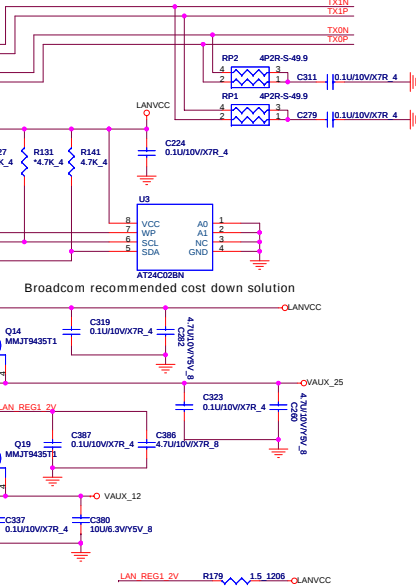
- (13) PCIE_LAN_RX1+ PCIE_LAN_RX1+ C 26
- (13) PCIE_LAN_RX1- PCIE_LAN_RX1- C 26
- (13) PCIE_LAN_TX1+ PCIE_LAN_TX1+ C 31
- (13) PCIE_LAN_TX1- PCIE_LAN_TX1- C 31
- (13,14,21,25,26) PLTRST# PLTRST# 10
- (4) CLK_PCIE_LAN+ REFCLK+ 28
- (4) CLK_PCIE_LAN- REFCLK- 28



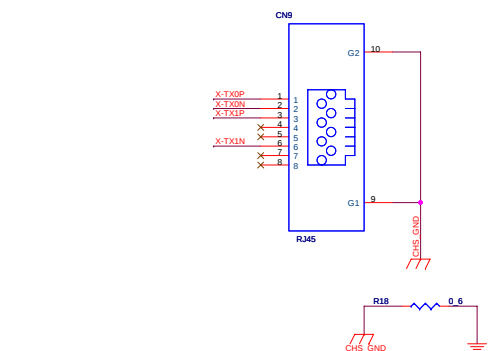
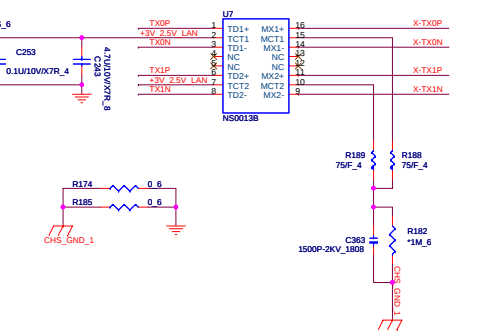
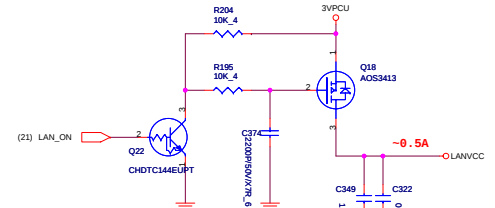
(4) LAN_CLK_REQ#

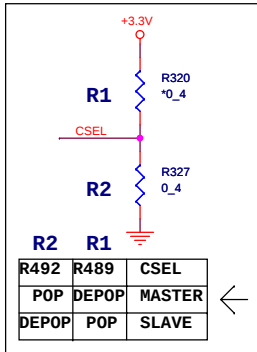
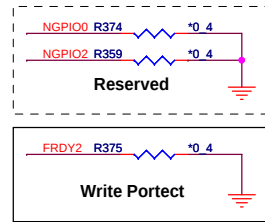
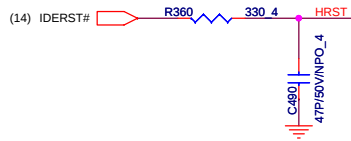
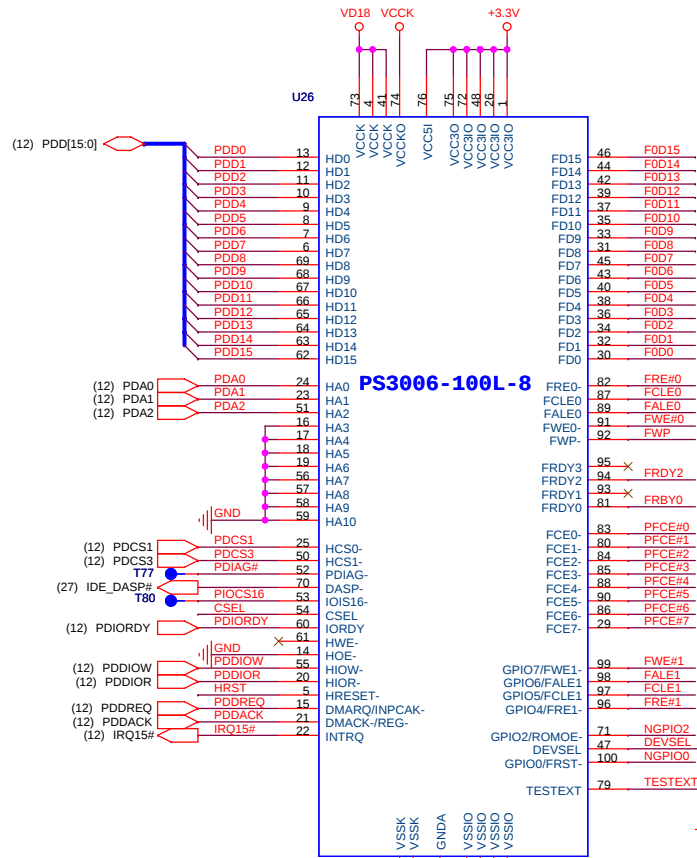
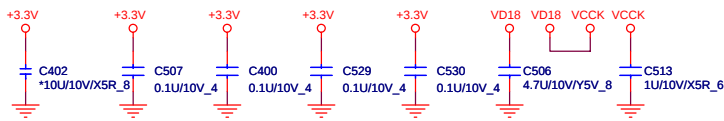


Placed near PHY chip



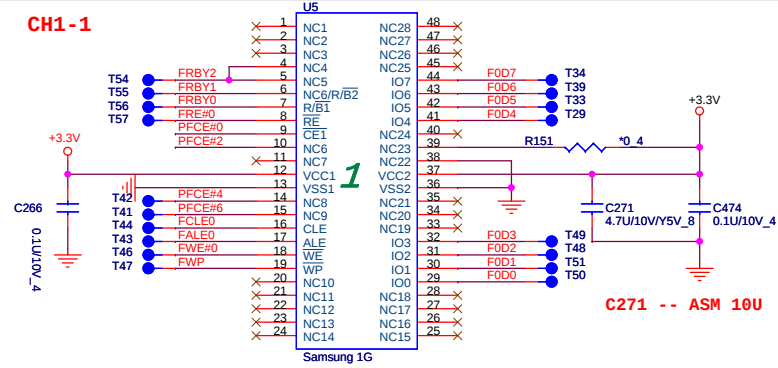
(4) LAN_CLK_REQ#



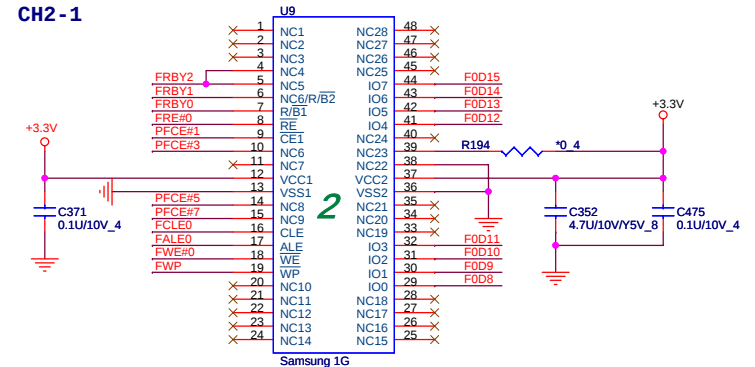


1CE	RF0
2CE	RF0, RF1
4CE	RF0, RF1, RF2

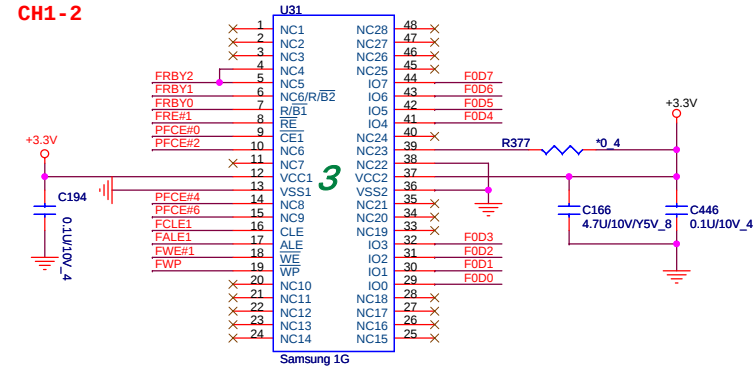
CH1-1



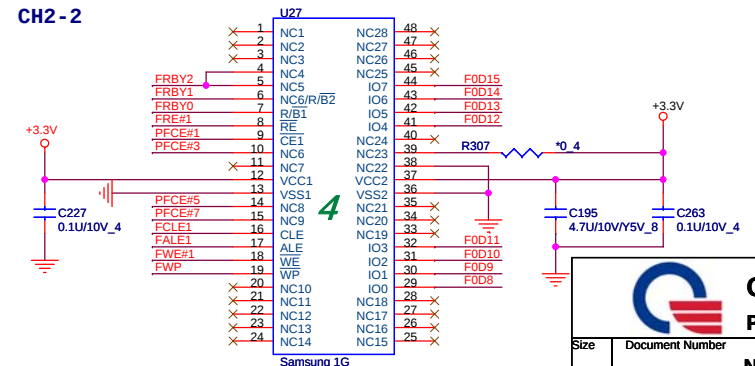
CH2-1



CH1-2



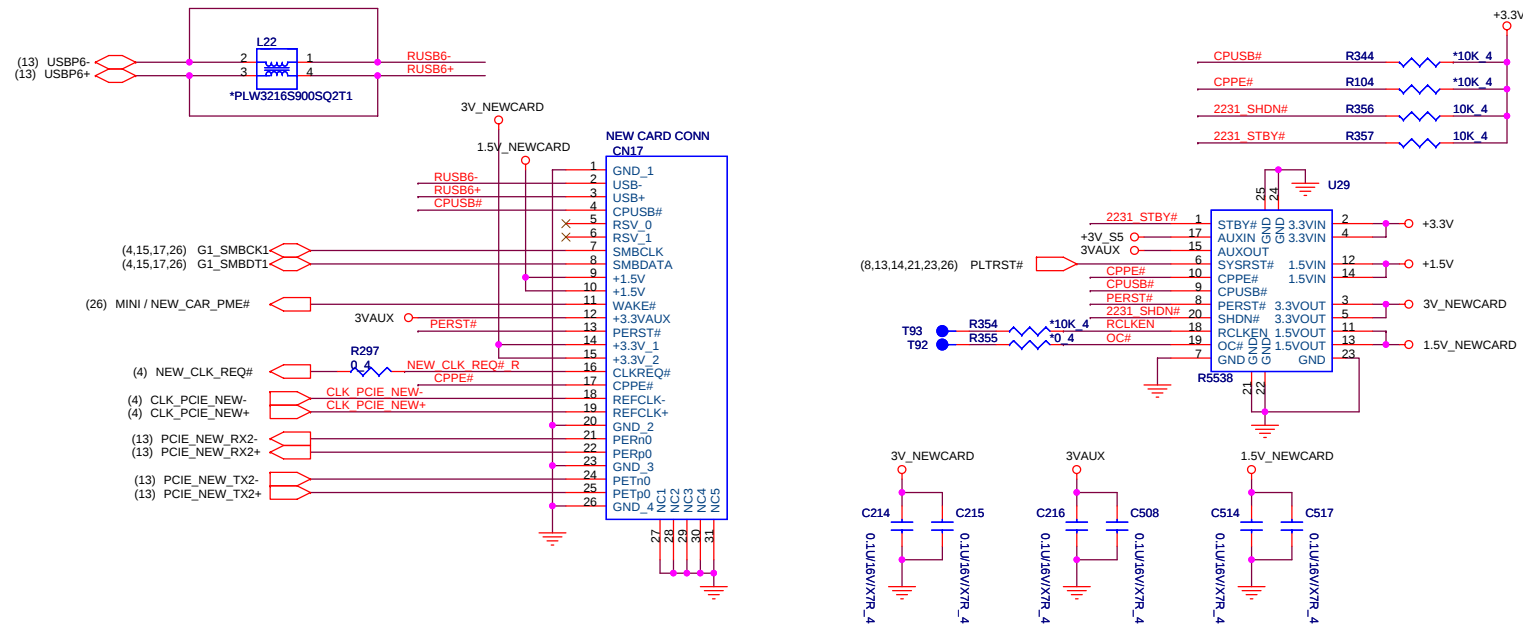
CH2-2



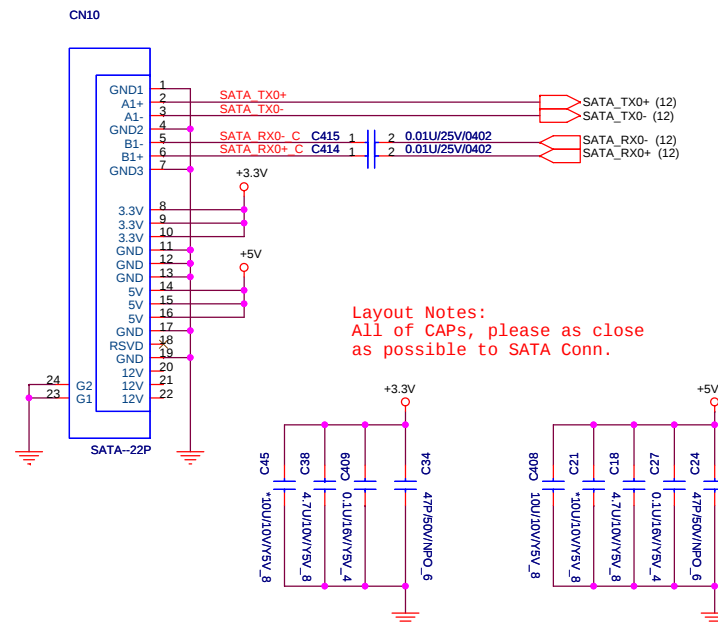
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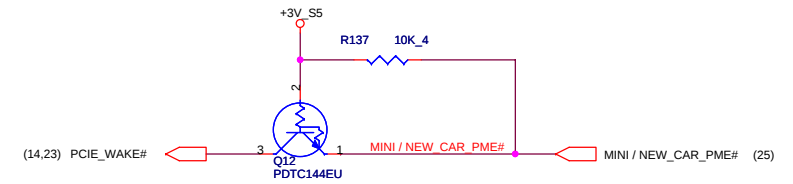
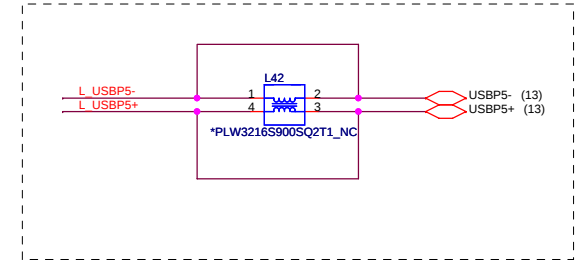
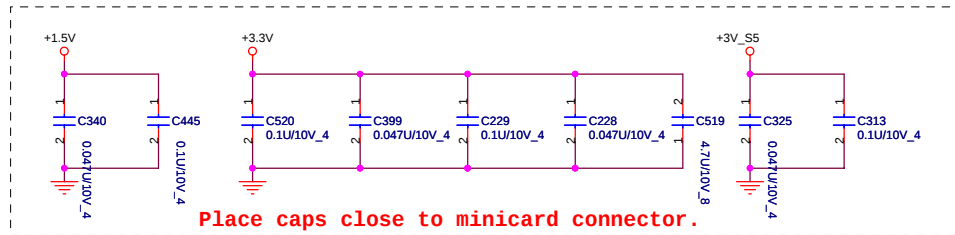
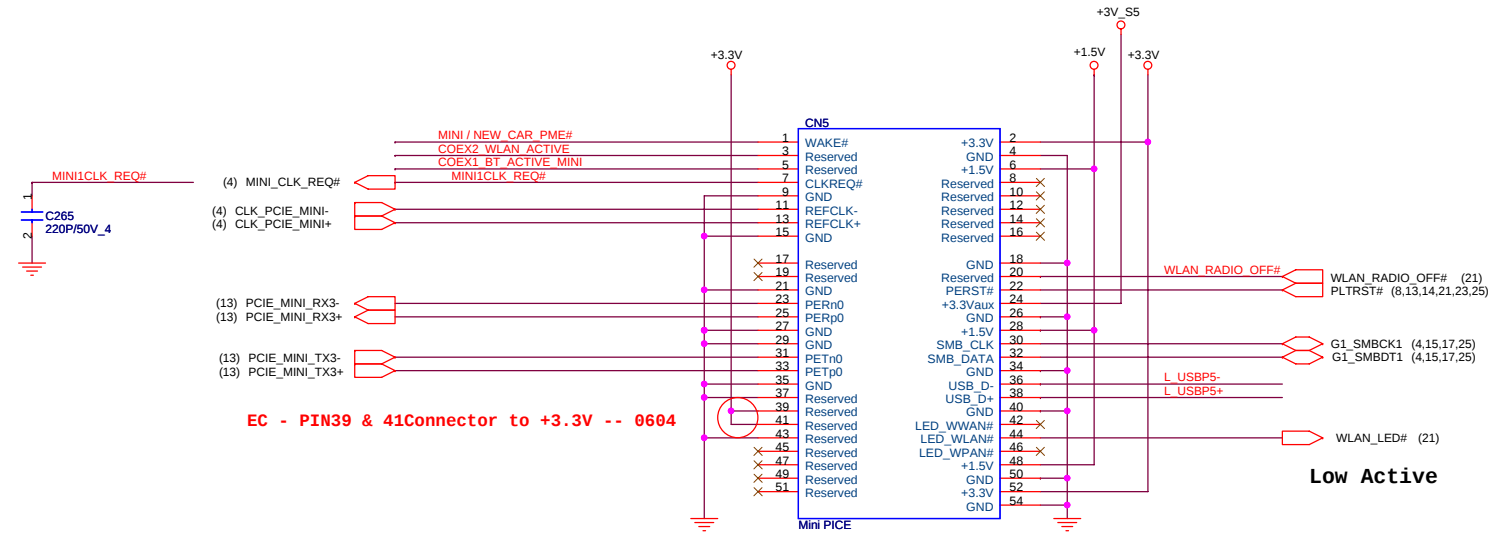
NEW CARD



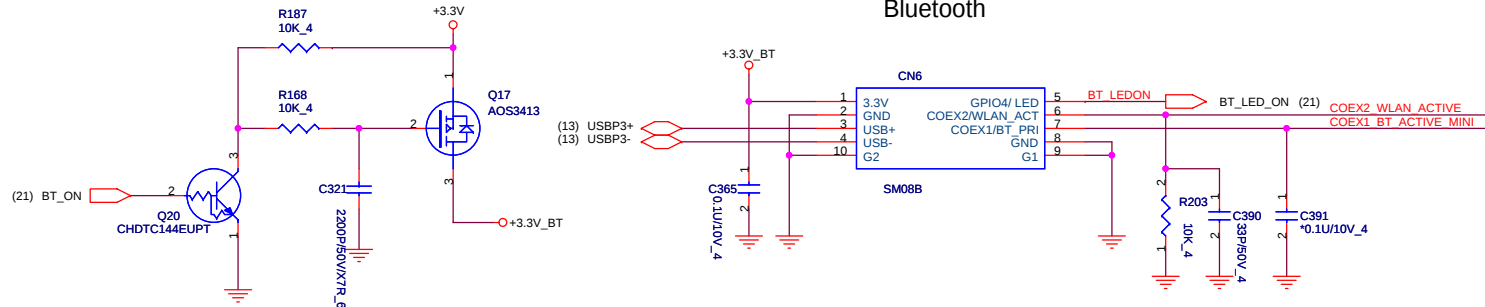
SATA CONNECTOR



MiniCard WLAN connector



Bluetooth

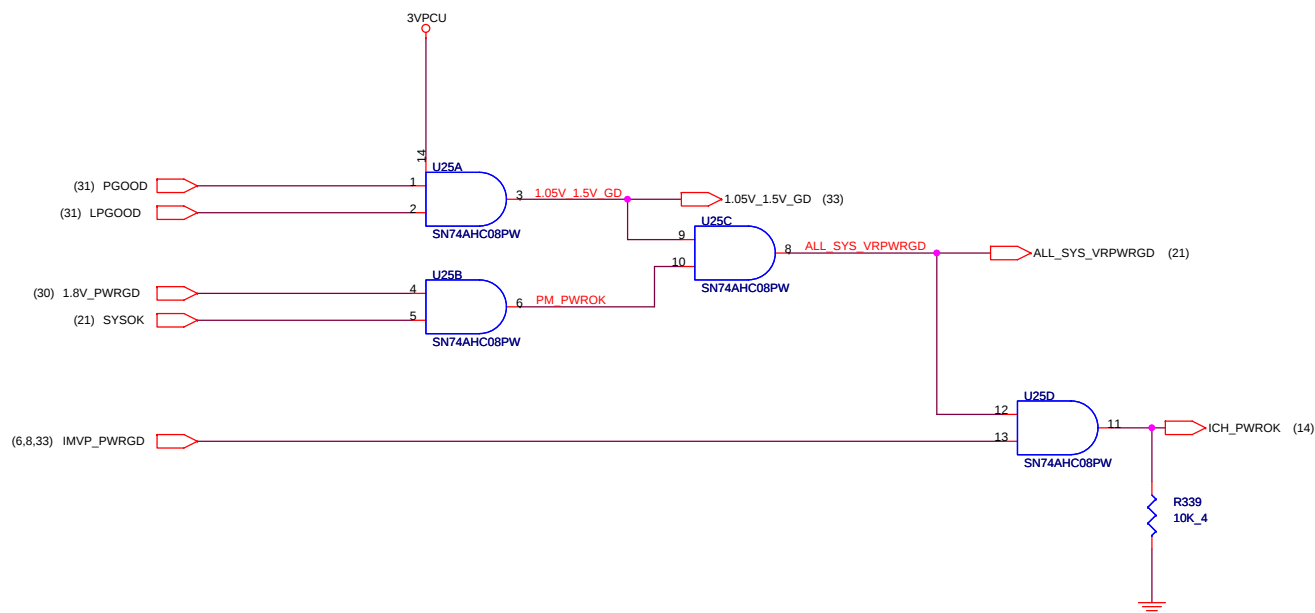


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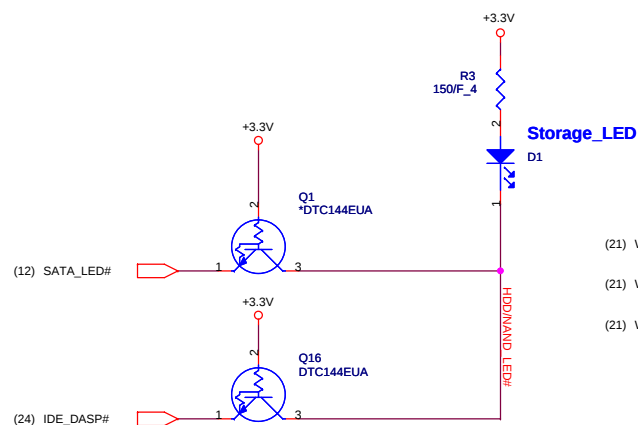
PROJECT : FL1

MINI CARD (WLAN) / BT

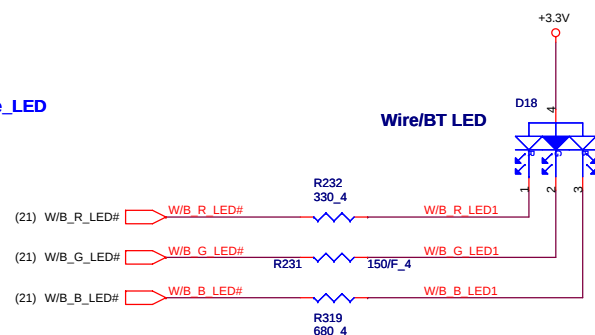
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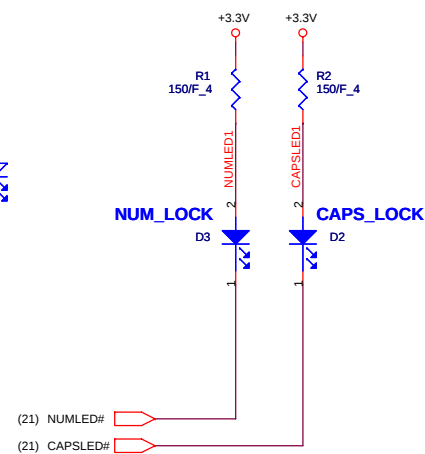
SATA/PATA LED



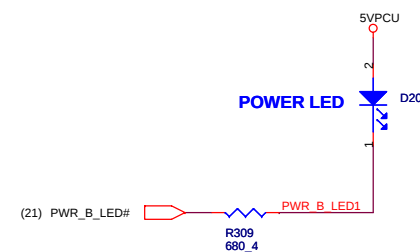
WLAN & BlueTooth LED



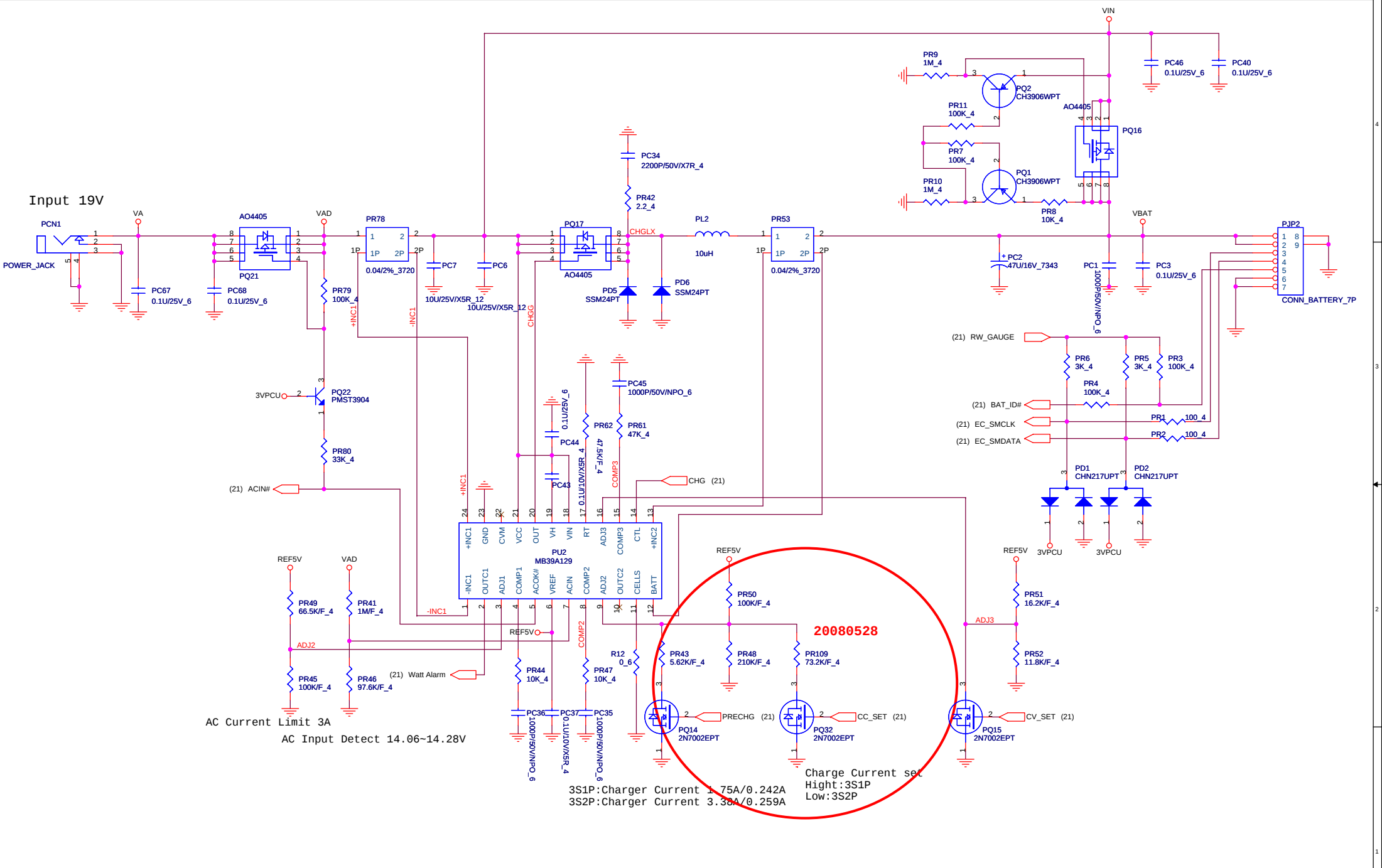
Number & CAPS Lock LED



Power Button LED



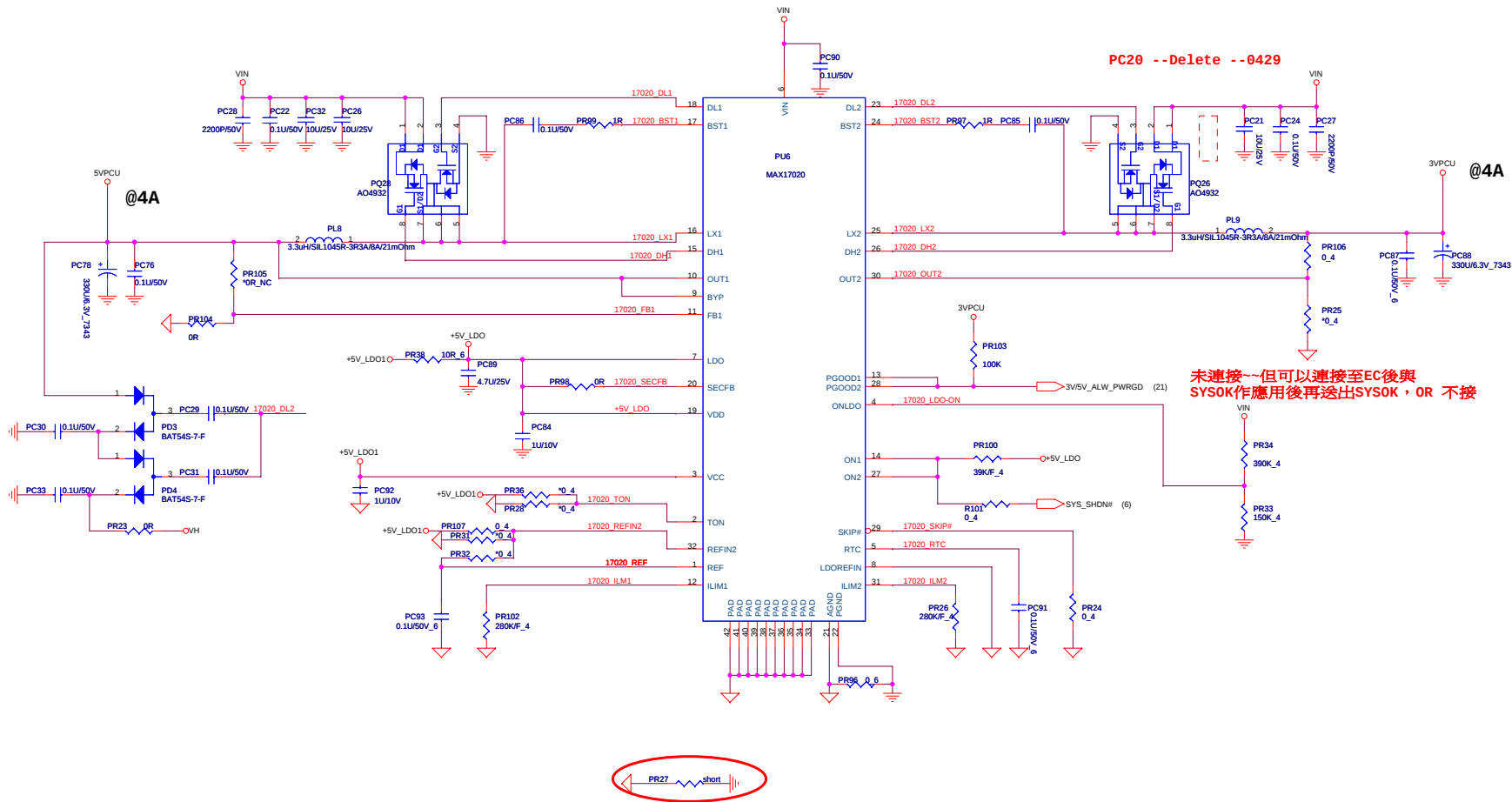
	Q1 & R5	Q16 & R6
SATA	ASM	No - ASM
PATA	No - ASM	ASM

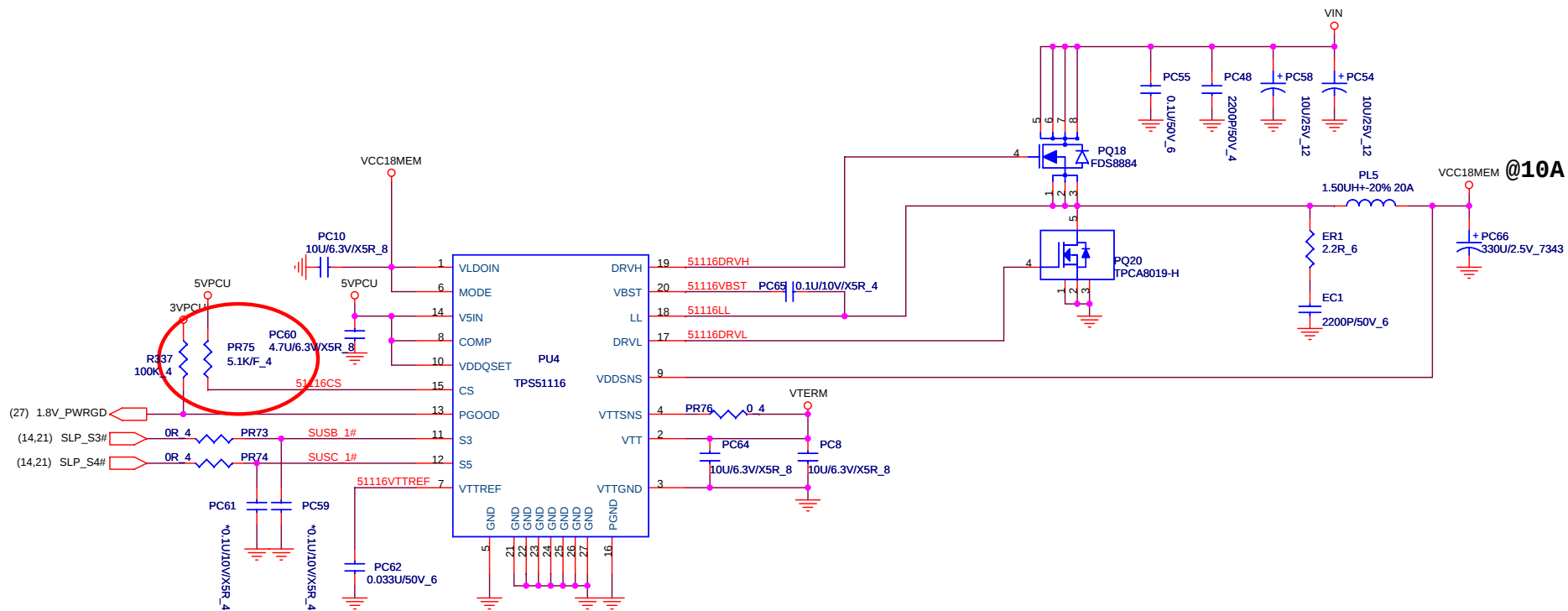


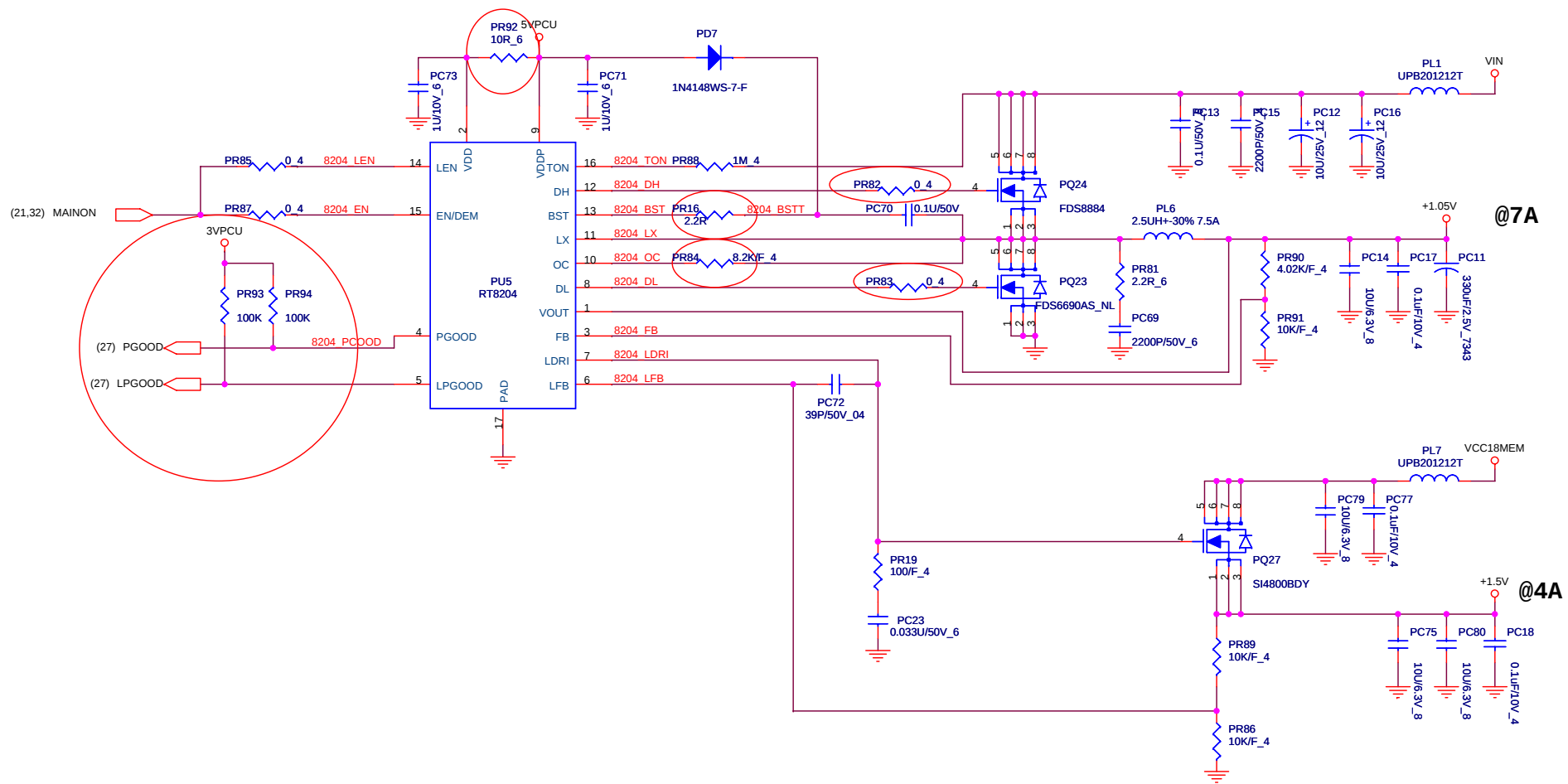
AC Current Limit 3A
AC Input Detect 14.06~14.28V

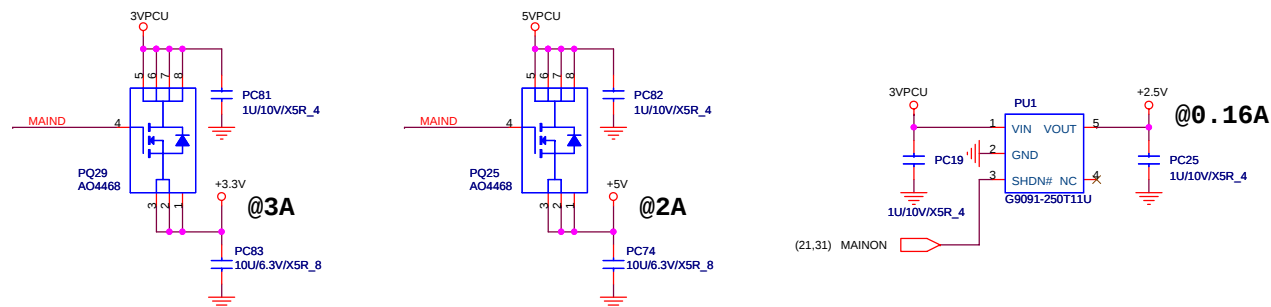
3S1P:Charger Current 1.75A/0.242A
3S2P:Charger Current 3.38A/0.259A

Charge Current set
High:3S1P
Low:3S2P

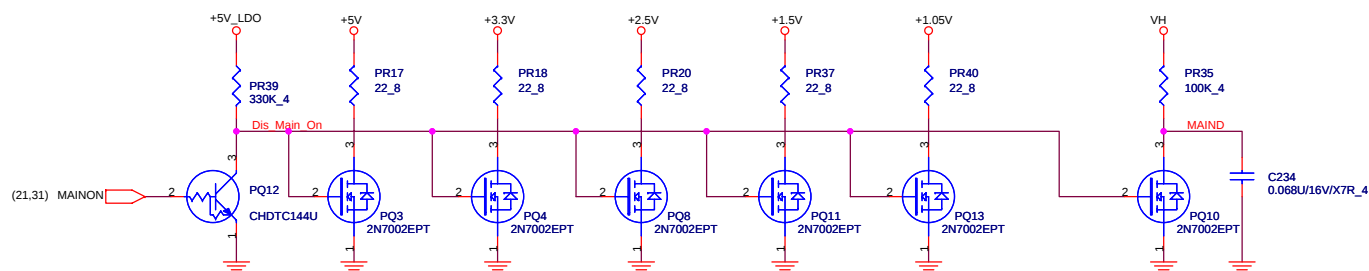




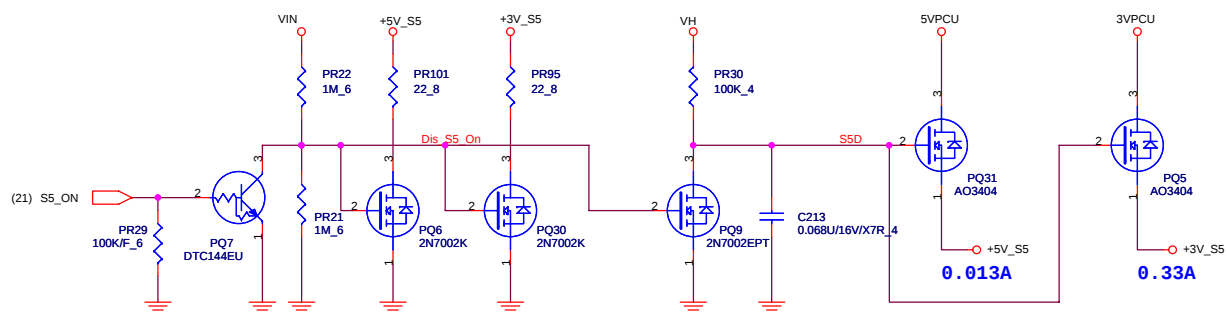




Discharge Circuit Controller by MAINON

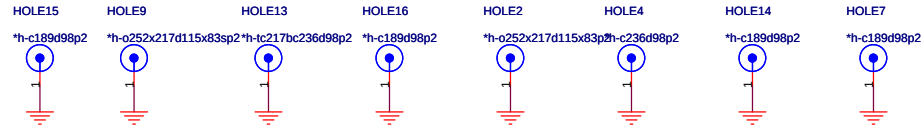


Discharge Circuit Controller by S5_ON

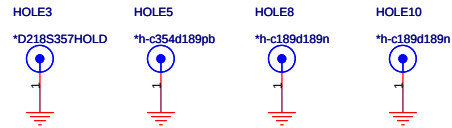


SCREW HOLE

正面沒有接地銅箔, 背面有



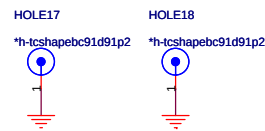
(d: 鑽孔的孔徑)背面有露銅



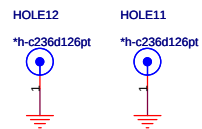
108HOLD



HDD -- HOLD



240HOLD



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