

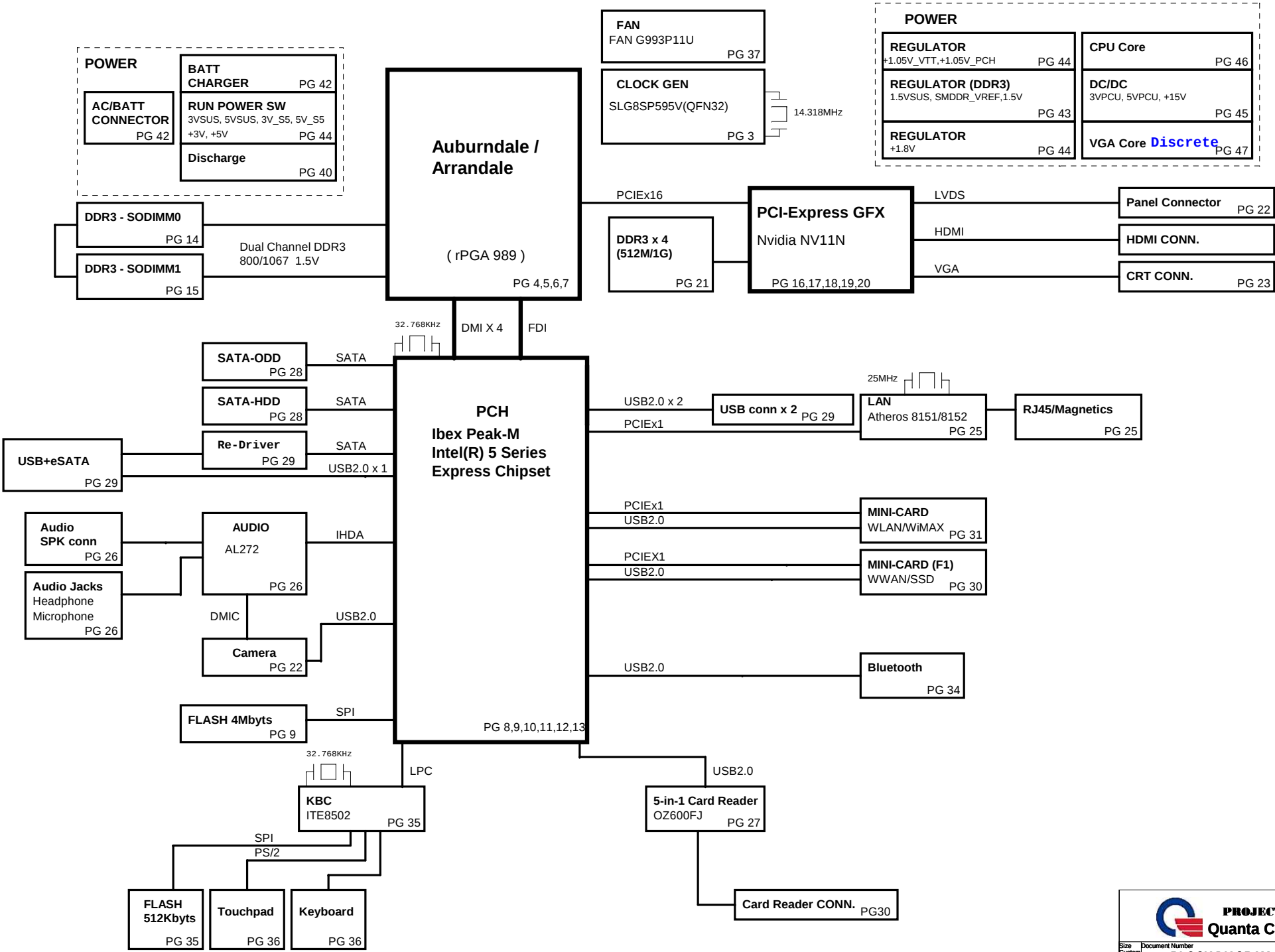
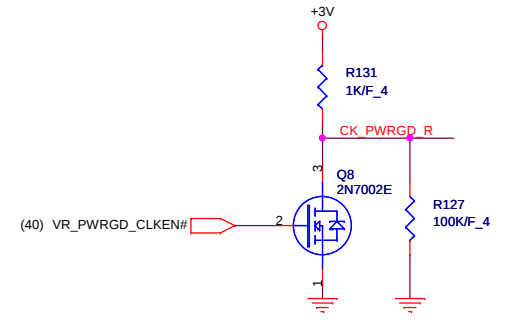
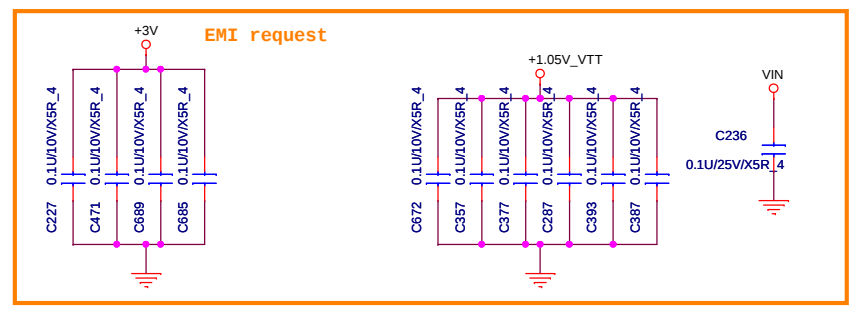
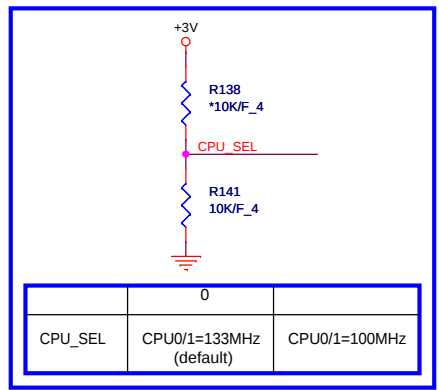
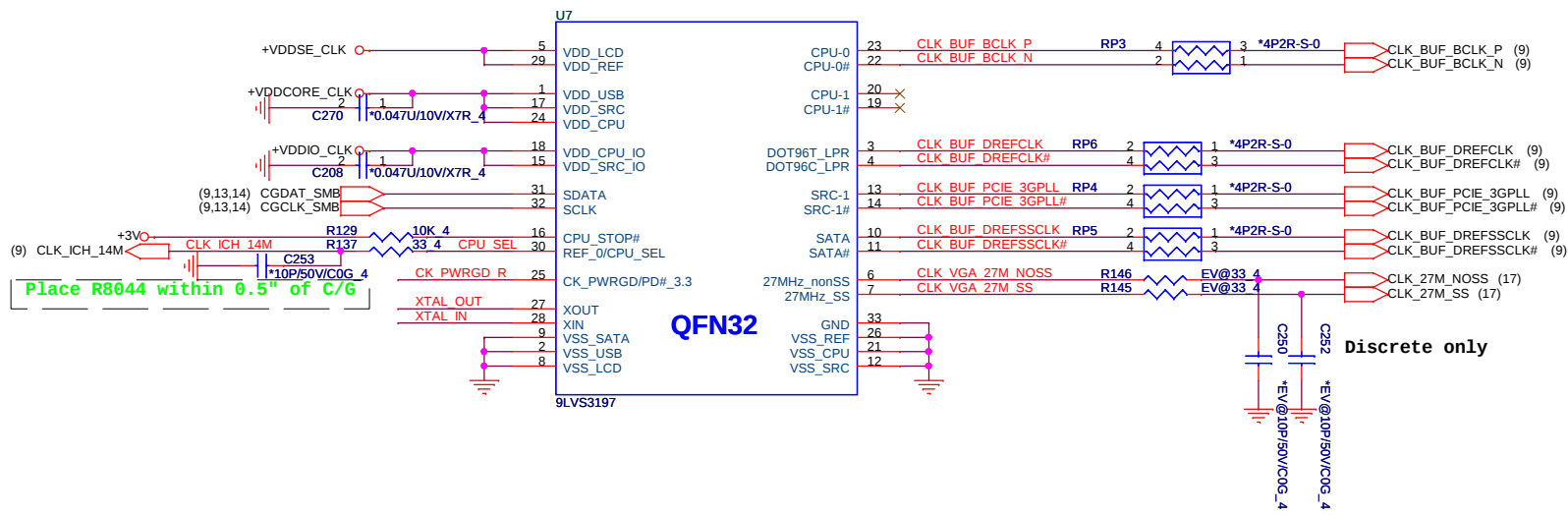
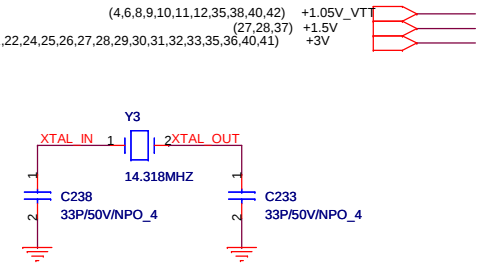
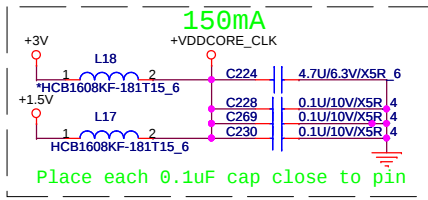
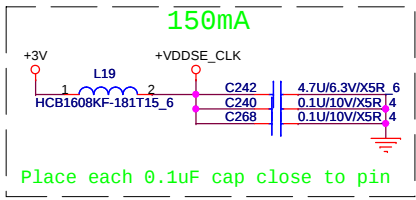
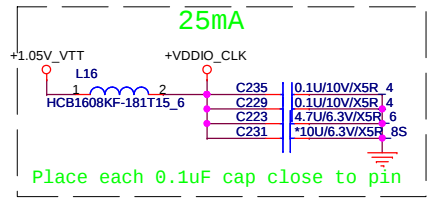


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Power States

| POWER PLANE      | VOLTAGE      | PAGE                                                                                                 | DESCRIPTION                                                        | CONTROL SIGNAL | ACTIVE IN |
|------------------|--------------|------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------|----------------|-----------|
| VIN              | 10V--+20V    | 23,32,43,44,45,46,47,48,49,50                                                                        | MAIN POWER                                                         |                | S0-S5     |
| +3VRTC           | +3.0V--+3.3V | 9,12,41                                                                                              | RTC                                                                |                | S0-S5     |
| 3VPCU            | +3.3V        | 9,23,27,30,32,35,39,41,43,44,47                                                                      | ITE8052 POWER                                                      | 3V5V_EN        | S0-S5     |
| 5VPCU            | +5V          | 14,43,44,45,46,47,49,50                                                                              | DC/DC POWER IC SOURCE                                              | 3V5V_EN        | S0-S5     |
| +15V             | +15V         | 23,38,43,45,46,47                                                                                    | LARGE POWER                                                        | 3V5V_EN        | S0-S5     |
| LANVCC           | +3.3V        | 27,43                                                                                                | LAN POWER                                                          | LAN_ON         |           |
| 5V_S5            | +5V          | 12,29,30,43                                                                                          | PCH SUS POWER                                                      | S5_ON          | S0-S3     |
| 3V_S5            | +3.3V        | 8,9,10,11,12,43,52                                                                                   | Sys Management,PCH Resume Well,Intel HD Audio,USB,WLAN WIMAX POWER | S5_ON          | S0-S3     |
| 5VSUS            | +5V          | 23,39,43,48                                                                                          | SLP_S4# CTRLD POWER                                                | SUSON          | S0-S3     |
| 3VSUS            | +3.3V        | 14,15,30,34,41,43,49                                                                                 | SLP_S4# CTRLD POWER                                                | SUSON          | S0-S3     |
| 1.5VSUS          | +1.5V        | 4,6,14,15,43,45,46,49,50                                                                             | SODIMM POWER                                                       | SUSON          | S0-S3     |
| 0.75VSMDDR_VTERM | +0.75V       | 14,15,43,45                                                                                          | DDR3 SODIMM REFERENCE POWER                                        | MAIN_ON        | S0        |
| +5V              | +5V          | 12,18,23,24,25,26,28,35,37,41,43,44                                                                  | SLP_S3# CTRLD POWER                                                | MAIN_ON        | S0        |
| +3V              | +3.3V        | 3,4,8,9,10,11,12,14,15,17,23,25,26,27,28,29,30,31,32,33,34,36,37,38,39,40,41,43,44,45,46,47,48,50,52 | SLP_S3# CTRLD POWER                                                | MAIN_ON        | S0        |
| +1.8V            | +1.8V        | 6,12,17,18,21,22,33,43,50                                                                            | LVDS,NVM POWER                                                     | MAIN_ON        | S0        |
| +1.5V            | +1.5V        | 12,18,19,20,31,32,34,45,46                                                                           | Mini PCIe,Express Card POWER                                       | MAIN_ON        | S0        |
| +1.05V_VTT       | +1.05V       | 4,6,11,12,43,46,48,52                                                                                | AuBurndale VTT POWER                                               | MAIN_ON        | S0        |
| +1.05V_PCH       | +1.05V       | 3,10,12,43,46,52                                                                                     | PCH CORE POWER                                                     | 1.05V_RUN_ON   | S0        |
| +VCC_GFX_CORE    | +0.9V--+1.2V | 18,21,43,49                                                                                          | VGA CORE POWER                                                     | GFXVR_EN       | S0        |
| VCC_CORE         |              | 6,43,48                                                                                              | CPU CORE POWER                                                     | VRON           | S0        |
| LCDVCC           | +3.3V        | 23                                                                                                   | LCD Power                                                          | ENVDD          | S0        |
| +5V_ODD          | +5V          | 28                                                                                                   | ODD Power                                                          | MAIN_ON        | S0        |
| +5V_HDD          | +5V          | 28                                                                                                   | HDD Power                                                          | MAIN_ON        | S0        |
| BAT-V            | +10V--+17V   | 44                                                                                                   | MAIN BATTERY                                                       | CHG_PBATT      | S0-S5     |

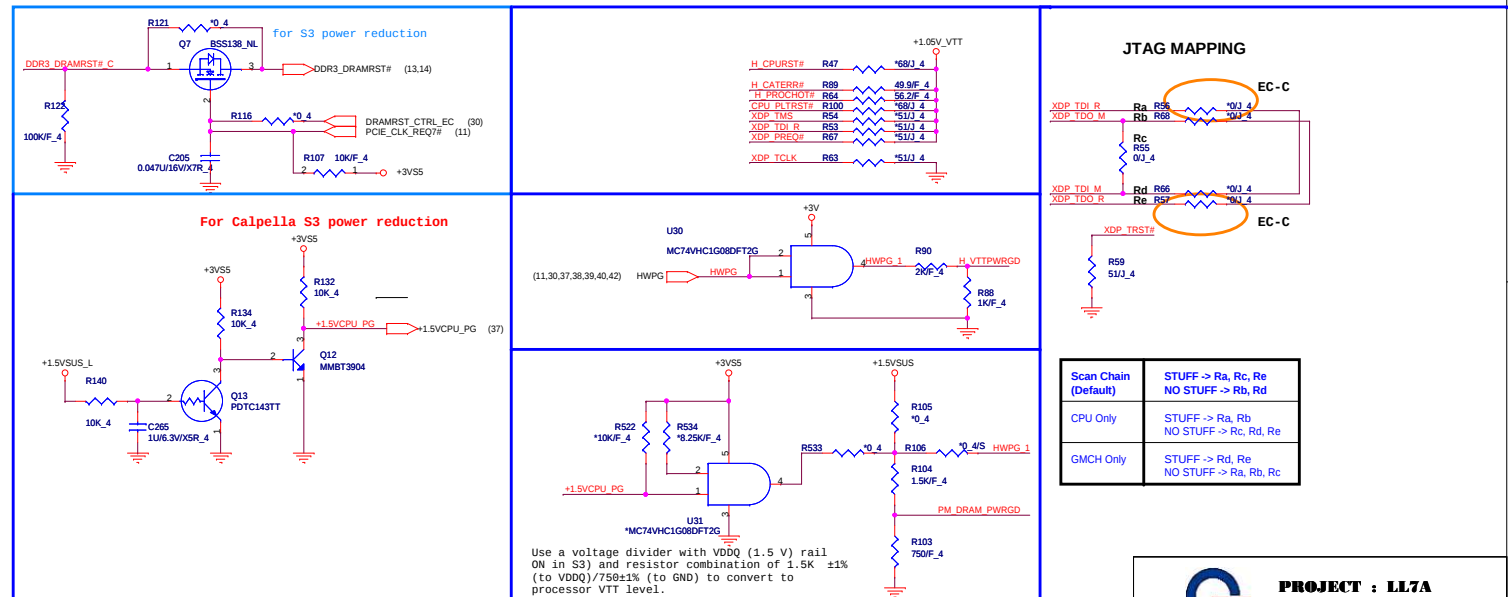
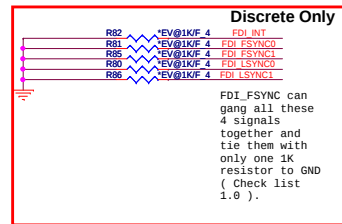
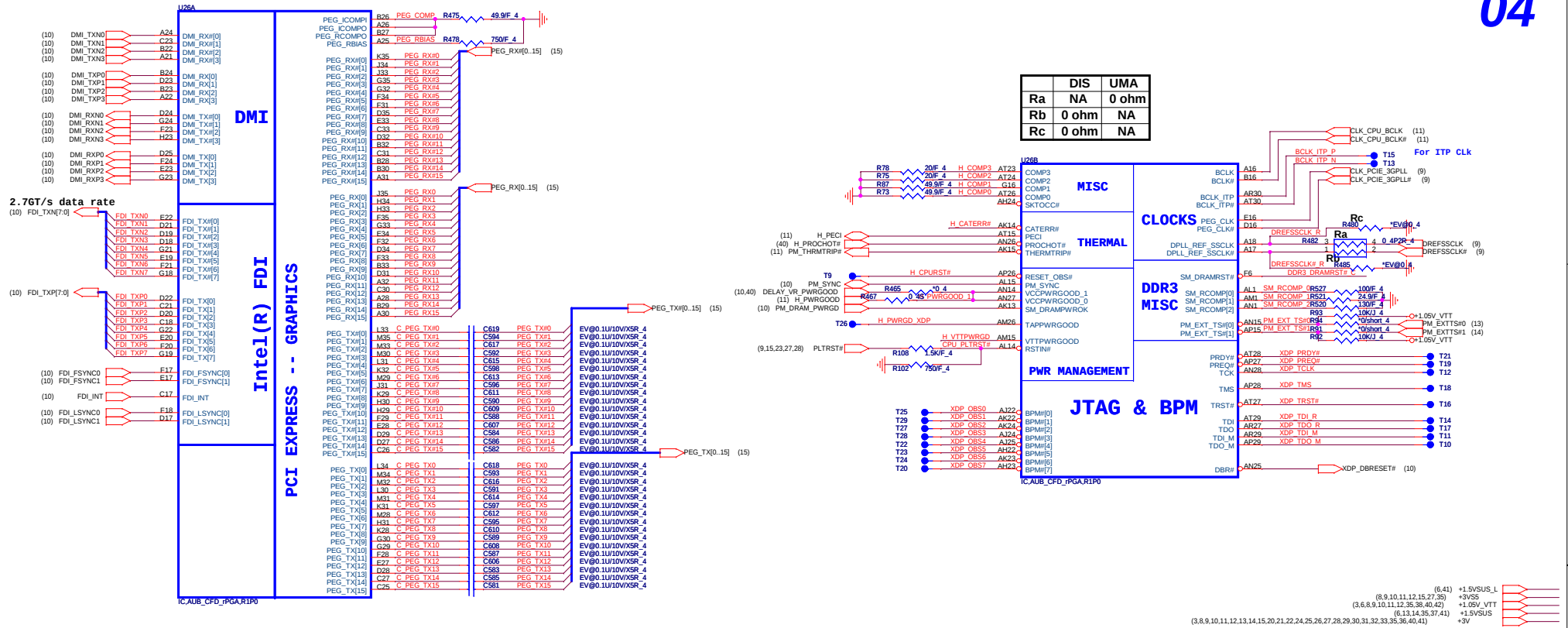


**PROJECT : LL7A**  
**Quanta Computer Inc.**

Size B Document Number  
**CLOCK GEN(9LVS3197)**

Date: Thursday, April 08, 2010 Sheet 3 of 42

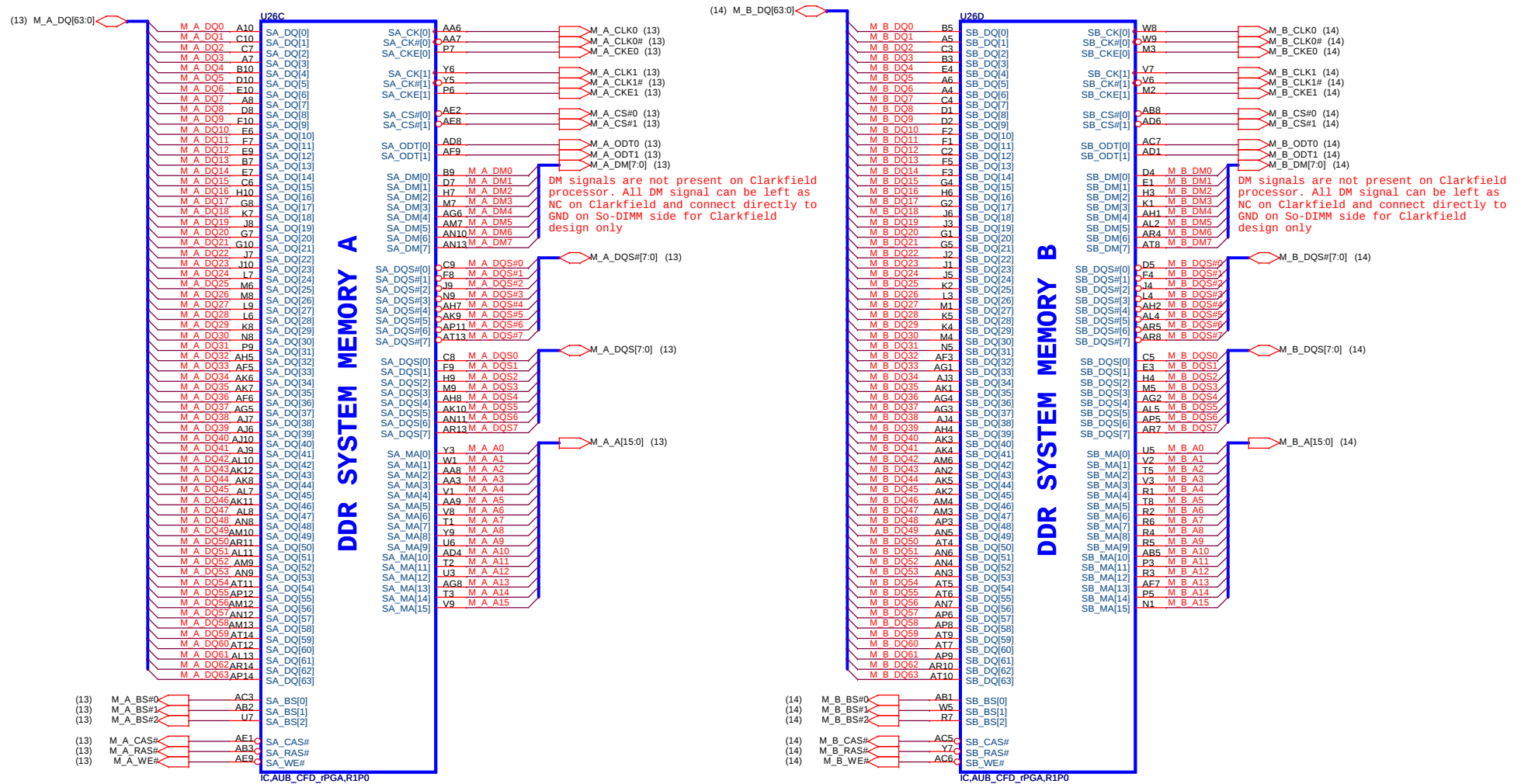
Rev 1A



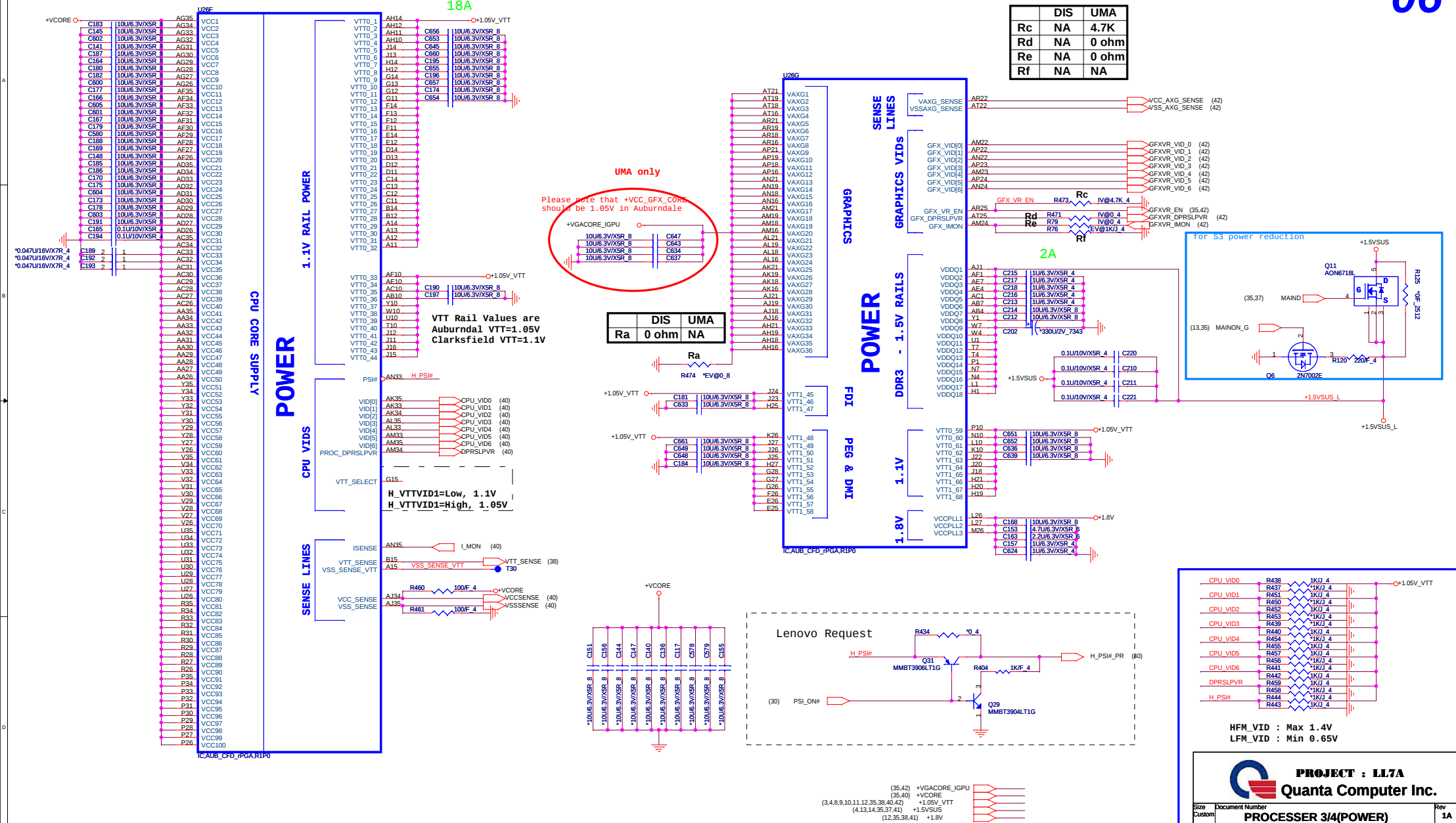
Use a voltage divider with VDDQ (1.5 V) ON in S3) and resistor combination of 1 (to VDDQ)/750±1% (to GND) to convert to processor VTT level.

|                      |                                           |
|----------------------|-------------------------------------------|
| Scan Chain (Default) | STUFF -> Ra, Rc, Re<br>NO STUFF -> Rb, Rd |
| CPU Only             | STUFF -> Ra, Rb<br>NO STUFF -> Rc, Rd, Re |
| GMCH Only            | STUFF -> Rd, Re<br>NO STUFF -> Ra, Rb, Rc |

## AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)



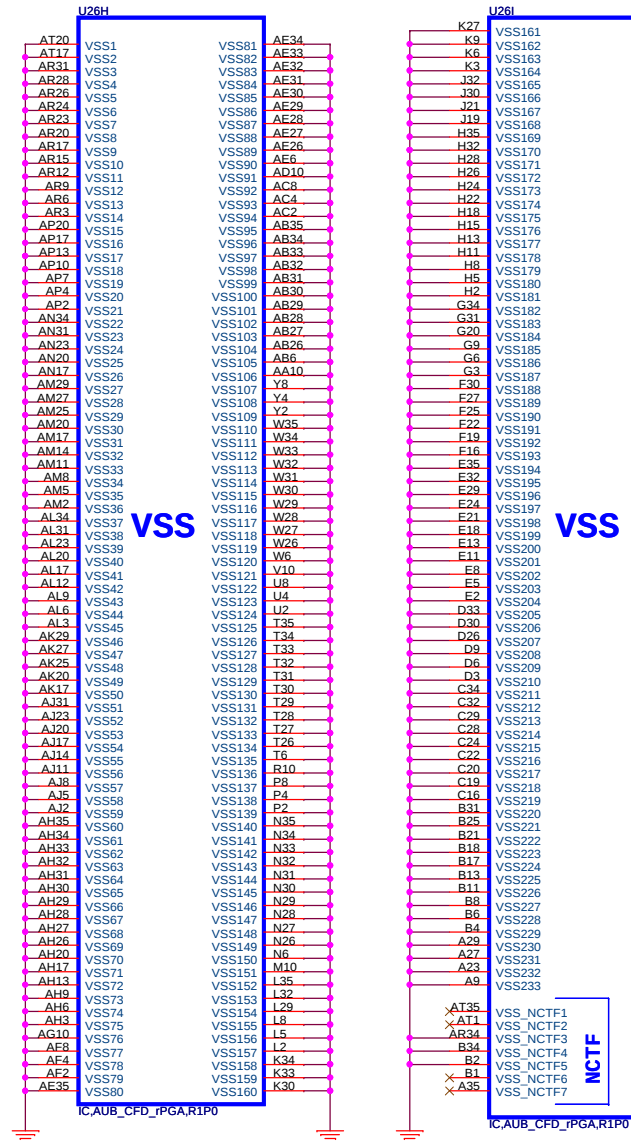
**PROJECT : LL7A**  
**Quanta Computer Inc.**



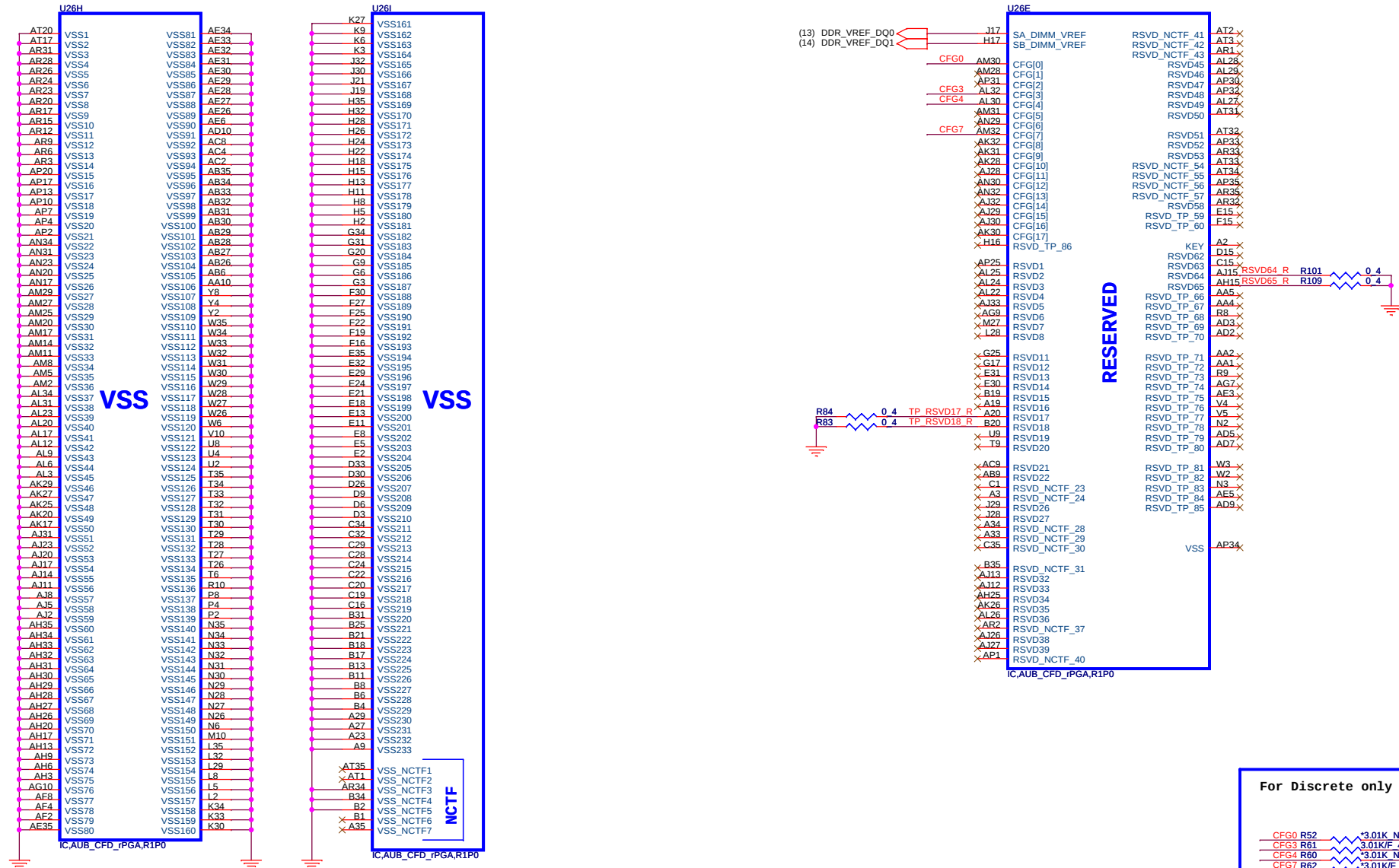
# AUBURNDALE/CLARKSFIELD PROCESSOR (GND)

# AUBURNDALE/CLARKSFIELD PROCESSOR( RESERVED, CFG)

07



The Clarkfield processor's PCI Express interface may not meet PCI Express 2.0 jitter specifications. Intel recommends placing a 3.01K +/- 5% pull down resistor to VSS on CFG[7] pin for both rPGA and BGA components. This pull down resistor should be removed when this issue is fixed.



|                                           | 1                                                                    | 0                                                                                  |
|-------------------------------------------|----------------------------------------------------------------------|------------------------------------------------------------------------------------|
| CFG4<br>(Display Port Presence)           | Disabled; No Physical Display Port attached to Embedded Display Port | Enabled; An external Display port device is connected to the Embedded Display port |
| CFG0<br>(PCI-Epress Configuration Select) | Single PEG                                                           | Bifurcation enabled                                                                |
| CFG3<br>(PCI-Epress Static Lane Reversal) | Normal Operation                                                     | Lane Numbers Reversed<br>15 -> 0, 14 -> 1                                          |

For Discrete only



CFG[ 1:0 ] - PCI\_Epress Configuration Select  
 \* 11= 1 x 16 PEG  
 \* 10= 2 x 8 PEG

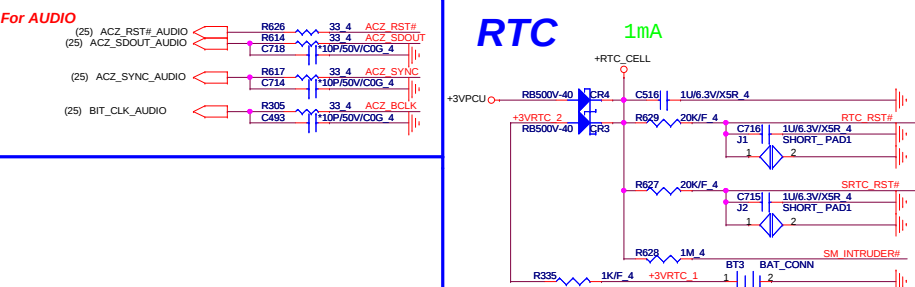
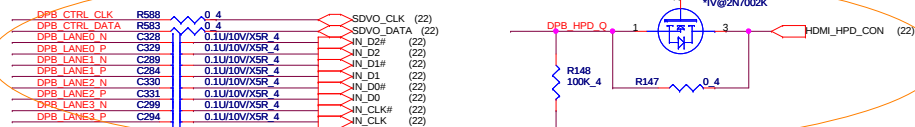
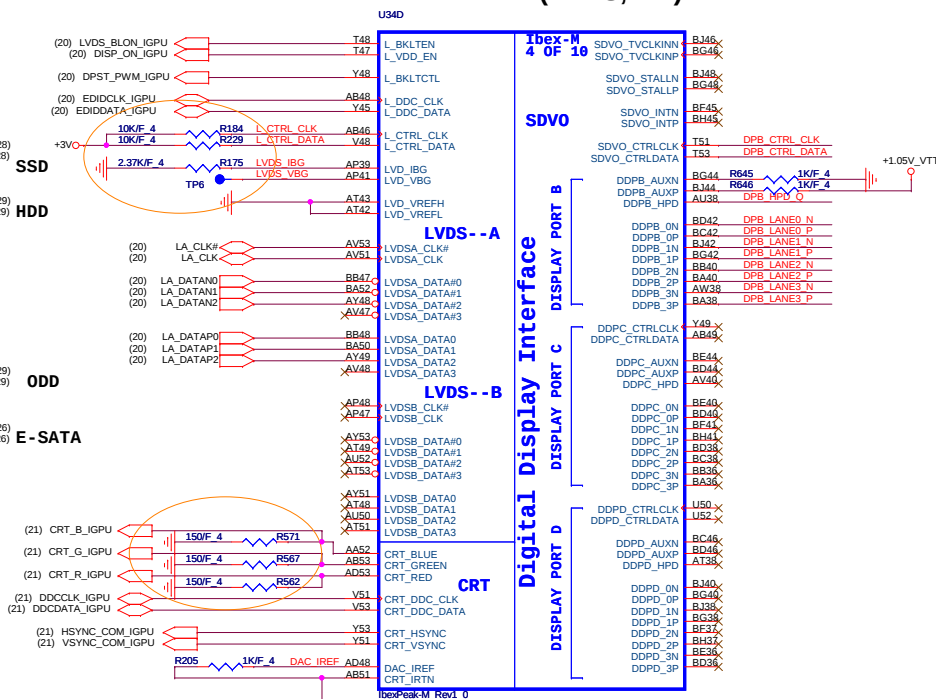
**PROJECT : LL7A**  
**Quanta Computer Inc.**

Size: Custom  
 Document Number: **PROCESSOR 4/4(GND)**  
 Date: Thursday, April 08, 2010  
 Sheet 7 of 42

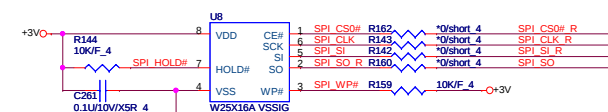
Rev: 1A



## IBEX PEAK-M (LVDS,DDI)



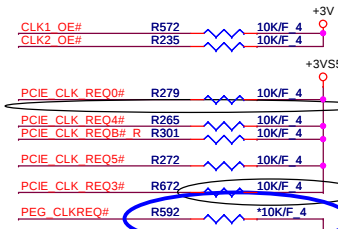
**4M byte SPI ROM**



|         |             |
|---------|-------------|
| Vendor  | PN          |
| Socket  | DG008000031 |
| WINBOND | AKE39ZP0N00 |
| MAX     | AKE39FP0N00 |

# IBEX PEAK-M (GND)

|      |        |        |     |
|------|--------|--------|-----|
| A77  | VSS159 | VSS259 | H49 |
| B11  | VSS160 | VSS260 | J24 |
| B15  | VSS161 | VSS261 | K11 |
| B19  | VSS162 | VSS262 | K43 |
| B23  | VSS163 | VSS263 | K47 |
| B31  | VSS164 | VSS264 | K7  |
| B35  | VSS165 | VSS265 | L14 |
| B39  | VSS166 | VSS266 | L18 |
| B43  | VSS167 | VSS267 | L22 |
| B47  | VSS168 | VSS268 | L32 |
| B7   | VSS169 | VSS269 | L36 |
| BG12 | VSS170 | VSS270 | L40 |
| BB12 | VSS171 | VSS271 | L52 |
| BB16 | VSS172 | VSS272 | M12 |
| BB20 | VSS173 | VSS273 | M16 |
| BB24 | VSS174 | VSS274 | M20 |
| BB34 | VSS175 | VSS275 | M38 |
| BB38 | VSS176 | VSS276 | M42 |
| BB42 | VSS177 | VSS277 | M46 |
| BB46 | VSS178 | VSS278 | M49 |
| BB5  | VSS179 | VSS279 | M5  |
| BC10 | VSS180 | VSS280 | M8  |
| BC14 | VSS181 | VSS281 | M9  |
| BC18 | VSS182 | VSS282 | M12 |
| BC2  | VSS183 | VSS283 | M16 |
| BC22 | VSS184 | VSS284 | M20 |
| BC32 | VSS185 | VSS285 | M38 |
| BC36 | VSS186 | VSS286 | M42 |
| BC40 | VSS187 | VSS287 | M46 |
| RC44 | VSS188 | VSS288 | M49 |
| RC52 | VSS189 | VSS289 | M5  |
| BH9  | VSS190 | VSS290 | M8  |
| BD48 | VSS191 | VSS291 | M9  |
| BD49 | VSS192 | VSS292 | M12 |
| BD5  | VSS193 | VSS293 | M16 |
| BD5  | VSS194 | VSS294 | M20 |
| BE12 | VSS195 | VSS295 | M38 |
| BE16 | VSS196 | VSS296 | M42 |
| BE20 | VSS197 | VSS297 | M46 |
| BE30 | VSS198 | VSS298 | M49 |
| BE34 | VSS199 | VSS299 | M5  |
| BE38 | VSS200 | VSS300 | M8  |
| BE42 | VSS201 | VSS301 | M9  |
| BE46 | VSS202 | VSS302 | M12 |
| BE48 | VSS203 | VSS303 | M16 |
| BE50 | VSS204 | VSS304 | M20 |
| BE6  | VSS205 | VSS305 | M38 |
| BE8  | VSS206 | VSS306 | M42 |
| BE8  | VSS207 | VSS307 | M46 |
| BE9  | VSS208 | VSS308 | M49 |
| BE9  | VSS209 | VSS309 | M5  |
| BE9  | VSS210 | VSS310 | M8  |
| BG18 | VSS211 | VSS311 | M9  |
| BG24 | VSS212 | VSS312 | M12 |
| BG4  | VSS213 | VSS313 | M16 |
| BG50 | VSS214 | VSS314 | M20 |
| BH11 | VSS215 | VSS315 | M38 |
| BH15 | VSS216 | VSS316 | M42 |
| BH19 | VSS217 | VSS317 | M46 |
| BH23 | VSS218 | VSS318 | M49 |
| BH31 | VSS219 | VSS319 | M5  |
| BH35 | VSS220 | VSS320 | M8  |
| BH39 | VSS221 | VSS321 | M9  |
| BH43 | VSS222 | VSS322 | M12 |
| BH47 | VSS223 | VSS323 | M16 |
| BH7  | VSS224 | VSS324 | M20 |
| C12  | VSS225 | VSS325 | M38 |
| C50  | VSS226 | VSS326 | M42 |
| D51  | VSS227 | VSS327 | M46 |
| E16  | VSS228 | VSS328 | M49 |
| E20  | VSS229 | VSS329 | M5  |
| E24  | VSS230 | VSS330 | M8  |
| E30  | VSS231 | VSS331 | M9  |
| E34  | VSS232 | VSS332 | M12 |
| E38  | VSS233 | VSS333 | M16 |
| E42  | VSS234 | VSS334 | M20 |
| E46  | VSS235 | VSS335 | M38 |
| E48  | VSS236 | VSS336 | M42 |
| E6   | VSS237 | VSS337 | M46 |
| E8   | VSS238 | VSS338 | M49 |
| F49  | VSS239 | VSS339 | M5  |
| F5   | VSS240 | VSS340 | M8  |
| G10  | VSS241 | VSS341 | M9  |
| G14  | VSS242 | VSS342 | M12 |
| G18  | VSS243 | VSS343 | M16 |
| G2   | VSS244 | VSS344 | M20 |
| G22  | VSS245 | VSS345 | M38 |
| G32  | VSS246 | VSS346 | M42 |
| G36  | VSS247 | VSS347 | M46 |
| G40  | VSS248 | VSS348 | M49 |
| G44  | VSS249 | VSS349 | M5  |
| G52  | VSS250 | VSS350 | M8  |
| AF39 | VSS251 | VSS351 | M9  |
| H16  | VSS252 | VSS352 | M12 |
| H20  | VSS253 | VSS353 | M16 |
| H24  | VSS254 | VSS354 | M20 |
| H30  | VSS255 | VSS355 | M38 |
| H34  | VSS256 | VSS356 | M42 |
| H38  | VSS257 | VSS357 | M46 |
| H42  | VSS258 | VSS358 | M49 |

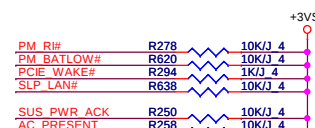
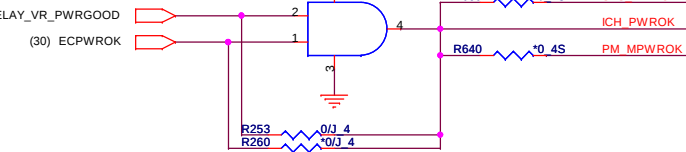


U34E

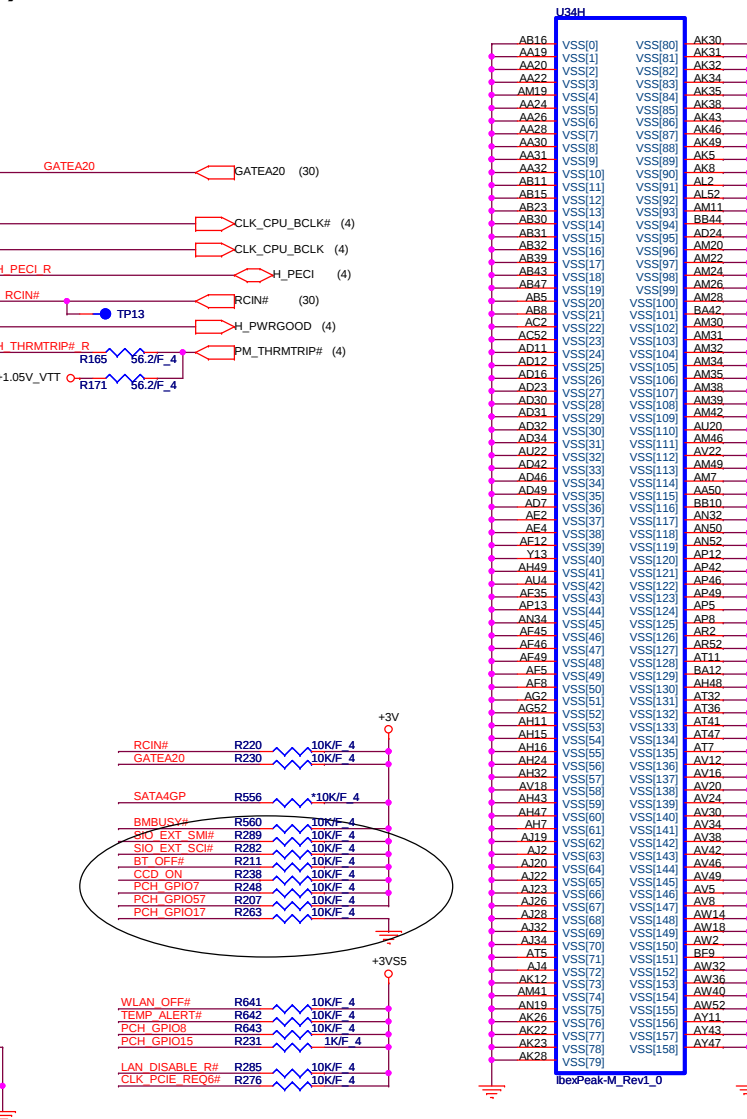
|                  |     |         |
|------------------|-----|---------|
| <del>X</del> H40 | AD0 | Ibex-M  |
| <del>X</del> N34 | AD1 | 5 OF 10 |



## Blue tooth



### IBEX PEAK-M (GND)



The schematic diagram illustrates the internal circuitry of the GFXP module. A 3V power source is connected to a resistor R671, which is in turn connected to the input of a logic component U40 (TC7SH08FUF). The output of U40 is connected to the HWPG output (pins 4, 30, 37, 38, 39, 40, 42). The GFXP output (pins 9, 15) is also shown. The module is labeled GFXP\_R and GFXP.

|           |                             |
|-----------|-----------------------------|
| SV_SET_UP | 1-X High = Strong (Default) |
|-----------|-----------------------------|

| DMI Termination Voltage |                                               |
|-------------------------|-----------------------------------------------|
| NV_CLE                  | Set to Vcc when LOW<br>Set to Vcc/2 when HIGH |

### No Reboot Strap

(8,25) ACZ\_SPKR   \*1K/F\_4  R580  +3V

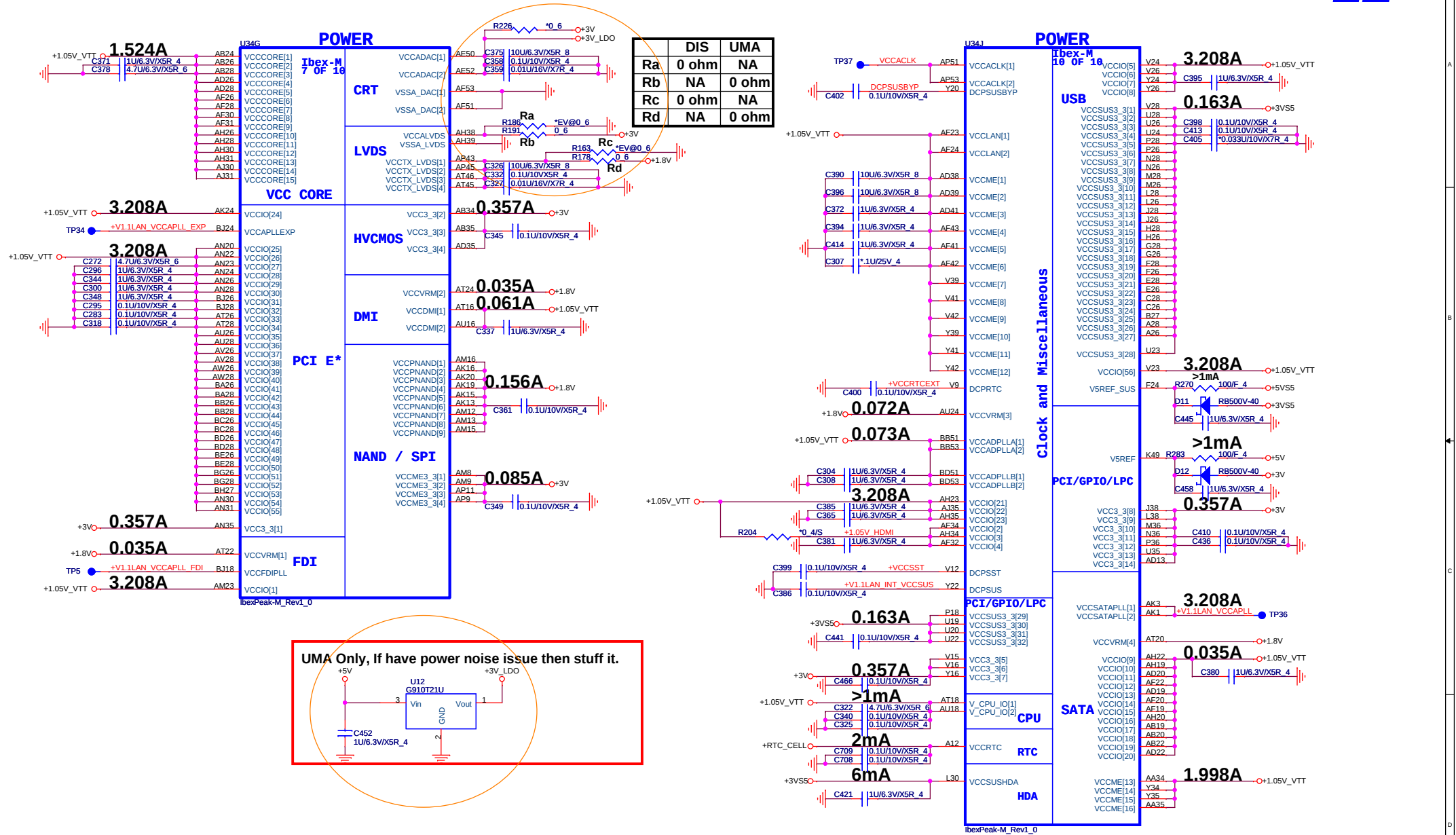
(8) PCH\_GPIO33   R264  \*100K/F\_4 

(3,4,6,8,9,10,12,35,38,40,42) +1.05V\_VTT  

15,20,21,22,24,25,26,27,28,29,30,31,32,33,35,36,40,41) +3V  

(4,8,9,10,12,15,27,35) +3VS5  

|                                                                                       |                                  |                                                      |           |
|---------------------------------------------------------------------------------------|----------------------------------|------------------------------------------------------|-----------|
|  |                                  | <b>PROJECT : LL7A</b><br><b>Quanta Computer Inc.</b> |           |
| Size                                                                                  | Document Number                  |                                                      | Rev       |
| Custom                                                                                | <b>PCH 4/5(GPIO &amp; STRAP)</b> |                                                      | <b>1A</b> |
| Date:                                                                                 | Thursday, April 08, 2010         | Sheet 11                                             | of 42     |

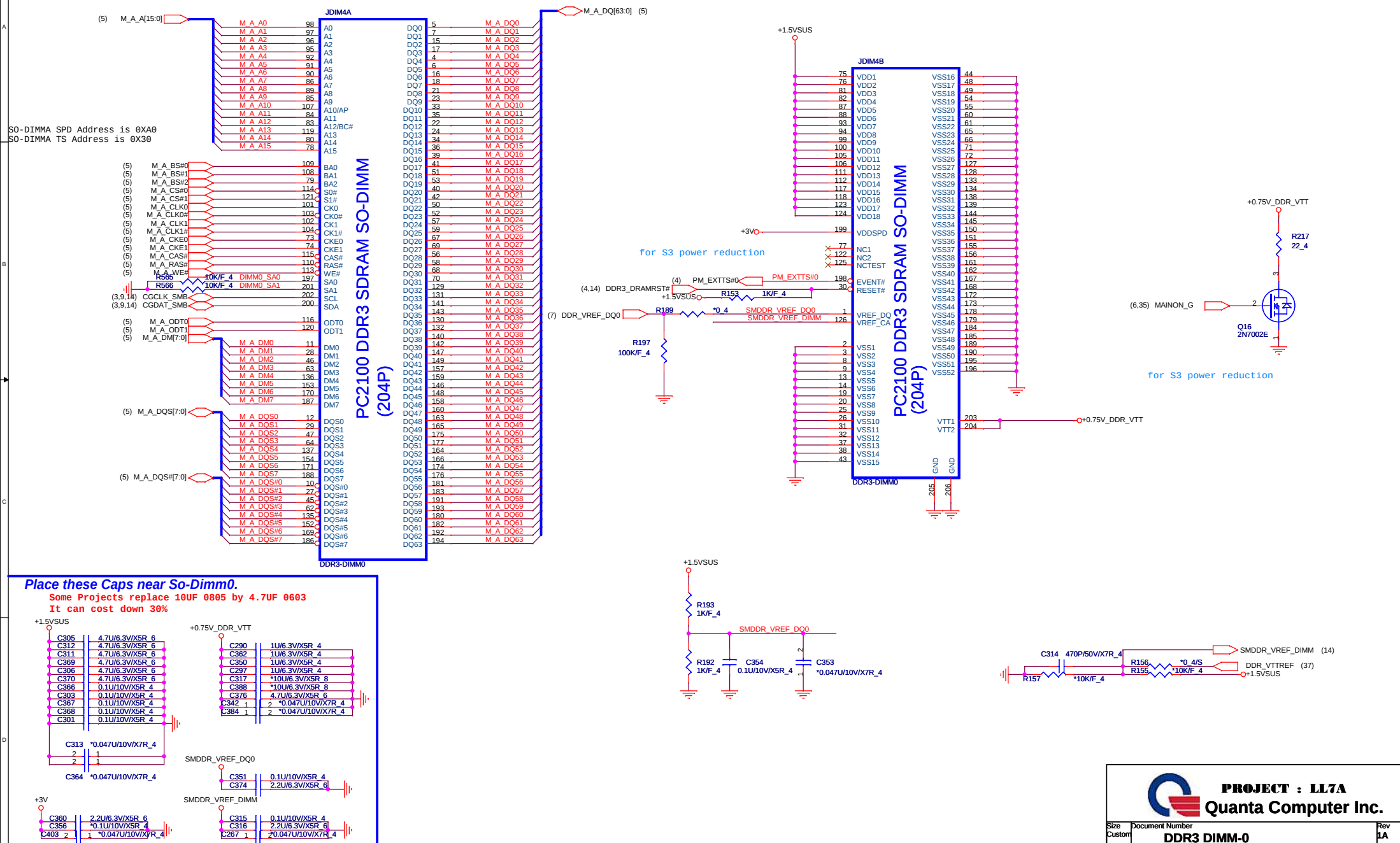


(3,4,6,8,9,10,11,35,38,40,42) +1.05V\_VTT  
(6,35,38,41) +1.8V  
(3,4,8,9,10,11,13,14,15,20,21,22,24,25,26,27,28,29,30,31,32,33,35,36,40,41) +3V  
(4,8,9,10,11,15,27,35) +3V55  
(20,21,22,25,29,30,31,32,33,35,36) +5V  
(26,35) +5V55

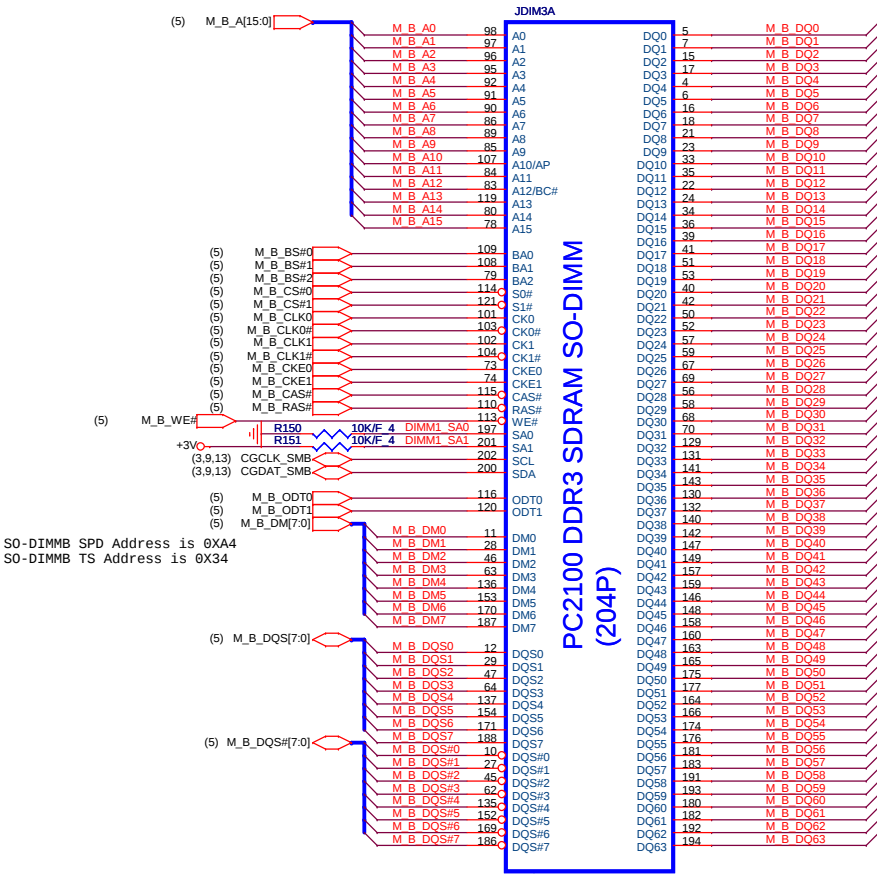
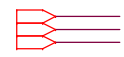


**PROJECT : LL7A**  
**Quanta Computer Inc.**

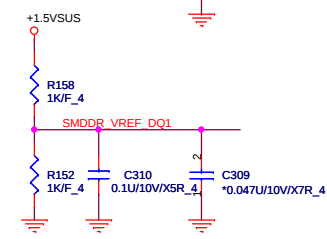
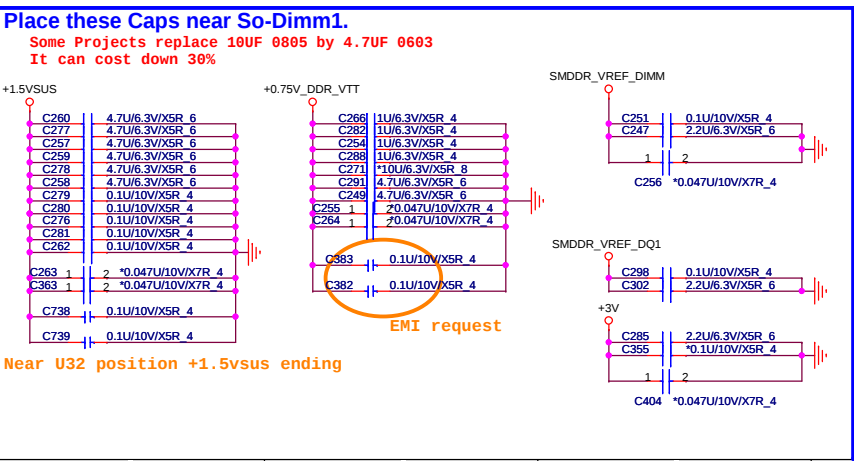
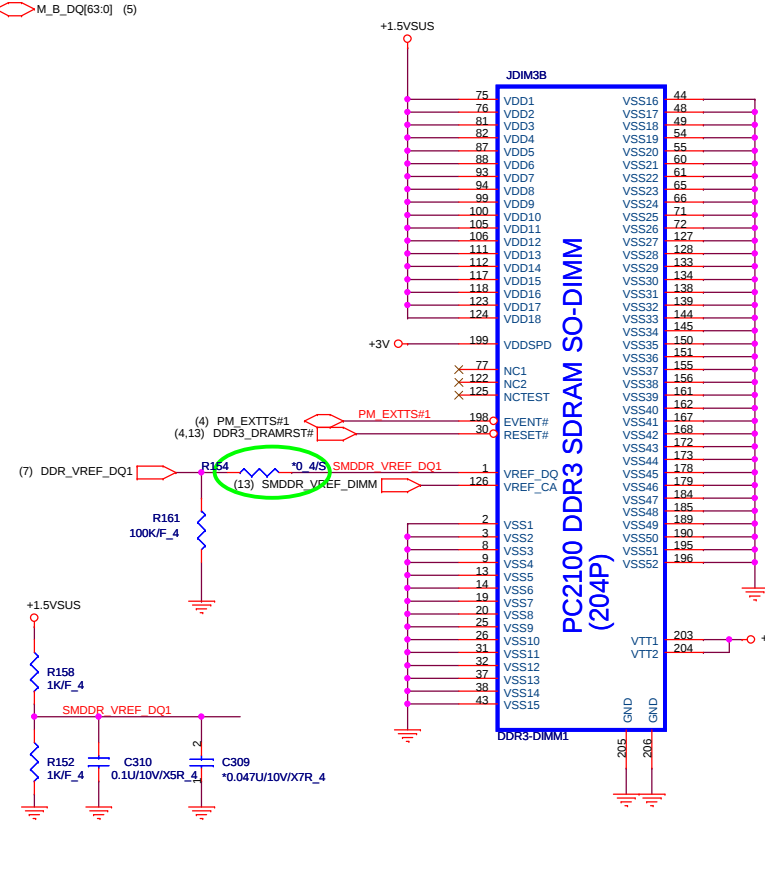
| Size   | Document Number          | Rev            |
|--------|--------------------------|----------------|
| Custom | PCH 5/5(POWER)           | 1A             |
| Date:  | Thursday, April 08, 2010 | Sheet 12 of 42 |



(3,4,8,9,10,11,12,13,15,20,21,22,24,25,26,27,28,29,30,31,32,33,35,36,40,41)  
 (4,6,13,35,37,41) +1.5VSUS  
 +3V  
 (13,35,37) +0.75V\_DDR\_VTT



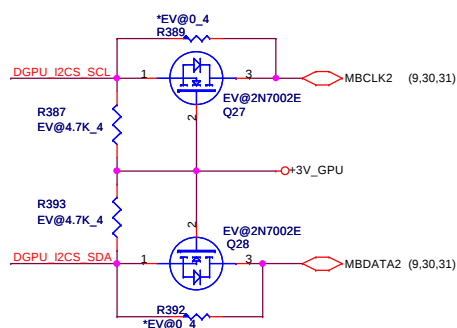
SO-DIMMB SPD Address is 0XA4  
 SO-DIMMB TS Address is 0X34









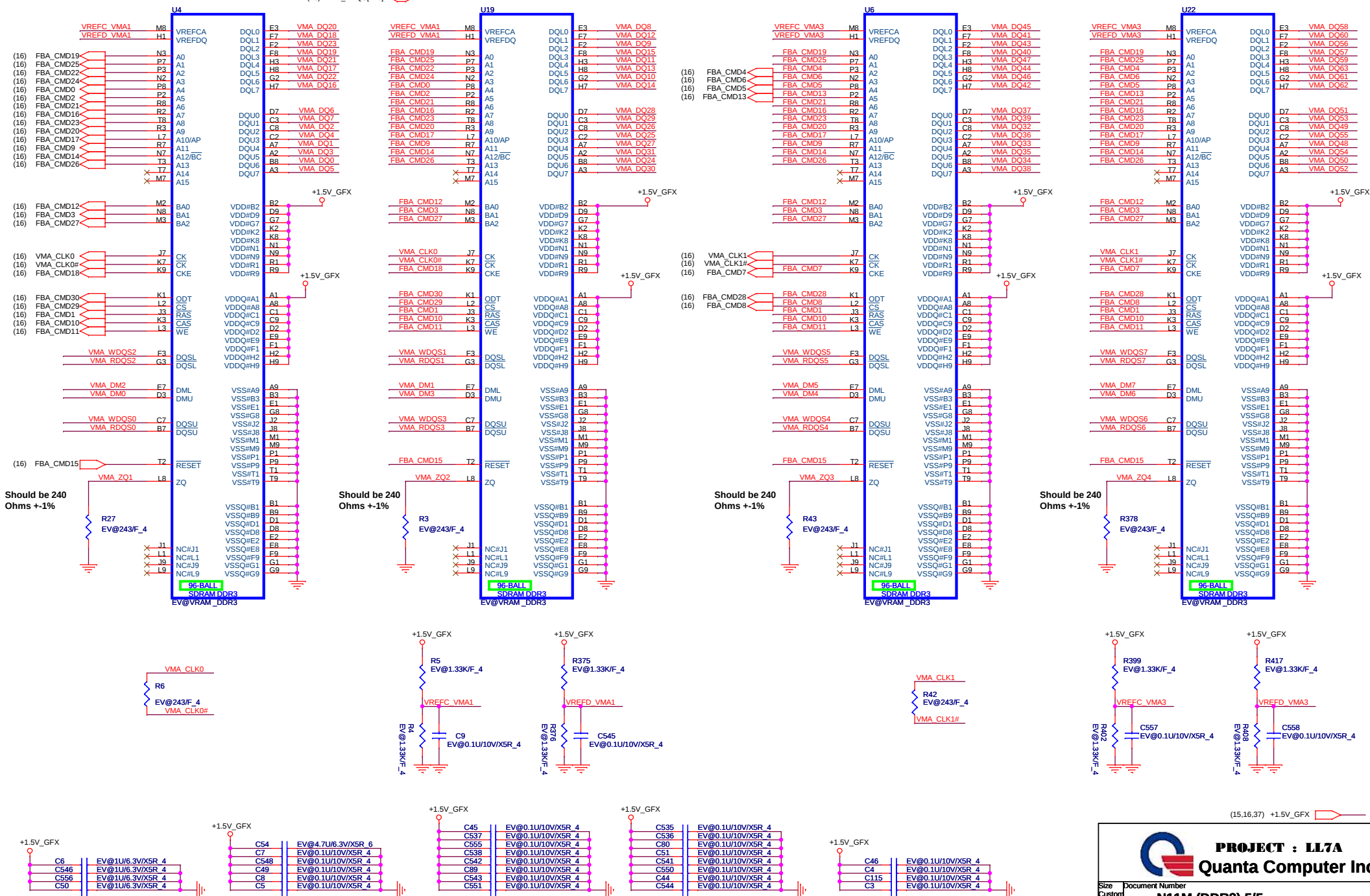
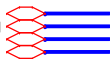


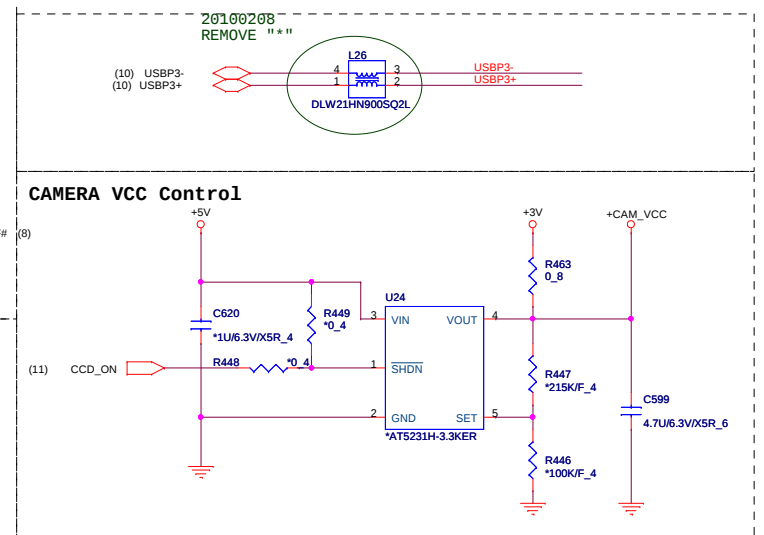
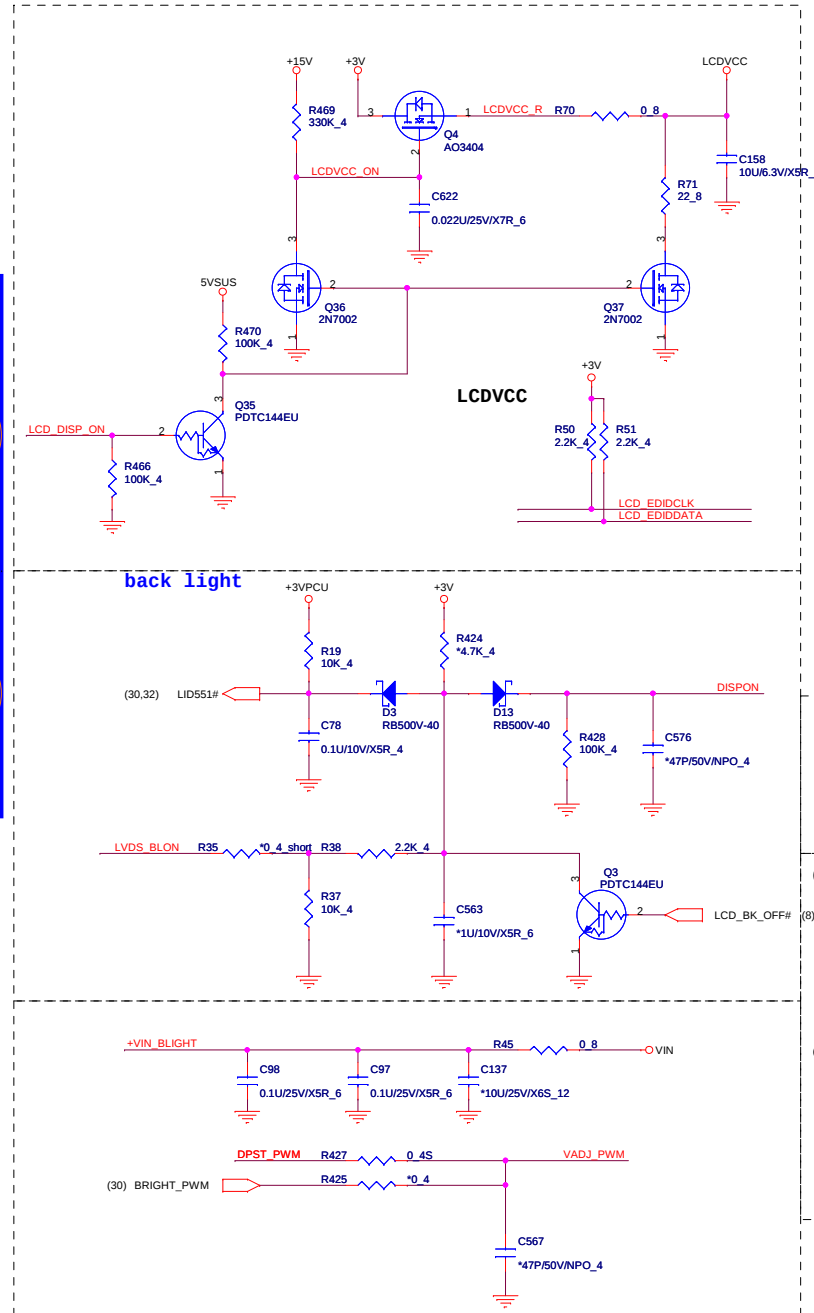
| GPU Type | STRAP0     | STRAP1     | ROM_SO  | ROM_SCLK |
|----------|------------|------------|---------|----------|
| N11M-GE1 | PU 45.3K/F | PU 34.8K/F | PD10K/F | PU15K/F  |
| N11M-GE2 | PU 45.3K/F | PU 34.8K/F | PD10K/F | PU15K/F  |

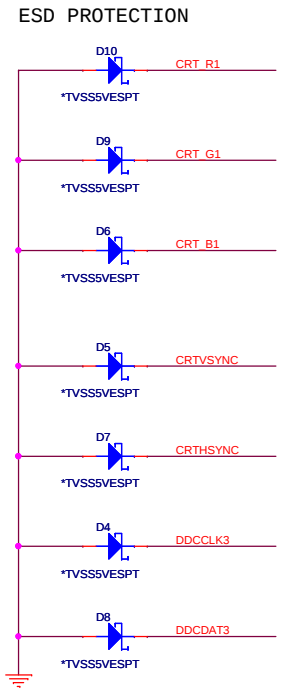
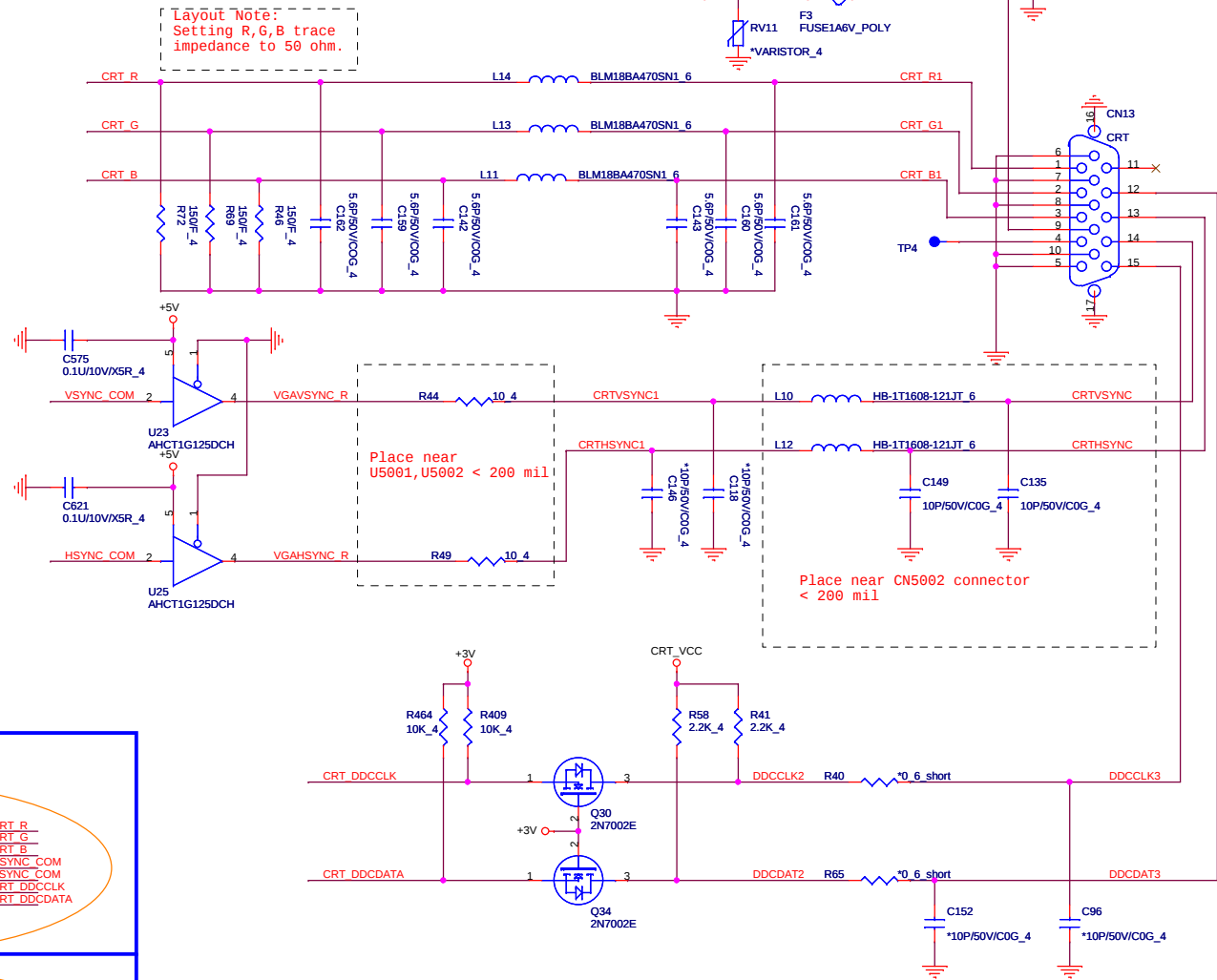
|                                |                                                        |                  |
|--------------------------------|--------------------------------------------------------|------------------|
| Size<br>Custom                 | Document Number<br><b>N11M(GPIO &amp; STRRAPS) 4/5</b> | Rev<br><b>1A</b> |
| Date: Thursday, April 08, 2010 | Sheet 18                                               | of 42            |

**CHANNEL A: 256MB/512MB DDR3**

```
(16) VMA_DQ[63..0]
(16) VMA_DM[7..0]
(16) VMA_WDQS[7..0]
(16) VMA_RDQS[7..0]
```







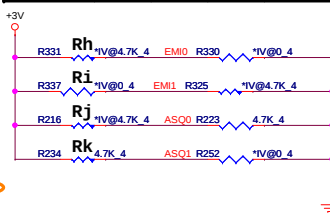
**FOR UMA**

|                    |      |     |             |
|--------------------|------|-----|-------------|
| (8) CRT_R_IGPU     | R405 | 0.4 | CRT_R       |
| (8) CRT_G_IGPU     | R411 | 0.4 | CRT_G       |
| (8) CRT_B_IGPU     | R414 | 0.4 | CRT_B       |
| (8) HSYNC_COM_IGPU | R400 | 0.4 | HSYNC_COM   |
| (8) VSYNC_COM_IGPU | R396 | 0.4 | VSYNC_COM   |
| (8) DDCCLK_IGPU    | R385 | 0.4 | CRT_DDCCLK  |
| (8) DDCDATA_IGPU   | R390 | 0.4 | CRT_DDCDATA |

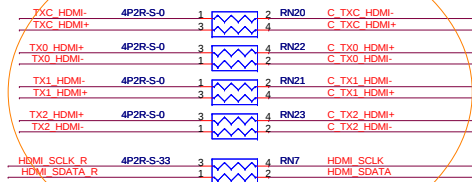
**FOR DISCRETE**

|                      |      |         |             |
|----------------------|------|---------|-------------|
| (17) EXT_CRT_RED     | R406 | *EV@0.4 | CRT_R       |
| (17) EXT_CRT_GREEN   | R412 | *EV@0.4 | CRT_G       |
| (17) EXT_CRT_BLUE    | R415 | *EV@0.4 | CRT_B       |
| (17) EXT_HSYNC_COM   | R397 | *EV@0.4 | HSYNC_COM   |
| (17) EXT_VSYNC_COM   | R394 | *EV@0.4 | VSYNC_COM   |
| (18) EXT_CRT_DDCCLK  | R386 | *EV@0.4 | CRT_DDCCLK  |
| (18) EXT_CRT_DDCDATA | R391 | *EV@0.4 | CRT_DDCDATA |

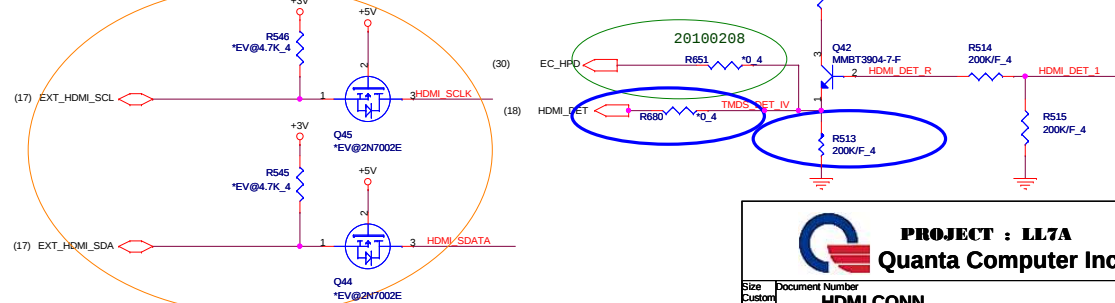
| Signals |           | <i>PDT</i> |      |      |
|---------|-----------|------------|------|------|
| EMI0    | <b>Rh</b> | NC         | NC   | NC   |
| EMI1    | <b>Ri</b> | NC         | NC   | NC   |
| ASQ0    | <b>Rj</b> | 4.7K       | NC   | NC   |
| ASQ1    | <b>Rk</b> | 499        | 1.2K | 4.7K |

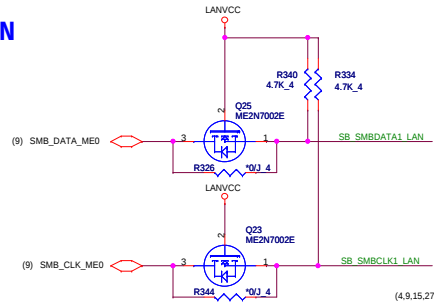


| Signals | PS8171 |       |
|---------|--------|-------|
| PIN 4   | LEVEL  | EQ    |
| PC1/PIO | HIGH   | 1.2db |
|         | MID    | 2db   |
|         | LOW    | ※ 0db |



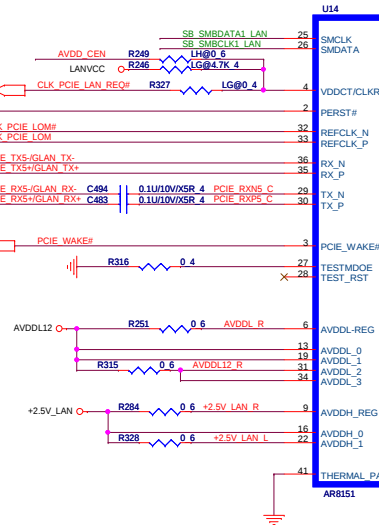
The schematic diagram illustrates the EV@499F\_4 circuit. A 5V input is connected to a network of resistors (R139, R543, R537, R536, R542, R133, R535, R111) and a capacitor (R97). The circuit is powered by a 5V source and includes a 2N7002 MOSFET (Q5) and a 100kF\_4 capacitor. The output is connected to a TX2\_HDMI+ signal line.



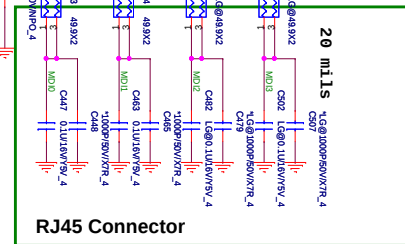
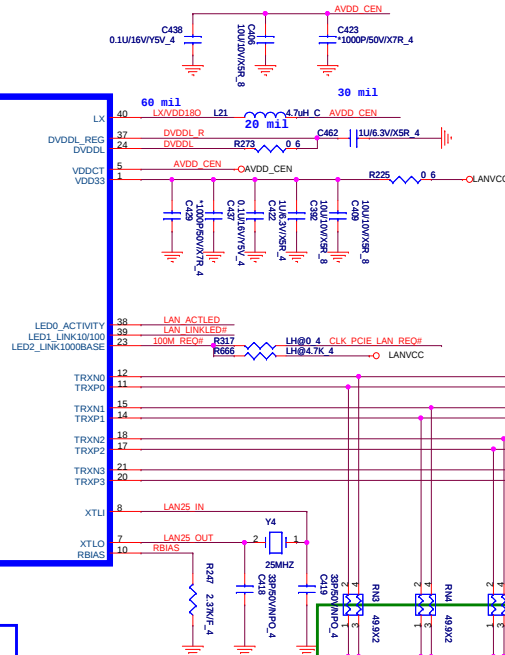


(4,9,15,27,28) PLTRST#  
(9) CLK\_PCIE\_LAN\_REQ#  
(9) CLK\_PCIE\_LOM#  
(9) CLK\_PCIE\_LOM#  
(9) PCIE\_TXS+GLAN\_TX-  
(9) PCIE\_TXS+GLAN\_TX+  
(9) PCIE\_RXS+GLAN\_RX-  
(9) PCIE\_RXS+GLAN\_RX+

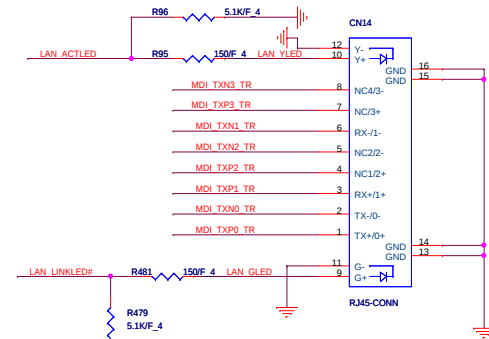
(10,27) PCIE\_WAKE#



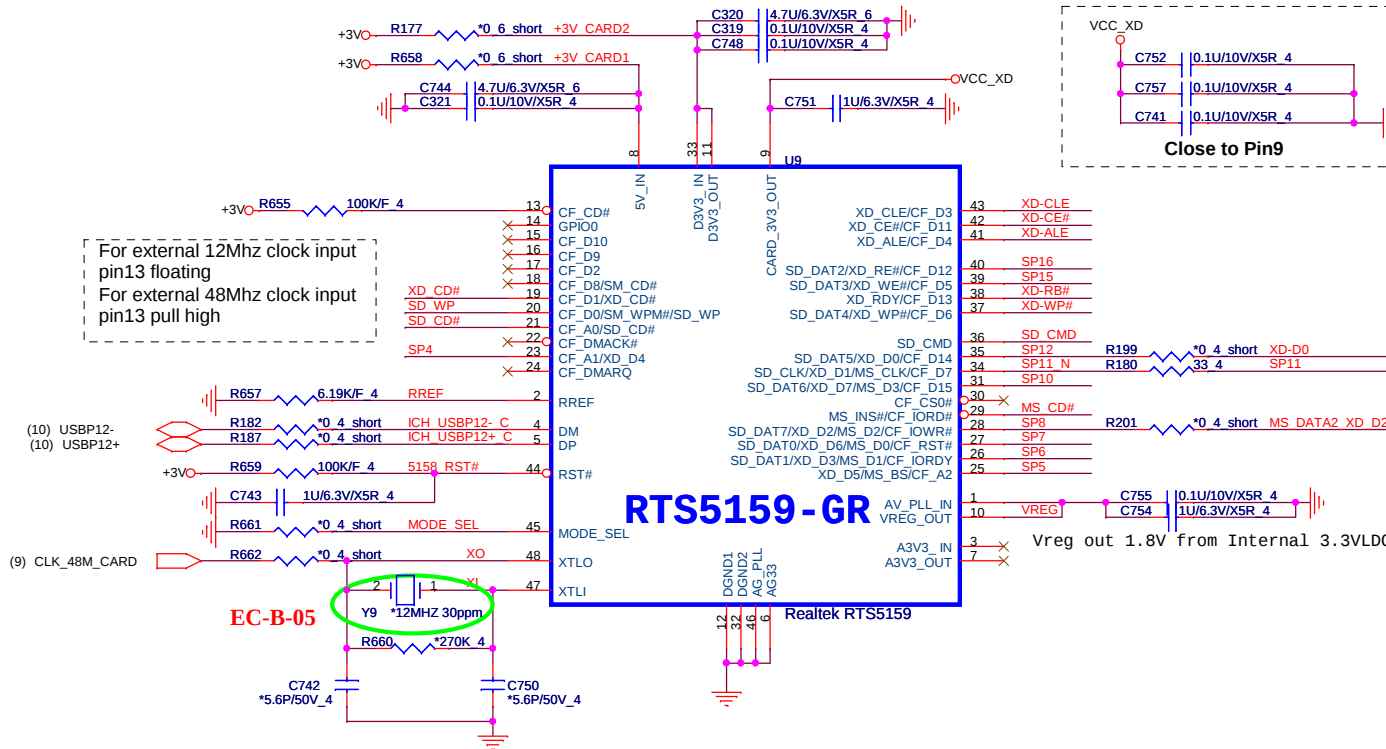
U14  
SMCLK  
SMDATA  
VDDCT/CLKREG#  
PERST#  
REFCLK\_N  
REFCLK\_P  
RX\_N  
RX\_P  
TX\_N  
TX\_P  
PCIE\_WAKE#  
TESTMODE  
TEST\_RST  
AVDDL2  
AVDDL12  
AVDDL0  
AVDDL1  
AVDDL2  
AVDDL3  
AVDDH\_REG  
AVDDH0  
AVDDH1  
THERMAL\_PAD  
AR8151



RJ45 Connector



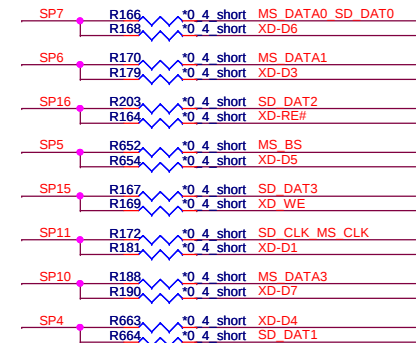
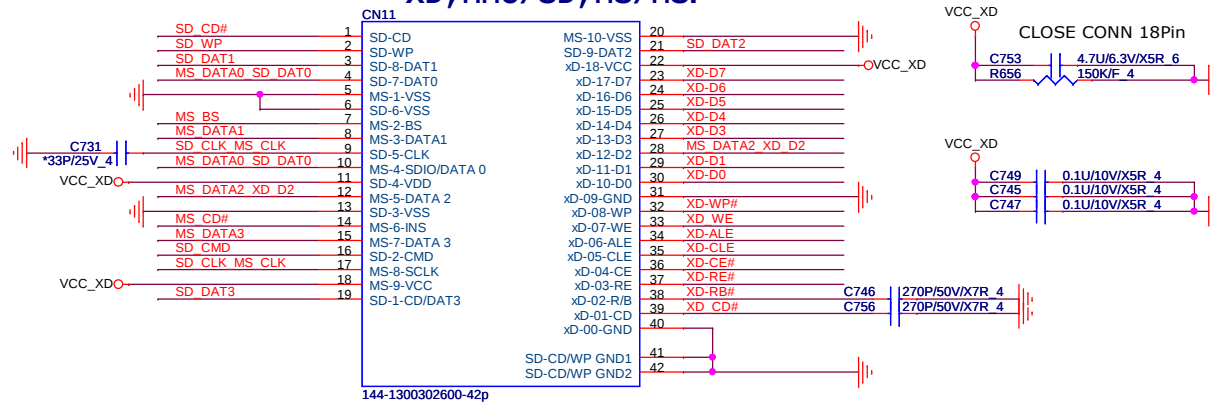
PROJECT : LL7A  
Quanta Computer Inc.



## Note:

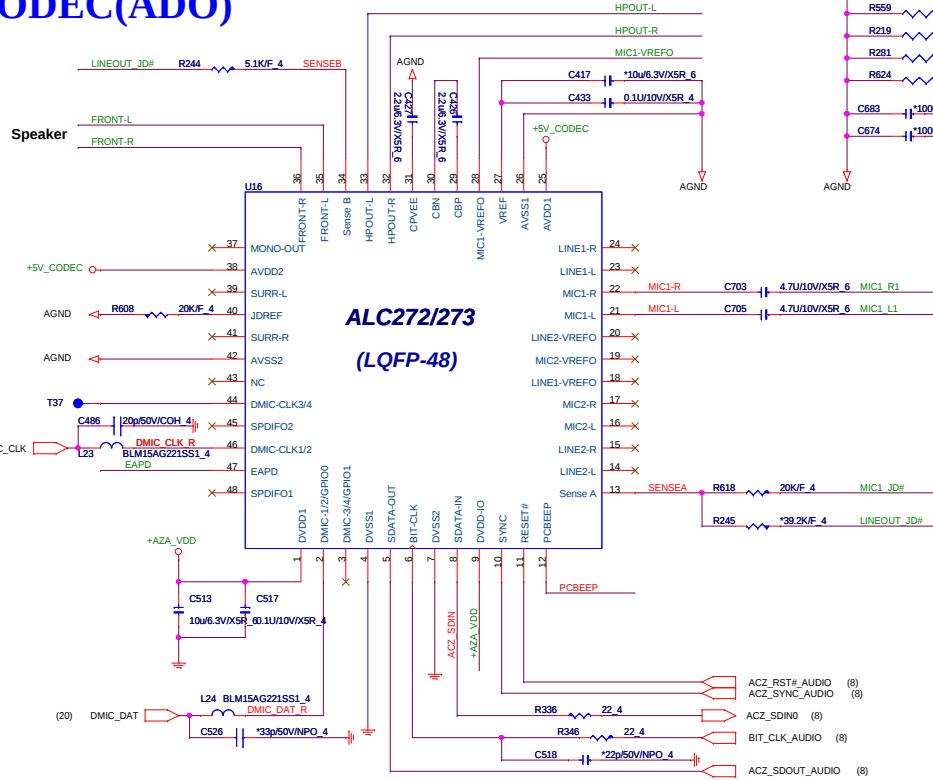
| SD/MMC | MS      | XD      |
|--------|---------|---------|
| SP0    |         |         |
| SP1    | SD WP   | XD_CD#  |
| SP2    | SD CD#  |         |
| SP3    | SD DAT1 | XD D4   |
| SP4    | MS BS   | XD D5   |
| SP5    | MS D1   | XD D3   |
| SP6    | MS D0   | XD D6   |
| SP7    | SD DAT0 | MS D2   |
| SP8    | SD DAT7 | XD D2   |
| SP9    | MS INS# |         |
| SP10   | SD DAT6 | MS D3   |
| SP11   | SD CLK  | MS SCLK |
| SP12   | SD DAT5 | XD D0   |
| SP13   | SD DAT4 | XD WP#  |
| SP14   |         | XD R/B# |
| SP15   | SD DAT3 | XD WE#  |
| SP16   | SD DAT2 | XD RE#  |
| SP17   |         | XD ALE  |
| SP18   |         | XD CE#  |
| SP19   |         | XD CLE  |

## For RTS5159

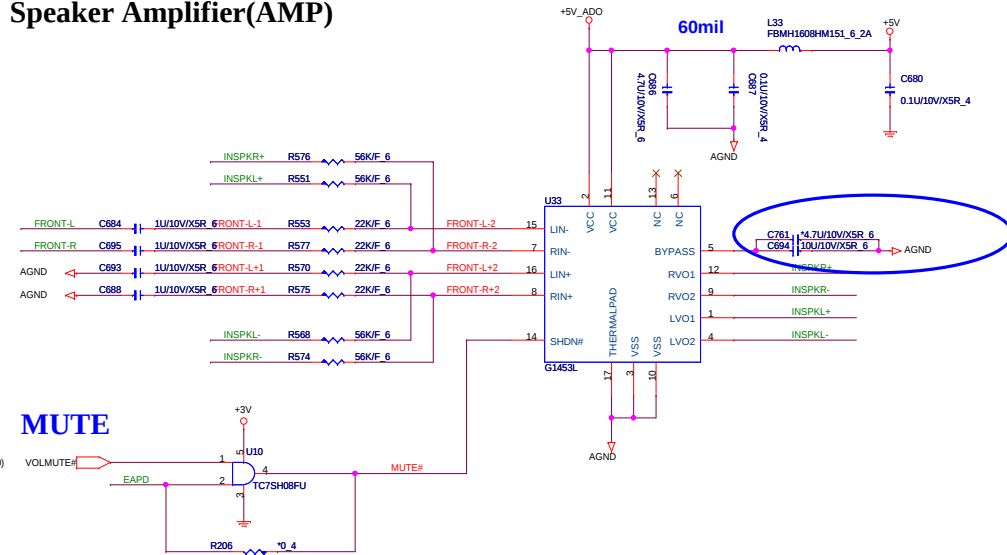
5 IN1 CARD READER  
XD, MMC/SD, MS/MSP

PROJECT : LL7A  
Quanta Computer Inc.

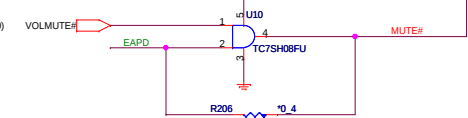
# CODEC(ADO)



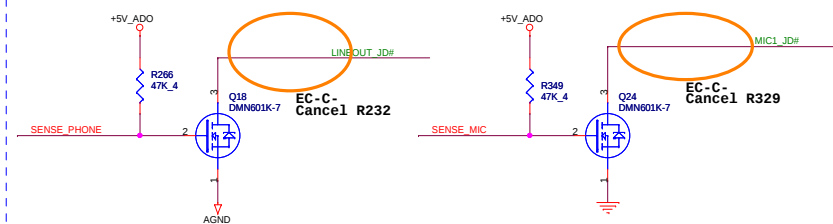
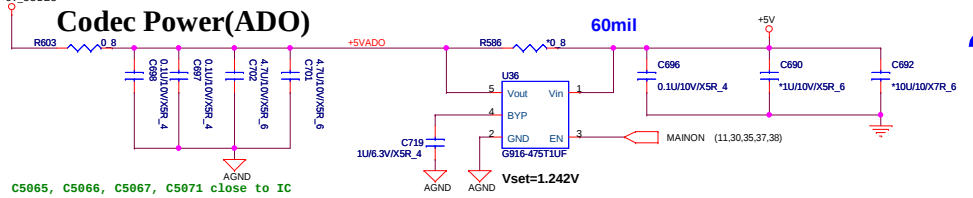
## Speaker Amplifier(AMP)



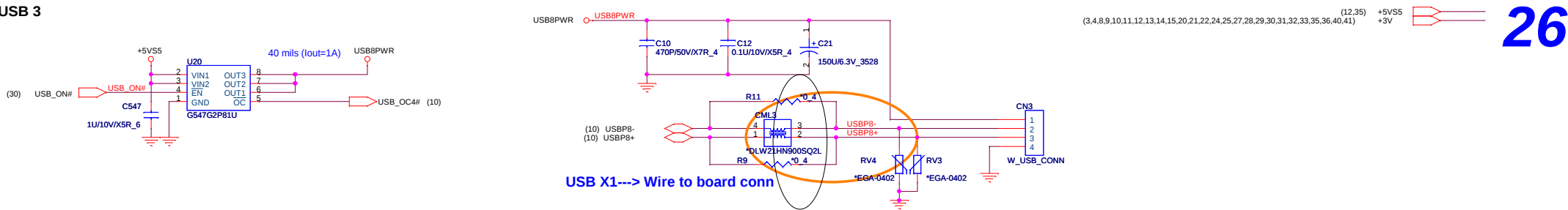
## MUTE



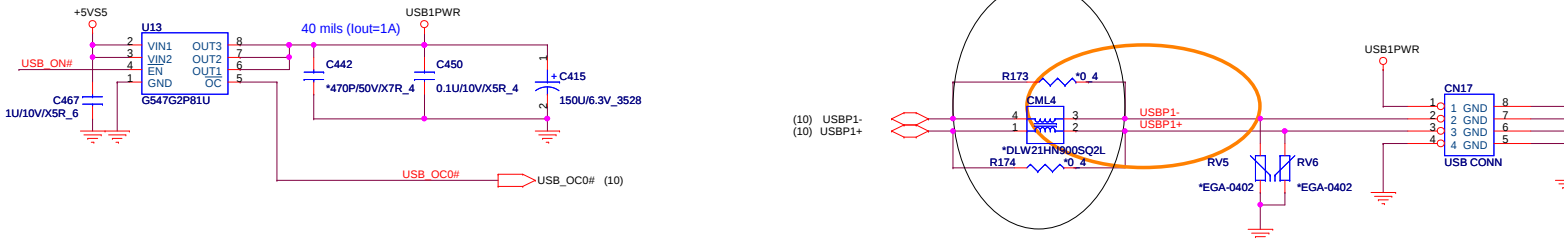
## Codec Power(ADO)



# USB 3

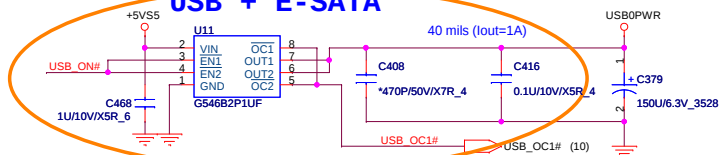


# USB 2

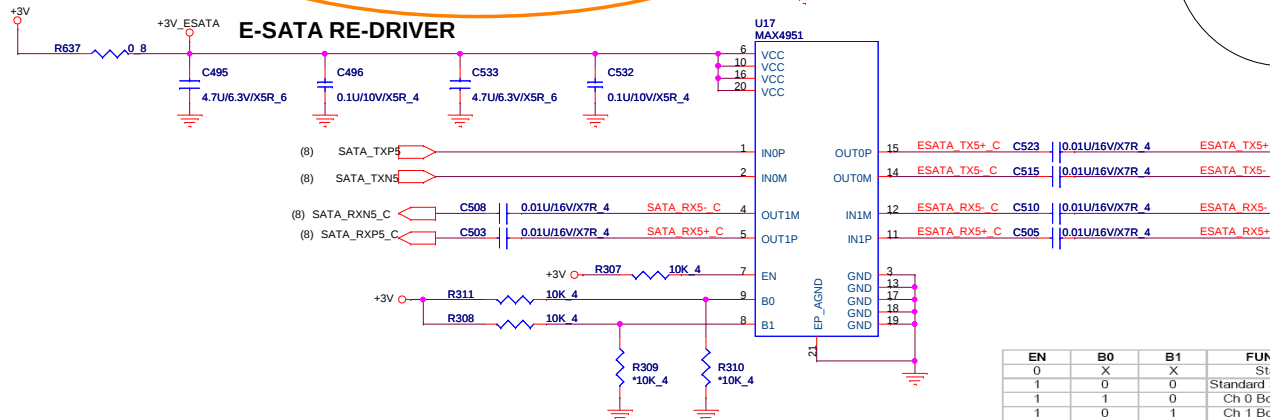


# USB 1

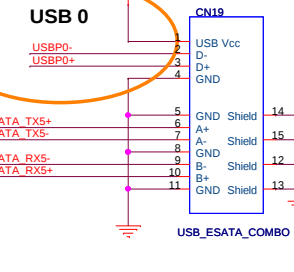
## USB + E-SATA



## E-SATA RE-DRIVER



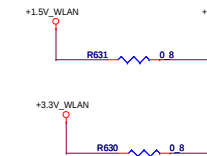
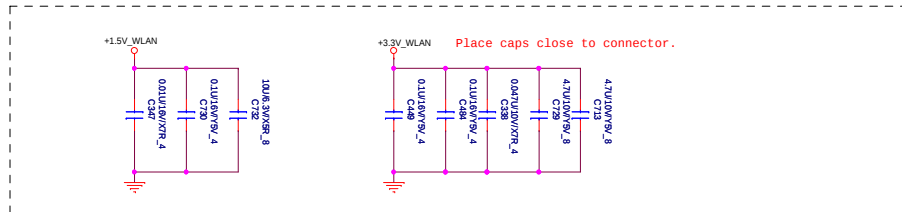
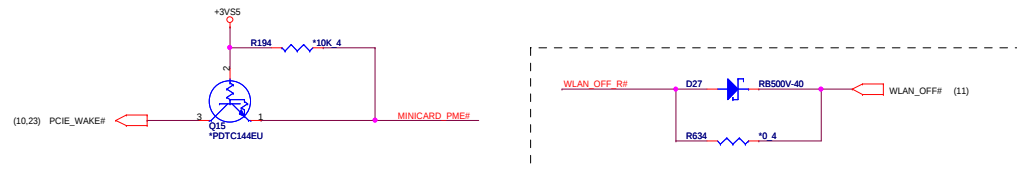
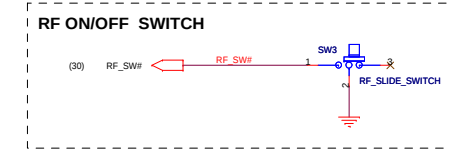
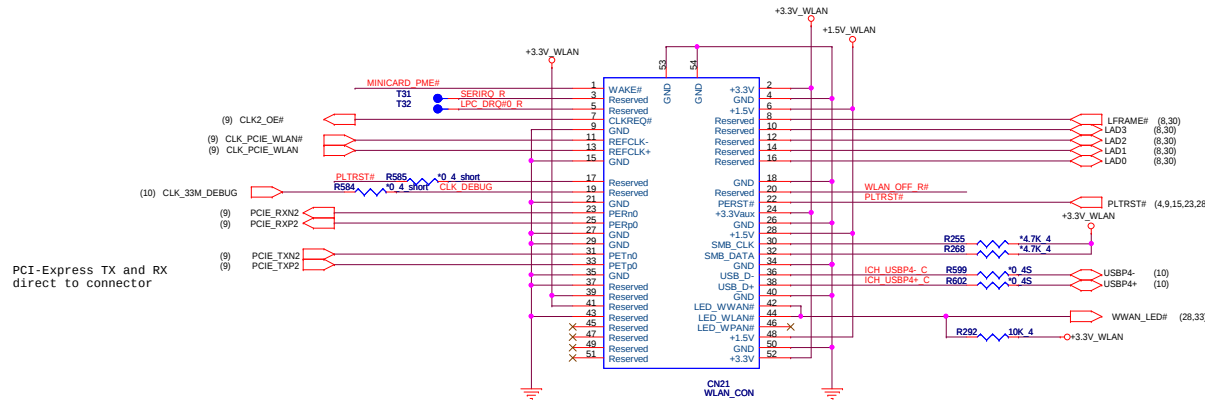
| EN | B0 | B1 | FUNCTION             |
|----|----|----|----------------------|
| 0  | X  | X  | Standby              |
| 1  | 0  | 0  | Standard SATA Output |
| 1  | 1  | 0  | Ch 0 Boost Output    |
| 1  | 0  | 1  | Ch 1 Boost Output    |
| 1  | 1  | 1  | Ch 0,1 Boost Output  |



# MiniCard WLAN/WiMAX connector

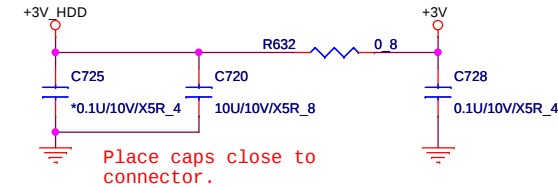
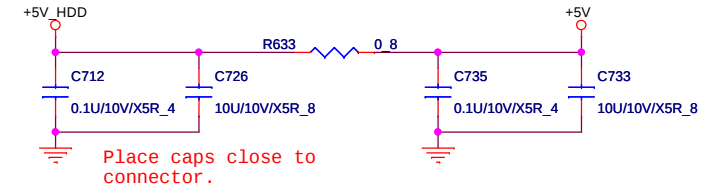
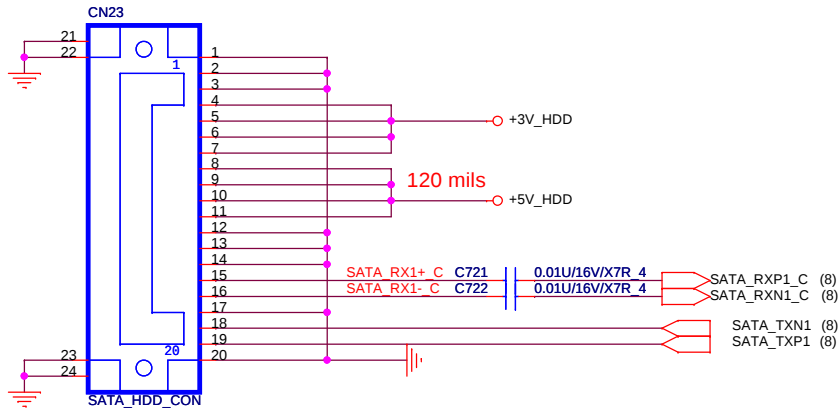
(3,4,8,9,10,11,12,13,14,15,20,21,22,24,25,26,28,29,30,31,32,33,35,36,40,41) +3V  
(3,28,37) +1.5V  
(4,8,9,10,11,12,15,35) +3V5

27

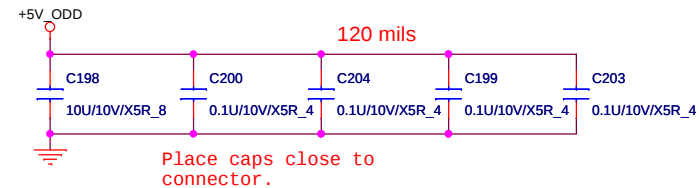
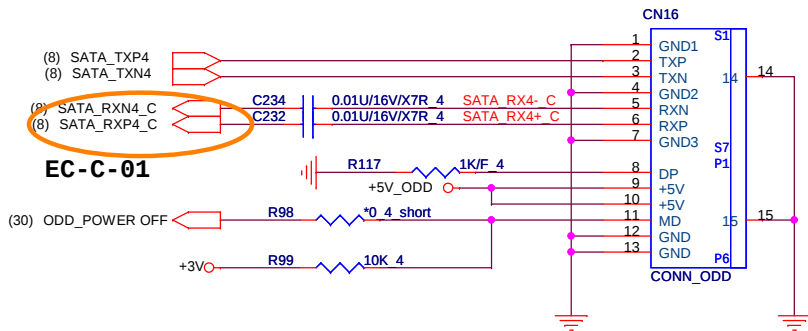




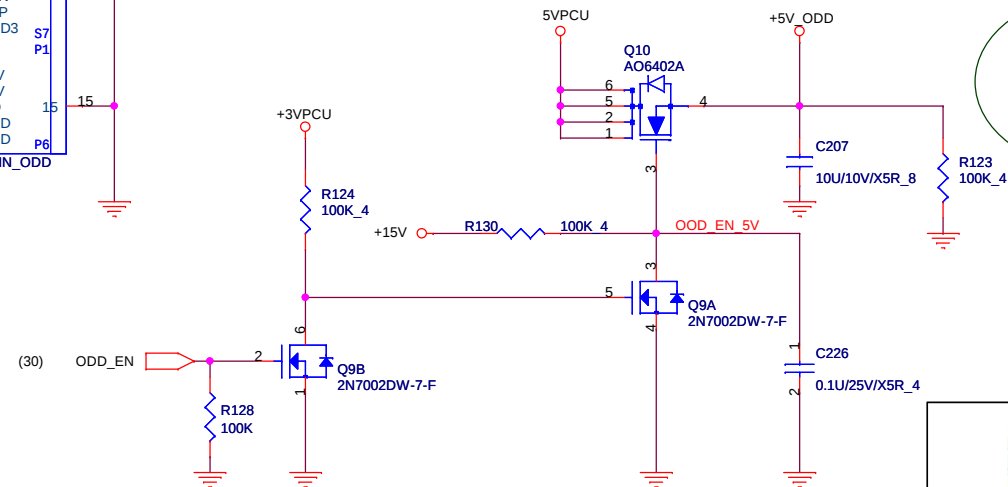
## SATA-HDD CONNECTOR



## SATA CD-ROM

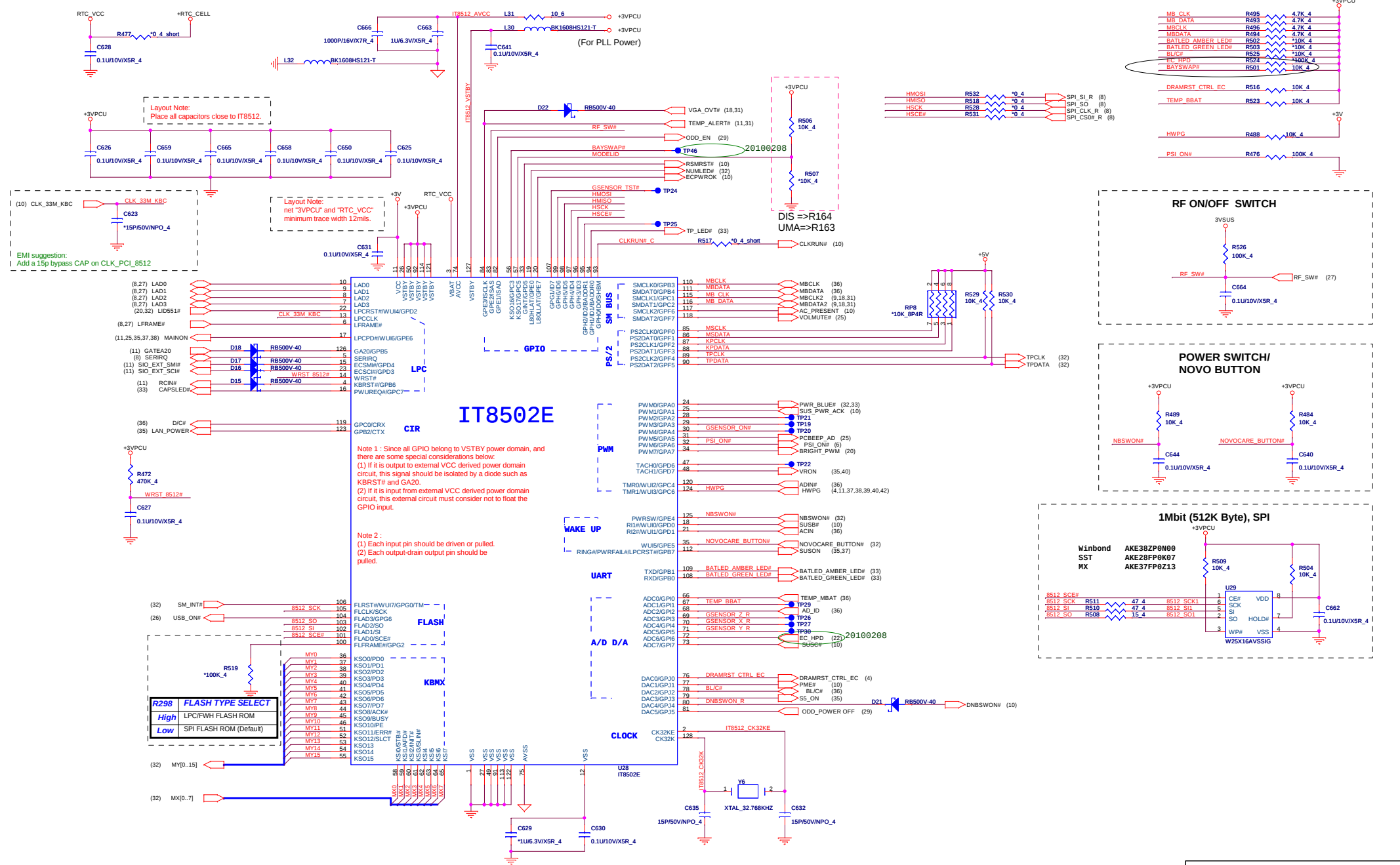


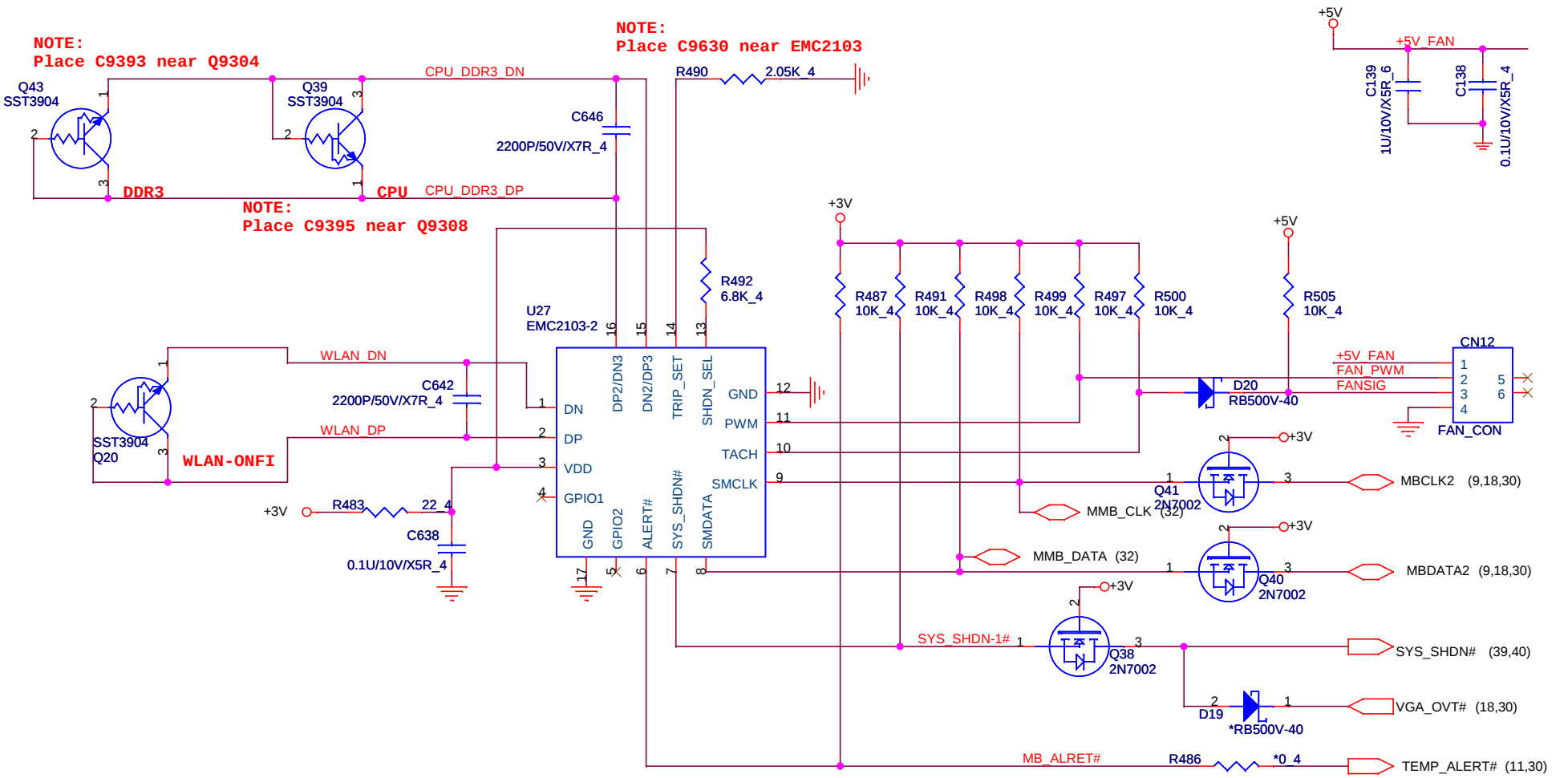
20100208 Del R110, +5V



**PROJECT : LL7A**  
**Quanta Computer Inc.**

| Size   | Document Number          | Rev            |
|--------|--------------------------|----------------|
| Custom | HDD/ODD                  | 3A             |
| Date:  | Thursday, April 08, 2010 | Sheet 29 of 42 |





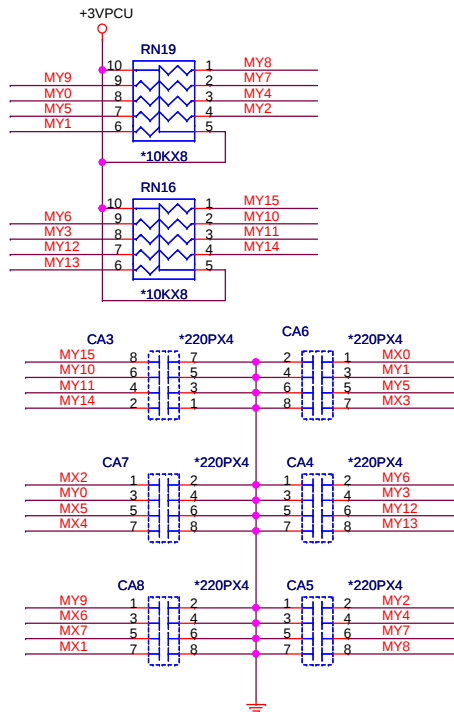
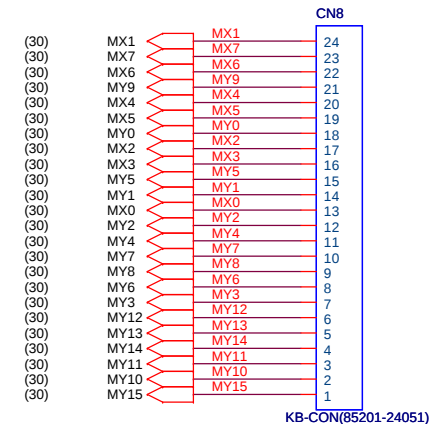


**PROJECT : LL7A**  
**Quanta Computer Inc.**

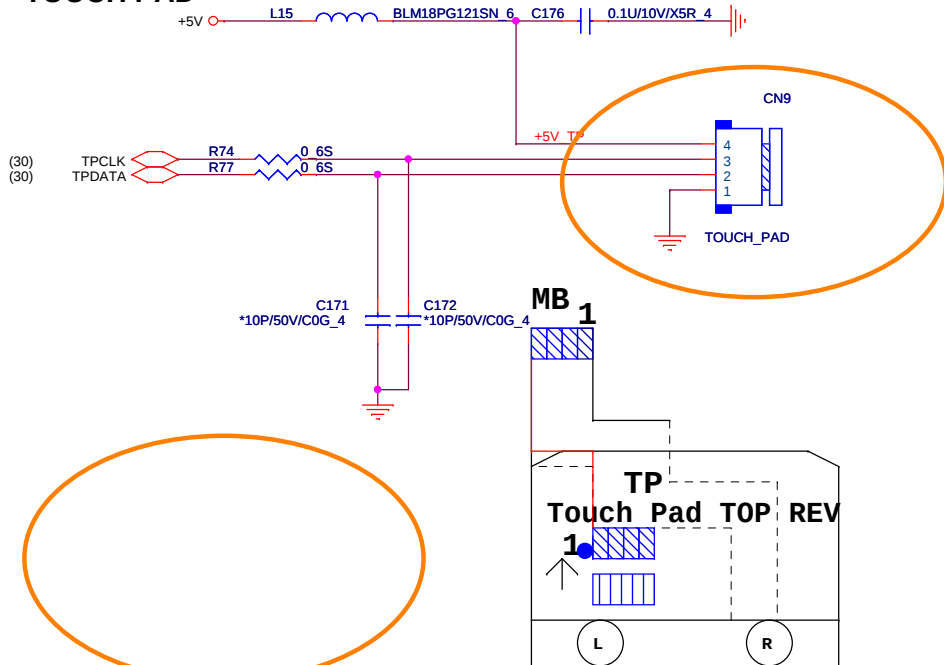
|                                |                      |                |
|--------------------------------|----------------------|----------------|
| Size                           | Document Number      | Rev            |
| Custom                         | <b>FAN / THERMAL</b> | <b>1A</b>      |
| Date: Thursday, April 08, 2010 |                      | Sheet 31 of 42 |

hexaim@hotmail.com

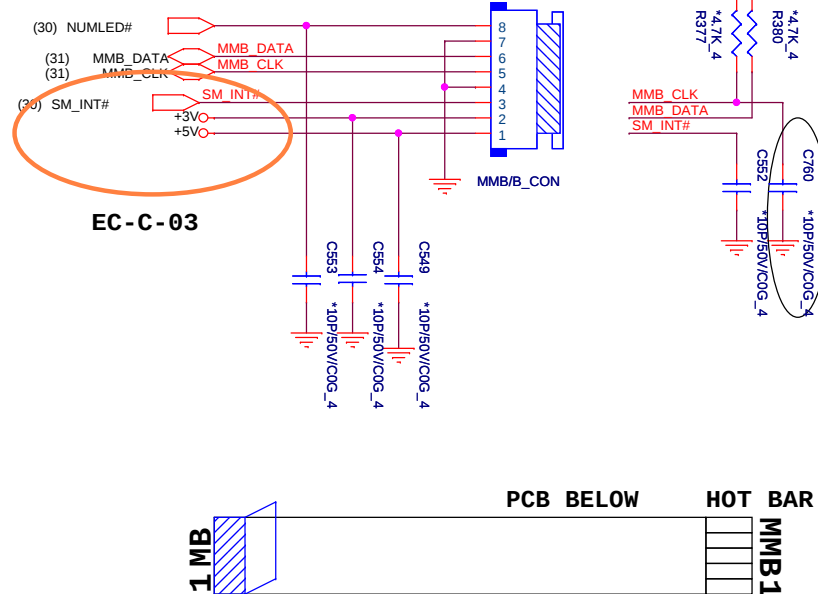
## KEYBOARD



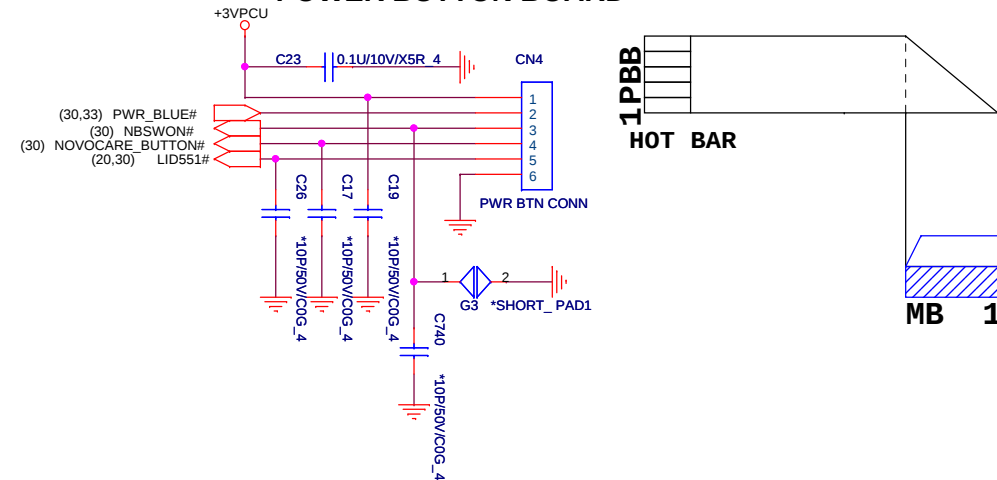
## TOUCH PAD



## CAPACITANCE BUTTON BOARD

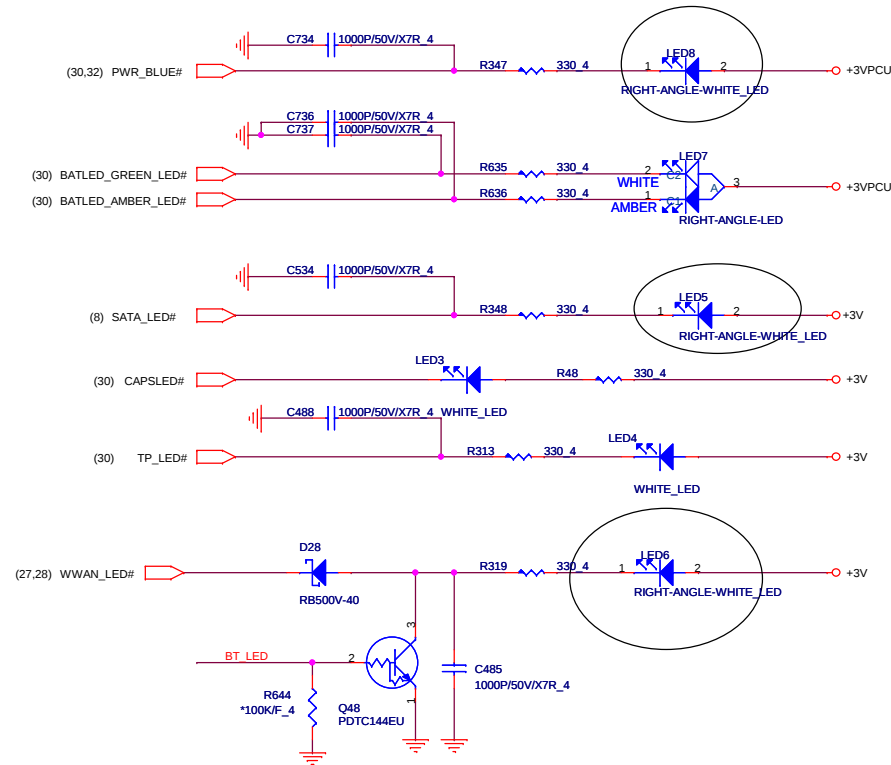


## POWER BUTTON BOARD

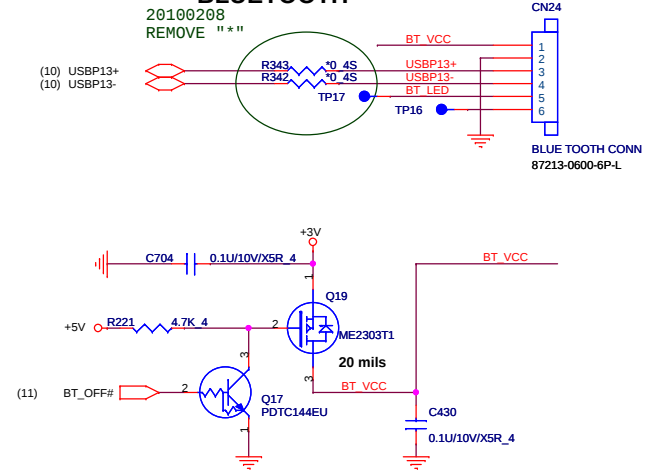


**PROJECT : LL7A**  
**Quanta Computer Inc.**

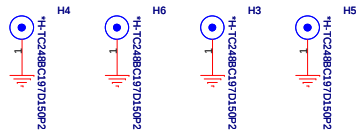
## LED



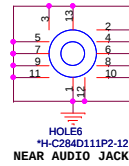
## BLUETOOTH



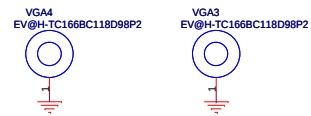
## CPU BKT



## CRT HOLE



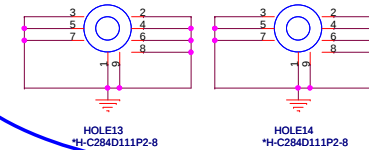
## GPU NUT



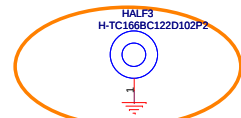
## ODD HOLE



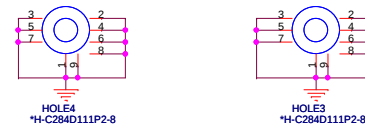
## KEYBOARD HOLE



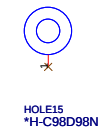
## WLAN nut



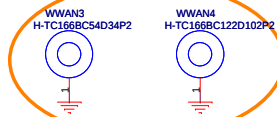
## BATTERY HOLE



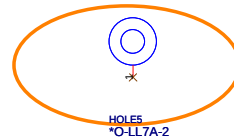
## WLAN CABLE HOLE



## WWAN NUT



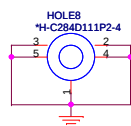
## WLAN CABLE HOLE



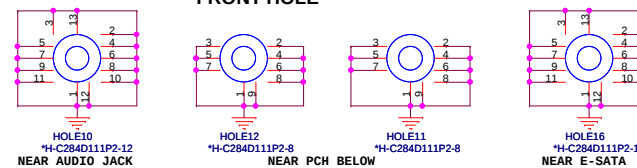
## EMI BATTERY PAD

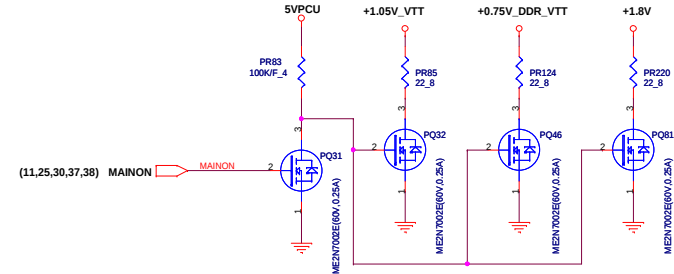
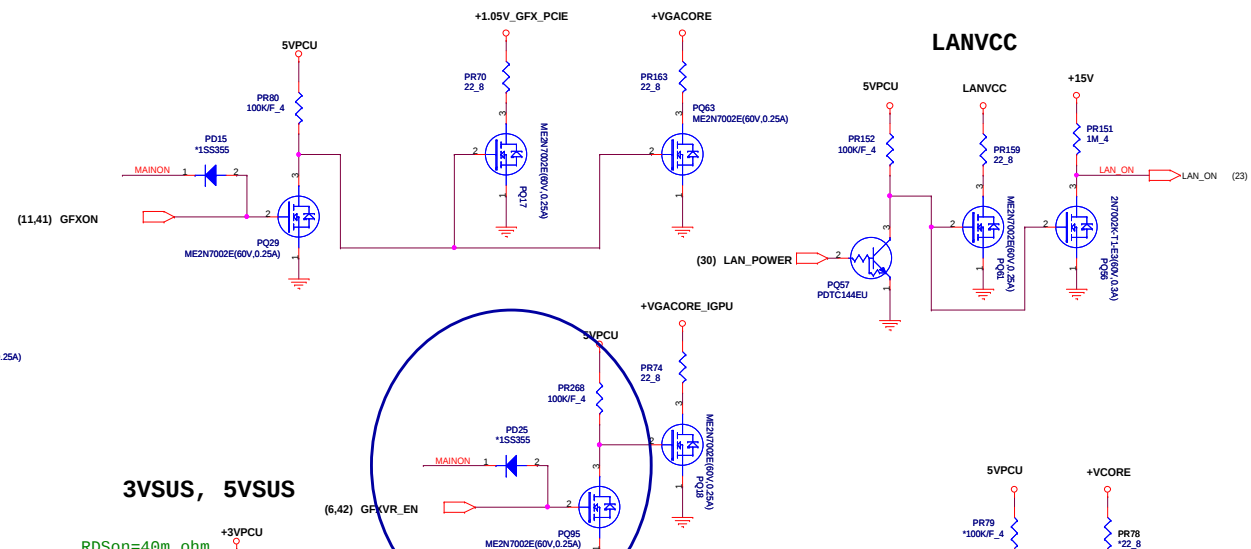


## DDR 3 HOLE

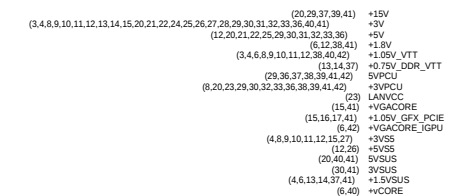
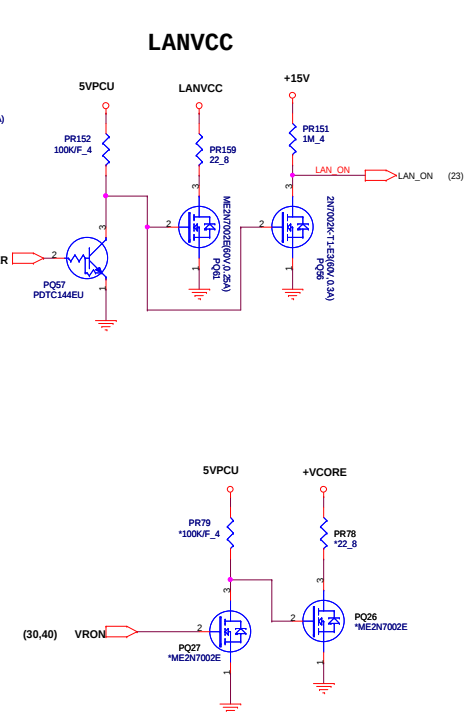


## FRONT HOLE



[illegible]

Schematic diagram of the power supply section of the MEZ7002E. It shows a transformer secondary winding connected to a bridge rectifier (PQ8). The positive output of the rectifier is connected to a filter capacitor (C1) and a load resistor (R1). The negative output of the rectifier is connected to ground. The input voltage to the transformer is 6(4,2) GFVR\_EN.









|                                |                                               |                  |
|--------------------------------|-----------------------------------------------|------------------|
| Size Custom                    | Document Number<br><b>1.05V_VTT (RT8204C)</b> | Rev<br><b>1A</b> |
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