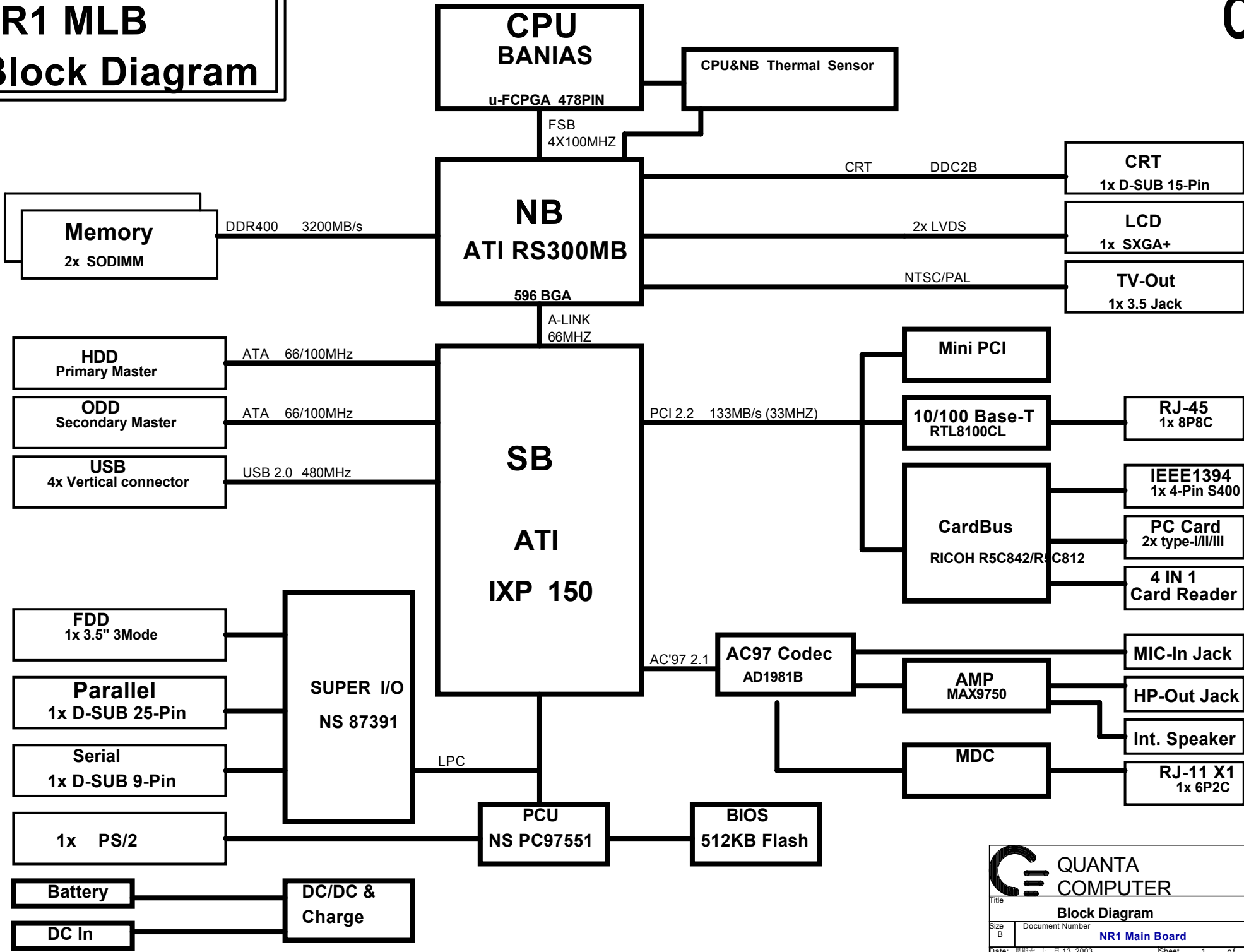


NR1 MLB

Block Diagram

01



Page List

01 Block Diagram
02 Page List
03 BANIAS CPU (HOST BUS)-1
04 BANIAS CPU (POWER/NC)-2
05 RS300MB-AGTL+&AGP&A_LINK 1/6

06 RS300MB-DDR I/F 2/6
07 RS300MB-VIDEO I/F&CLKGEN 3/6
08 RS300MB-POWER 4/6
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10 DDR SDRAM(on board)

11 DDR SODIMMX2
12 DDR Terminator A CHANNEL
13 DDR Terminator B CHANNEL
14 CRT/TV OUT PORT
15 MEM CLK BUFFER/EXT CLK GEN

16 SB150M-ALINK/PCI/CPU/LP-1/4
17 SB150M-IDE/USB-2/4
18 SB150M-PWR & DECOUPLING-3/4
19 SB150M-STRAPS-4/4
20 R5C842_PCI/1394

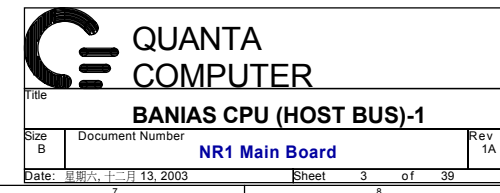
21 R5C842_CARDBUS/MEDIA
22 SD MS_XD SLOT
23 LAN-1(82541EI)
24 LAN-2
25 AUDIO CODEC

26 AUDIO AMP & INT. SPEAKER
27 MINI PCI & MDC
28 IDE HDD,ODD CONNECT
29 PC87391 SUPER IO
30 PARALLEL/SERIAL PORT

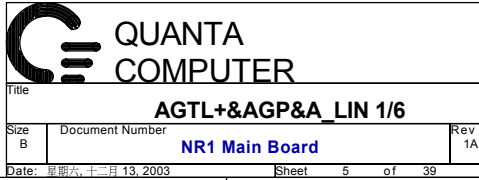
31 USB CONNECT
32 PCU-87591 & BIOS
33 Int. Keyboard &TP Conn.
34 DC/DC 3V&5V
35 DC/DC 2.5/1.8/1.5VSUS

36 CPU CORE (MAX1907)
37 BATTERY CHARGER
38 BATTERY CONNECTOR
39 1.05/1.2/1.8 & VGA POWER

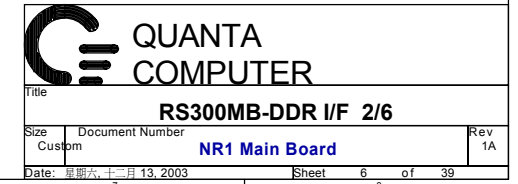
 QUANTA COMPUTER			
Title COVER PAGE			
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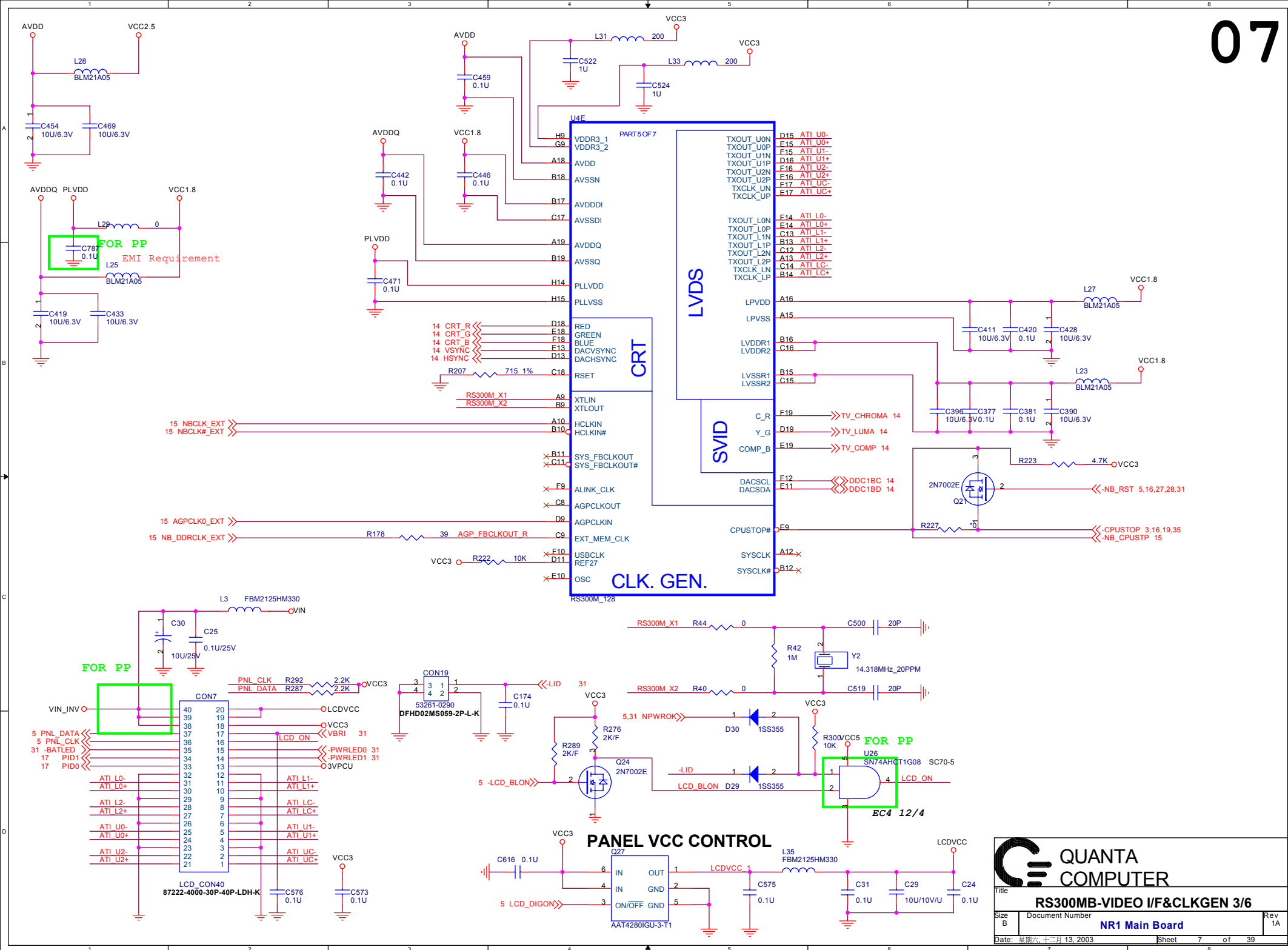


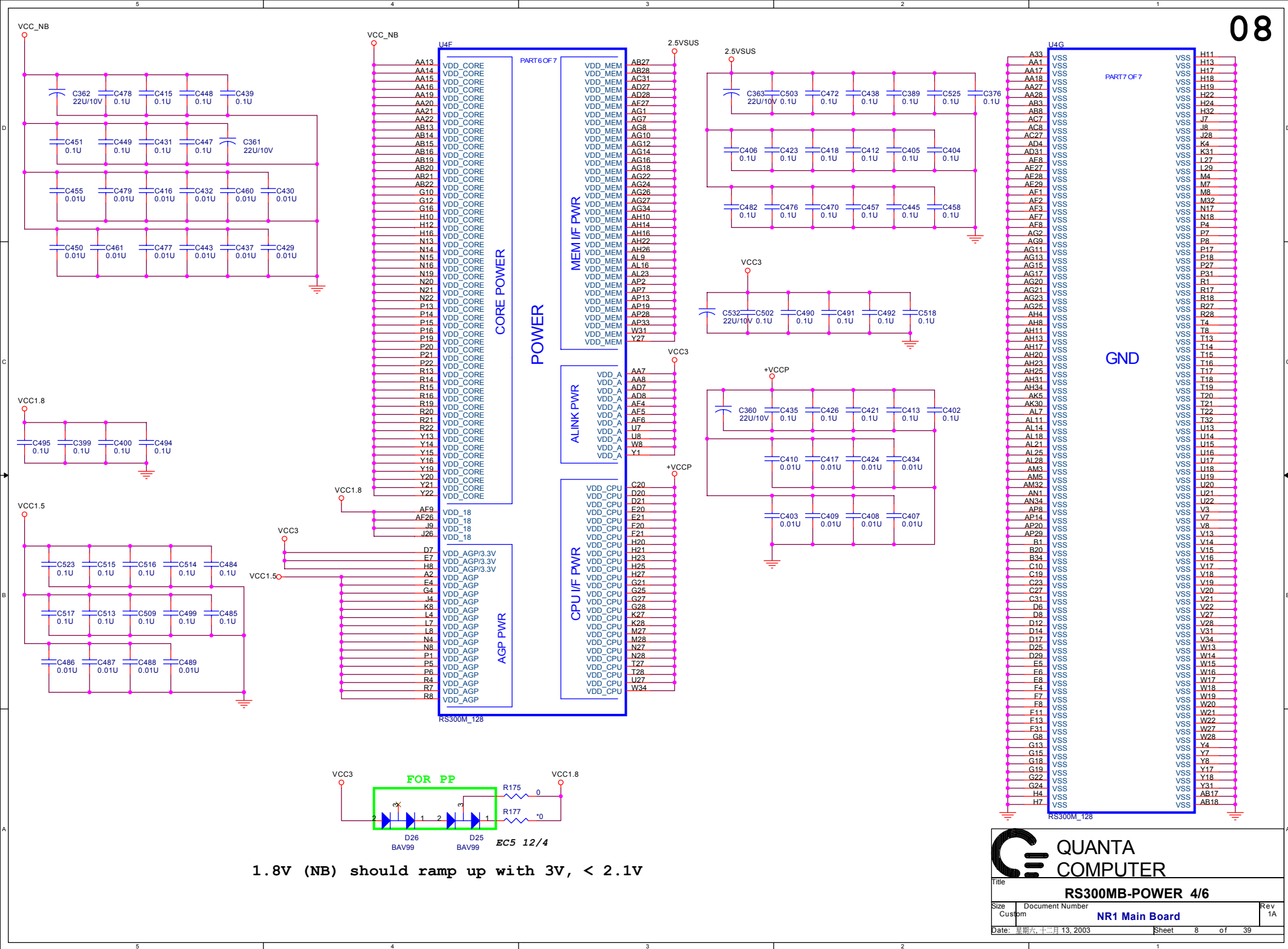


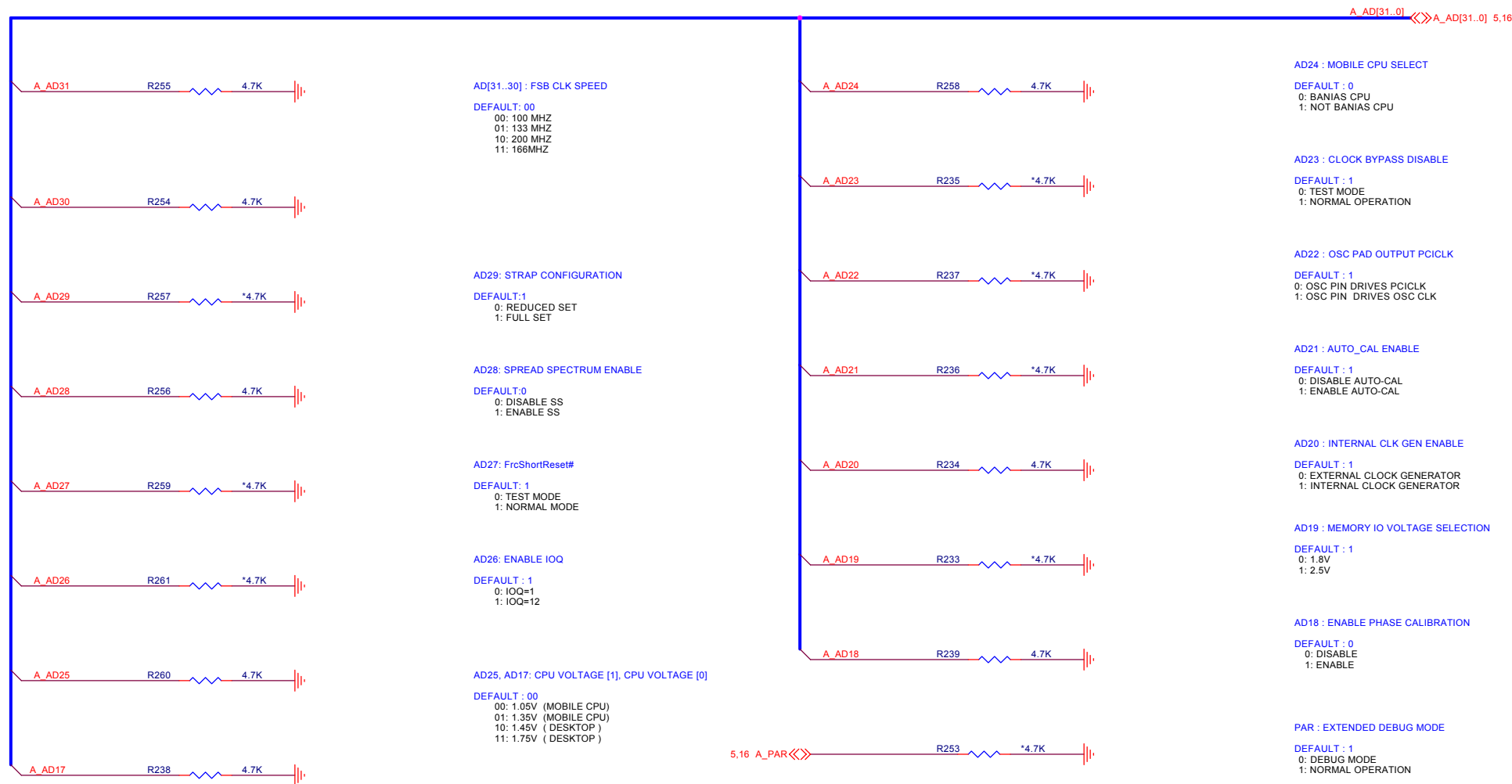


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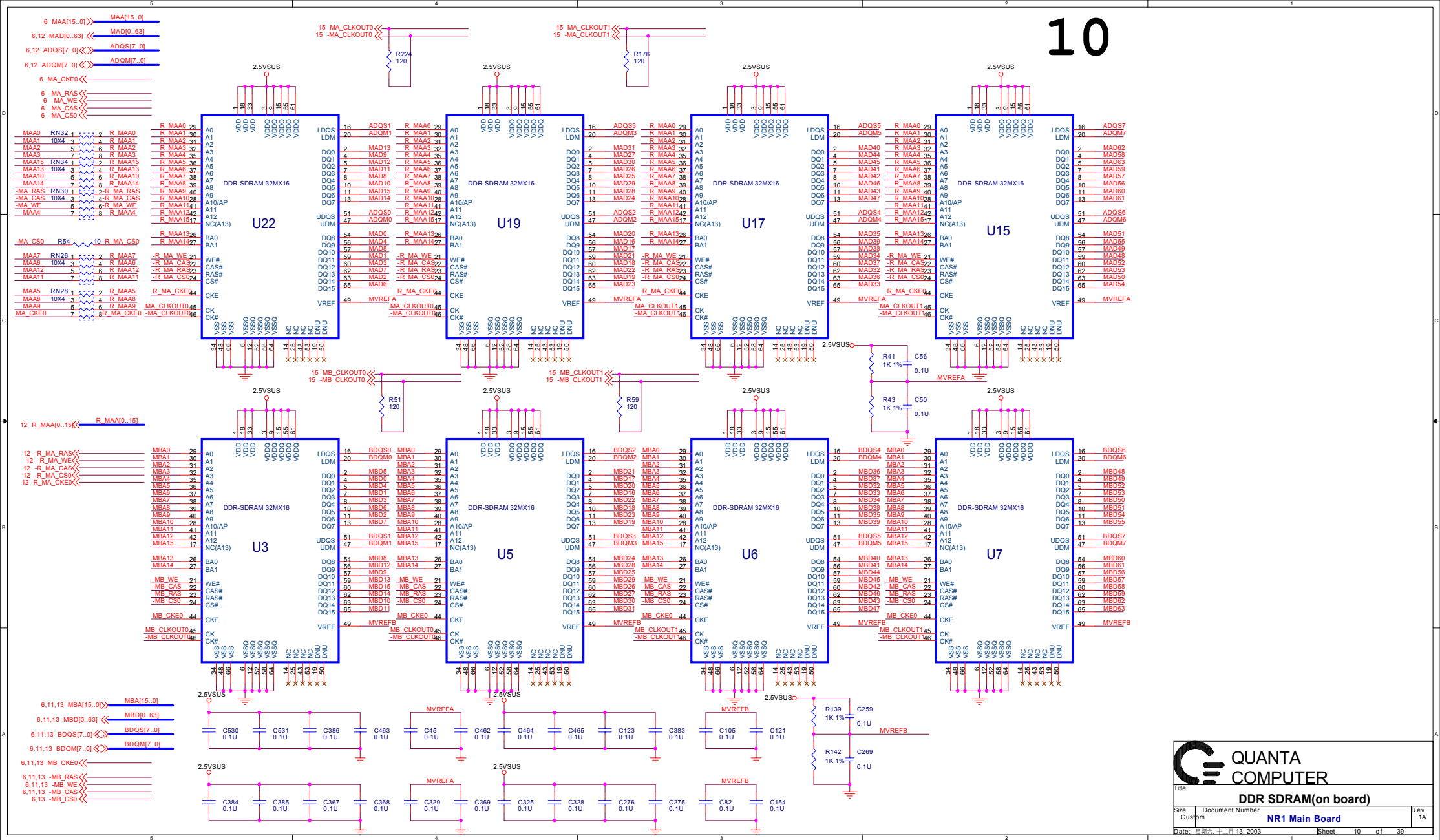








 QUANTA COMPUTER		
Title		
DDR SDRAM(on board)		
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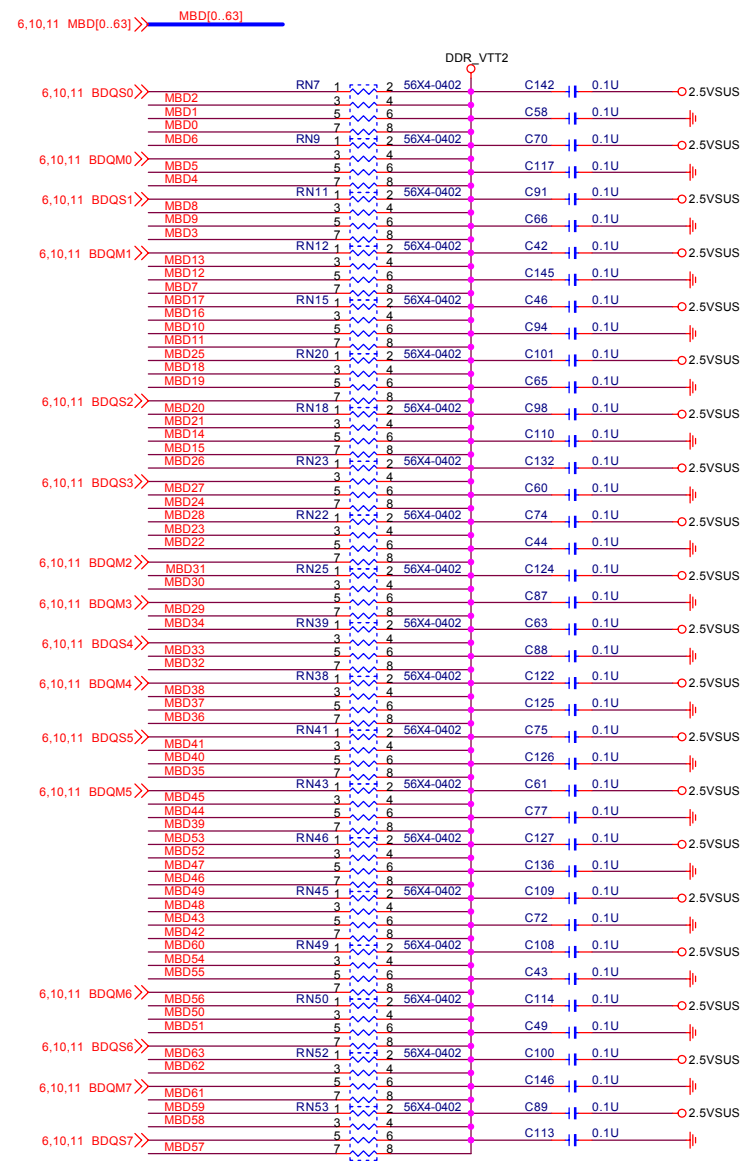
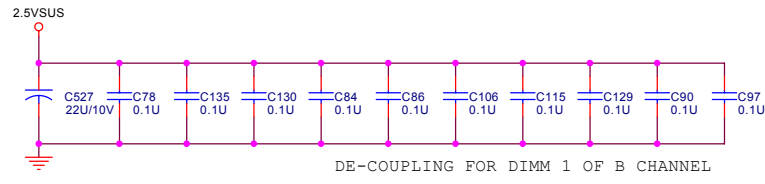
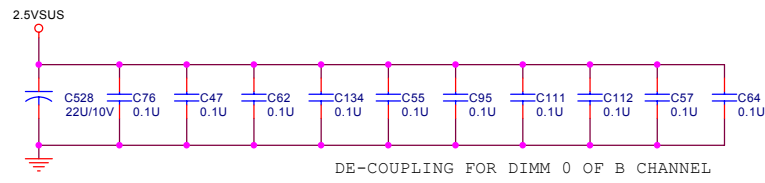
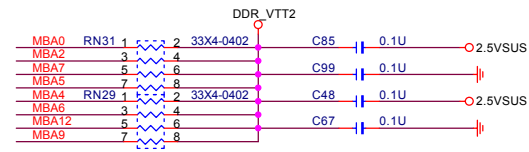
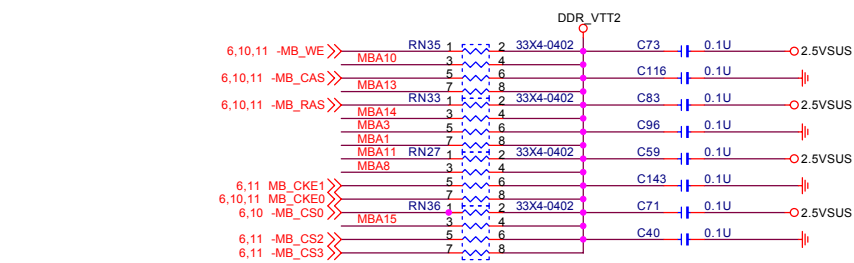
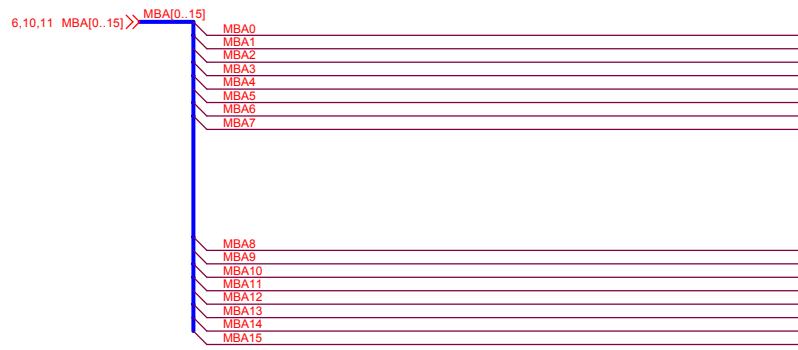
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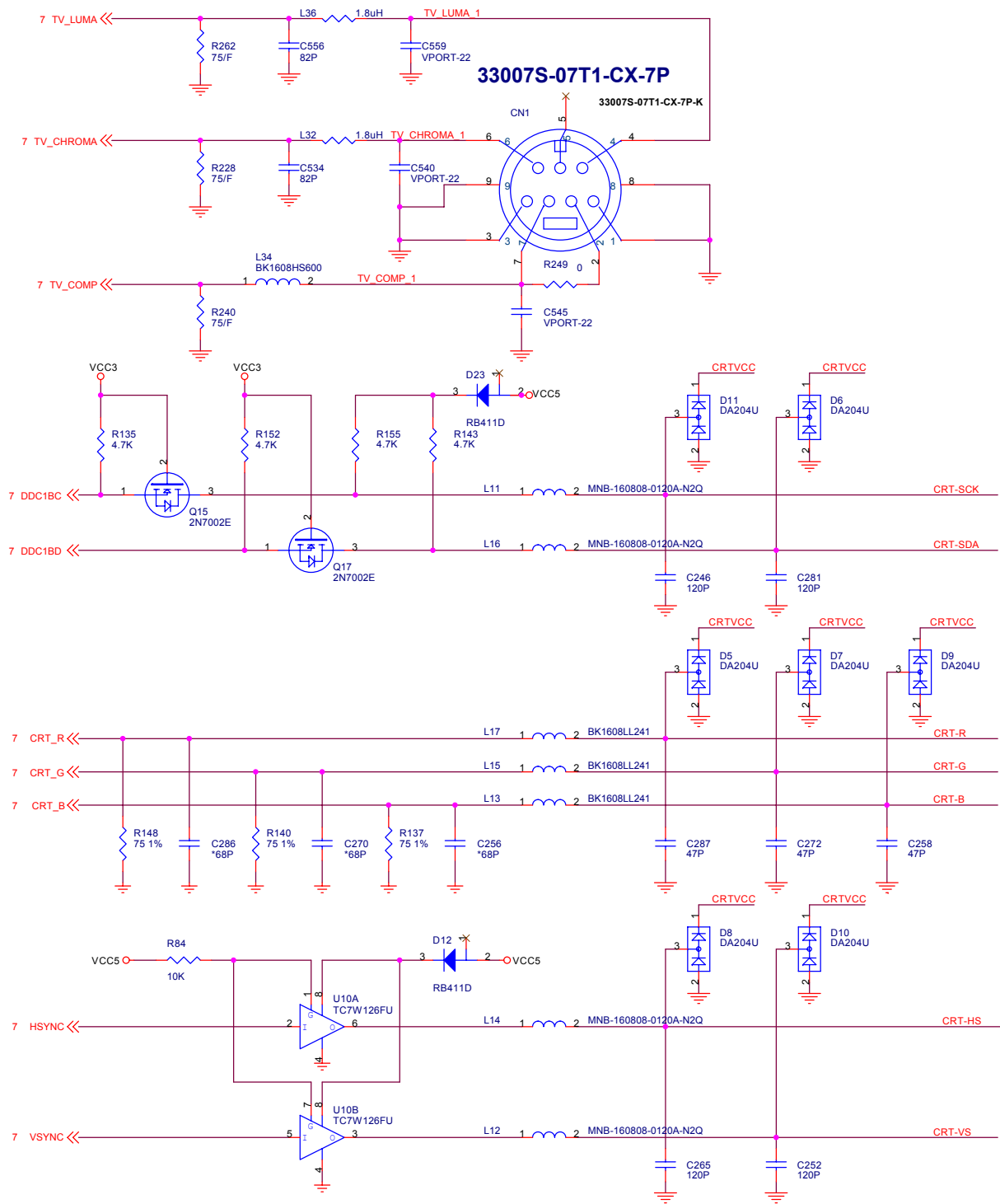


DDR TERMINATOR CHANNEL B

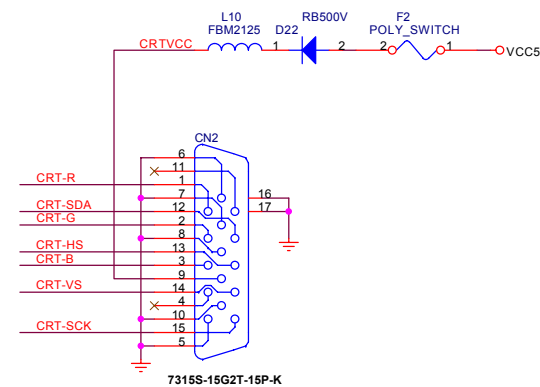
MBA Terminator

13





CRT CONNECTOR



QUANTA
COMPUTER

Title

CRT/FDD PORT

Size

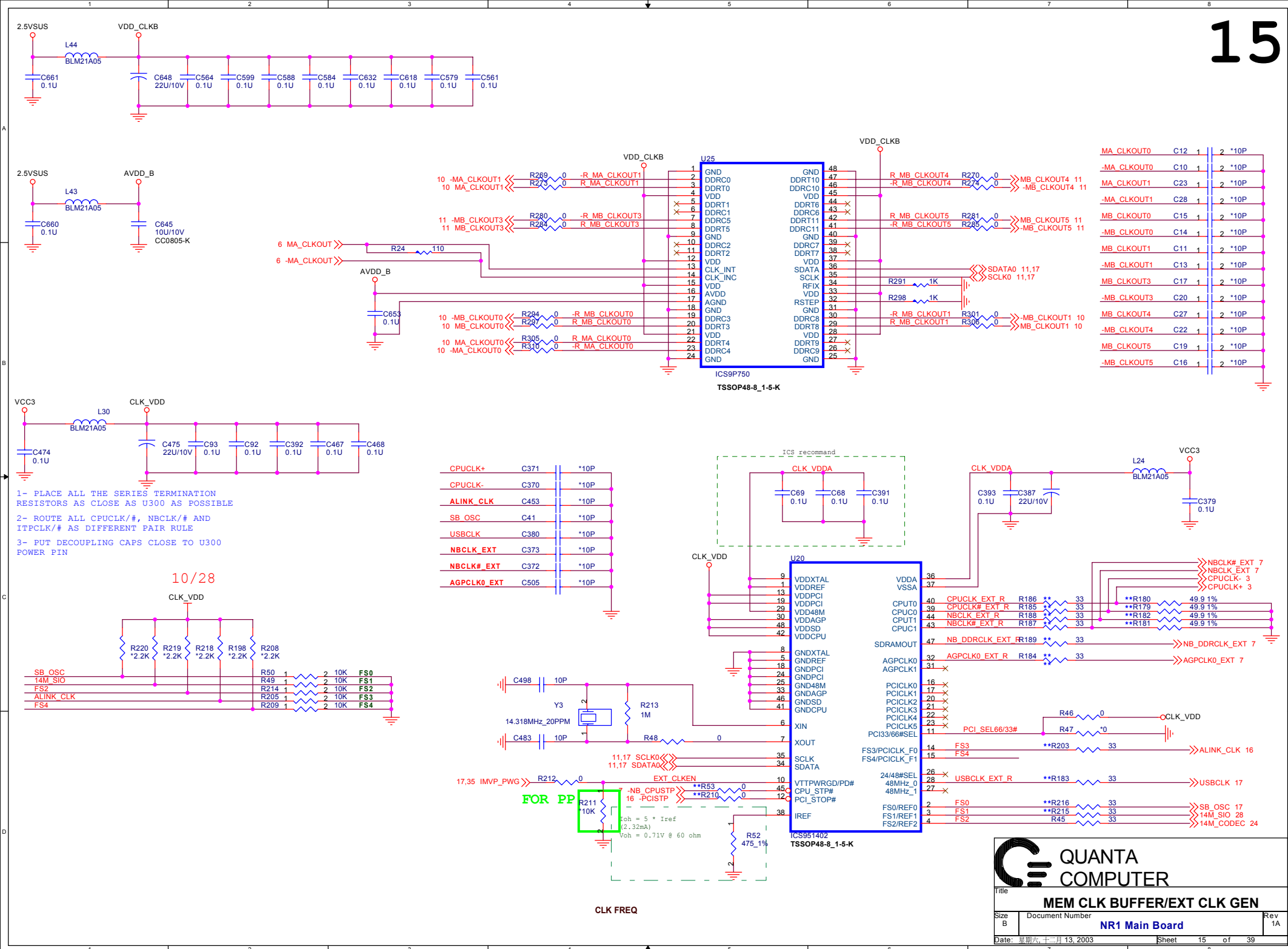
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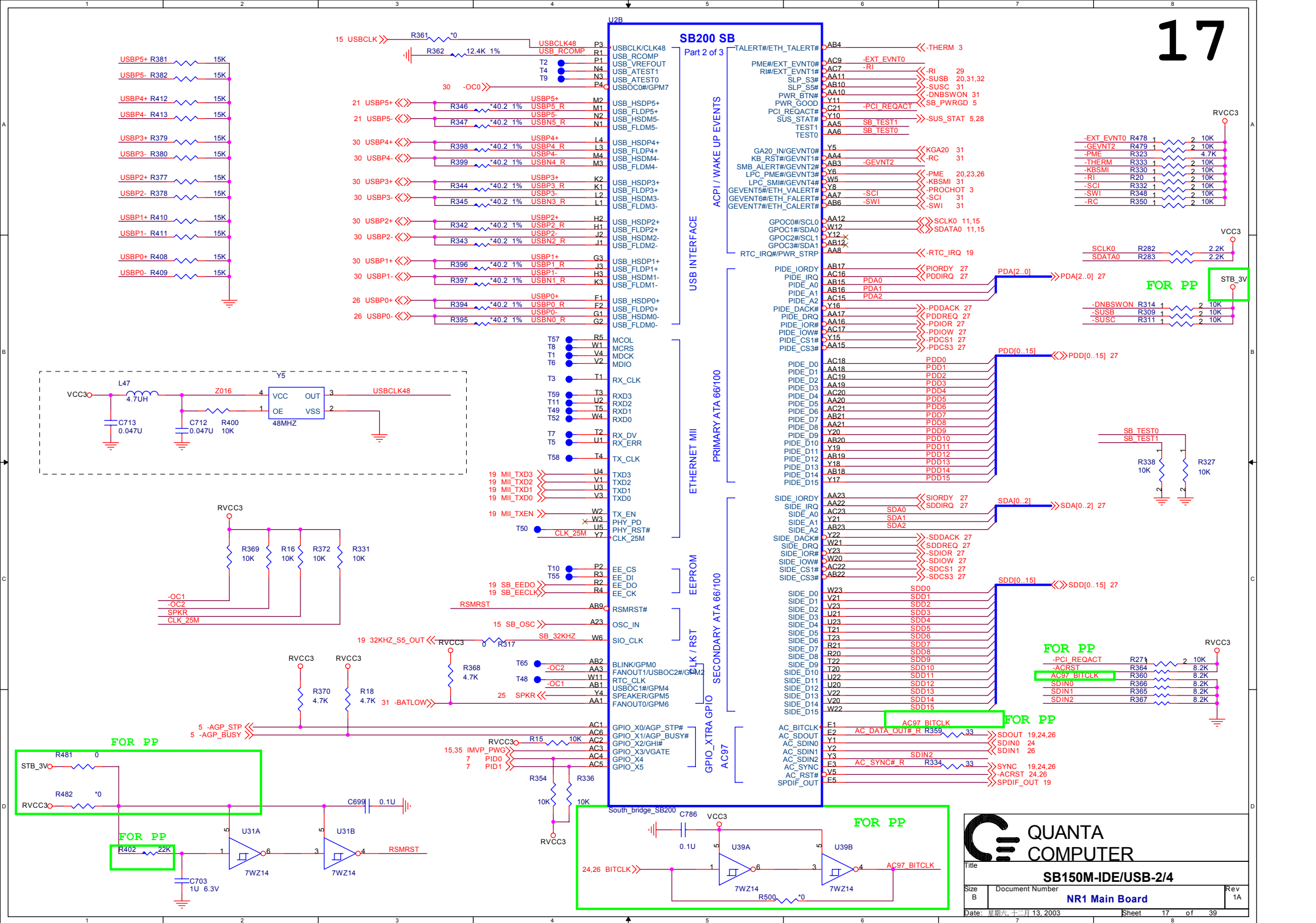
NR1 Main Board

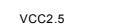
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RGA-15[®]

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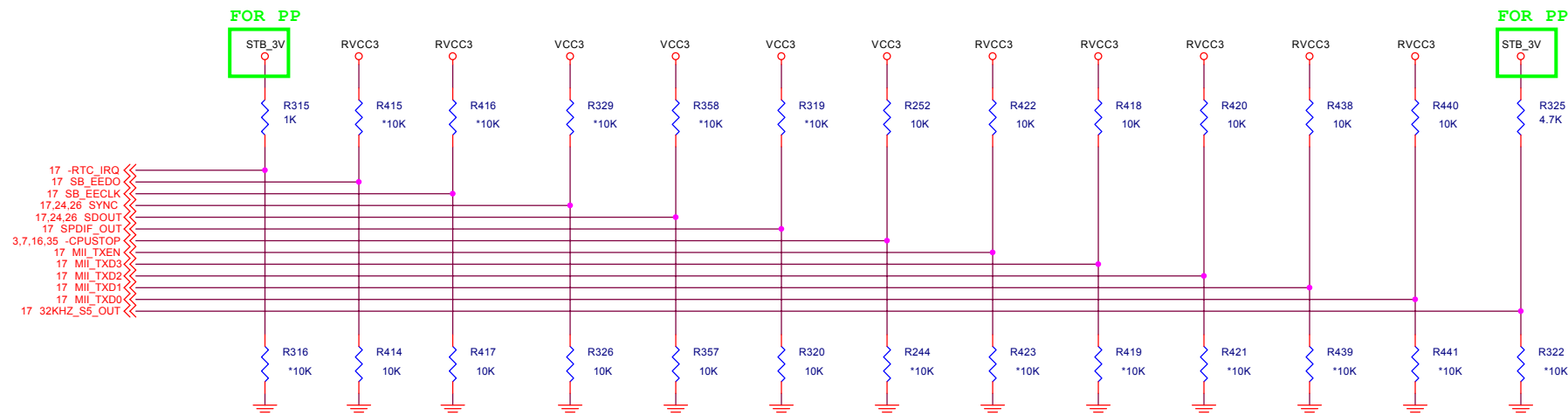
SB150M-PWR & DECOUPLING-3/4

NR1 Main Board

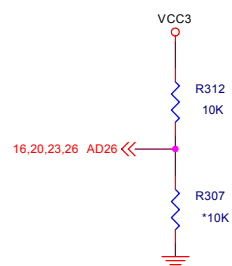
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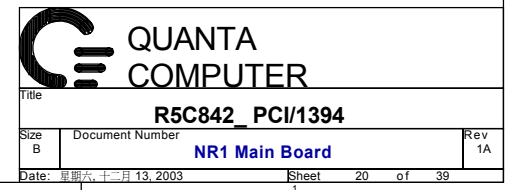
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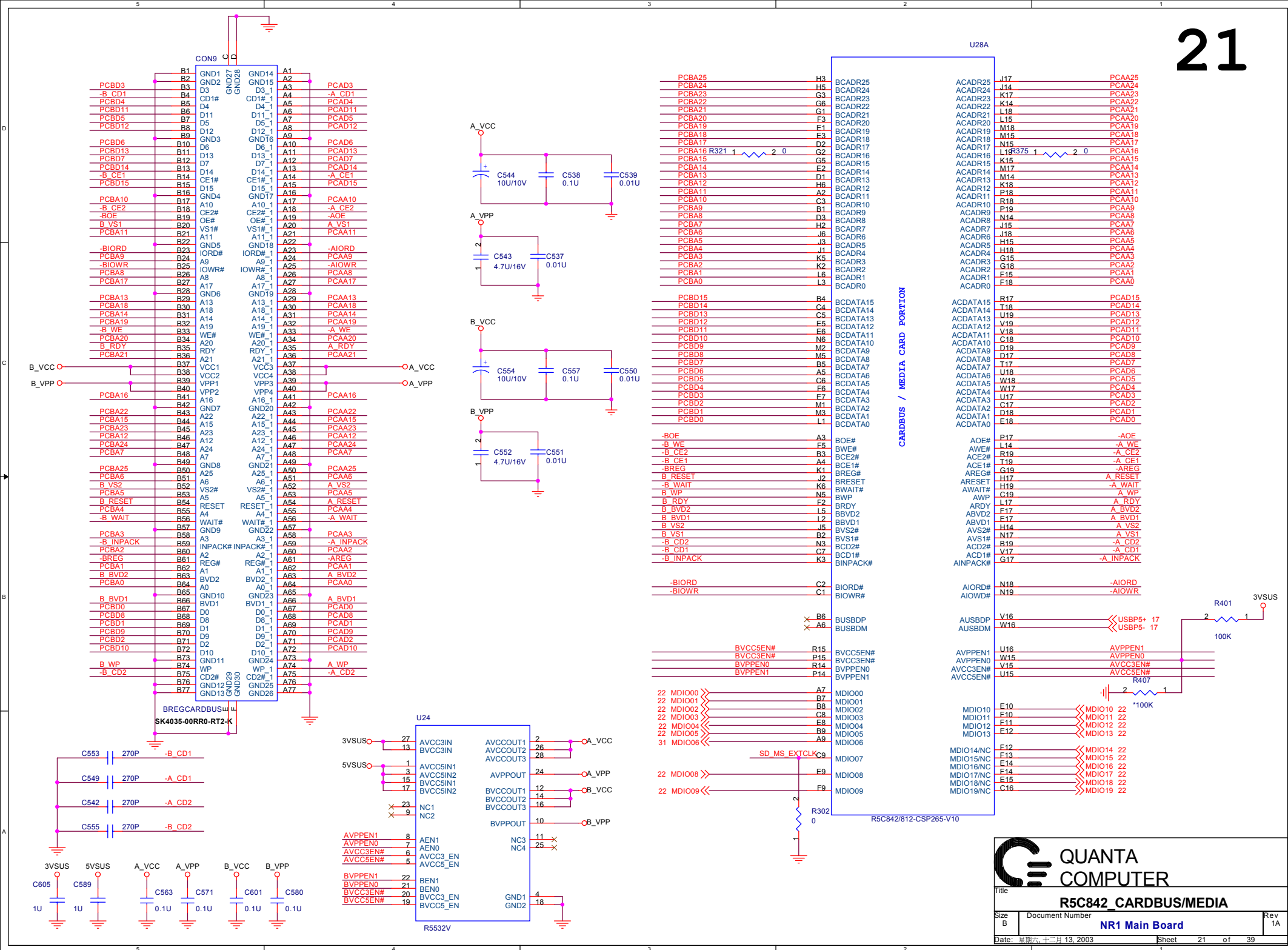


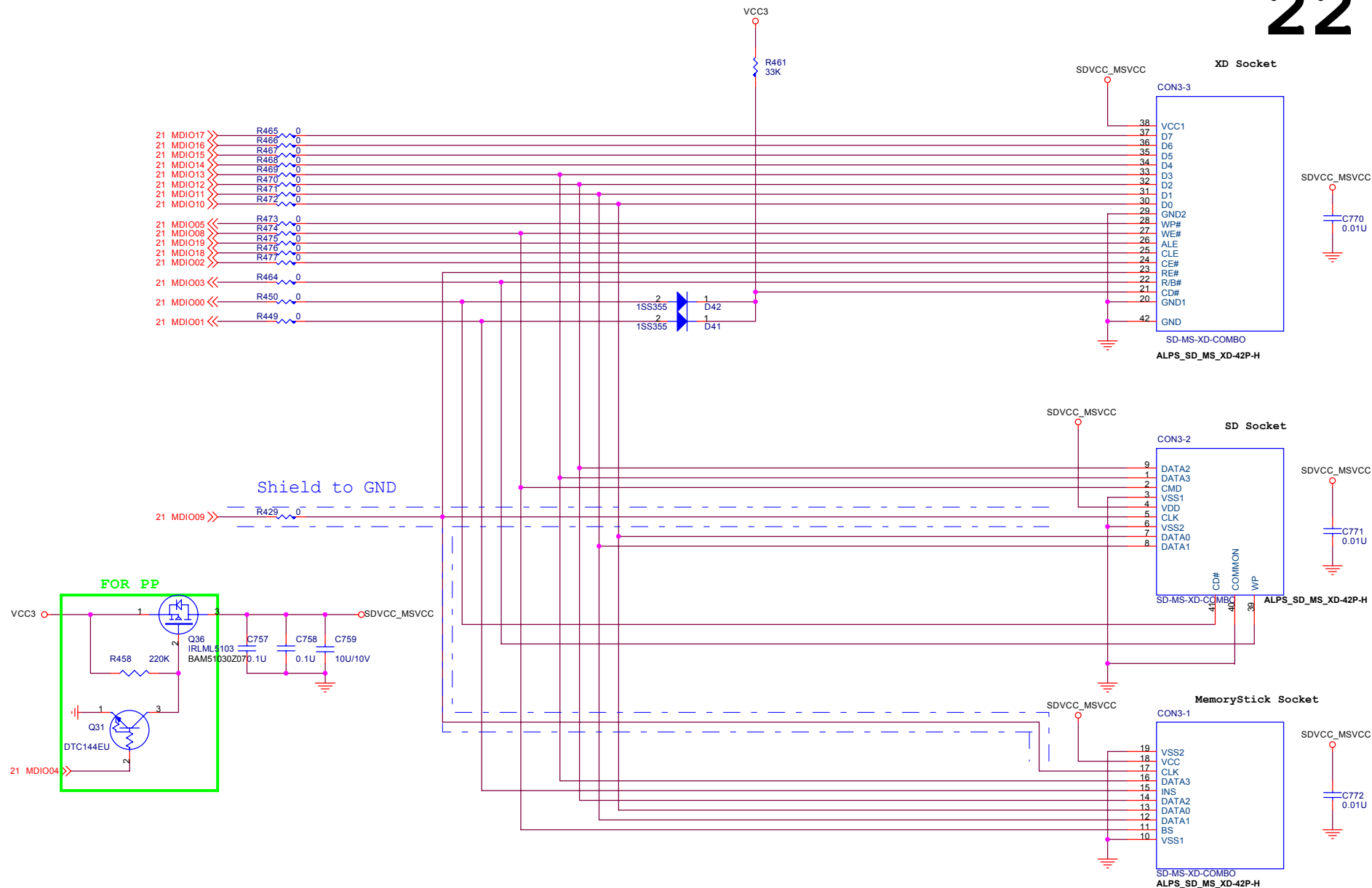
STRAP HIGH	ACPWRON	EEDO	EECK	SYNC	SDOUT	SPDIF_OUT	-CPUSTOP	TX_EN	ETHERNET TXD[3:0]	32KHZ_S5
	MANUAL PWRON DEFAULT	USE DEBUG STRAPS DEFAULT	ROM ON PCI BUS	INIT ACTIVE HIGH	33MHz NB BUS	SIO 24MHz	ENABLE SPEED STEP DEFAULT	DISABLE CPU FREQ SETTING DEFAULT		PROCESSOR FREQ MULTIPLIER
STRAP LOW	AUTO PWR ON	IGNORE DEBUG STRAPS	ROM ON LPC BUS DEFAULT	INIT ACTIVE LOW (PIII) DEFAULT	HISPEED A-LINK DEFAULT	SIO 48MHz DEFAULT	DISABLE SPEED STEP	ENABLE CPU FREQSETTING		32KHZ INPUT TO SB200 (EXT RTC)



AD26
BIOS use LPC cycle DEFAULT
BIOS uSE FWH Cycle







11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474
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Si

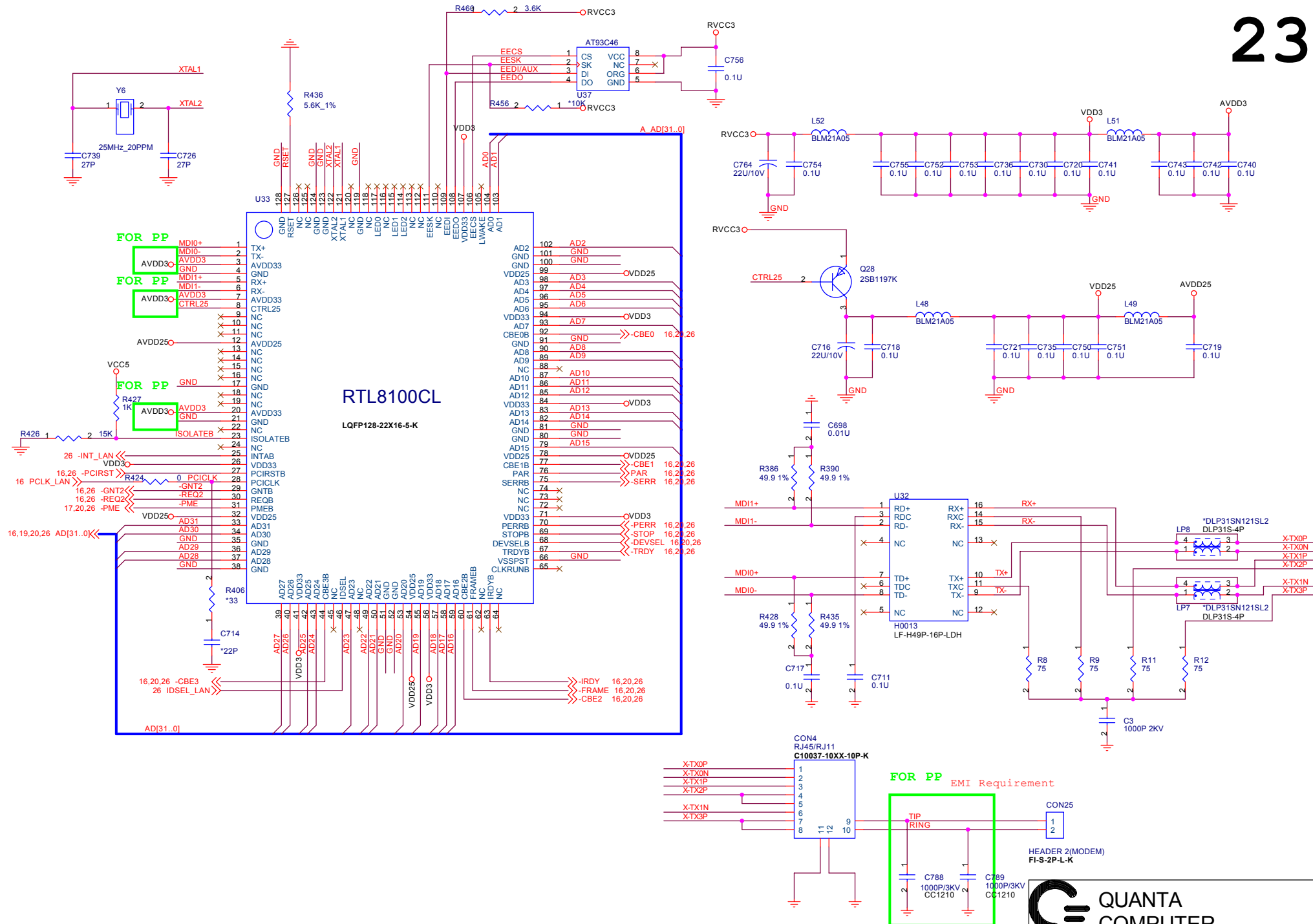
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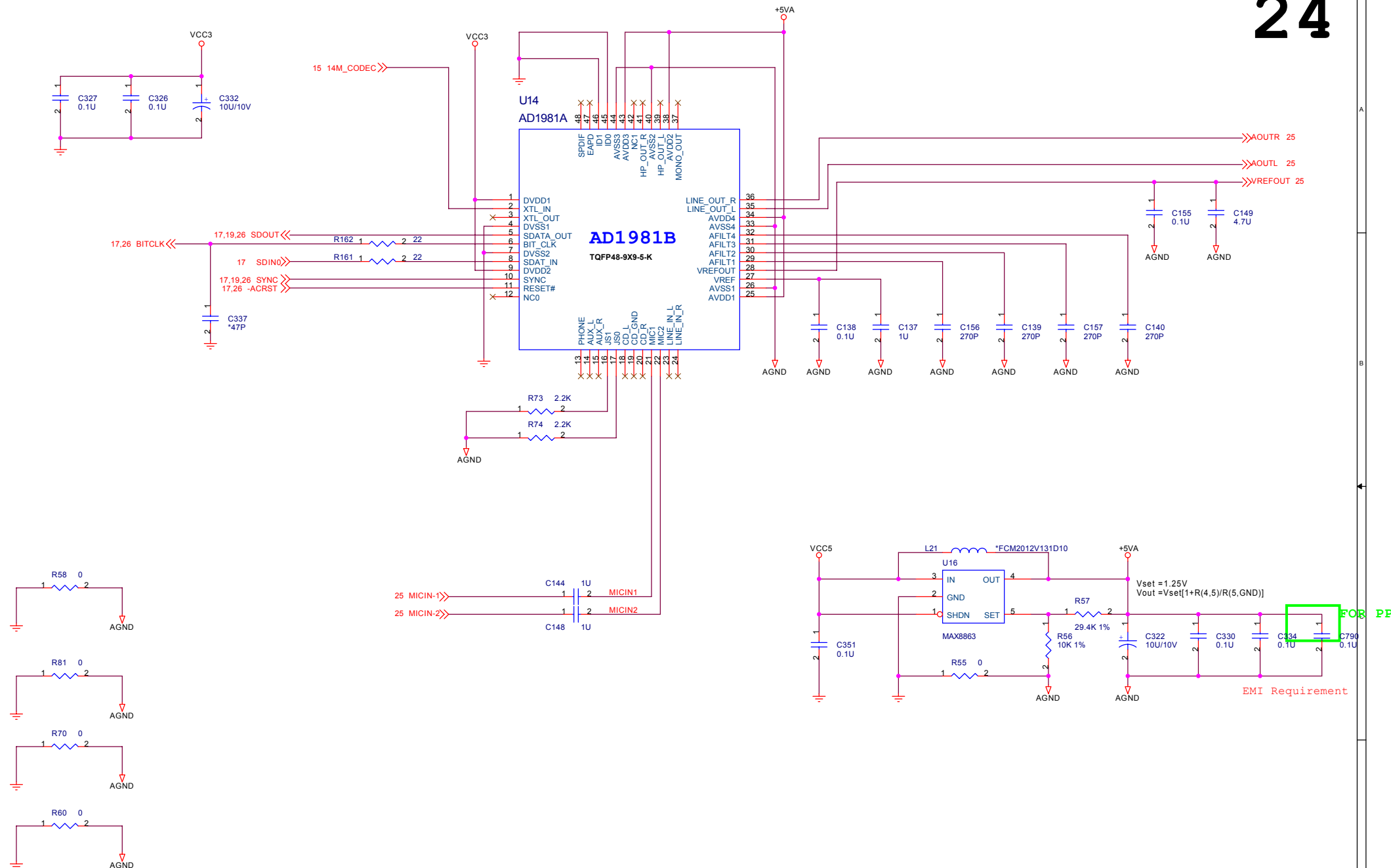
NR1 Main Board

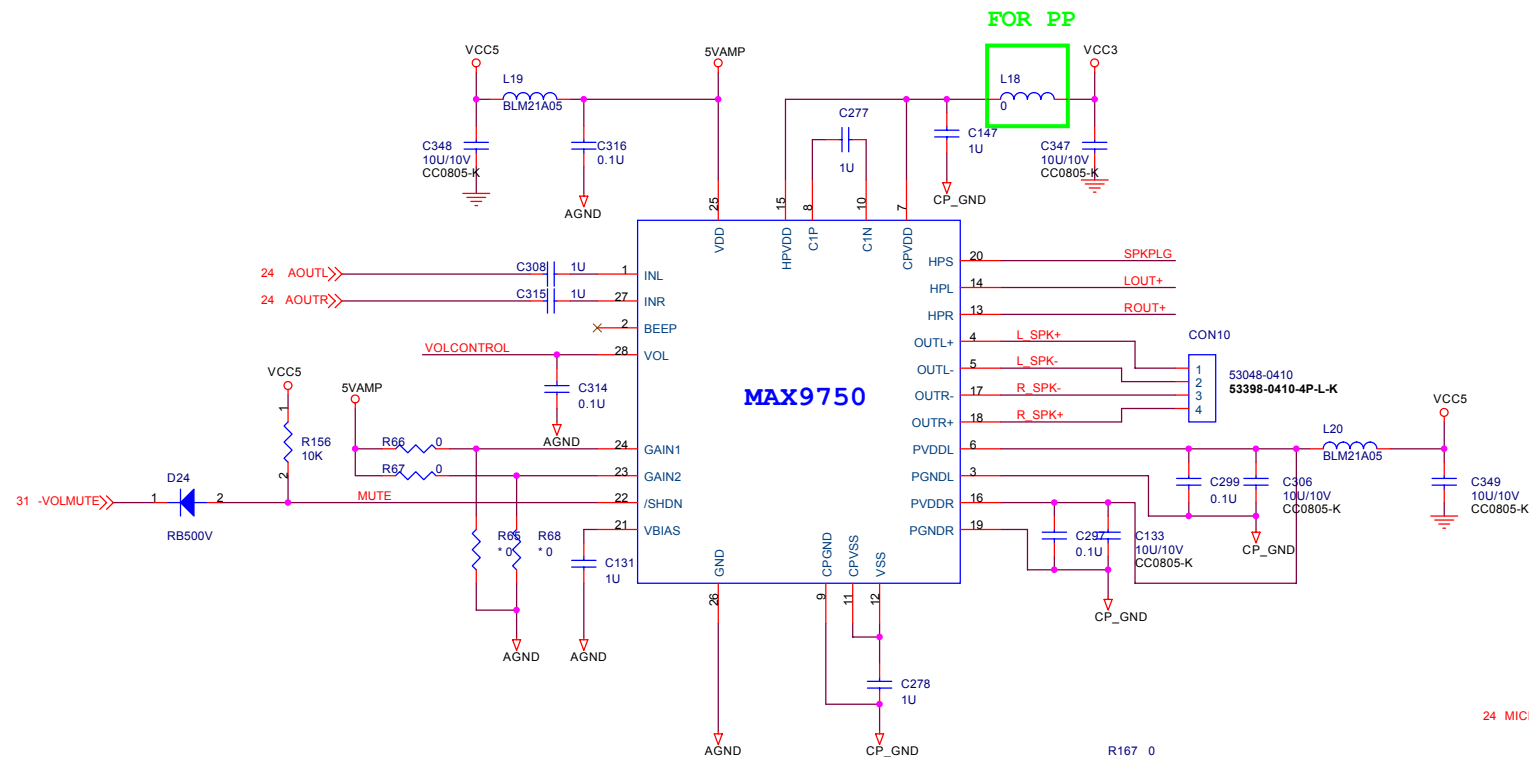
Date: 5/1/2018

Date: 星期六, 十二月 13, 2003

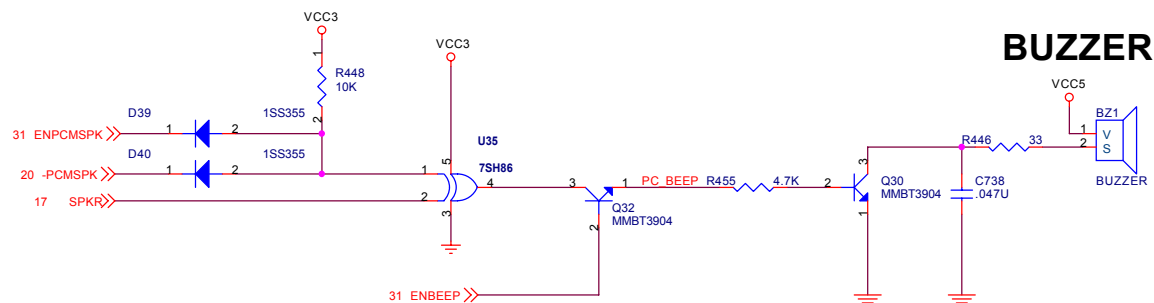
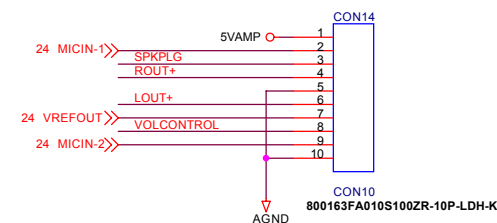
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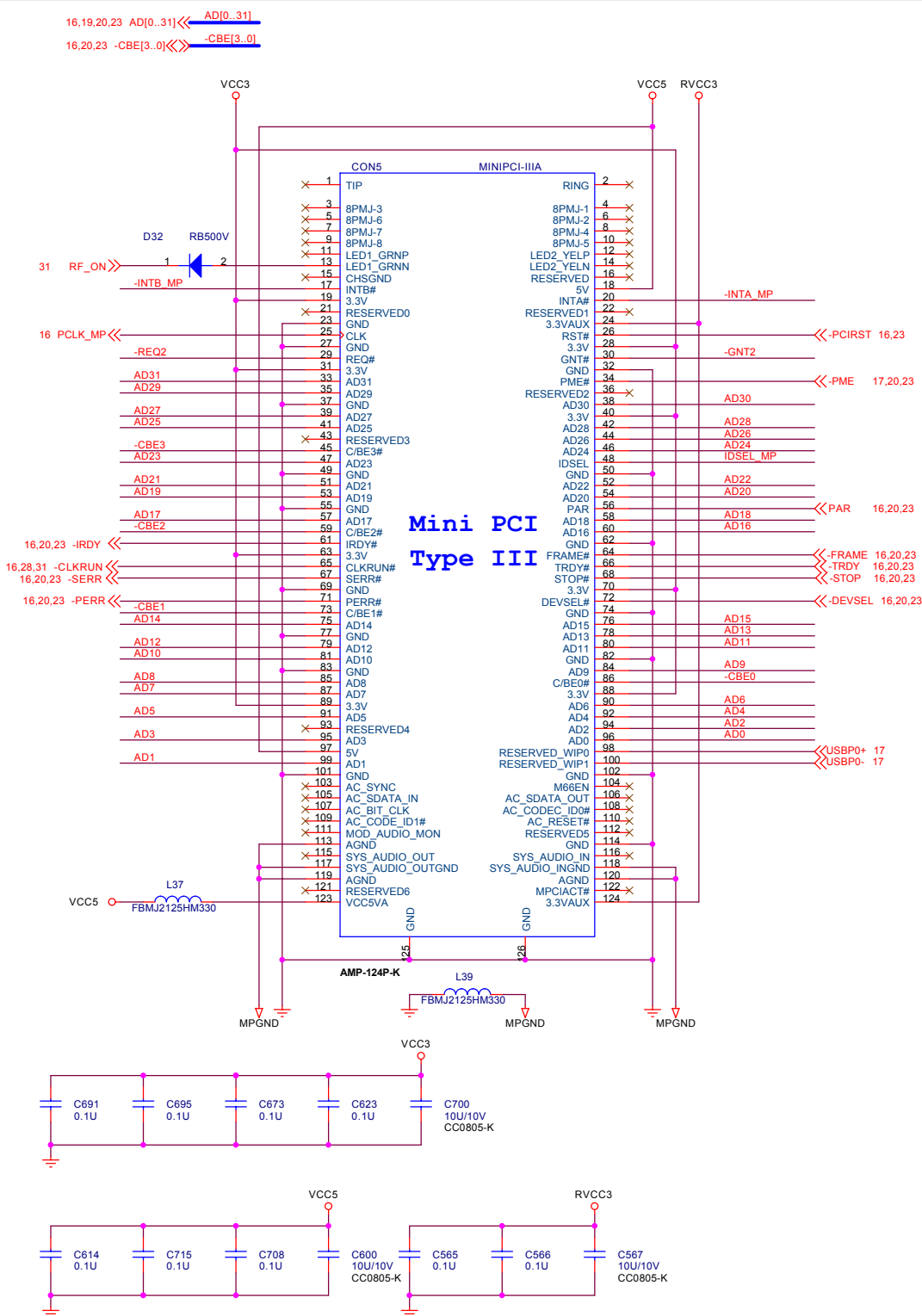
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1A

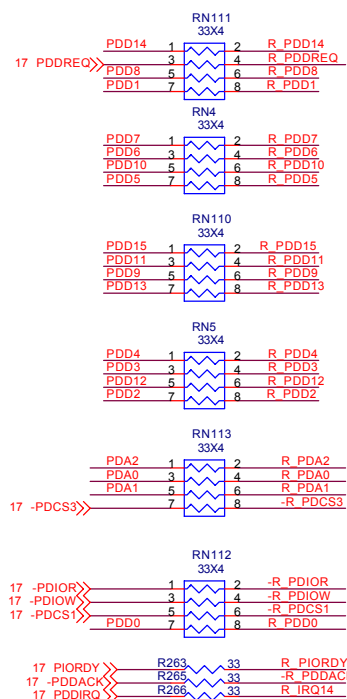




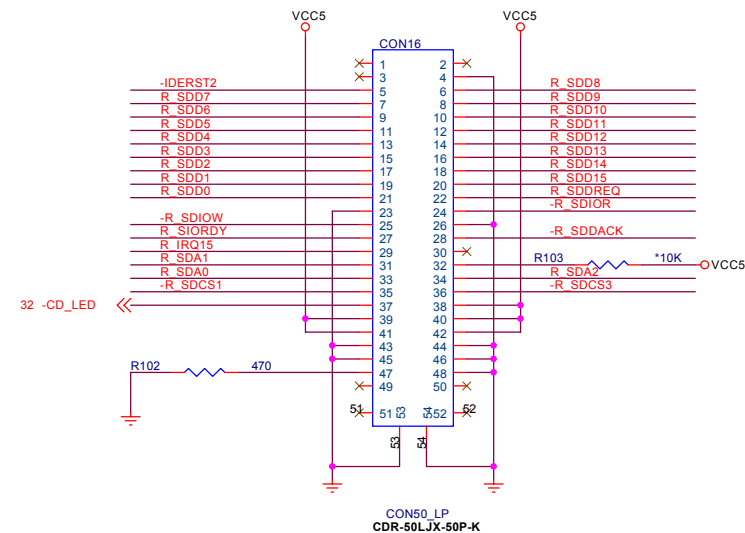
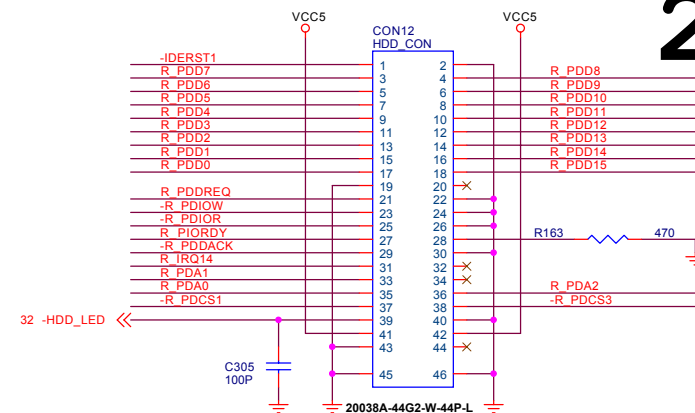
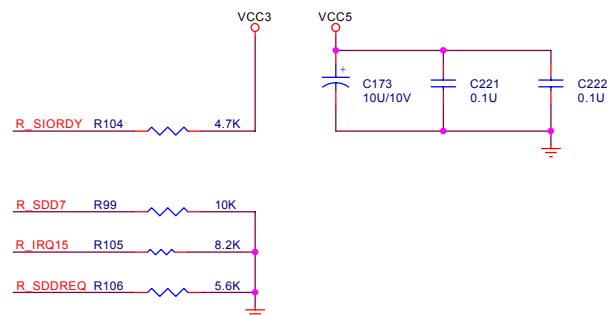
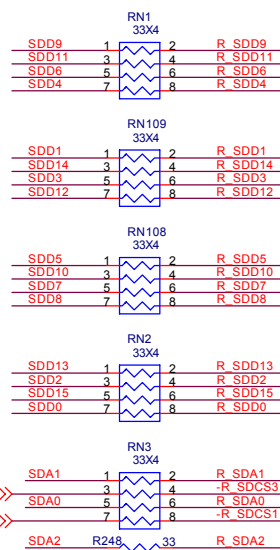
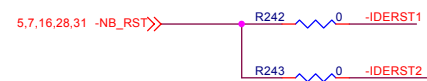
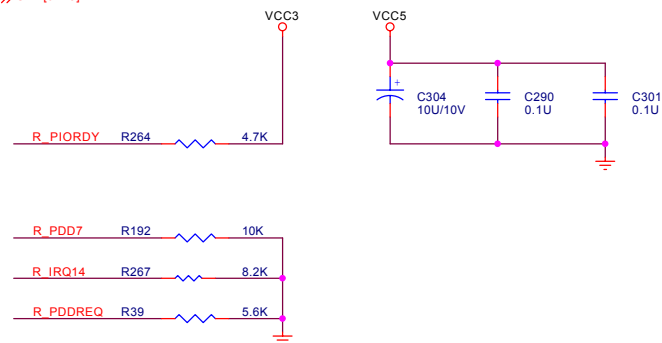
CP_GND returns are all 'star' connected, then a single connection to AGND:



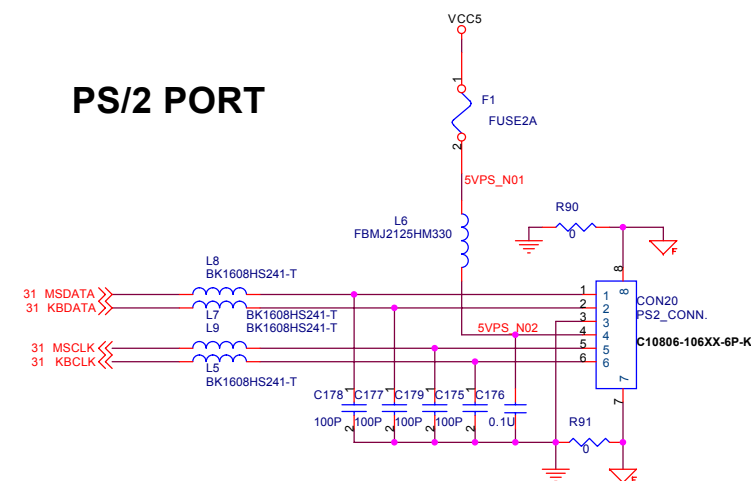
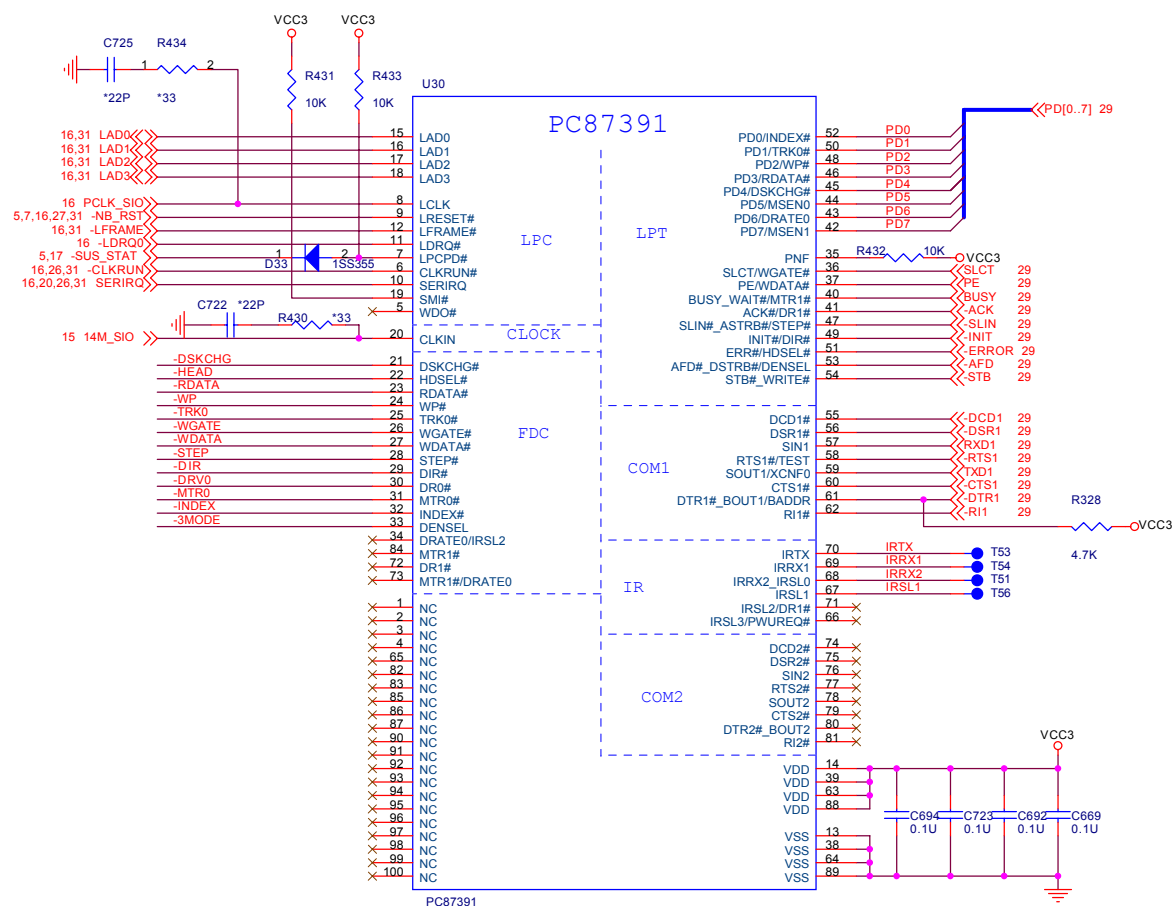




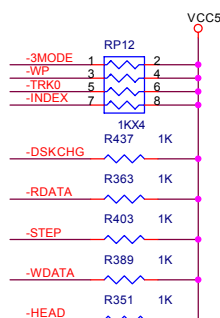
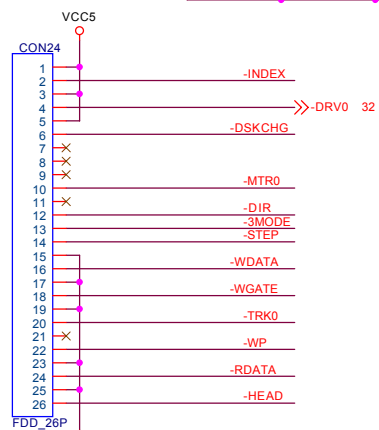
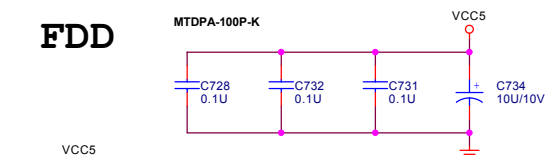
SDA[0..2] << SDA[0..2] 17
PDA[2..0] << PDA[2..0] 17
PDD[0..15] << PDD[0..15] 17
SDD[0..15] << SDD[0..15] 17



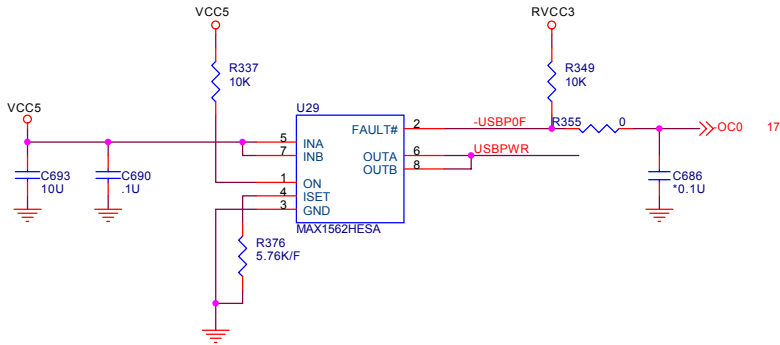
QUANTA
COMPUTER



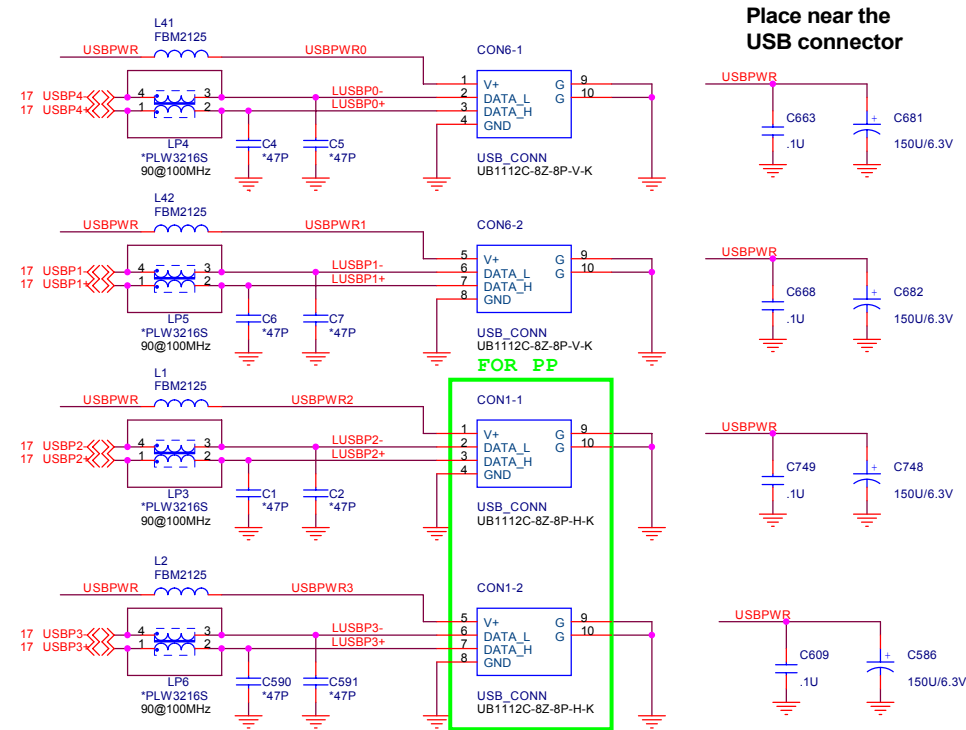
FDD



USB Power and Over current



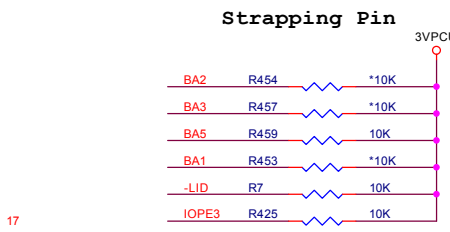
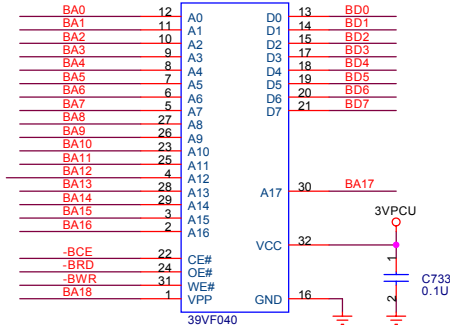
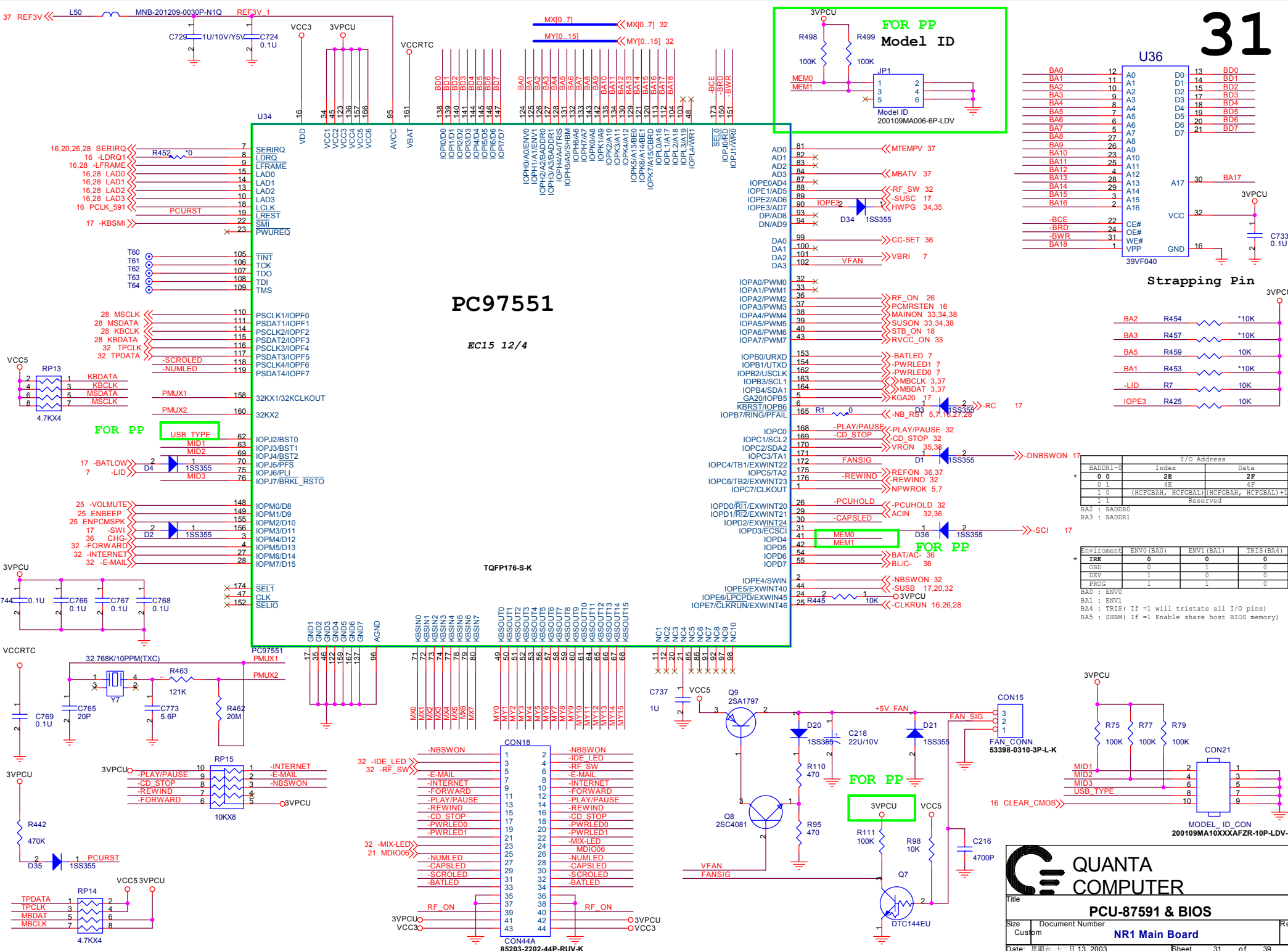
USB Connector



Place near the USB connector

FOR PP

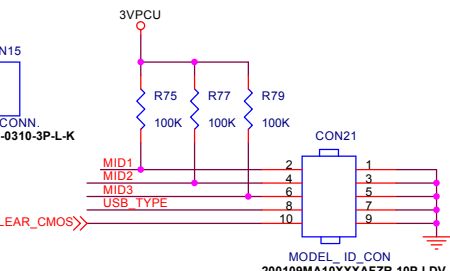
31



I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL) (HCFGBAH, HCFGBAL)+1	
1 1	Reserved	

Environment	ENV0 (BA0)	ENV1 (BA1)	TRIS (BA4)
IRB	0	0	0
DEV	1	0	0
PROG	1	1	0

BA0 : ENV0
BA1 : ENV1
BA4 : TRIS (If =1 will tristate all I/O pins)
BA5 : SHBM (If =1 Enable share host BIOS memory)



QUANTA COMPUTER

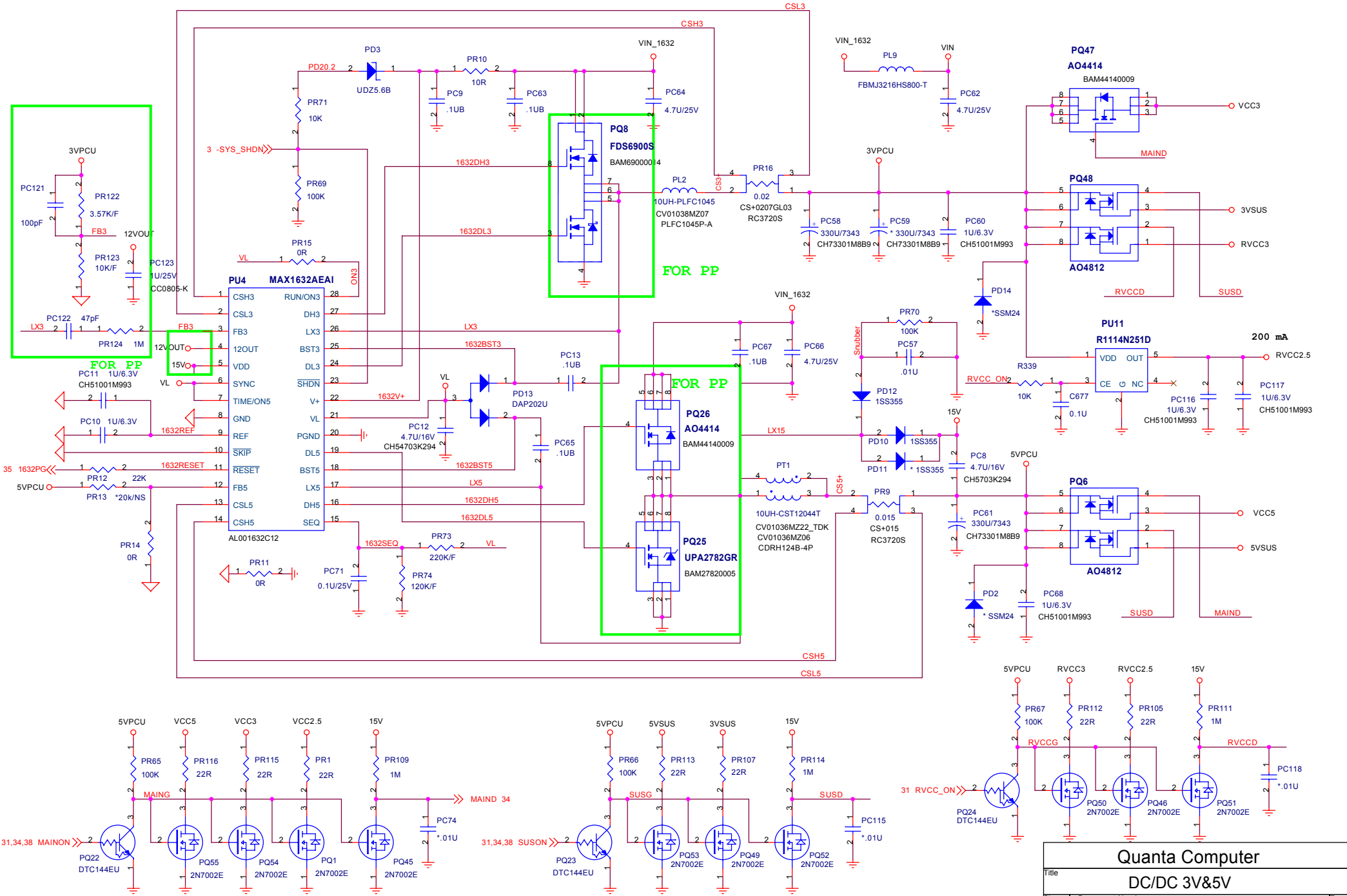
PCU-87591 & BIOS

NR1 Main Board

Date: 星期六, 十二月 13, 2003

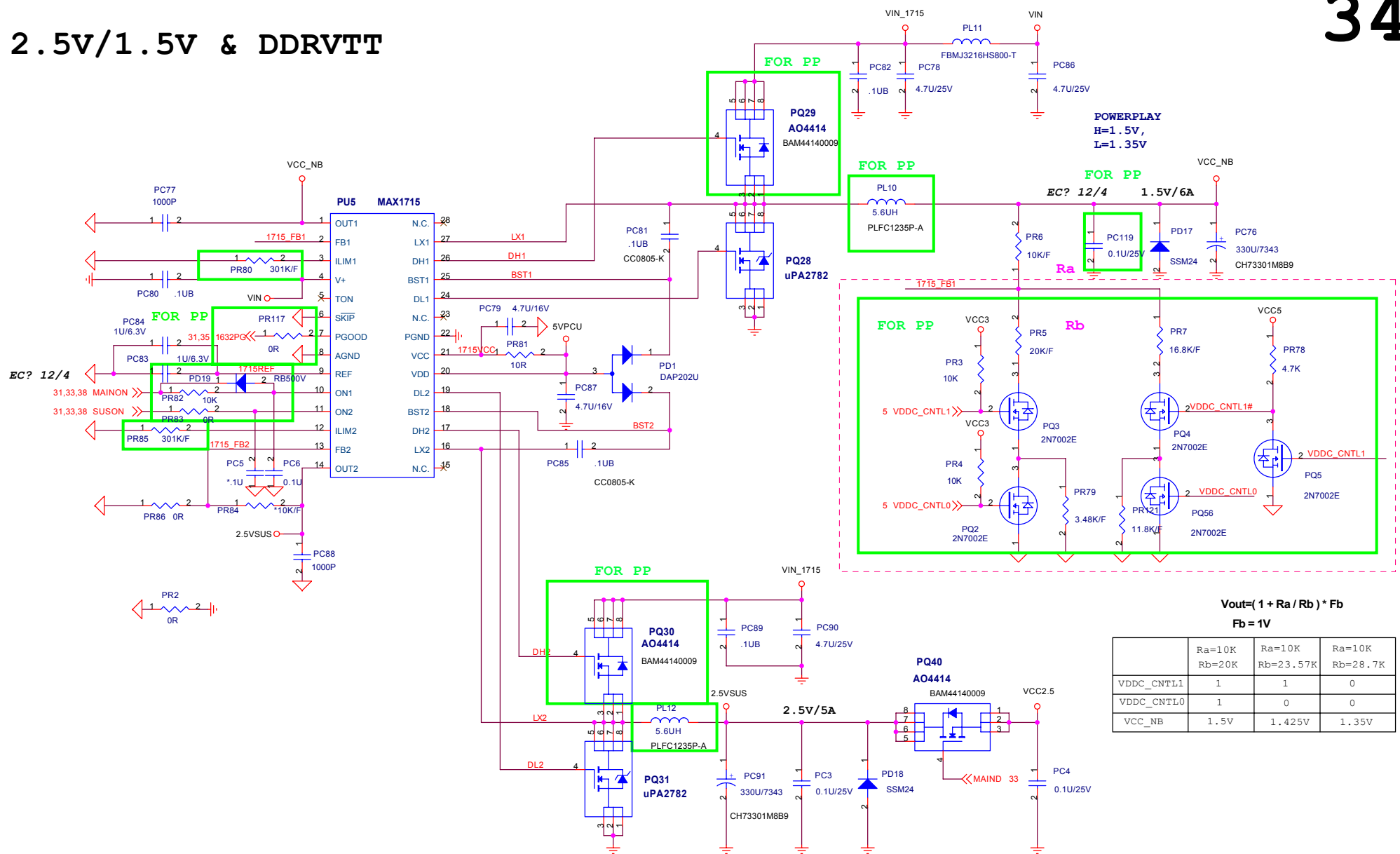
Sheet 31 of 39

DC/DC 3.3V&5V POWER

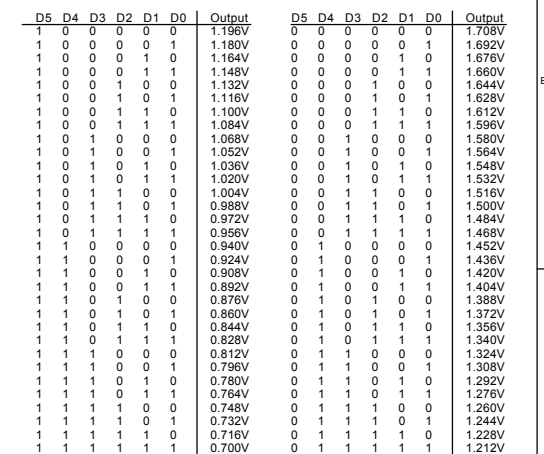


Quanta Computer		
DC/DC 3V&5V		
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2.5V/1.5V & DDRVTT



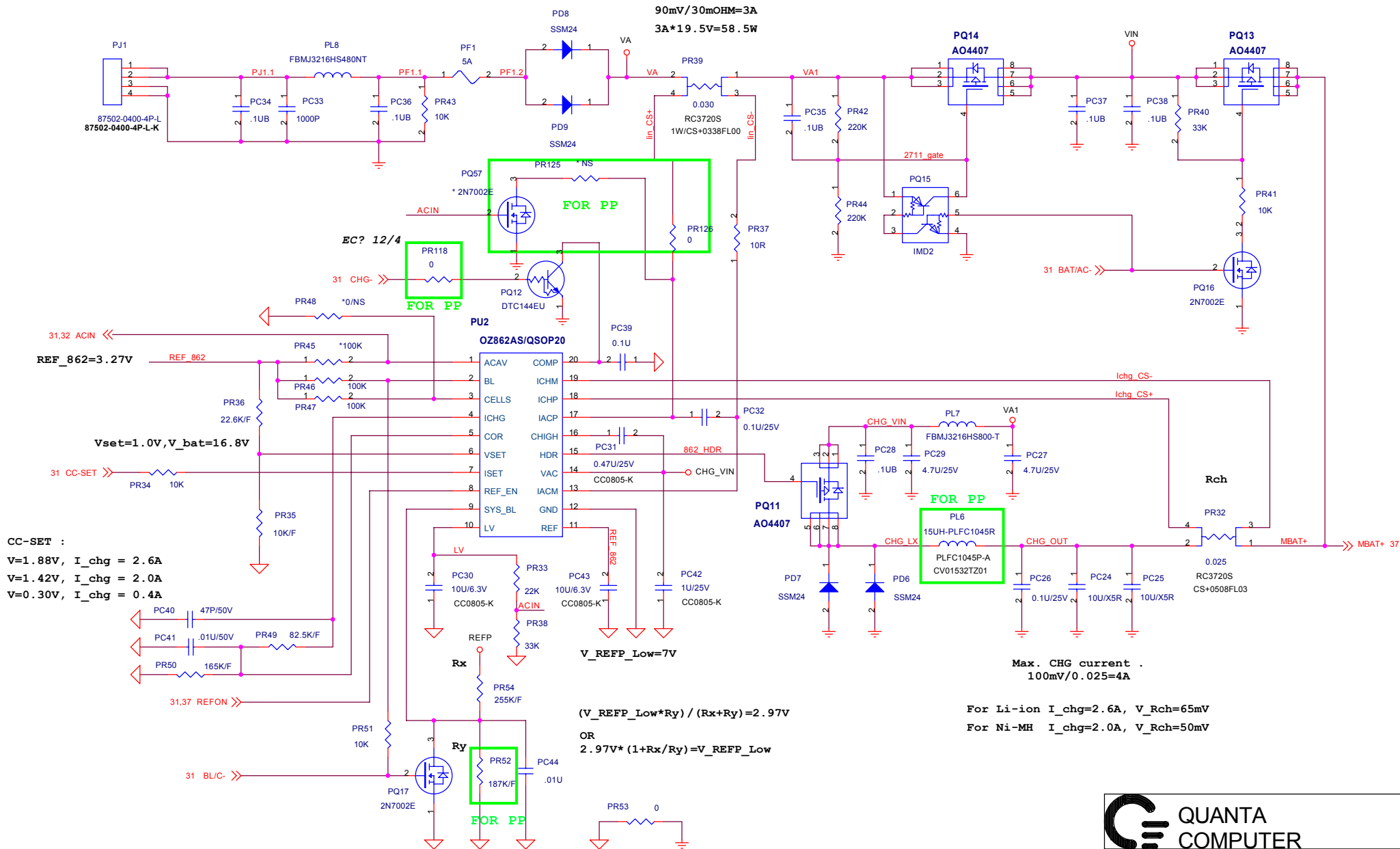
	Ra=10K Rb=20K	Ra=10K Rb=23.57K	Ra=10K Rb=28.7K
VDDC_CNTL1	1	1	0
VDDC_CNTL0	1	0	0
VCC_NB	1.5V	1.425V	1.35V



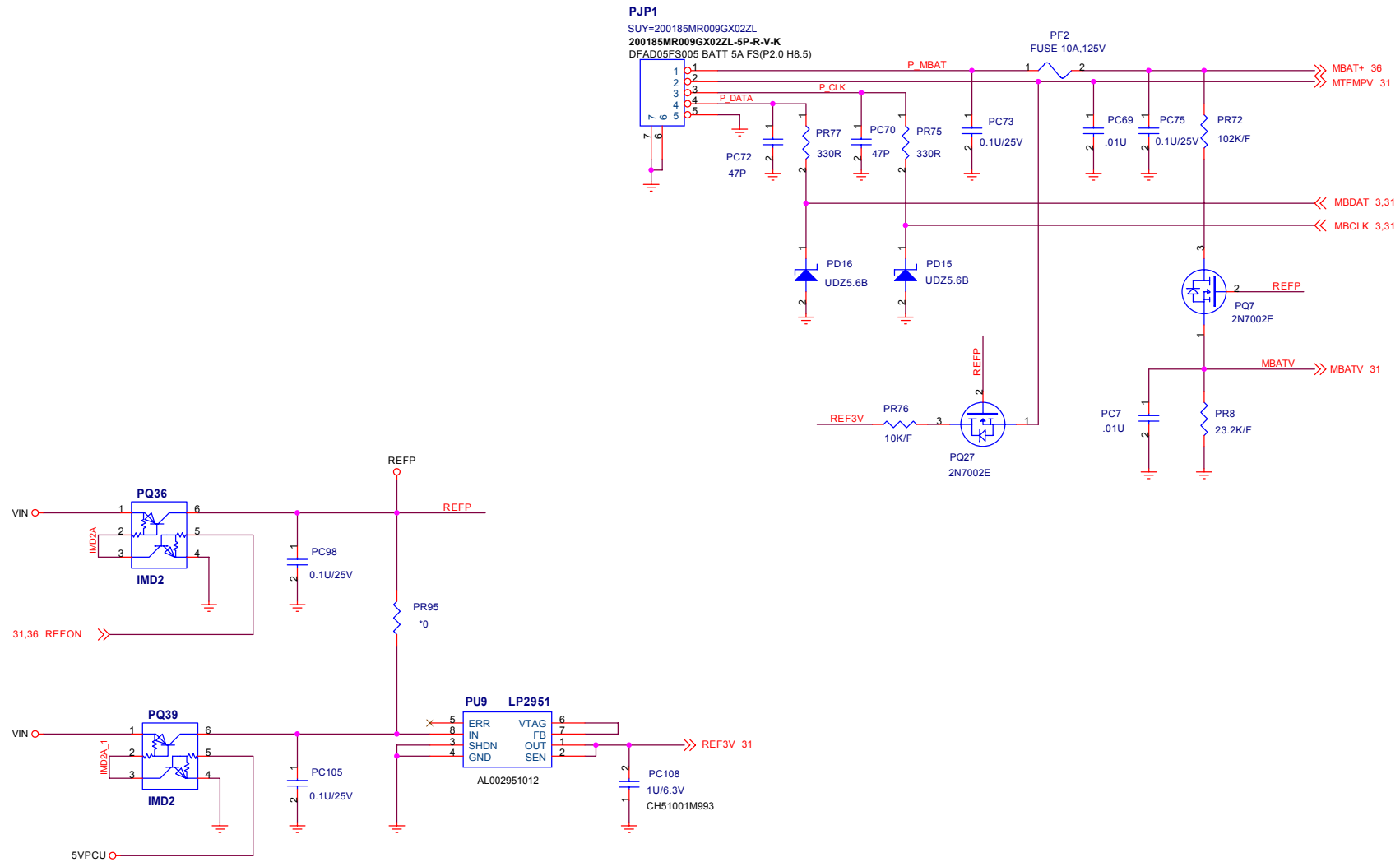
Battery Charger

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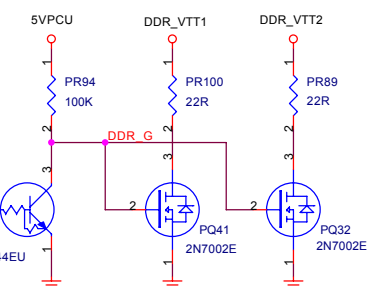
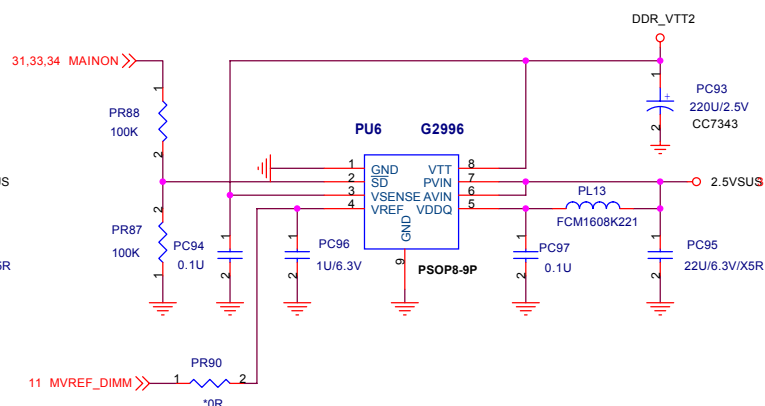
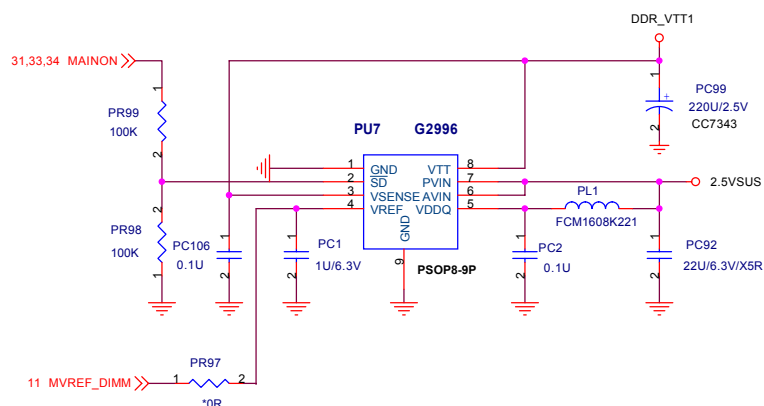
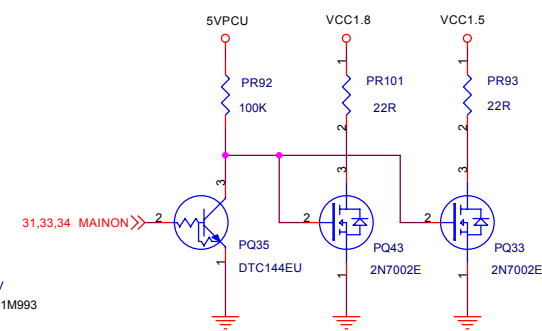
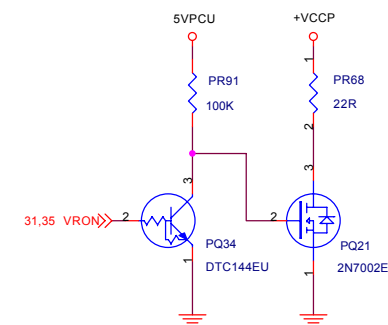
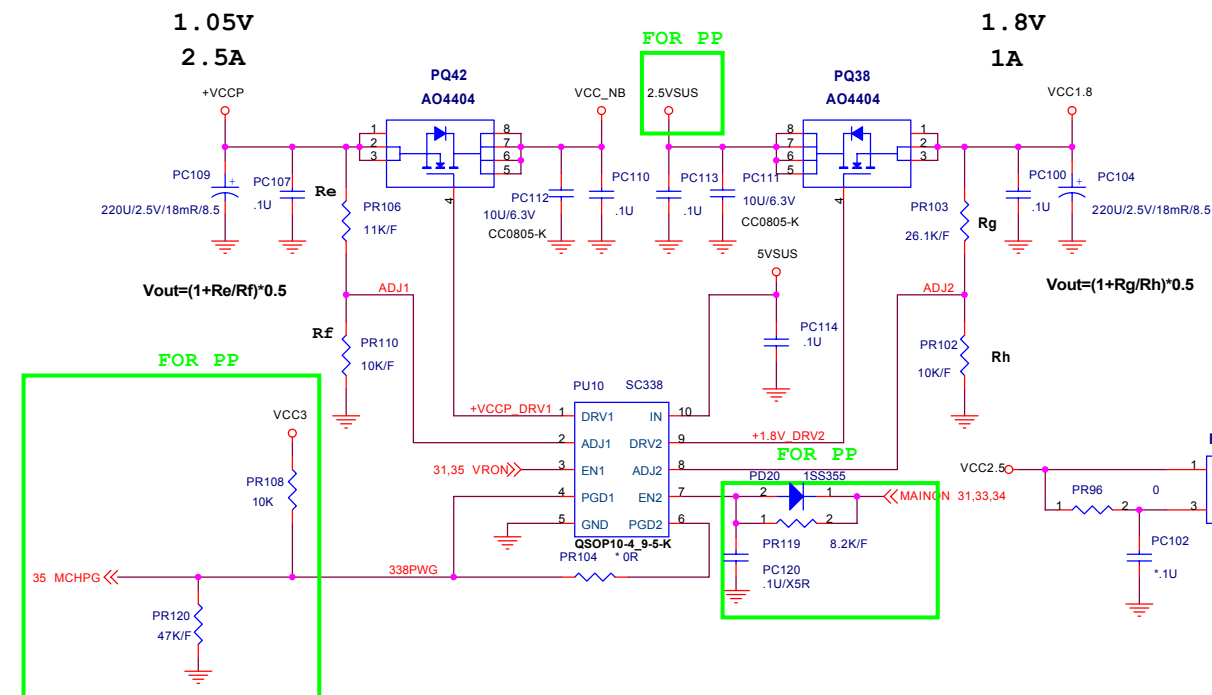
DC19V/3.16A



Battery Connector



VCCP 1.05V/1.8V/1.5V/DDR_VTT



- 54321
- D
- C
- B
- A
- 1.12/10 PAGE31 ADD ON BOARD MEMORY SIZE JUMPER
- 2.12/10 PAGE5 LCD EDID PULL HIGH FORM VCC1.5 CHANGE TO VCC3
- 3.12/10 PAGE25 L18 FORM BEAD CHANGE TO 0 OHM
- 54321
- D
- C
- B
- A

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