

IL1 Block Diagram

B --> C
2008-01-08

VTERM(+0.9V)
VTT(+1.05V)
+1.5VSUS
+1.5V
+1.8VMEM
+1.8V
+2.5V
3VPCU
+3.3V
+3.3VSUS
LCD_3.3V
LCD_5V
+5V

C7-M (ULV)

VCORE:+1.196 ~ +0.748
VCCP:+1.05V
VCCA:+1.8V or +1.5V

VID[0:6]

CPU VCORE

+/- CPU CLK

+/- HCLK

Clock Gengerator
ICS9UM700

V4
BUS

CRT

7" LCD

VX800(U)

VTT:VCCP (+1.05V)
VCC15:+1.5V or +1.2V
VCC33:+3.3V
VCCMEM:+1.8V
VSUS15:+1.5VSUS
VSUS33:+3.3VSUS

CHA

DDRII

256M/512M/1GB

1.8" HDD(option)

IDE

NAND FLASH
Controler SM223

1/2/4 GB
NAND FLASH

USB

Camera Conn.

Camera
Module

USB

WLAN Conn.

WLAN
Module

USB

USB PORT X2

PCI BUS

10/100 Ethernet
RTL8100CL

RJ-45

Modem DDA
CSP1040

RJ-11

HDA CODEC
ALC267

HD Audio

Int.
SPK

Audio
Jack

Int.
Mic

SD/MMC/MS
3 IN 1 Card SLOT

Card
Reader

LPC BUS

EC
KB3310

SPI
Flash

Int. KB

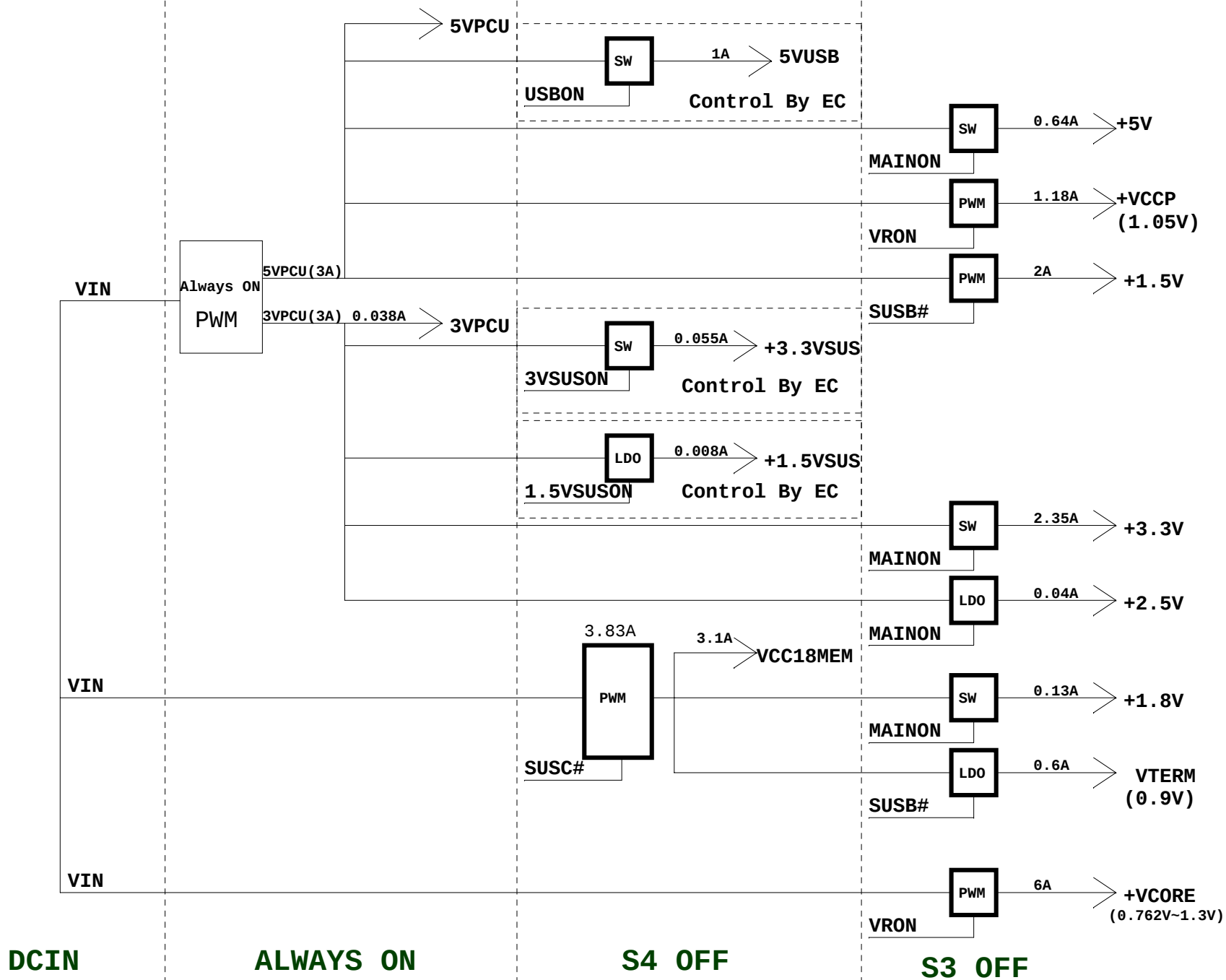
T/P

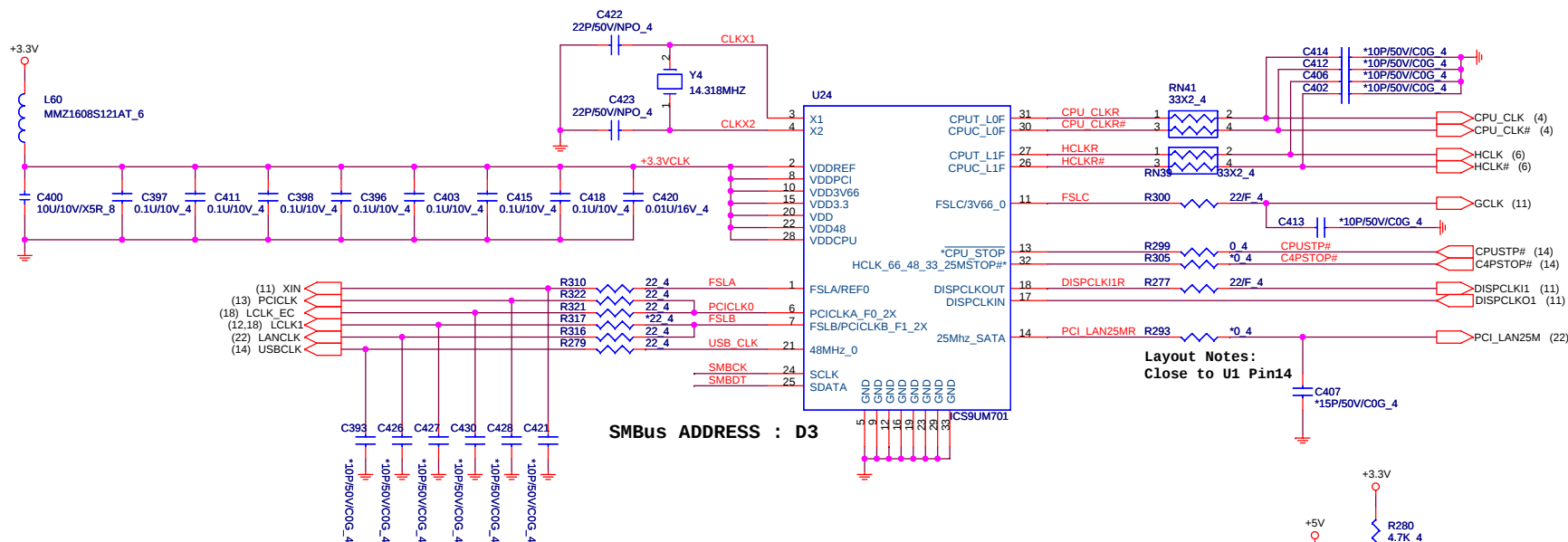
Battery

Charger



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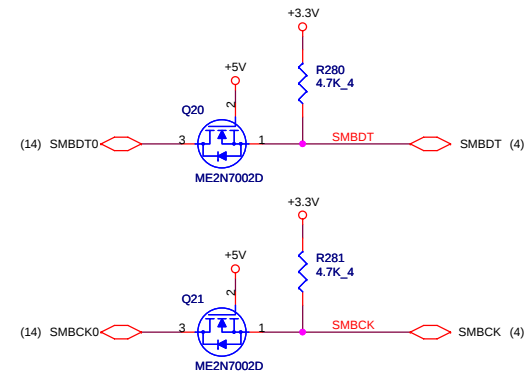
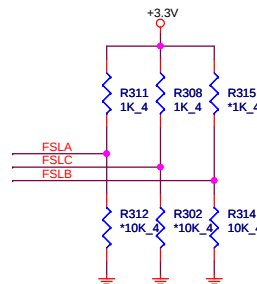




SMBus ADDRESS : D3

ICS9UM701 ON : 0 OFF : 1

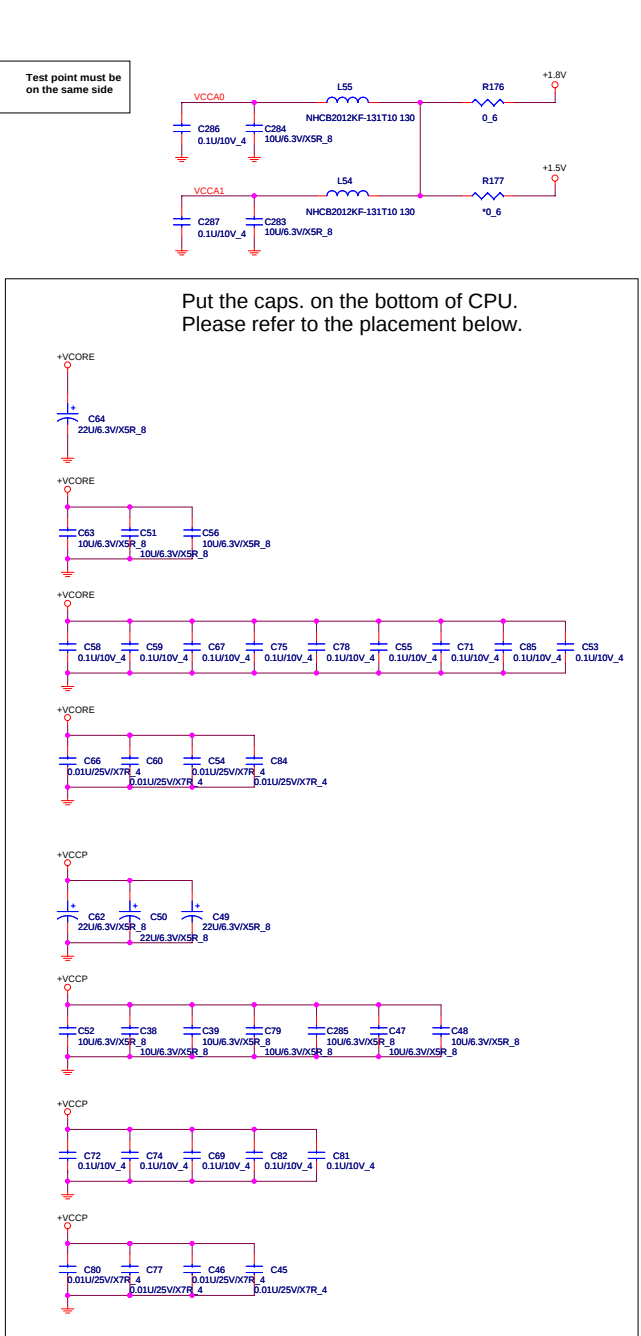
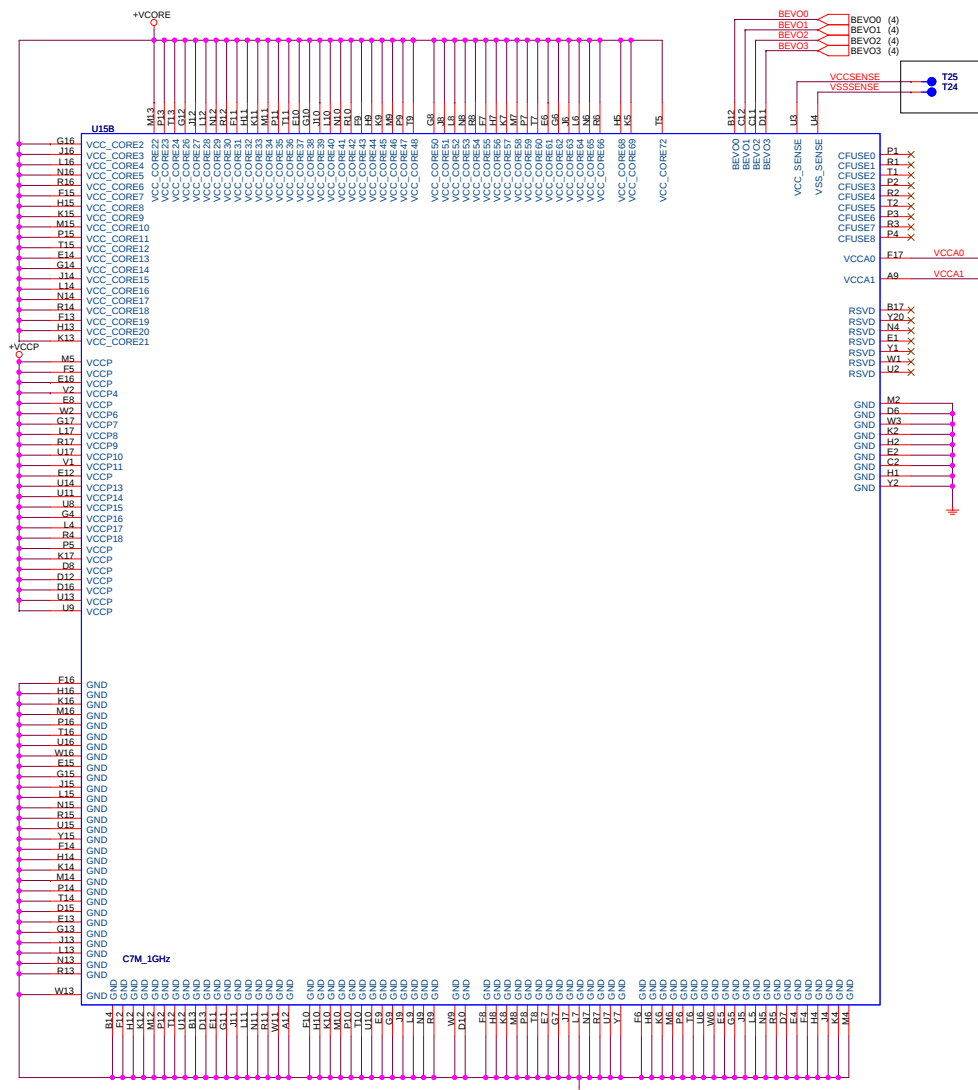
3	2	1		
FSLA	FSLC	FSLB	CPU	PCI
0	0	0	266.66	33
1	0	0	133.33	33
0	0	1	200	33
1	0	1	166.66	33
0	1	0	333.33	33
1	1	0	100	33
0	1	1	400	33
1	1	1	200	33



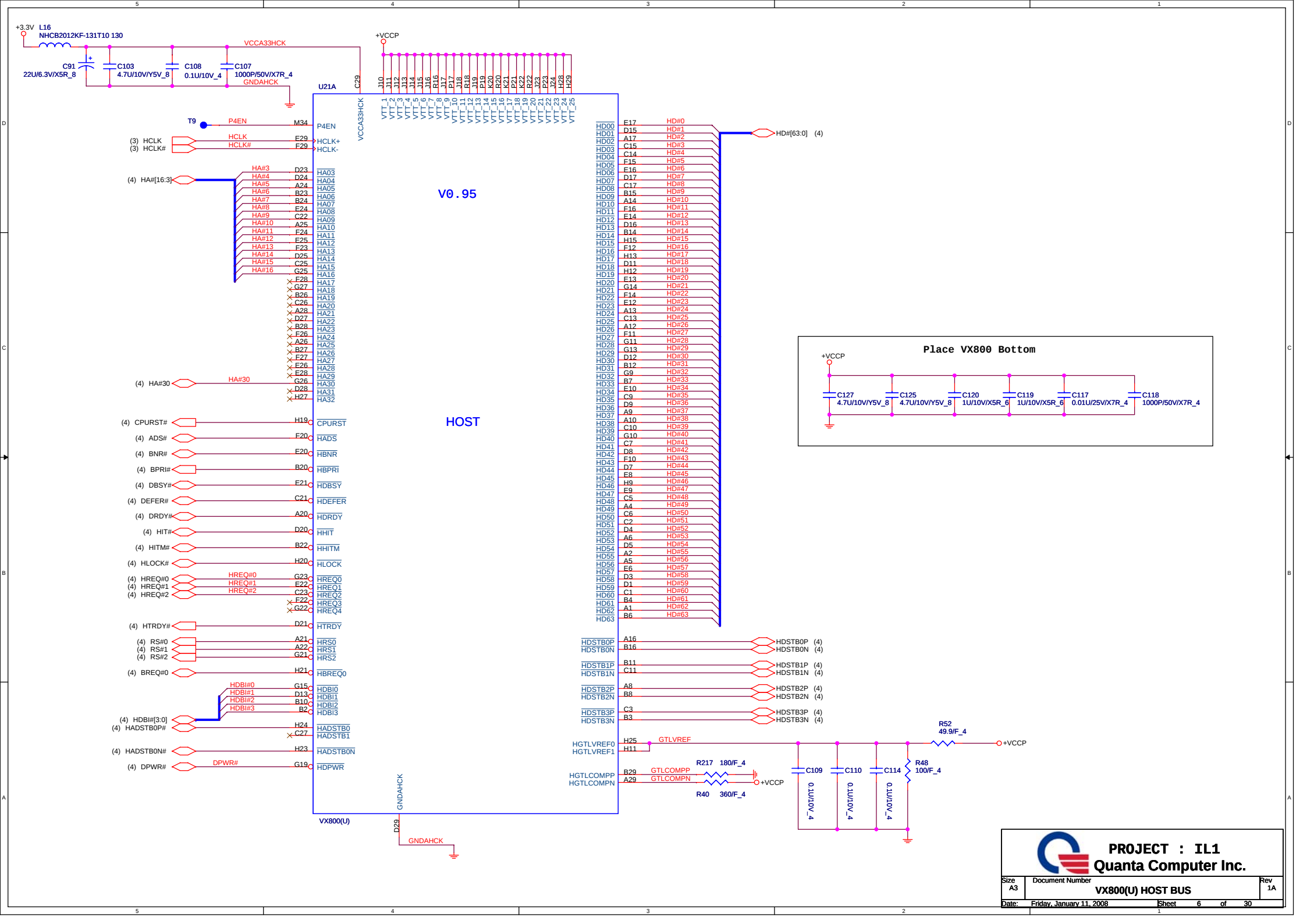
Layout Notes:
Close to U1 Pin14

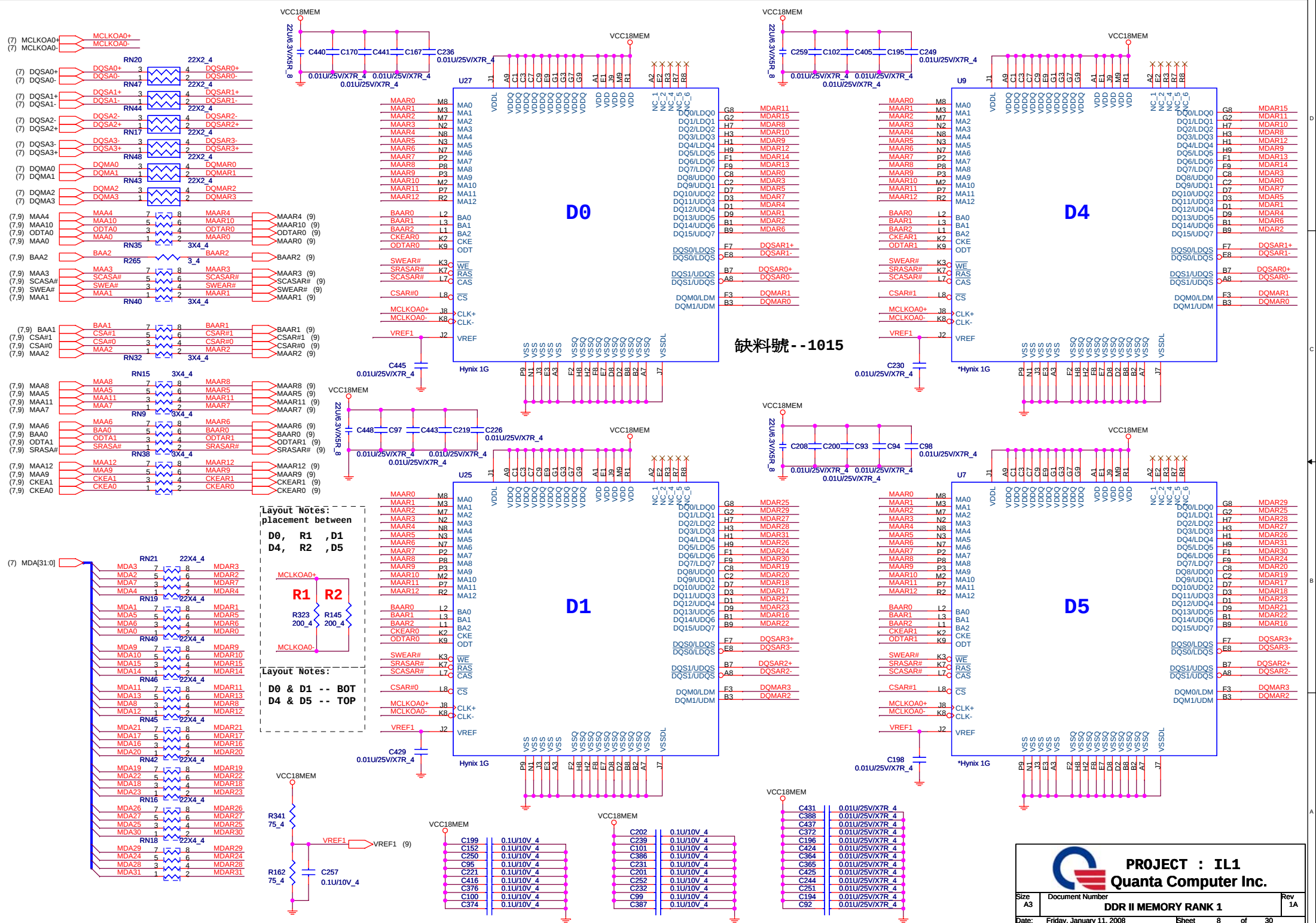


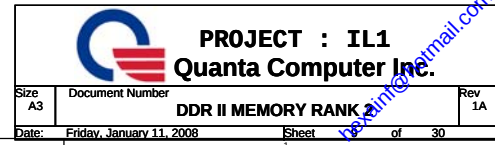
PROJECT : IL1
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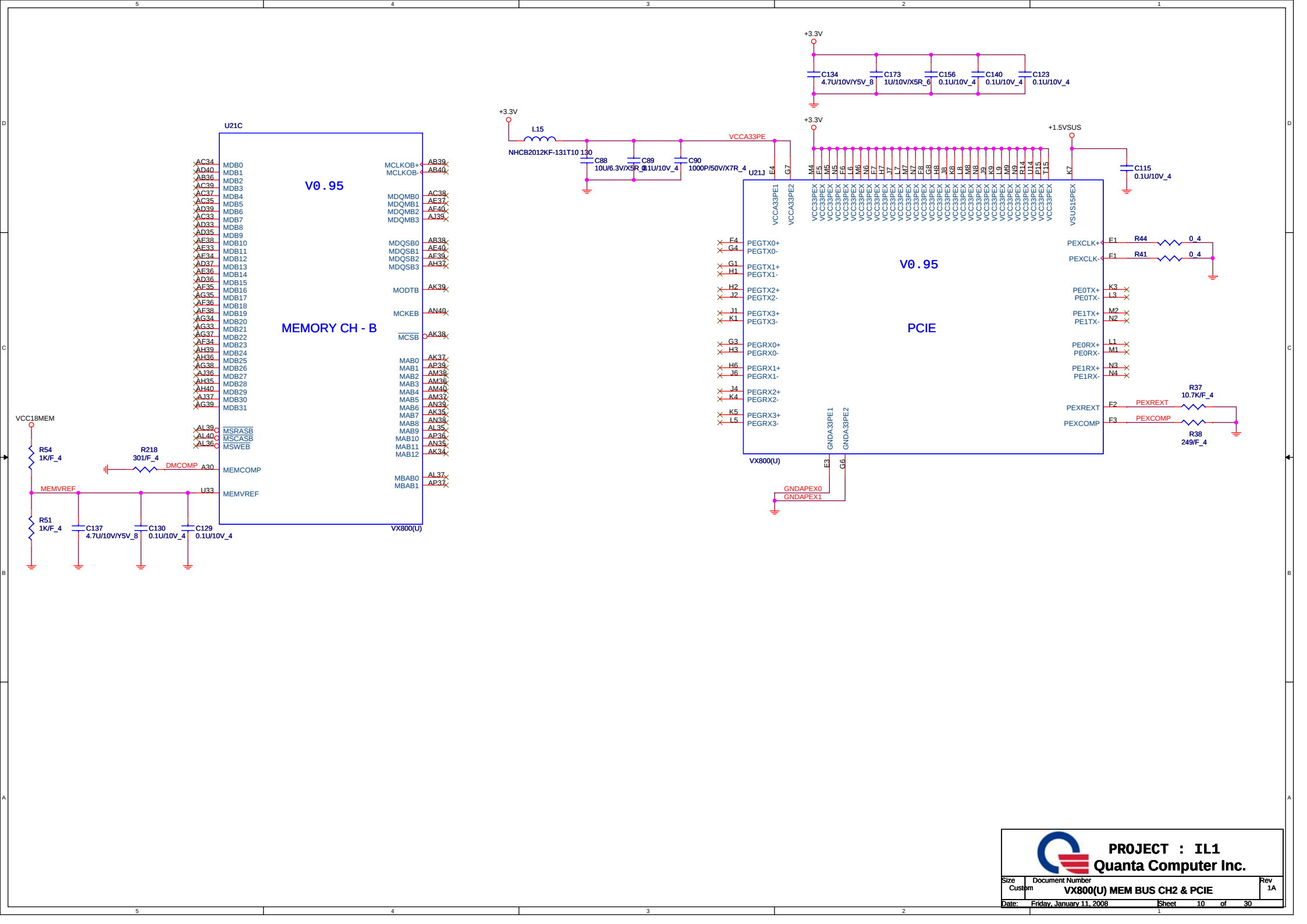


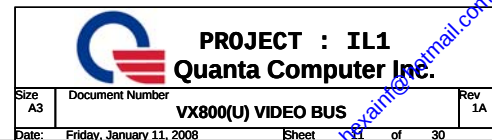
Hotmail.com

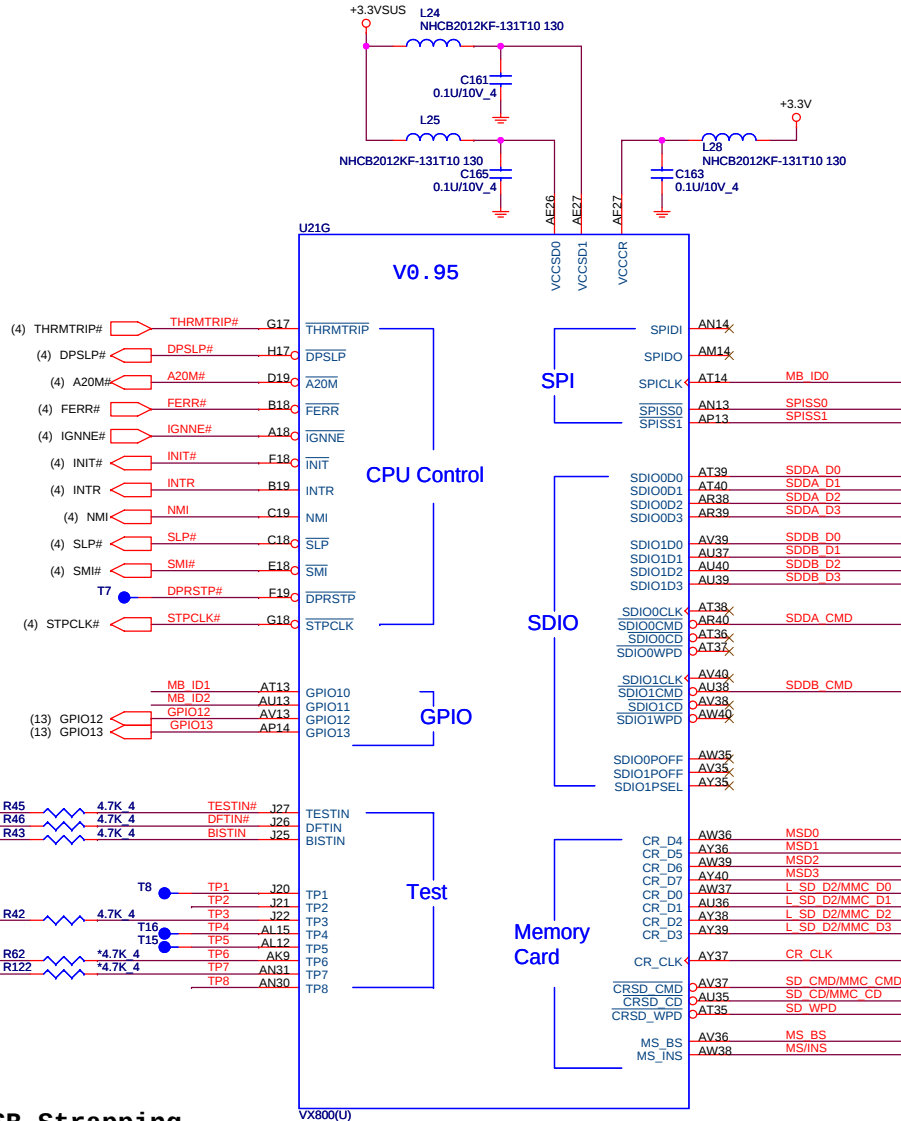




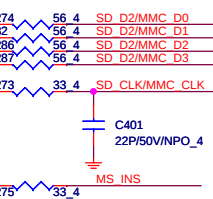








EMI Request



TP2

0 (default)	Normal mode
1	Debug mode

TP8(SWMODE)

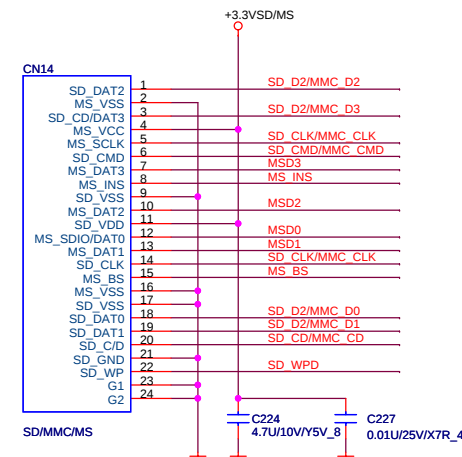
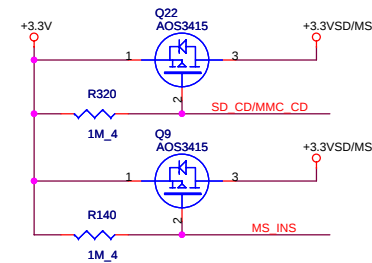
0 (default)	Voltage mode
1	Current mode

SB Strapping

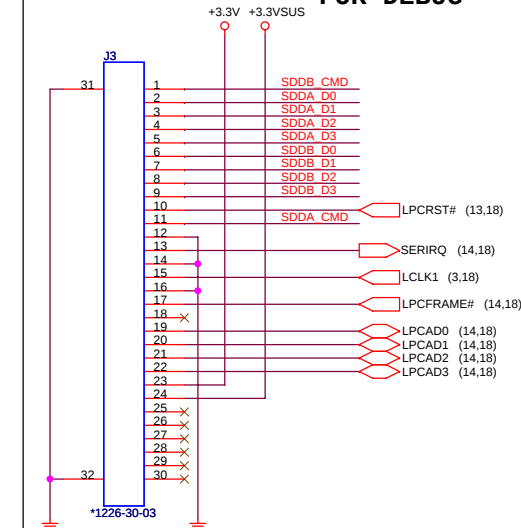
Pin	0:on 1:off	Function
SPISS1/ SPISS0 (2/1)	00	IDE
	01	NFC
	10	CE ATA

M/B ID Strapping

MBID	MB_ID2	MB_ID1	MB_ID0
A	0	0	0
B	0	0	1
C	0	1	0

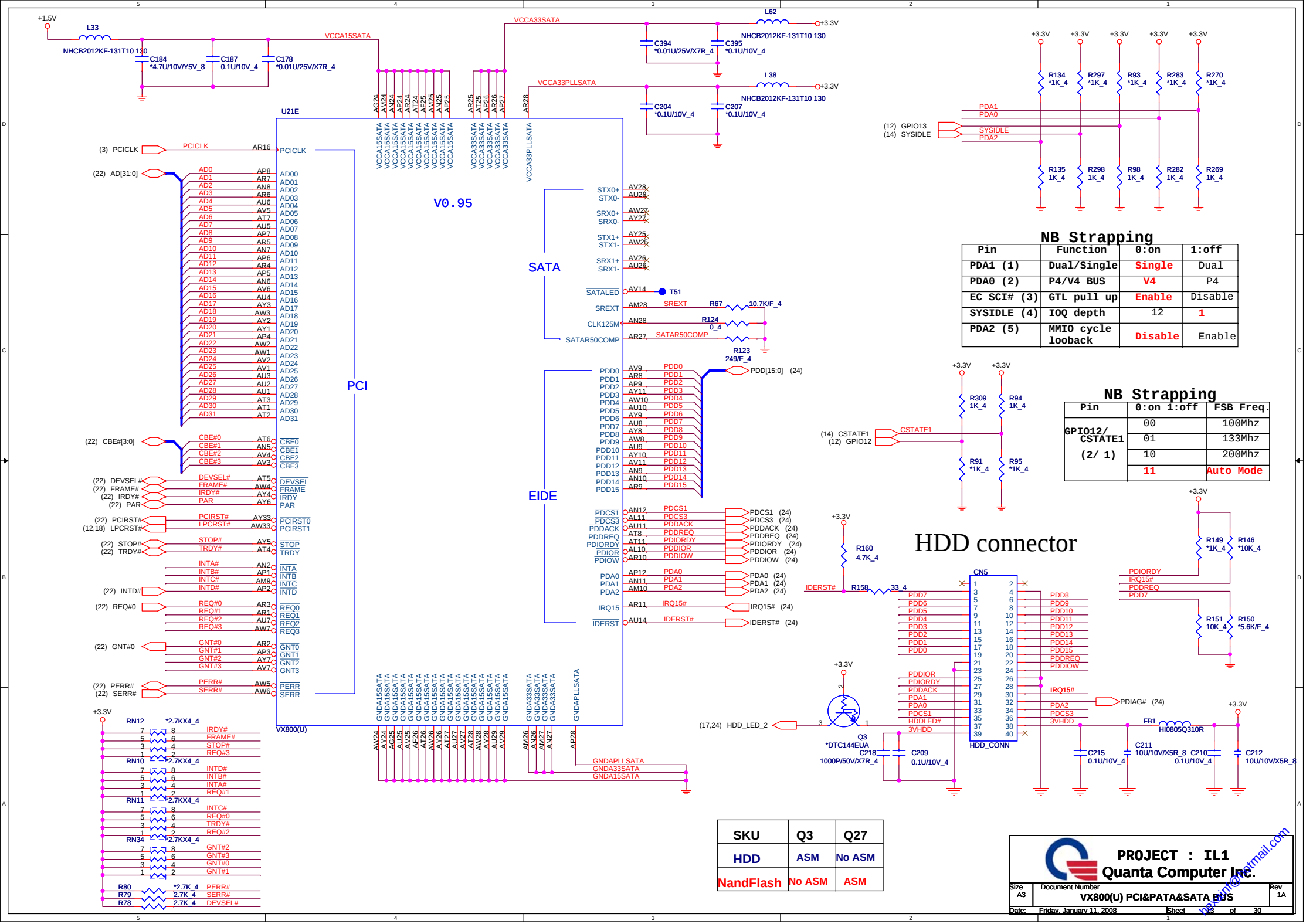


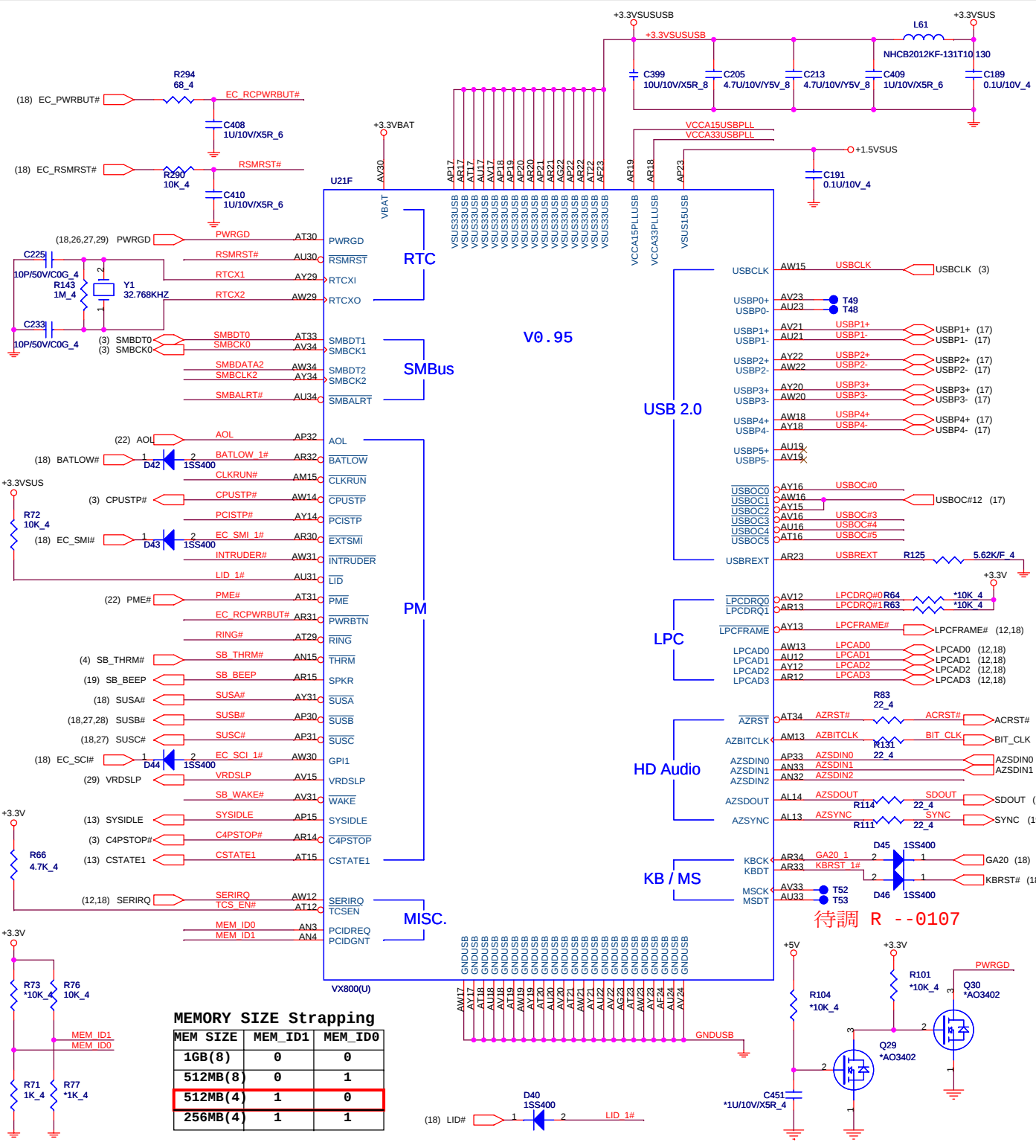
FOR DEBUG



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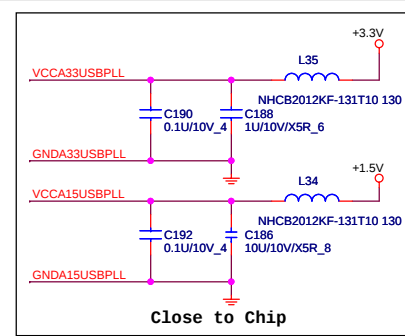
Size	Document Number	Rev
A3	VX800(U) SPI/CARD READER	1A
Date:	Friday, January 11, 2008	Sheet 12 of 30





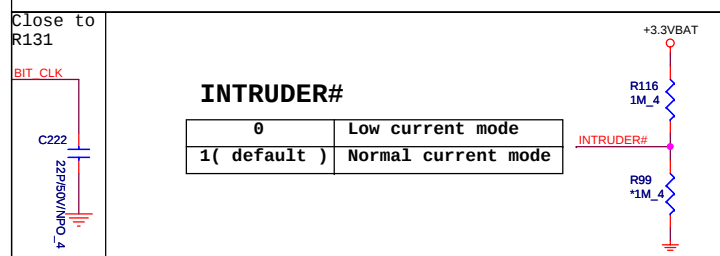
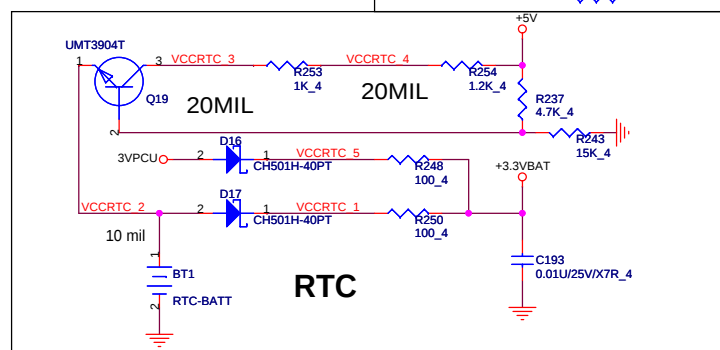
MEMORY SIZE Strapping

MEM SIZE	MEM_ID1	MEM_ID0
1GB(8)	0	0
512MB(8)	0	1
512MB(4)	1	0
256MB(4)	1	1



Reserve for internal pull-up resistor verify

Pin	Function	Value
SB_WAKE#	R127	*10K 4
AOL	R129	*10K 4
BATLOW_1#	R120	*10K 4
EC_SCI_1#	R339	*10K 4
EC_SMI_1#	R296	*10K 4
SUSA#	R126	*10K 4
PME#	R130	*4.7K 4
RING#	R139	*10K 4
SMBALRT#	R102	*10K 4
SMBCK0	R136	*4.7K 4
SMBD0	R137	*4.7K 4
SMBCLK2	R81	*4.7K 4
SMBDATA2	R106	*4.7K 4
GA20_1	R84	*4.7K 4
KBRST_1#	R138	*4.7K 4
USBOC#0	R268	*10K 4
USBOC#3	R87	*10K 4
USBOC#4	R88	*10K 4
USBOC#5	R97	*10K 4
SB_THRM#	R86	*4.7K 4
SERIRQ	R306	*4.7K 4
PCISTP#	R284	*4.7K 4
CPUSTP#	R267	*4.7K 4
CLKRUN#	R85	*4.7K 4
VRDCLP	R117	*4.7K 4
C4PSTOP#	R304	*4.7K 4



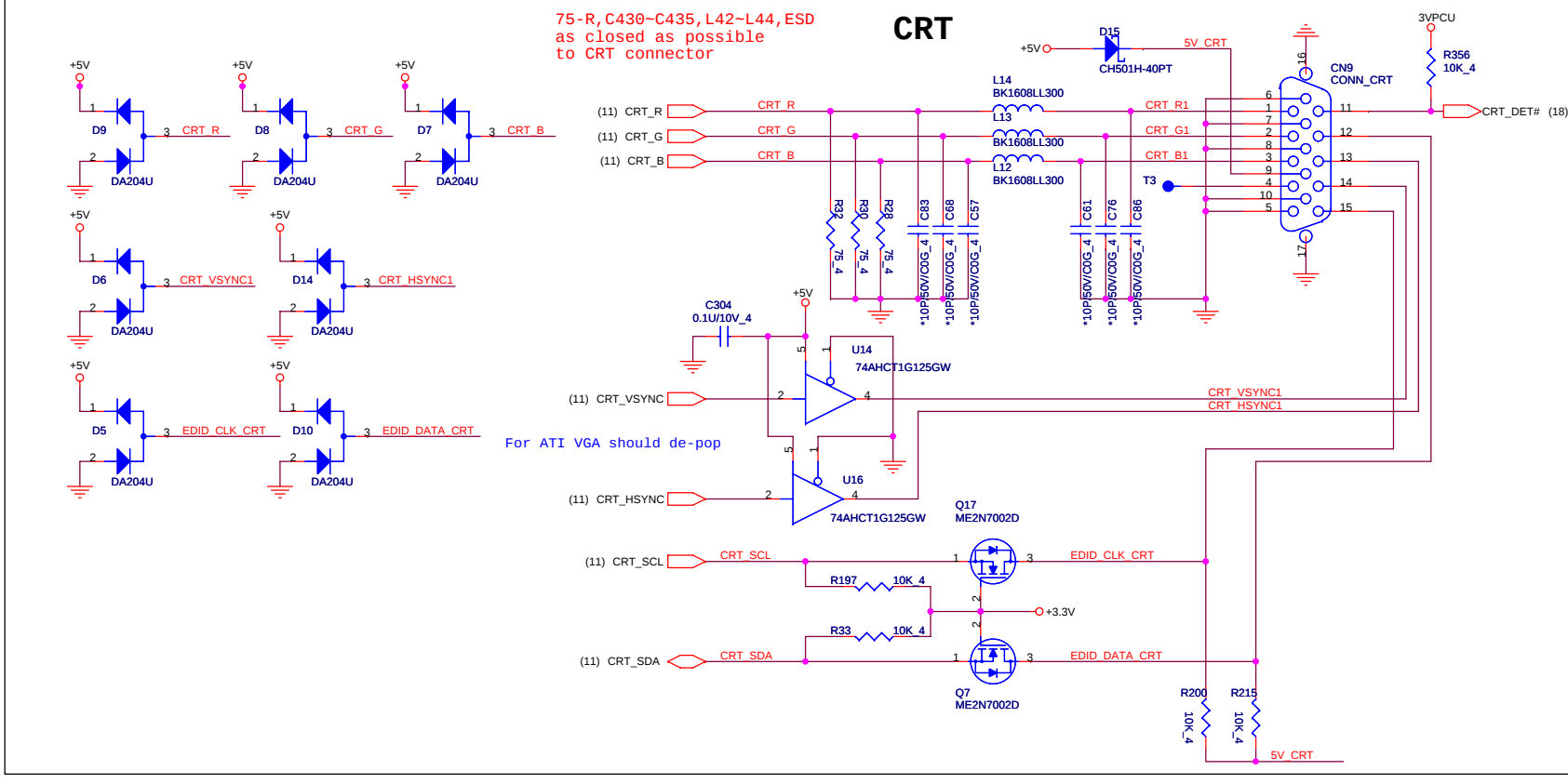
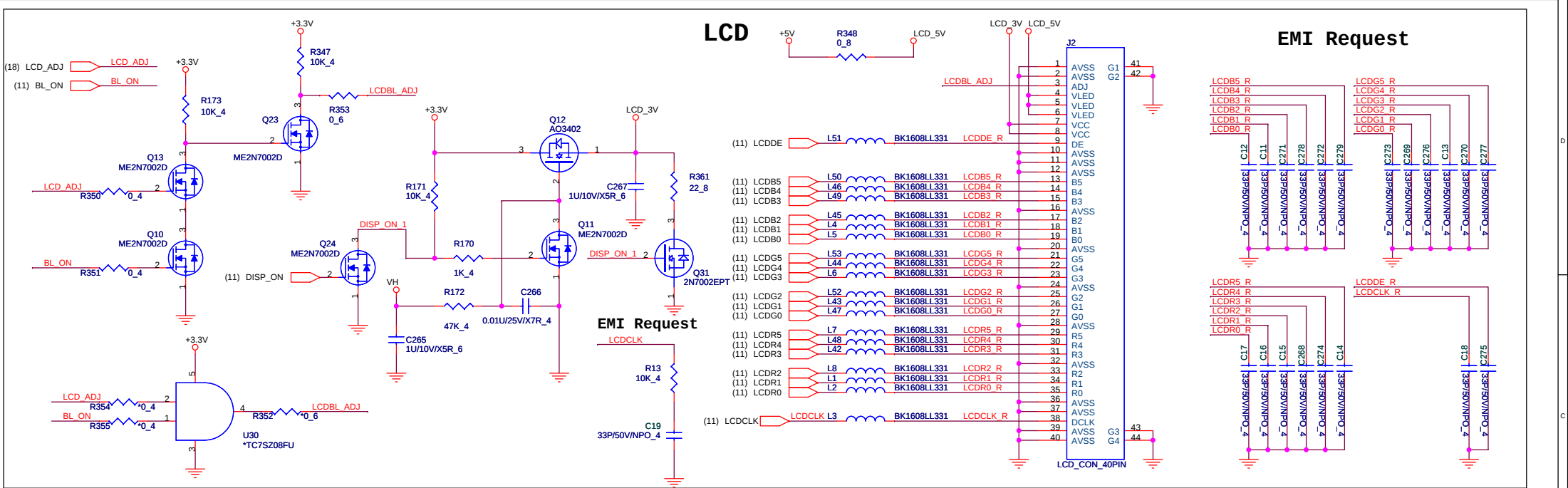
SB Strapping

Pin	Function	0:on	1:off
SB_BEEP(1)	CPU freq. strapping	Enable	Disable
AZSYNC(2)	LPC FWH command	Enable	Disable
AZSDOUT(3)	System Auto Reboot	Enable	Disable
AZBITCLK(4)	LPC/SPI ROM	LPC	SPI

PROJECT : IL1
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Size	Document Number	Rev
A3	VX800(U) USB&LPC&DAC&PM	1A

Date: Friday, January 11, 2008 Sheet 14 of 30



EMI Request

LCDR5 R
LCDR4 R
LCDR3 R
LCDR2 R
LCDR1 R
LCDR0 R

LCDG5 R
LCDG4 R
LCDG3 R
LCDG2 R
LCDG1 R
LCDG0 R

LCDDE R
LCDCLK R

LCDR5 R
LCDR4 R
LCDR3 R
LCDR2 R
LCDR1 R
LCDR0 R

LCDG5 R
LCDG4 R
LCDG3 R
LCDG2 R
LCDG1 R
LCDG0 R

LCDDE R
LCDCLK R

PROJECT : IL1
Quanta Computer Inc.

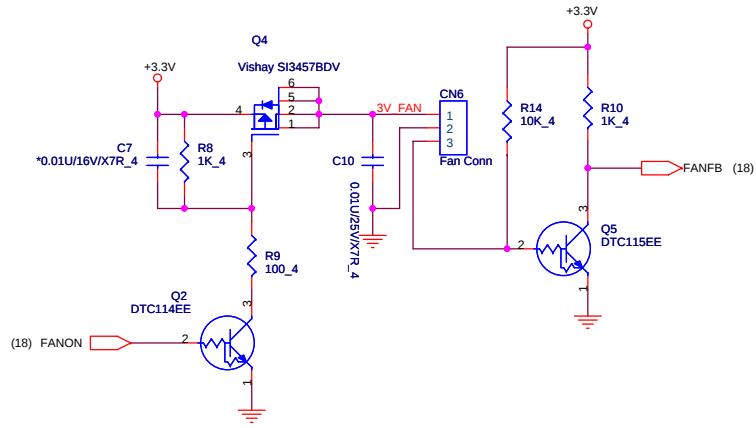
Size A3
Document Number
Date: Friday, January 11, 2008

CRT & LCD

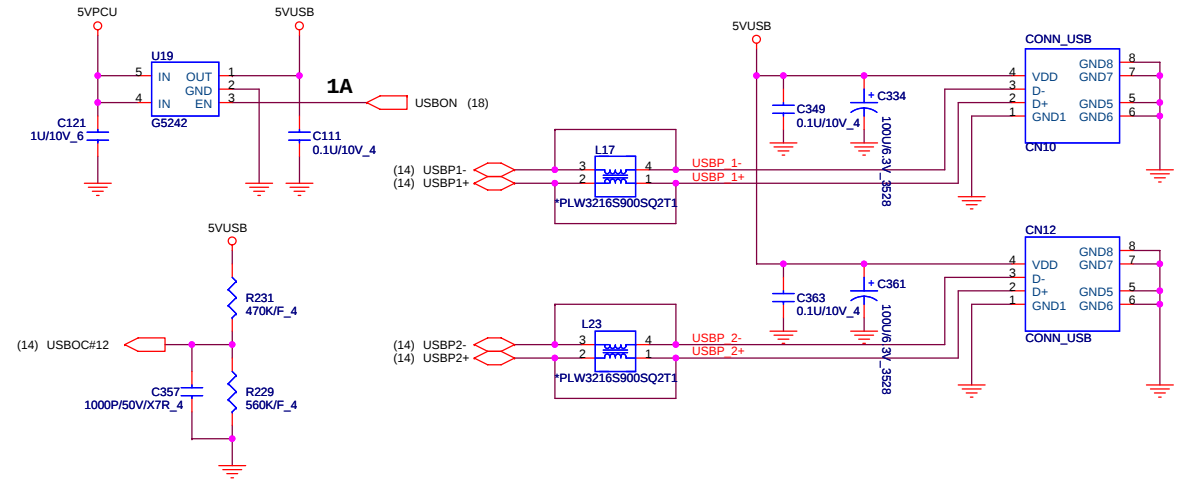
Sheet 16 of 30

Rev 1A

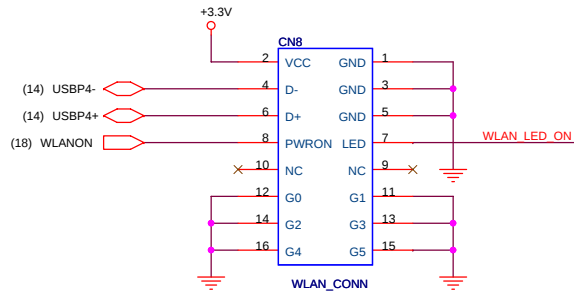
FAN



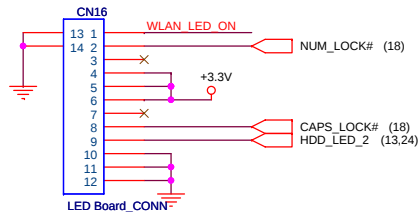
USB X 2 PORTS



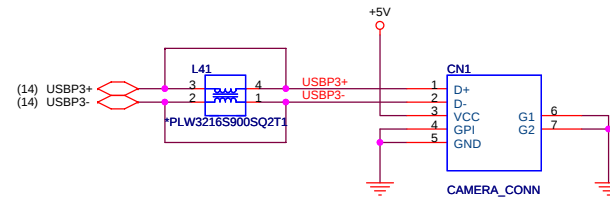
WLAN



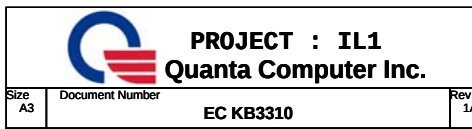
LED Board

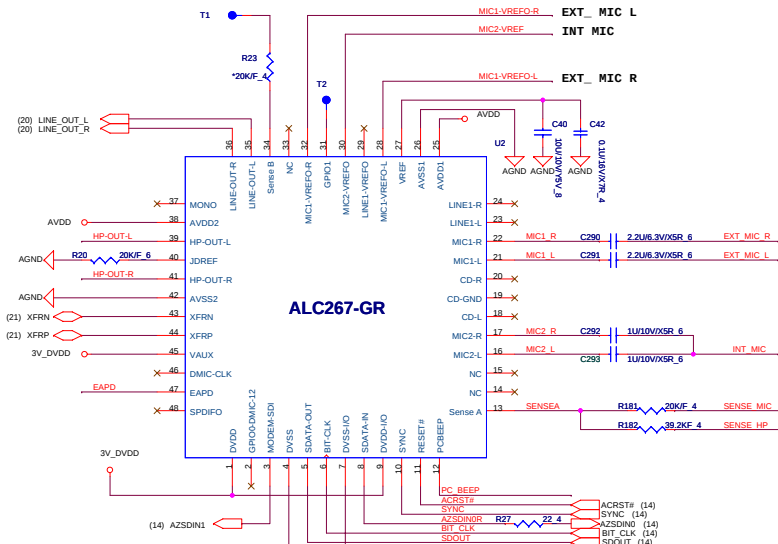
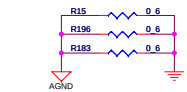


CAMERA

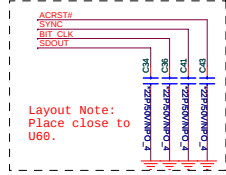
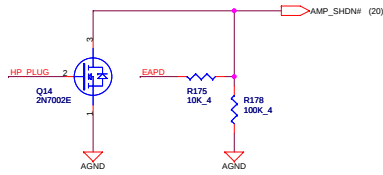


PROJECT : IL1
Quanta Computer Inc.

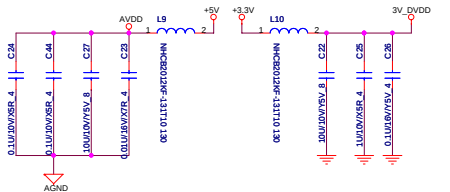
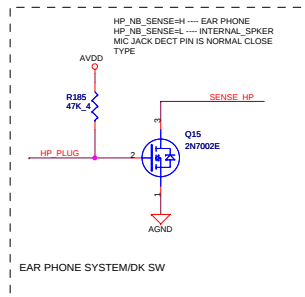
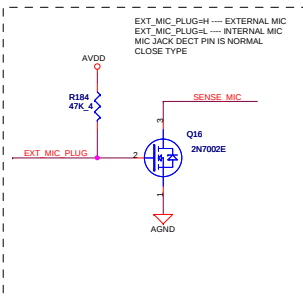
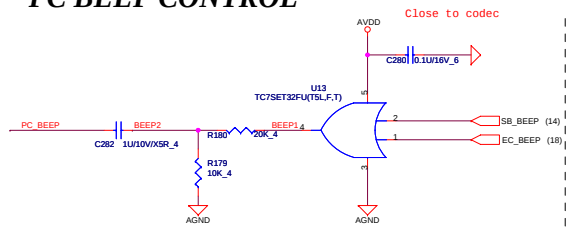




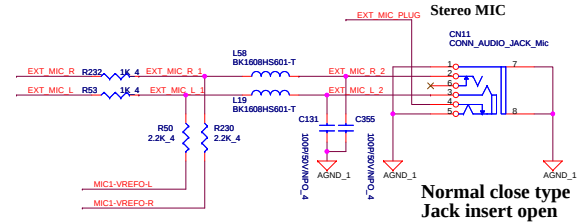
HP_PLUG	EAPD	AMP_SHDN#
0	0	0
0	1	1
1	0	0
1	1	0



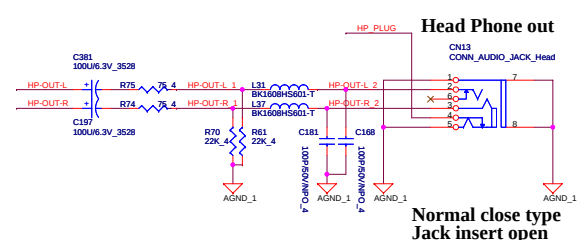
PC BEEP CONTROL



External Microphone Jack

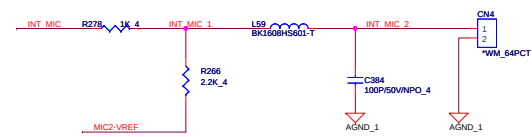


Normal close type Jack insert open



Normal close type Jack insert open

INT_MIC



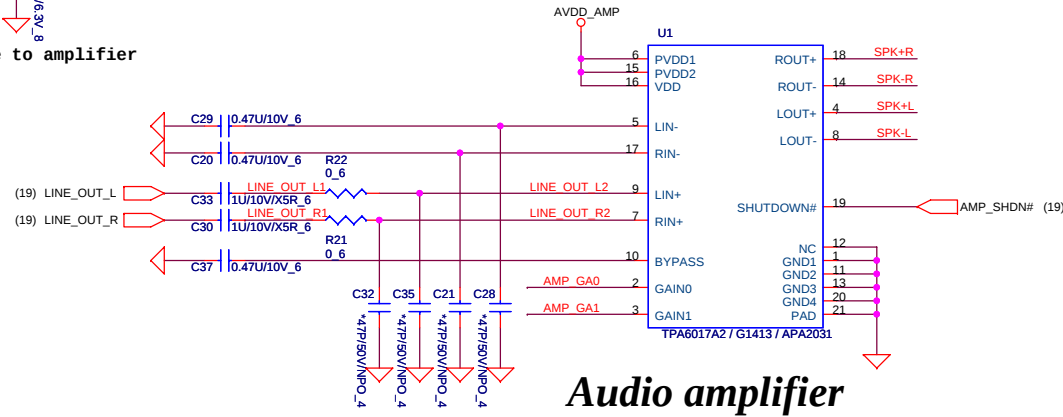
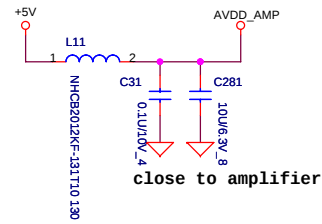
Full Scale Output Voltage

LINE - OUT ---- 1.4Vrms/Typical

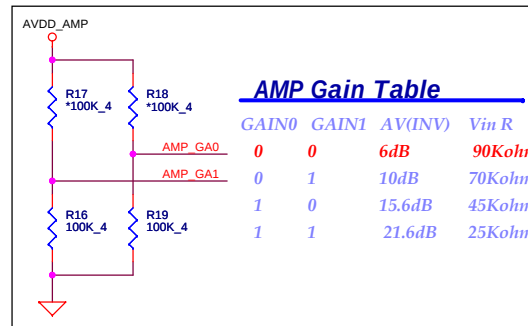
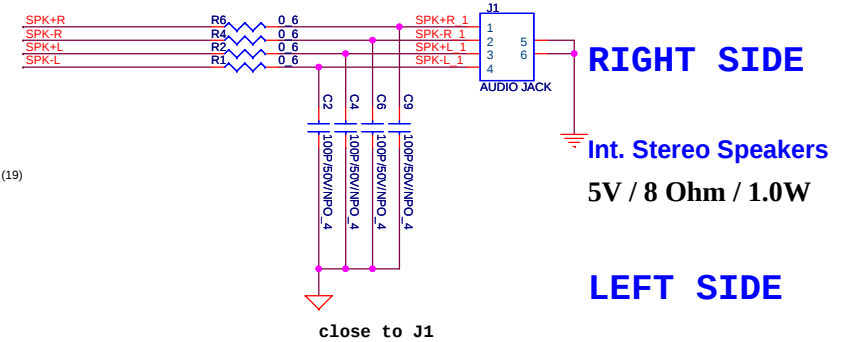
HP - OUT ---- 1.4Vrms/Typical

Step Size = 1.0 dB / 0 ~ - 64 dB / Total 64 Step

Gain = 6 dB / Input = 1.4Vrms (0dB) / Output = 2.794Vrms / Speaker 0.976W



Audio amplifier

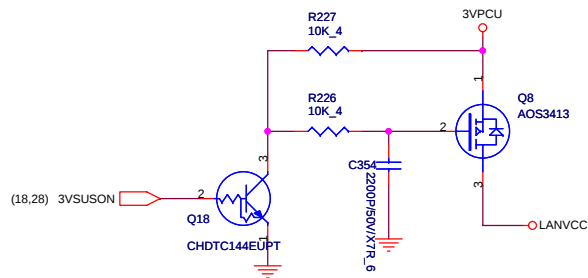


AMP Gain Table

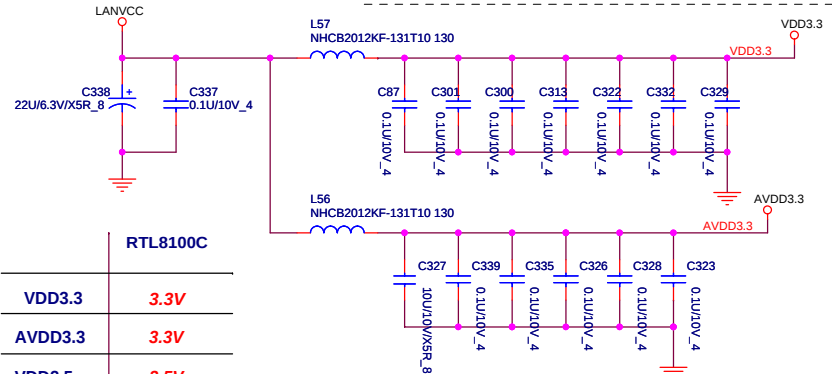
GAIN0	GAIN1	AV(INV)	Vin R
0	0	6dB	90Kohm
0	1	10dB	70Kohm
1	0	15.6dB	45Kohm
1	1	21.6dB	25Kohm



PROJECT : IL1
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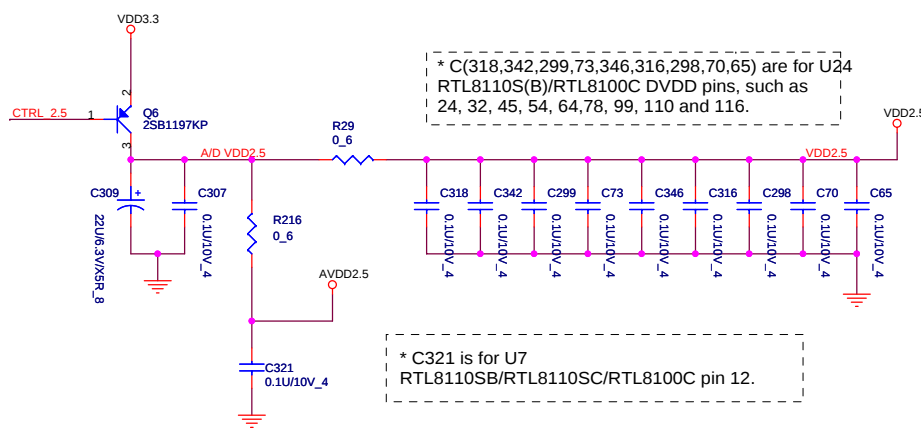


* C(87,301,300,313,322,332,329) are for U24 RTL8110S(B/C)/RTL8100C VDD33 pins, such as 26, 41, 56, 71, 84, 94 and 107.



	RTL8100C
VDD3.3	3.3V
AVDD3.3	3.3V
VDD2.5	2.5V
AVDD2.5	2.5V

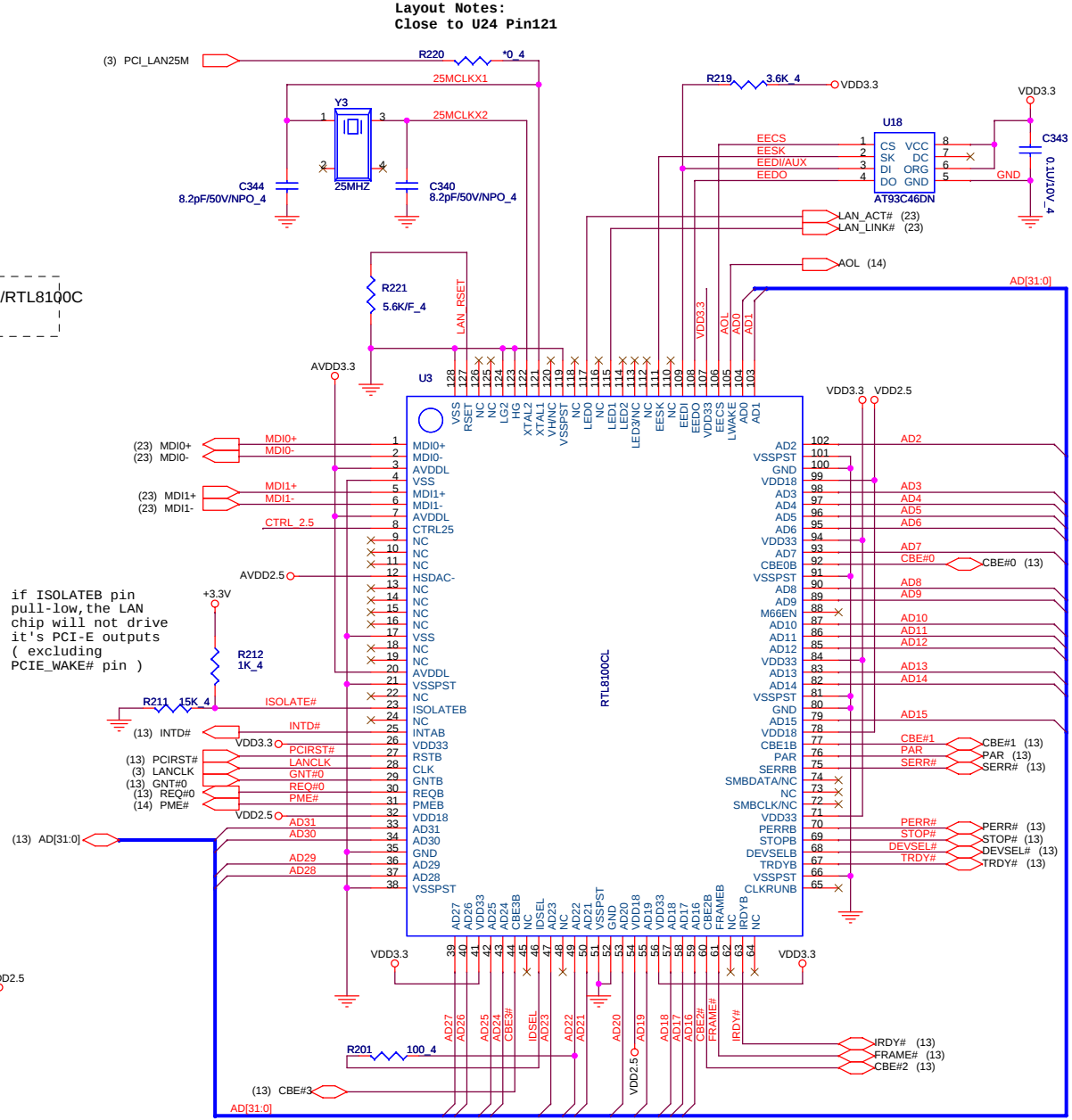
* C(327,339,335,326,328,323) are for U24 RTL8110S(B/C)/RTL8100C AVDDL pins, such as 3, 7, 16, and 20.



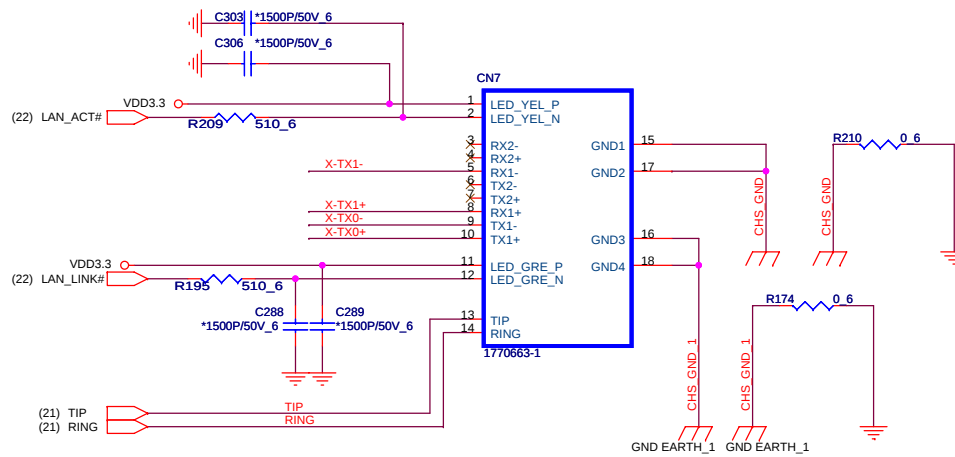
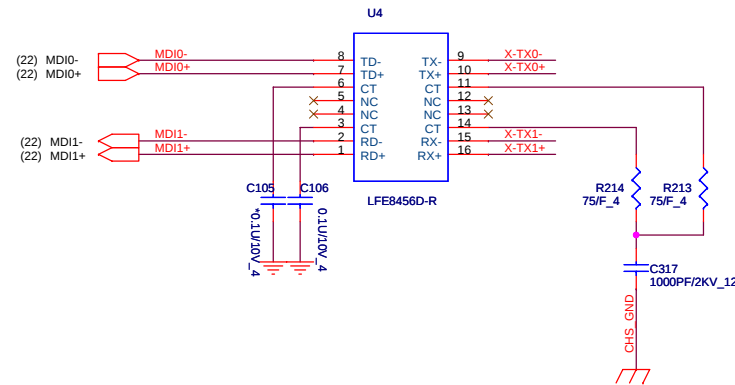
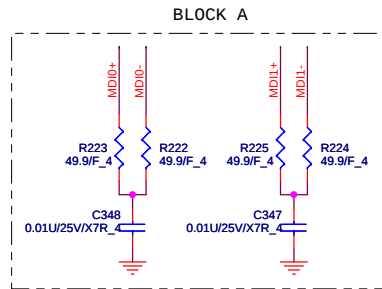
* C(318,342,299,73,346,316,298,70,65) are for U24 RTL8110S(B)/RTL8100C DVDD pins, such as 24, 32, 45, 54, 64, 78, 99, 110 and 116.

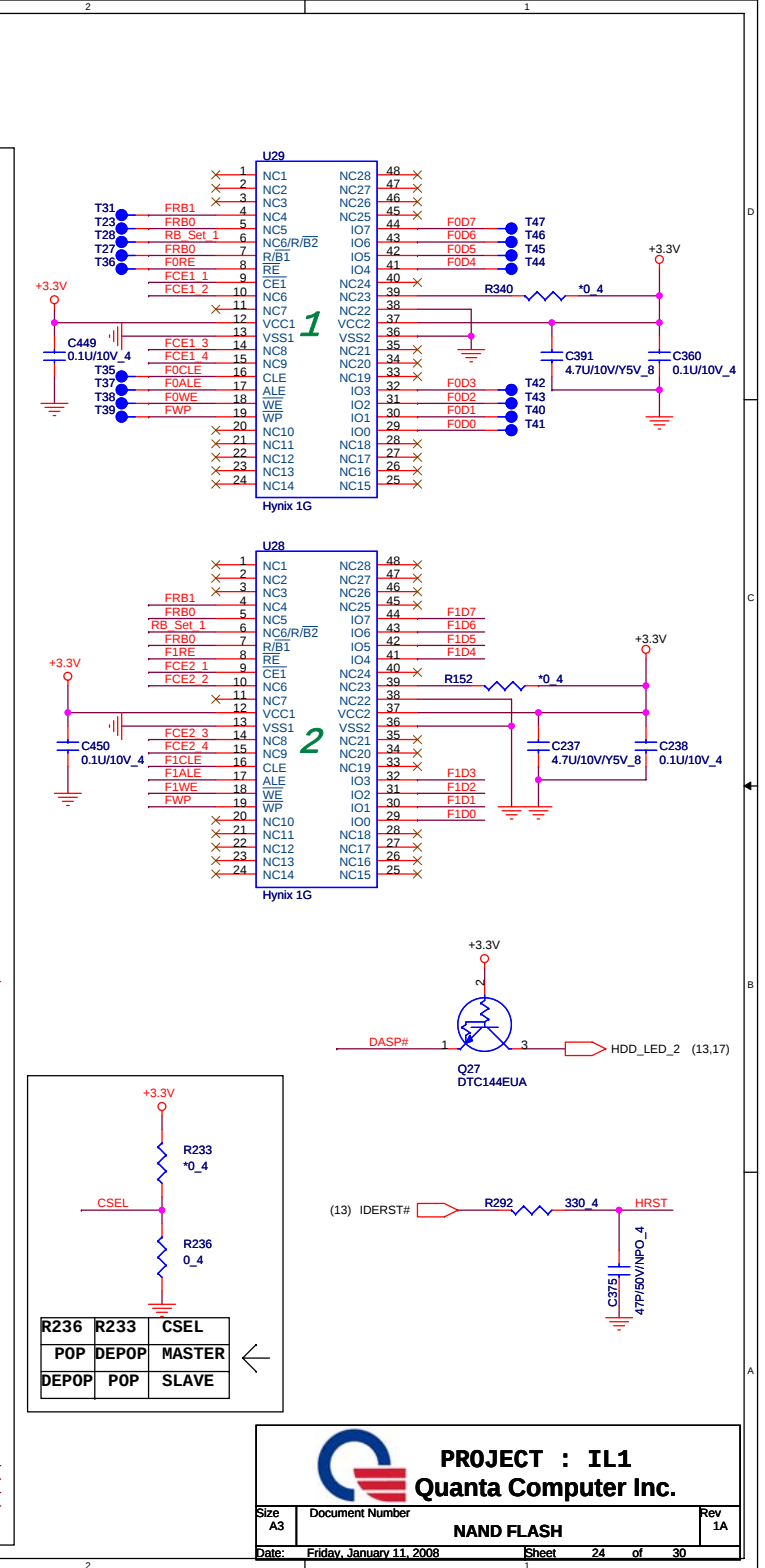
* C321 is for U7 RTL8110SB/RTL8110SC/RTL8100C pin 12.

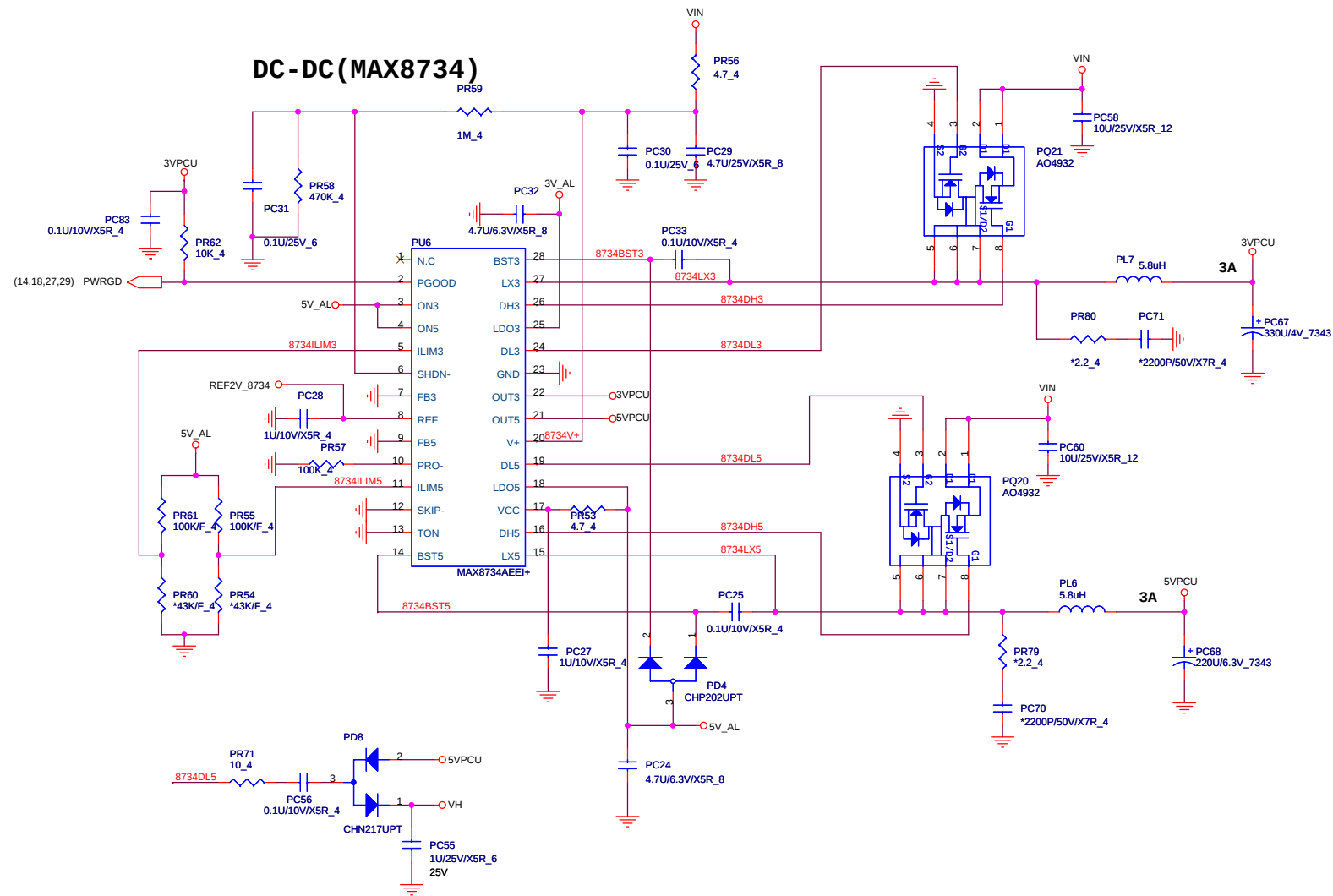
if ISOLATEB pin pull-low, the LAN chip will not drive it's PCI-E outputs (excluding PCIE_WAKE# pin)

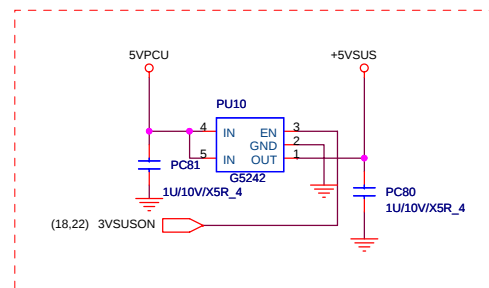
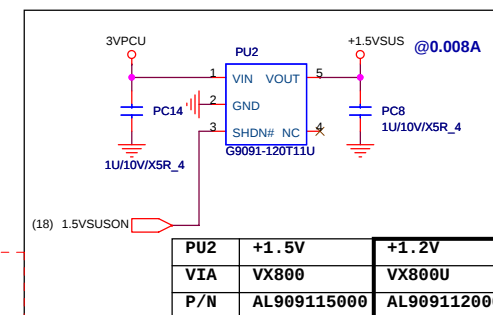
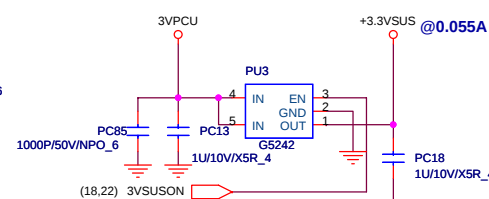
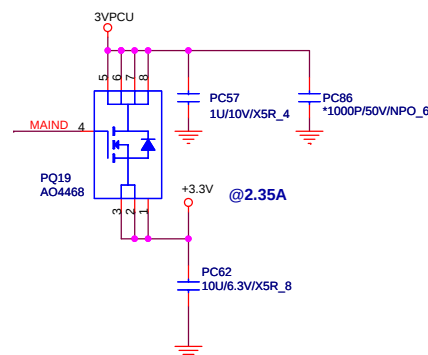
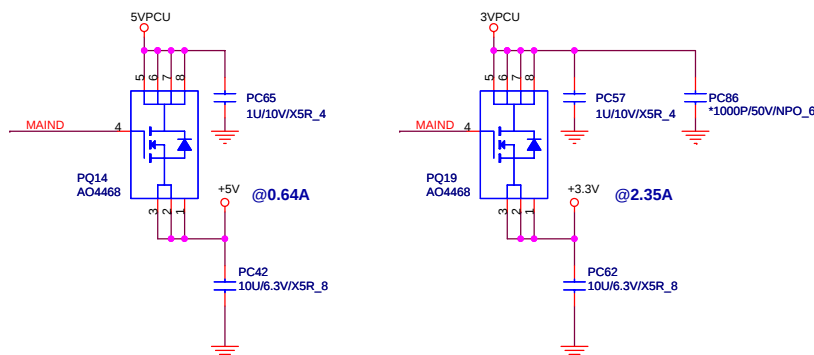
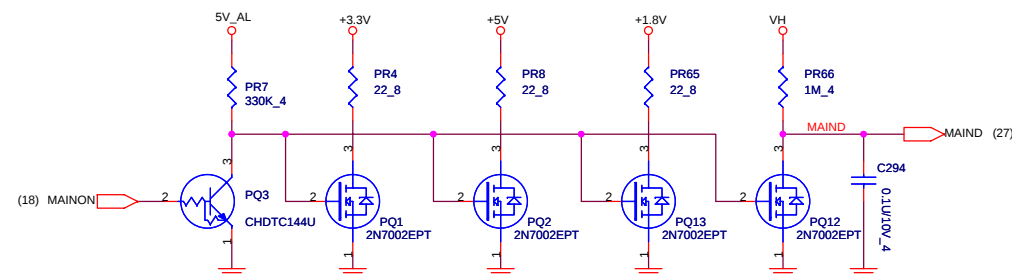
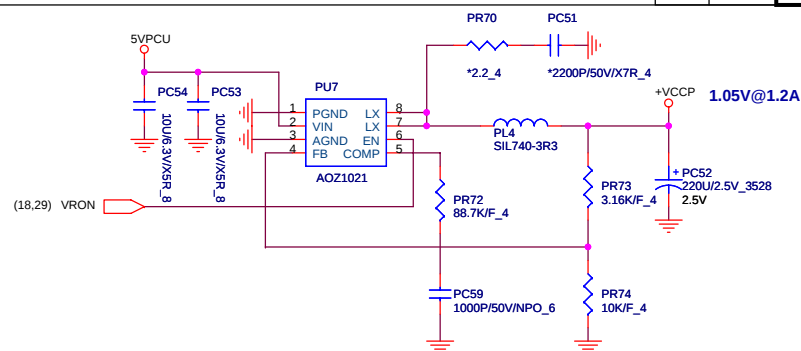
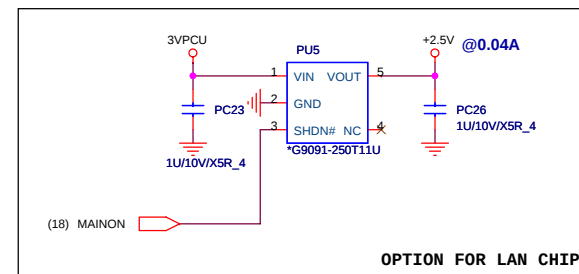
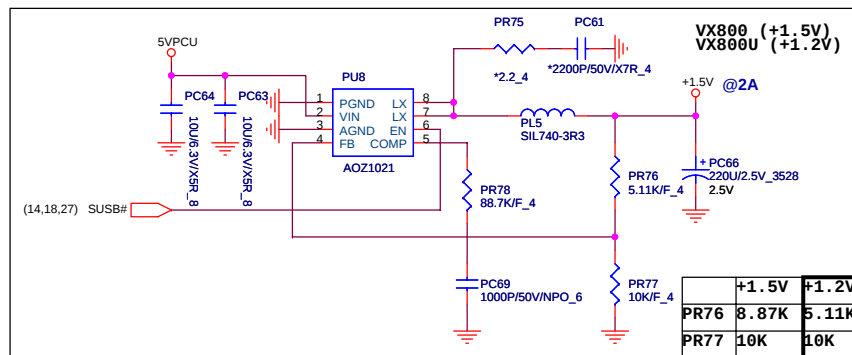


PCI-IDSEL:
INTD & AD22



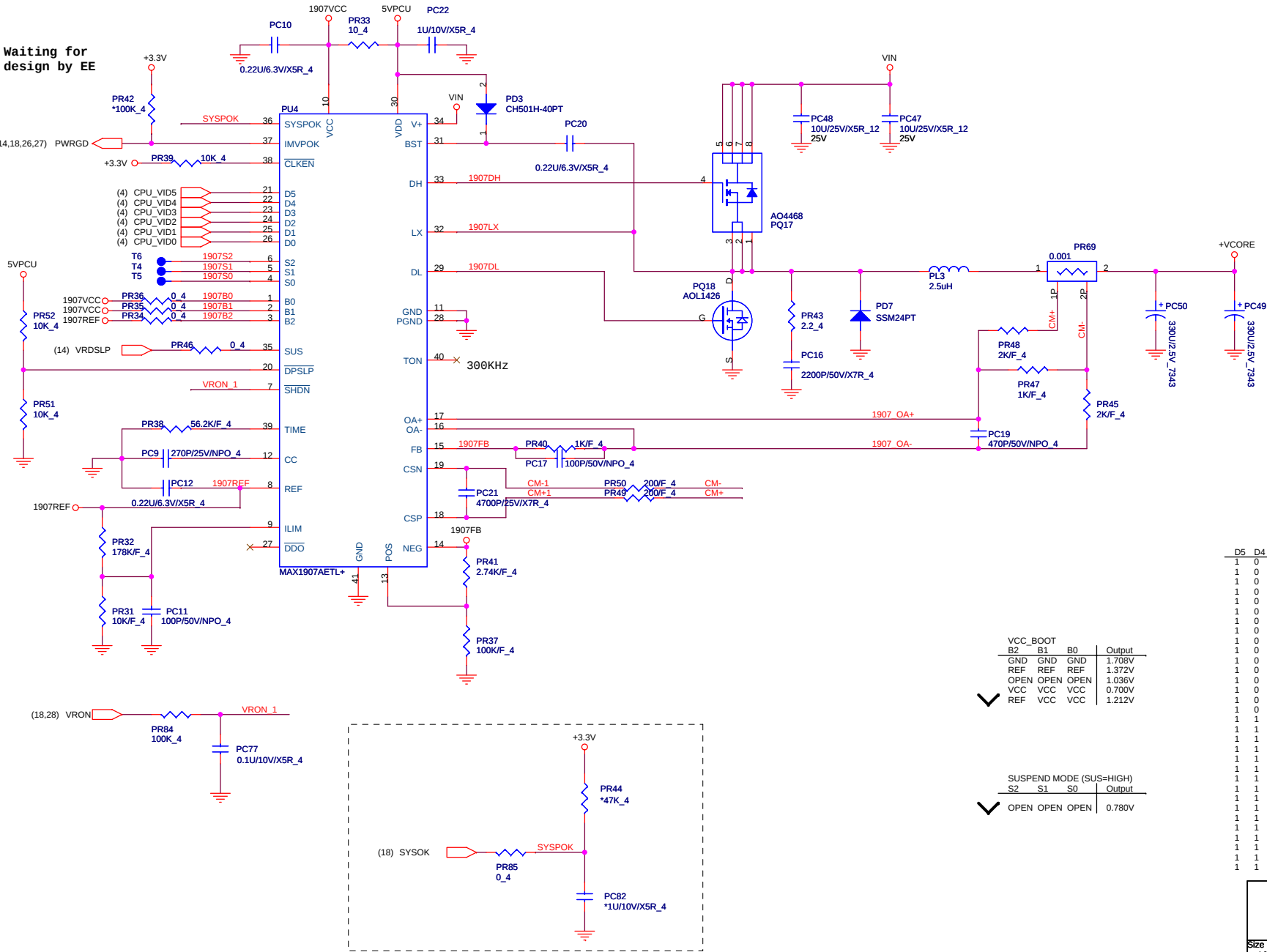






CPU CORE(MAX1907)

Waiting for design by EE



VCC_BOOT	B2	B1	B0	Output
GND	GND	GND	REF	1.708V
REF	REF	REF	REF	1.372V
OPEN	OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	VCC	0.700V
REF	VCC	VCC	VCC	1.212V

SUSPEND MODE (SUS=HIGH)	S2	S1	S0	Output
OPEN	OPEN	OPEN	OPEN	0.780V

D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V
1	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.660V
1	0	0	0	1	0	1.644V
1	0	0	0	1	1	1.628V
1	0	0	0	1	0	1.612V
1	0	0	0	1	1	1.596V
1	0	0	0	1	0	1.580V
1	0	0	0	1	1	1.564V
1	0	0	0	1	0	1.548V
1	0	0	0	1	1	1.532V
1	0	0	0	1	0	1.516V
1	0	0	0	1	1	1.500V
1	0	0	0	1	0	1.484V
1	0	0	0	1	1	1.468V
1	0	0	0	1	0	1.452V
1	0	0	0	1	1	1.436V
1	0	0	0	1	0	1.420V
1	0	0	0	1	1	1.404V
1	0	0	0	1	0	1.388V
1	0	0	0	1	1	1.372V
1	0	0	0	1	0	1.356V
1	0	0	0	1	1	1.340V
1	0	0	0	1	0	1.324V
1	0	0	0	1	1	1.308V
1	0	0	0	1	0	1.292V
1	0	0	0	1	1	1.276V
1	0	0	0	1	0	1.260V
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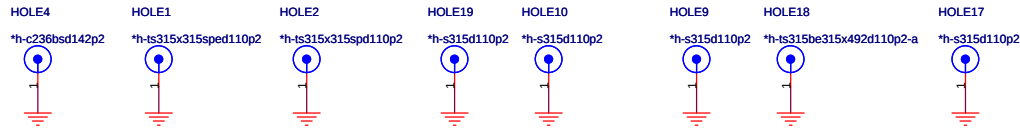


PROJECT : IL1

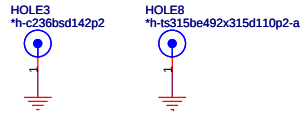
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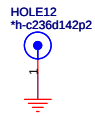
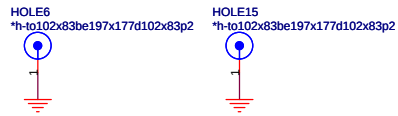
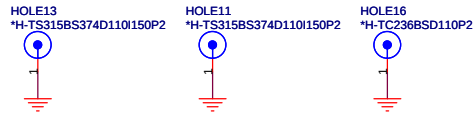
SCREW HOLE



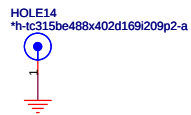
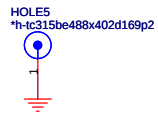
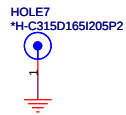
Mount NUT - TOP



Mount NUT - TOP



Mount NUT - BOT



PROJECT : IL1
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