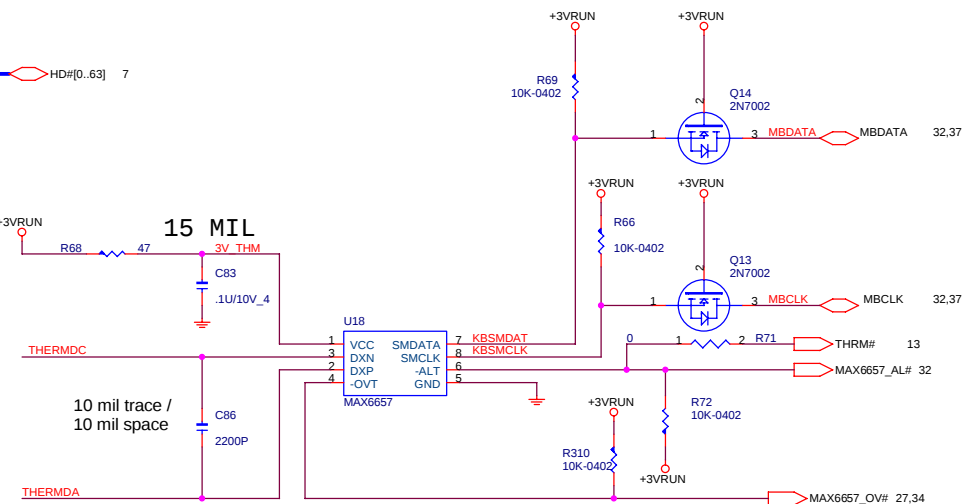
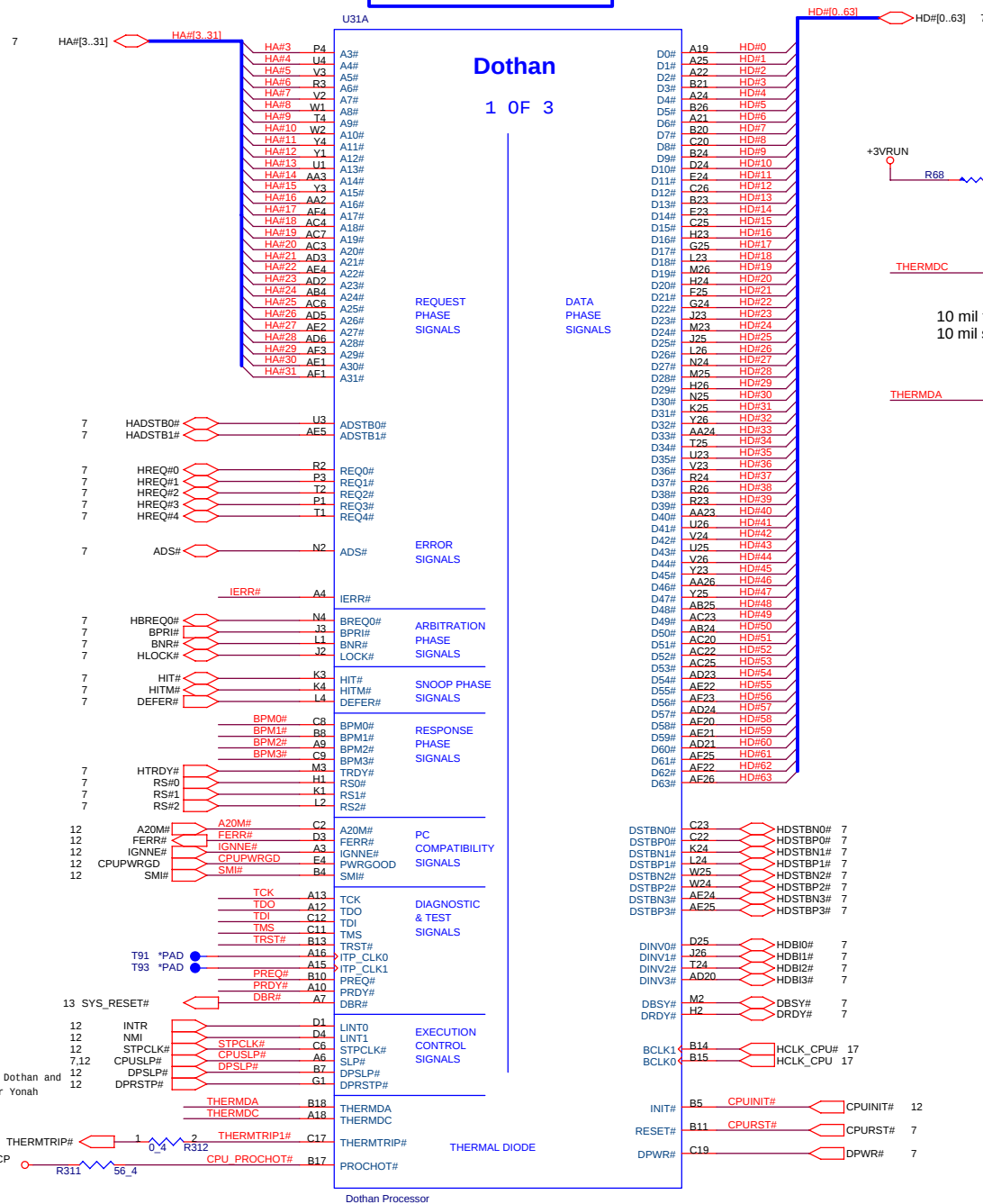
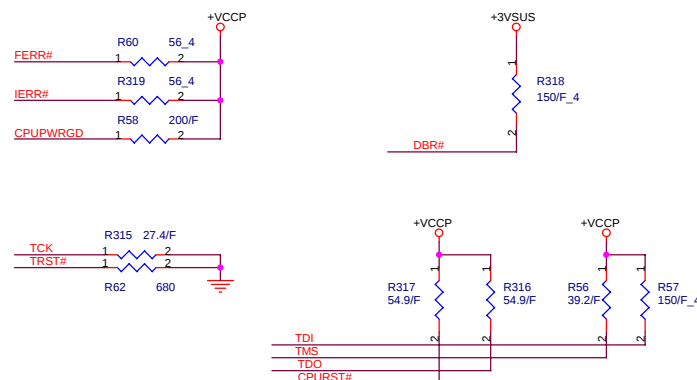


CT_0505: Change footprint to BGA479M-SOCKET from L100505 from MPGA479M



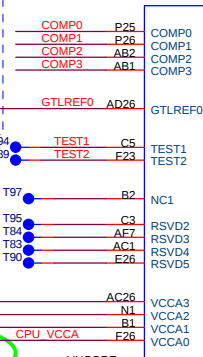
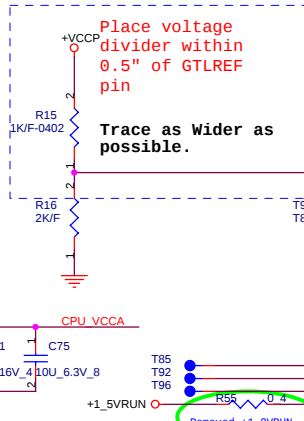
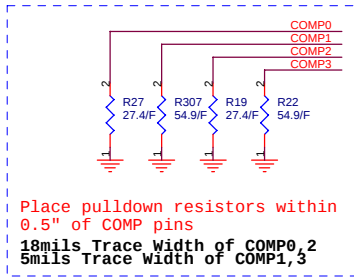
ITP disable guidelines			
Signal	Resistor Value	Connect To	Resistor Placement
TDI	150 ohm +/- 5%	VTT	Within 2.0" of the CPU
TMS	39 ohm +/- 5%	VTT	Within 2.0" of the CPU
TRST#	680 ohm +/- 5%	GND	Within 2.0" of the CPU
TCK	27 ohm +/- 5%	GND	Within 2.0" of the CPU
TDO	Open	VTT	Within 2.0" of the CPU

Note: Populate R58, R62 when ITP connector is populated.



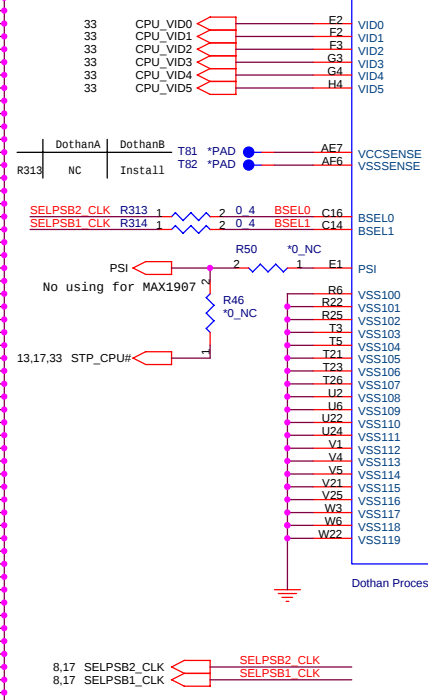
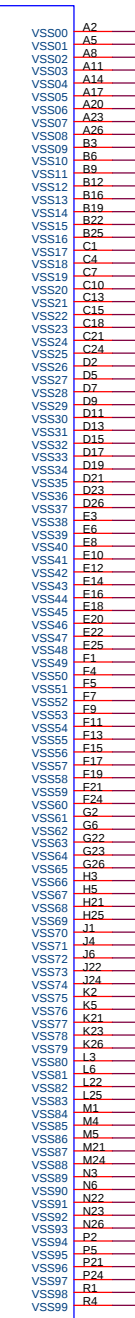
PROJECT : ED2L
Quanta Computer Inc.

Size	Document Number Dothan (HOST)	Rev E3
Date:	Wednesday, March 23, 2005	Sheet 5 of 38



Dothan
2 OF 3

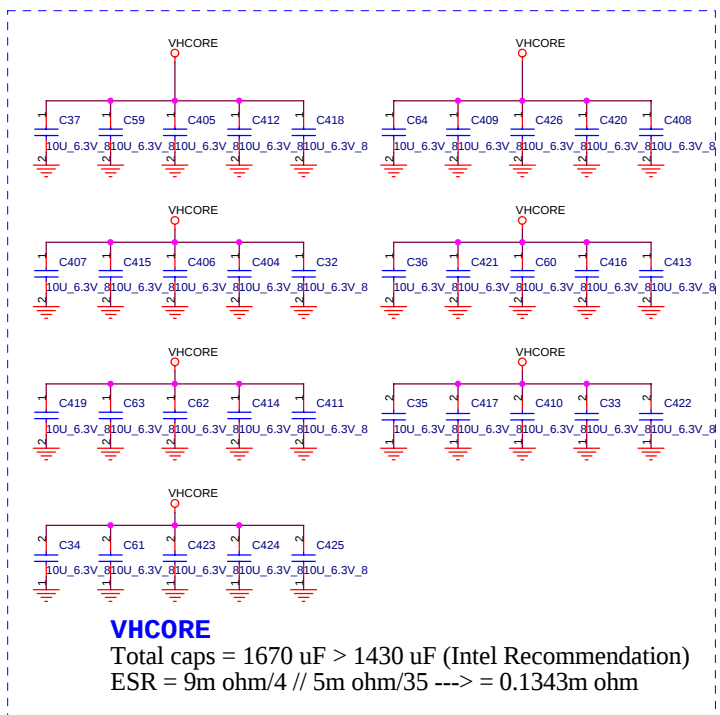
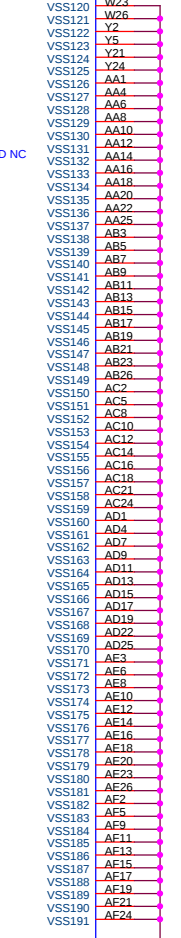
POWER, GROUND, RESERVED SIGNALS



Dothan
3 OF 3

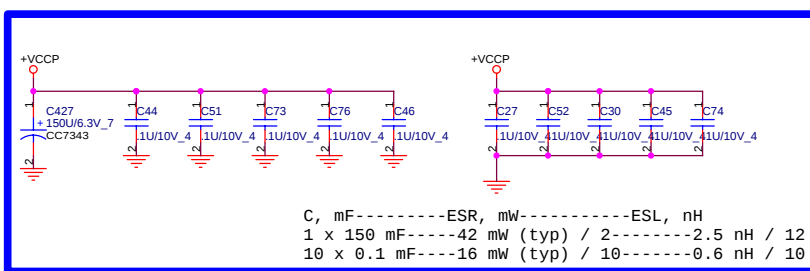
POWER, GROUND AND NC

VID

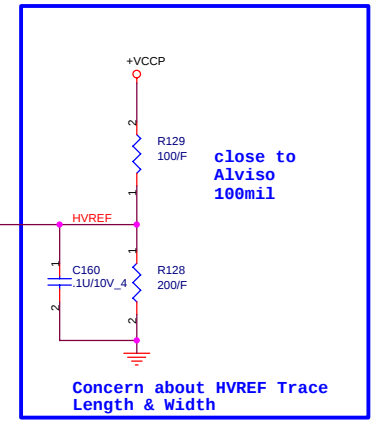
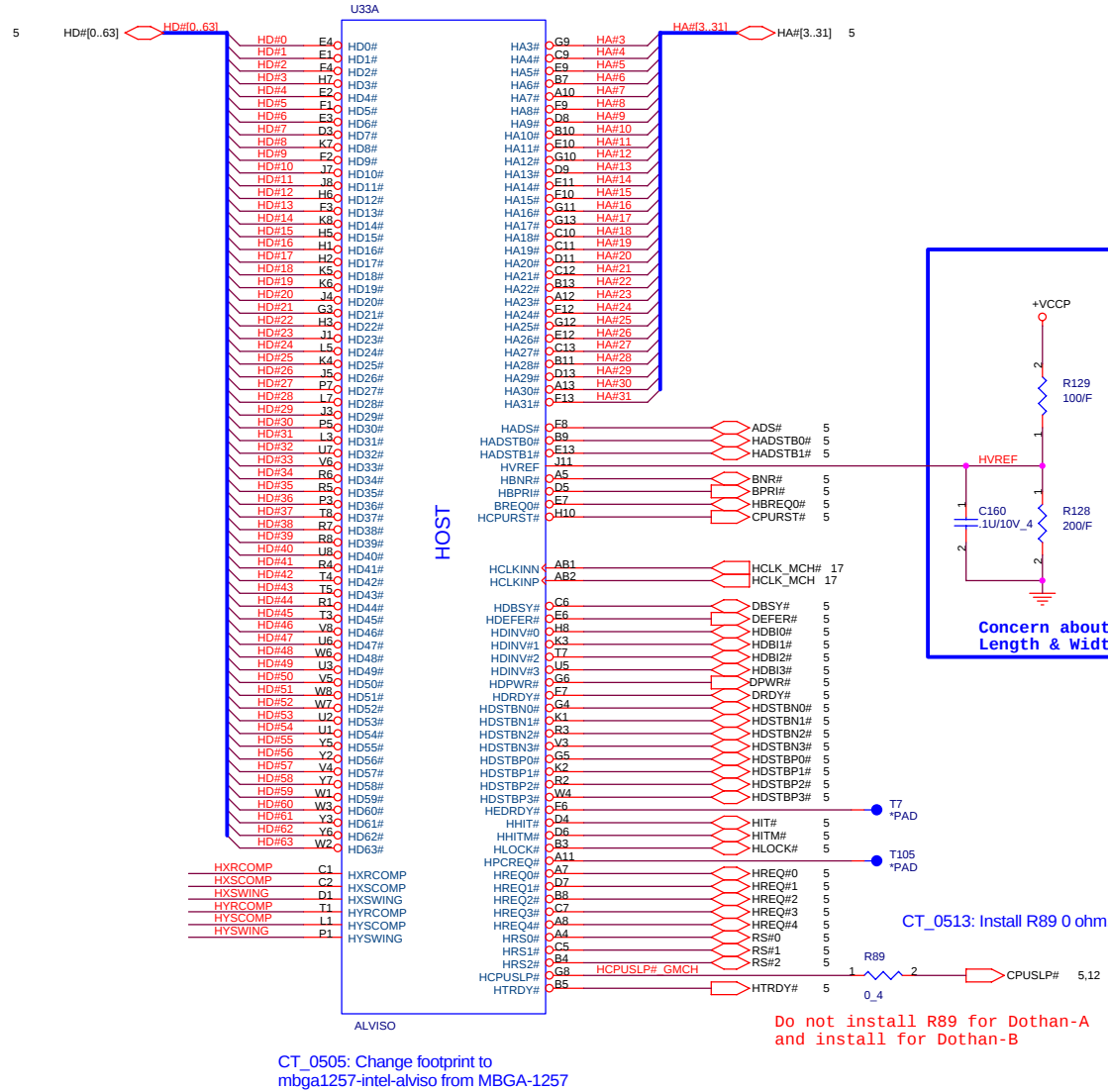
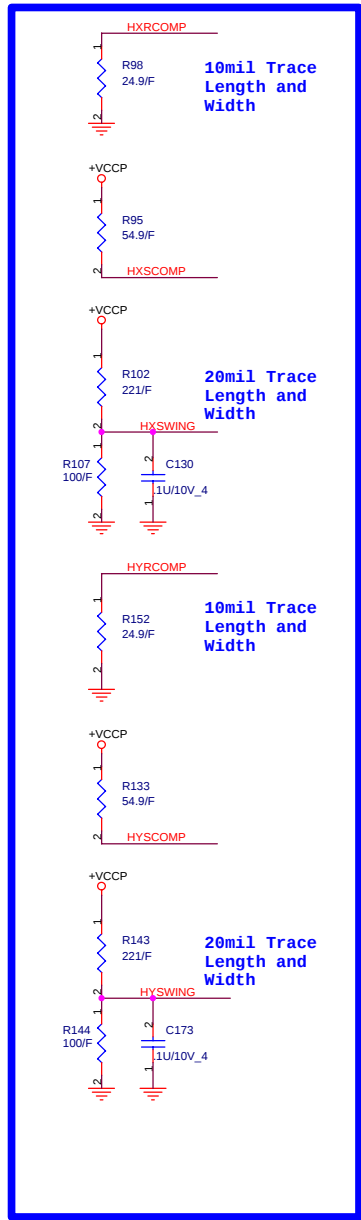


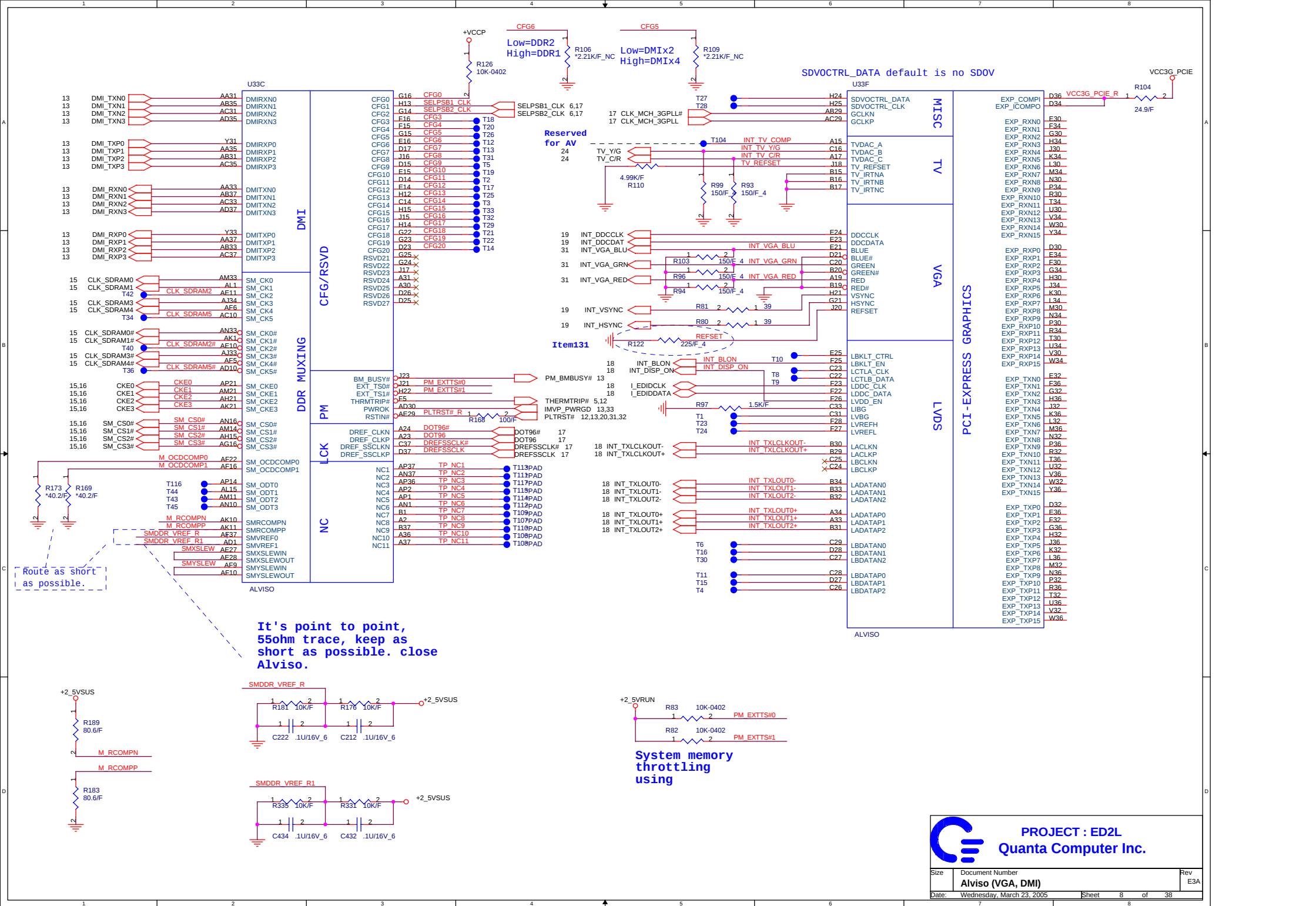
VHCORE

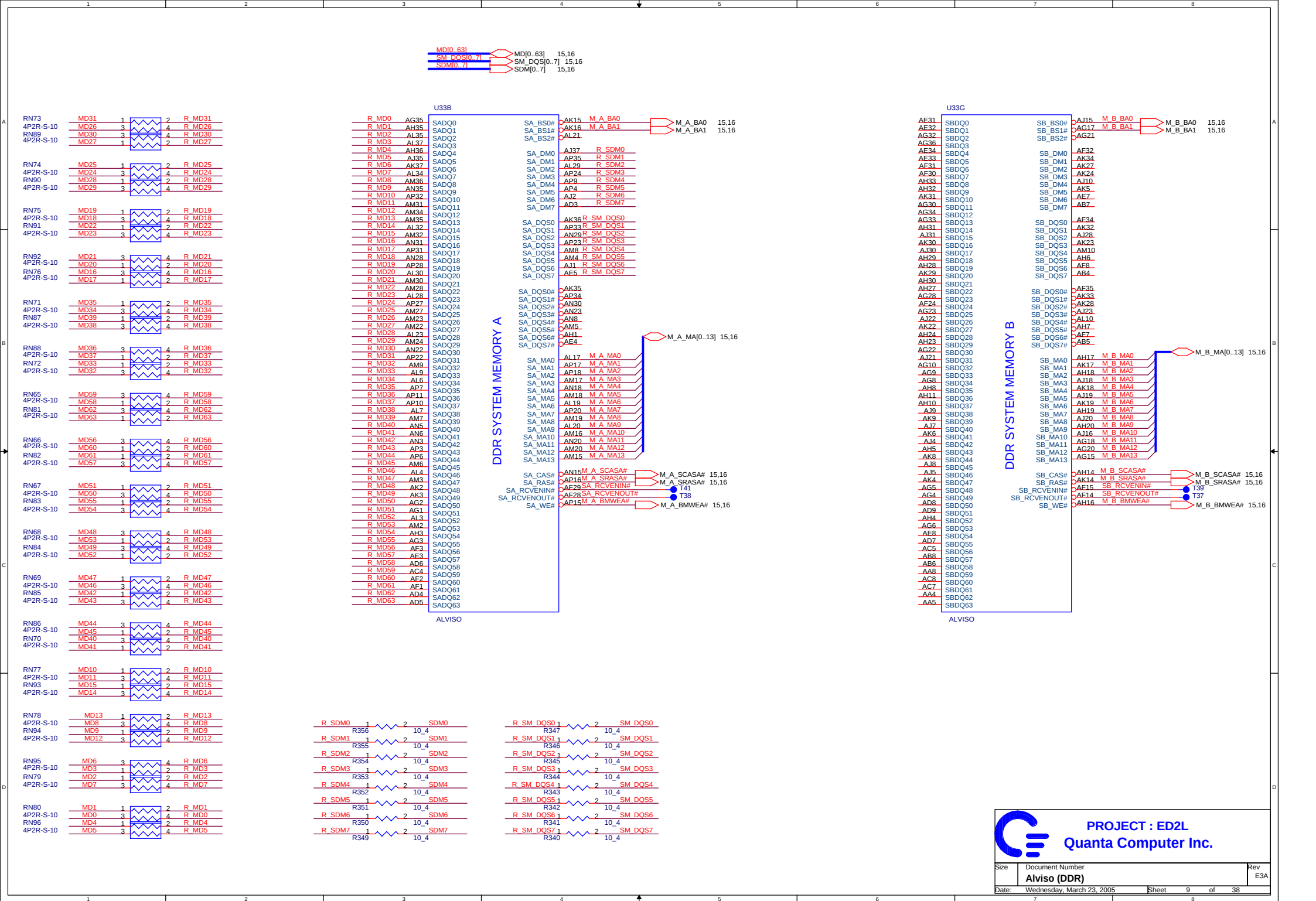
Total caps = 1670 uF > 1430 uF (Intel Recommendation)
ESR = 9m ohm/4 // 5m ohm/35 ----> = 0.1343m ohm



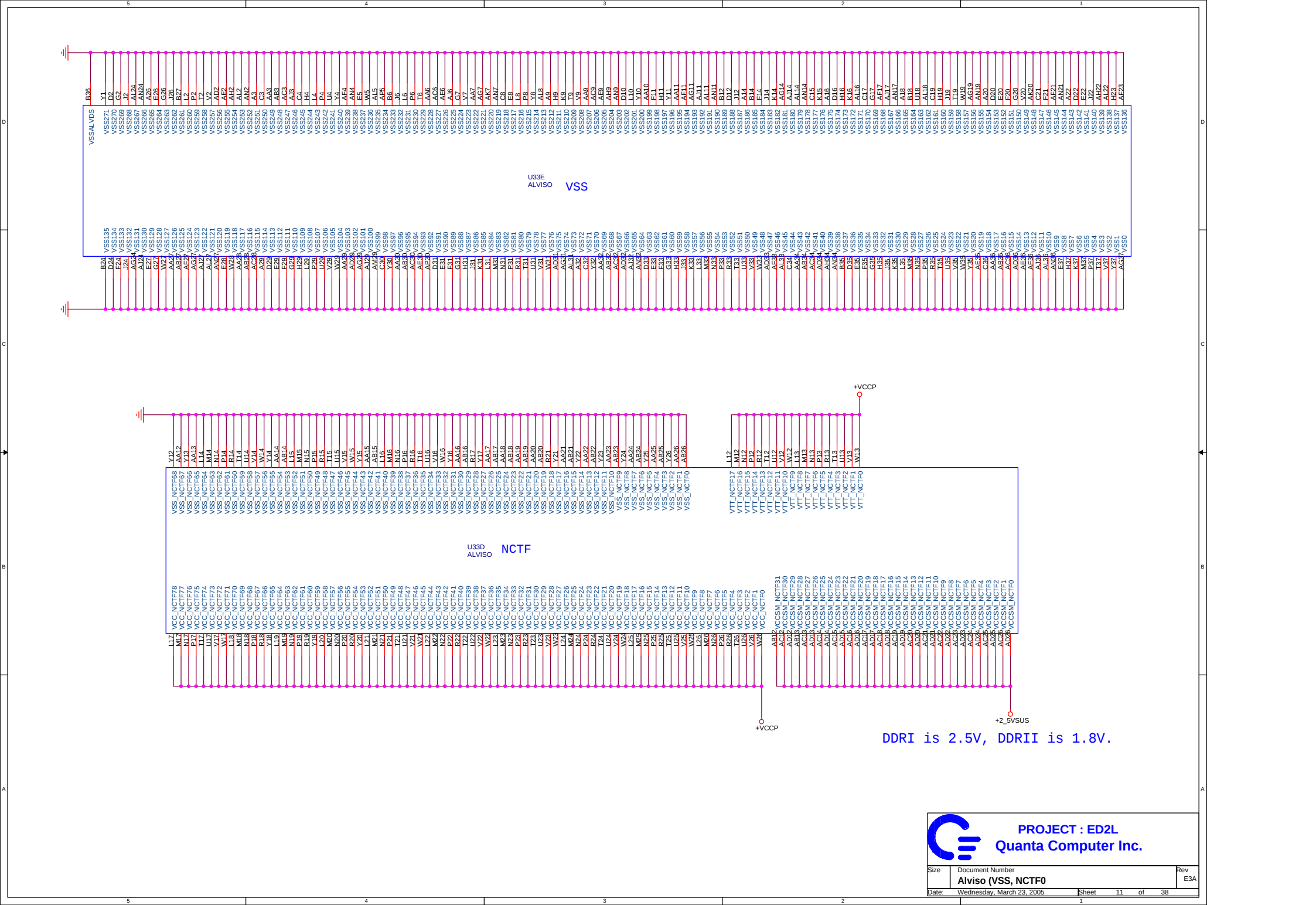
C, mF-----ESR, mW-----ESL, nH
1 x 150 mF-----42 mW (typ) / 2-----2.5 nH / 12
10 x 0.1 mF-----16 mW (typ) / 10-----0.6 nH / 10

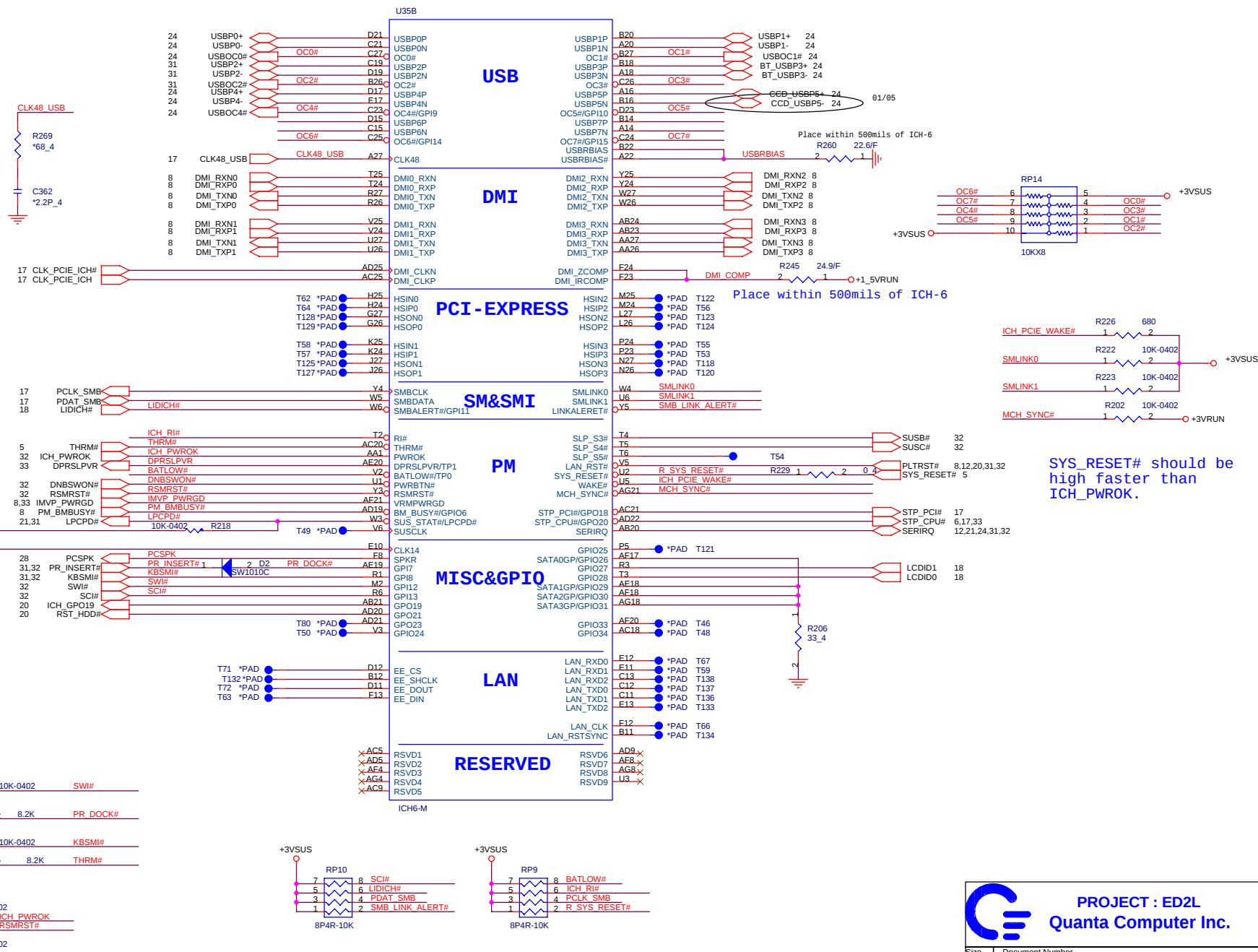


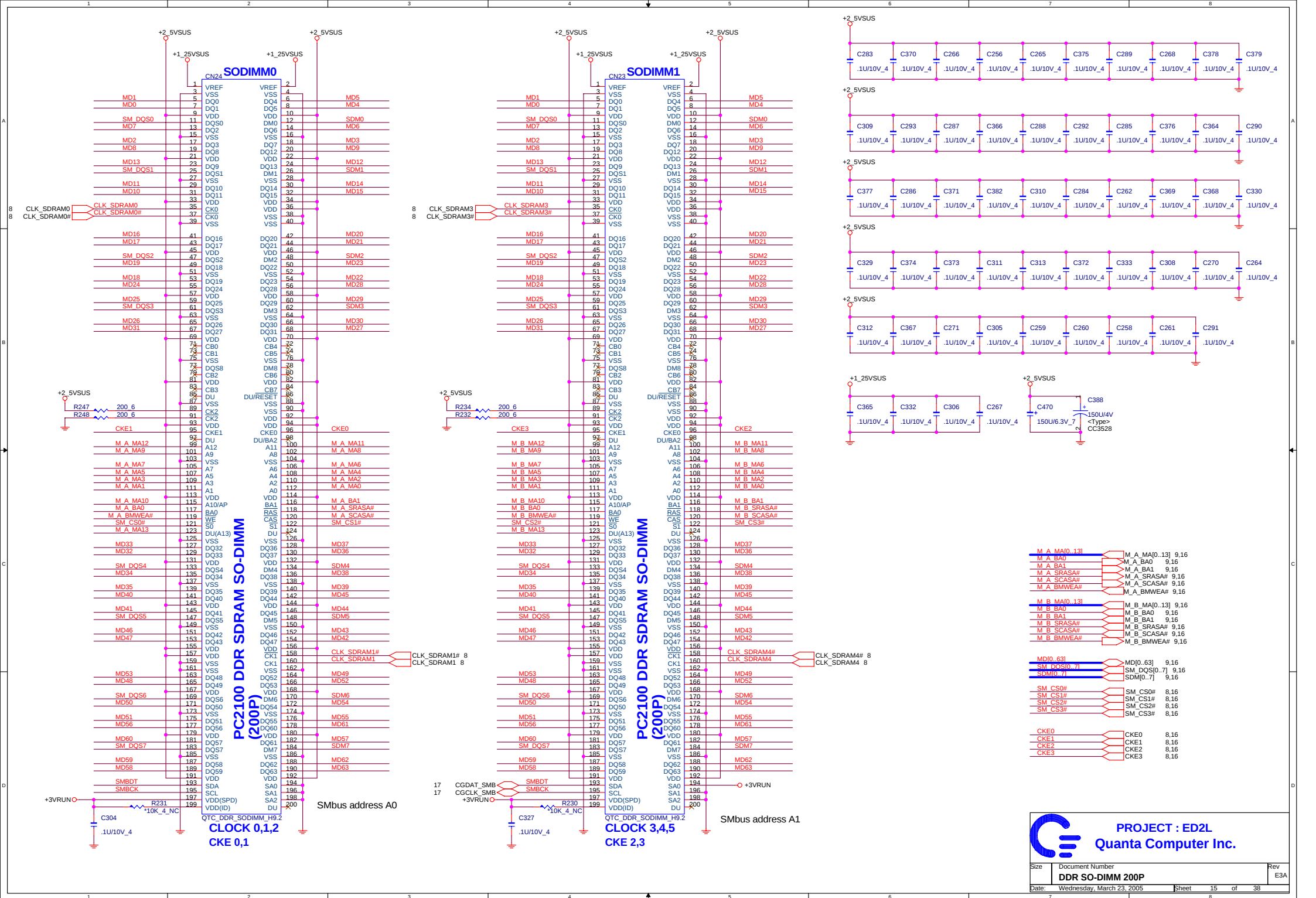


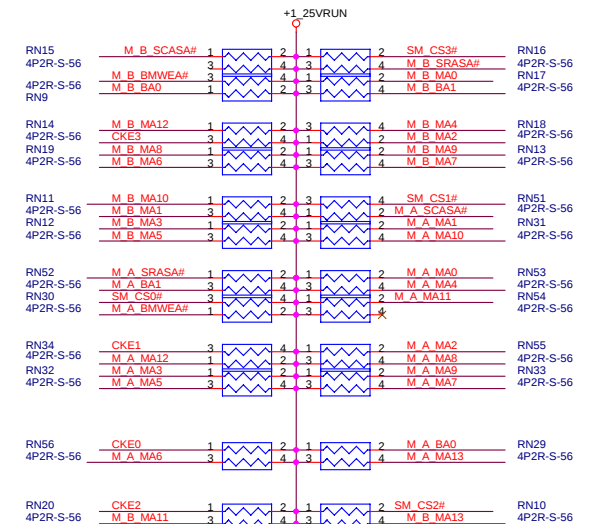
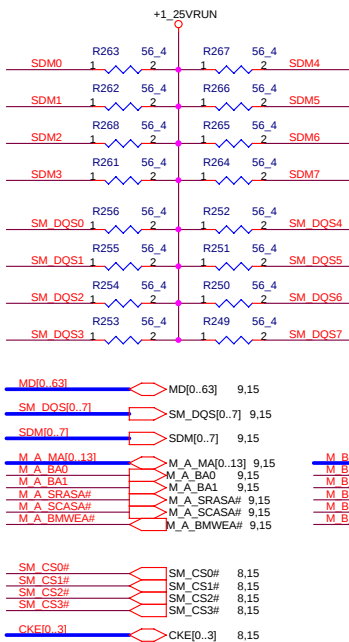
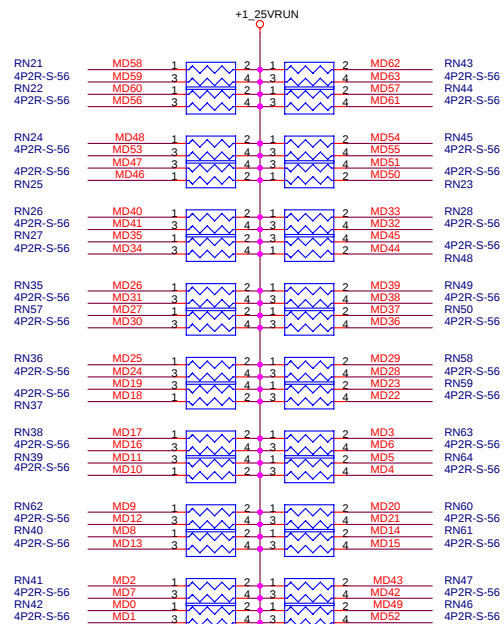
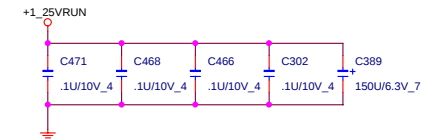
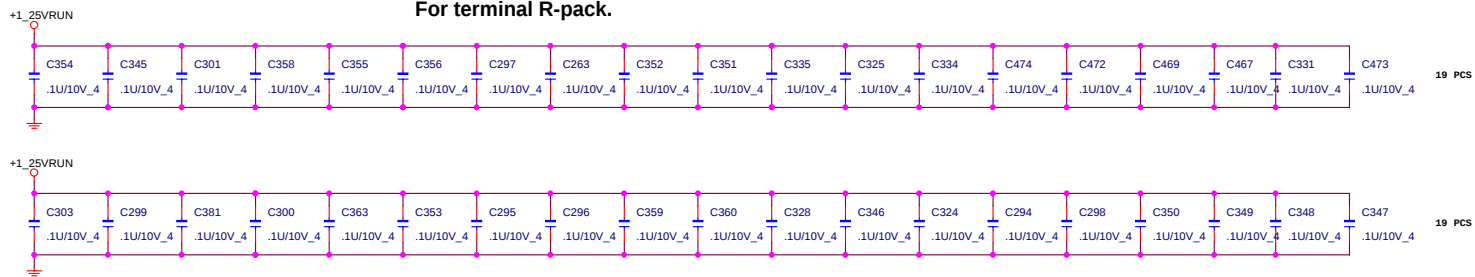


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	Alviso (Power)	E3A
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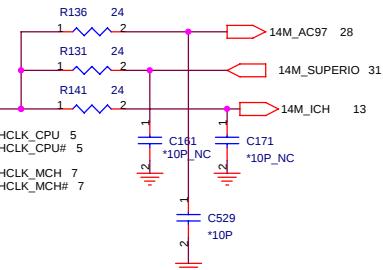
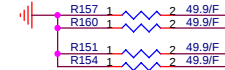




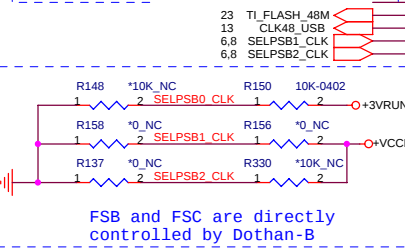
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

	DothanA	DothanB
R137	Install	NC
R330	NC	NC

Place these termination to close CK410M. Cause those Pin-out is for Current-Mode.



SMBus address D2



FSB and FSC are directly controlled by Dothan-B

Iref=5mA, Ioh=4*Iref

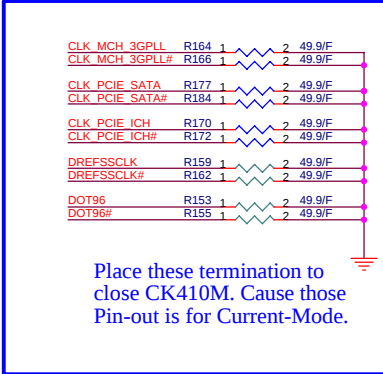
CT_0505: Change footprint to TSSOP56-8_1-5 from TSSOP56-240

Tie to VCC (Logic 1) is for ITP using. Tie to GND (Logic 0) is for PCIE using.

CK-410M PIN 35,36 Strap Pin. for ITP or PCIE using.

These are for backdrive issue

CT_0229: Change MOS to RHU002N06 due to layout concern.

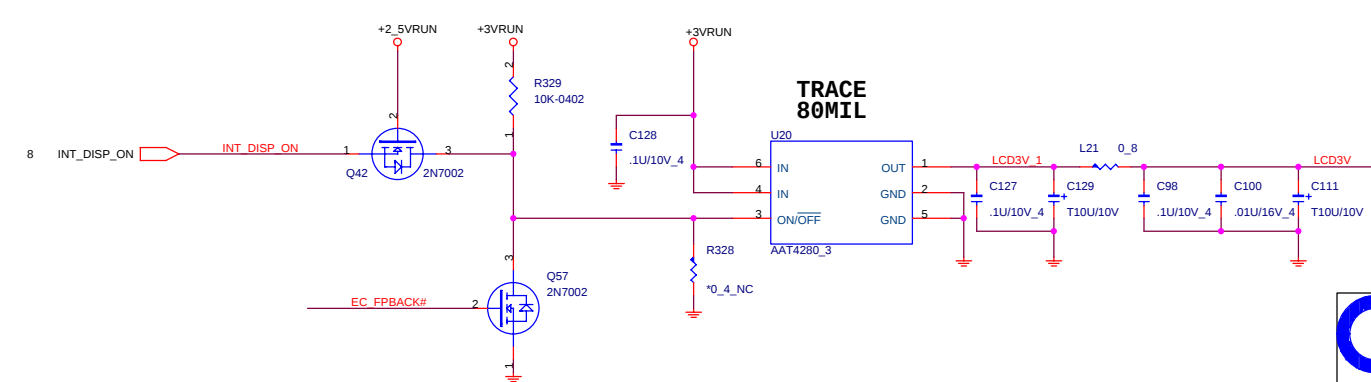
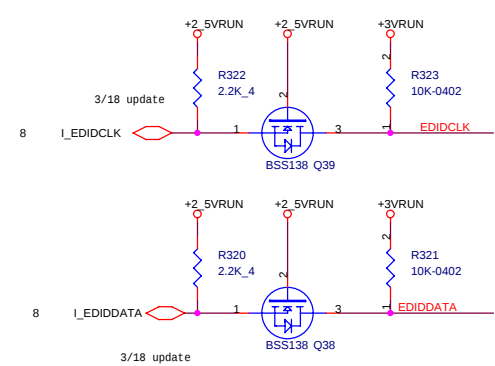
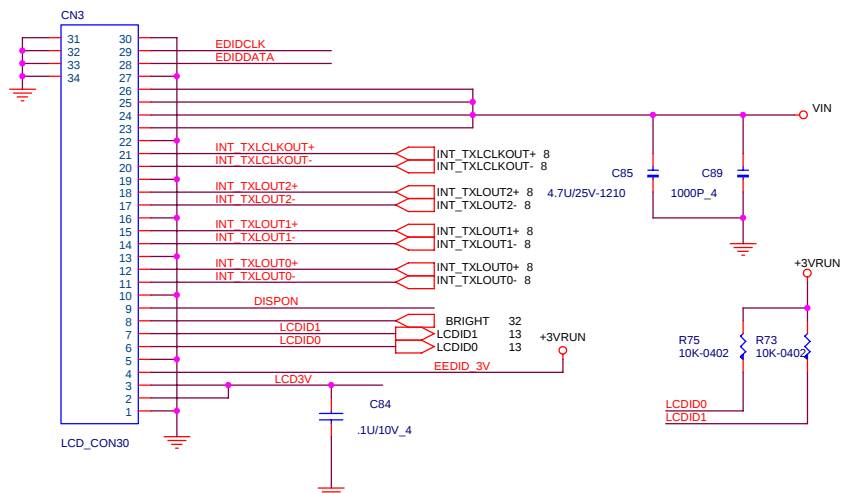
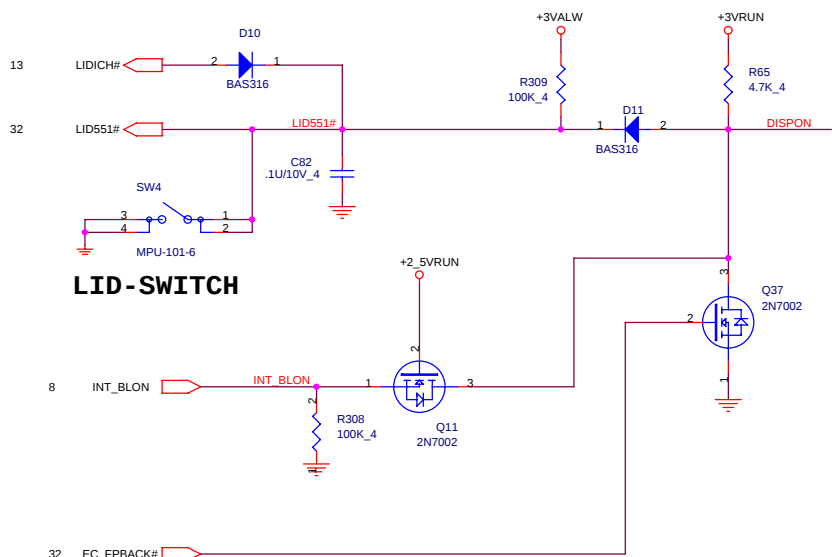


Place these termination to close CK410M. Cause those Pin-out is for Current-Mode.

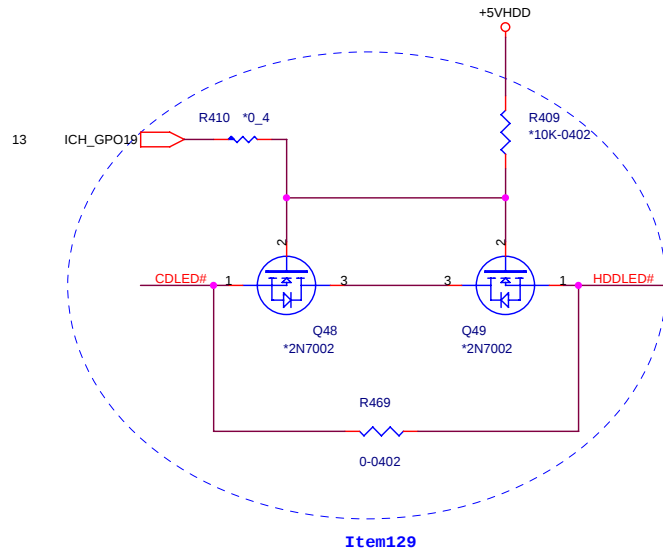
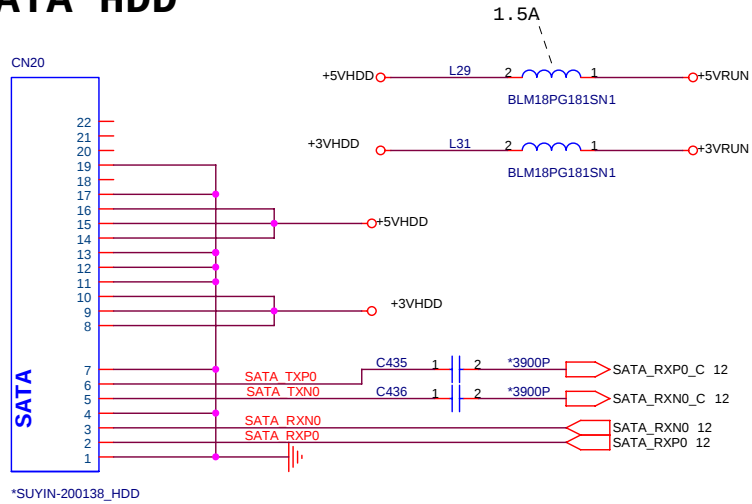
Bypass CAPs need to follow Bypass CAP. Routing Rule, no vias between CAP to CHIPSET VCC Pin or GND.



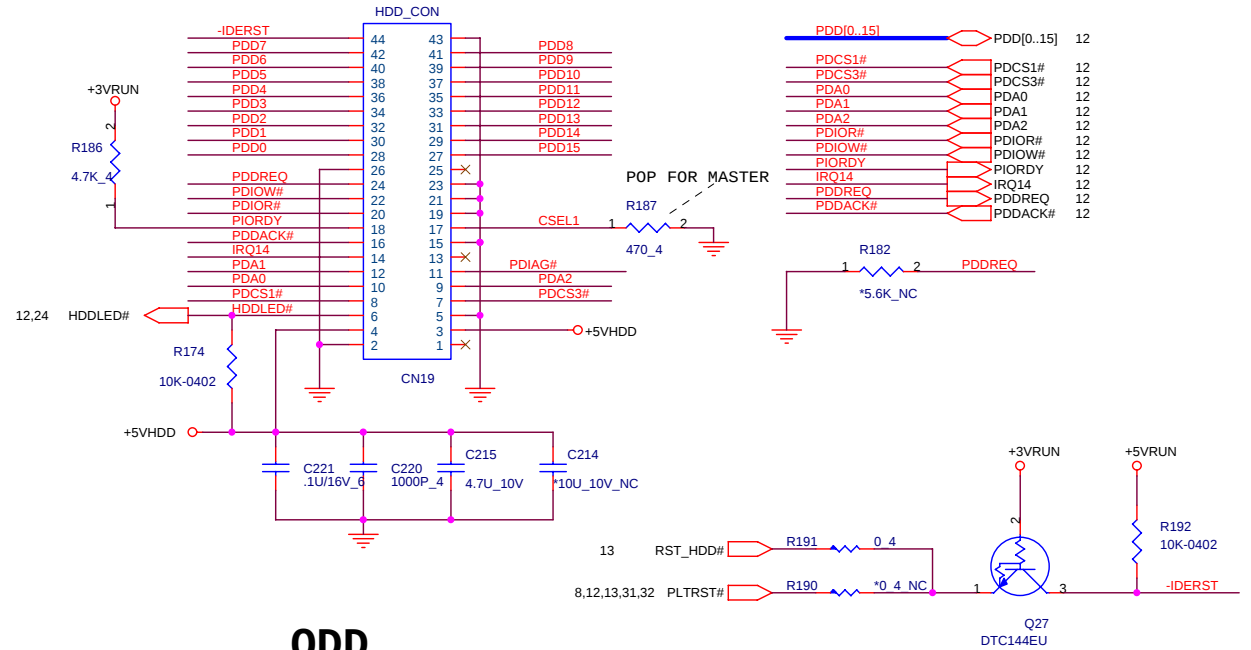
PROJECT : ED2L
Quanta Computer Inc.



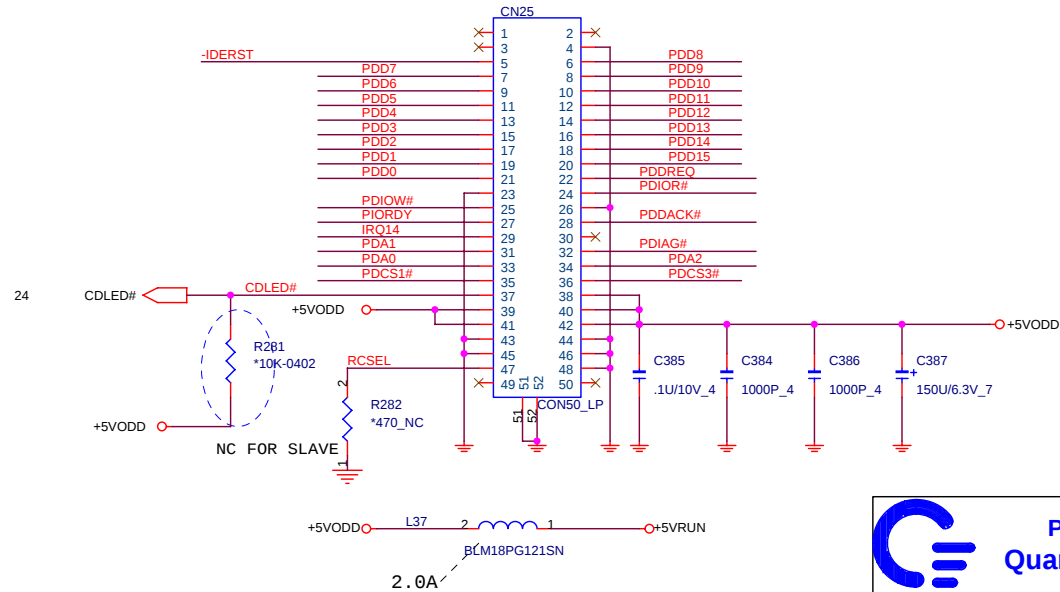
SATA HDD



PATA HDD

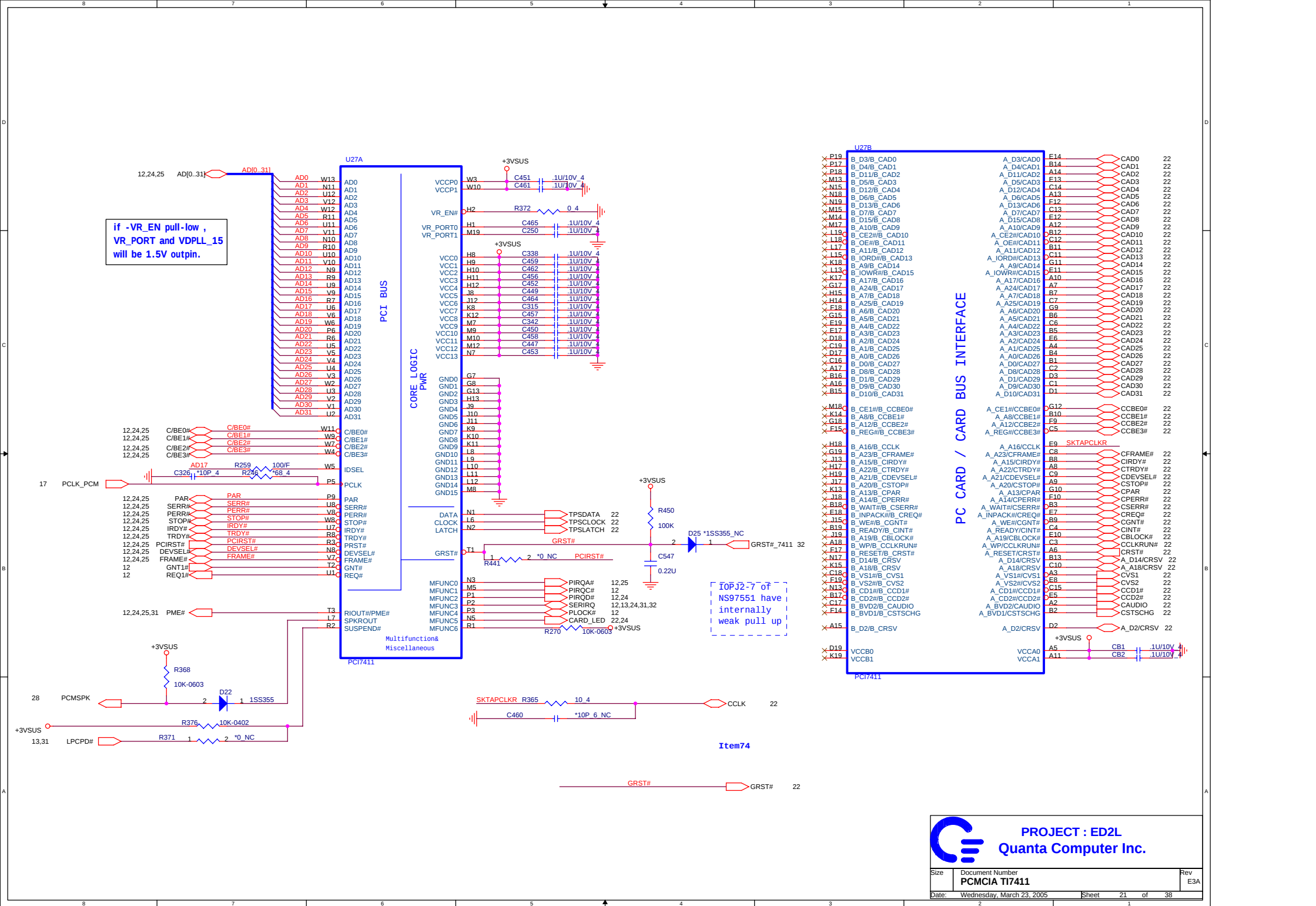


ODD

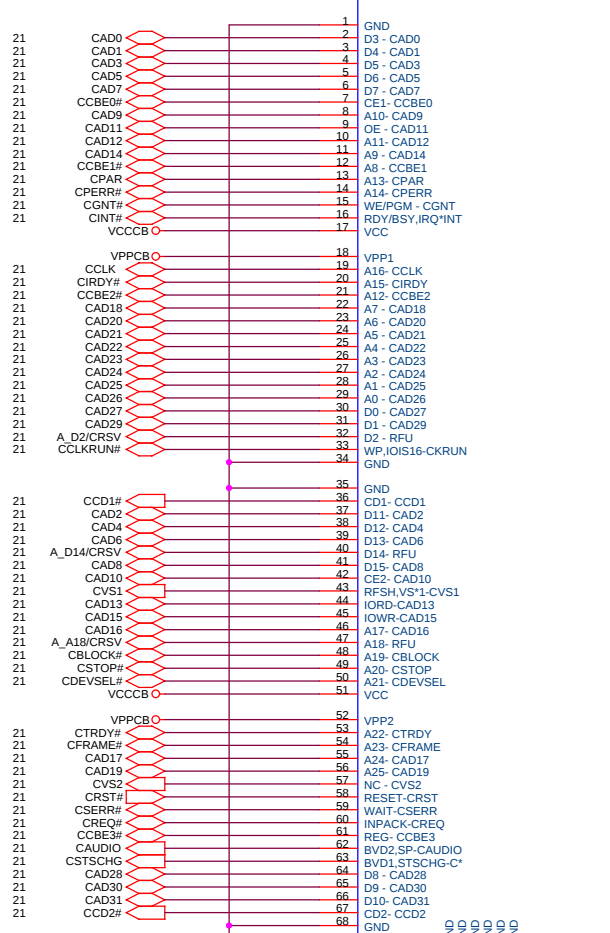


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Quanta Computer Inc.

Size	Document Number	Rev
	HDD & CDROM Connector	E3A
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PCMCIA SOCKET

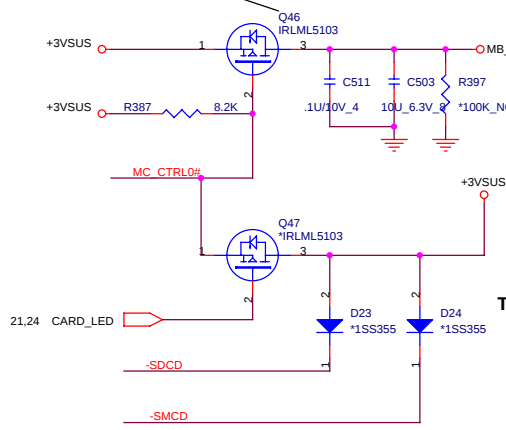


MC_PWR_CTL low
active(default)
, or change
register to high
active

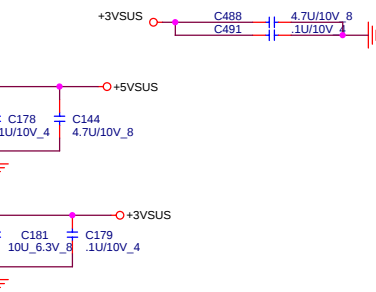
Flash Media Layout Guidelines:

1. Signal traces should be 60 Ohm +/- 10%.
2. All signal traces should be routed with equal propagation delay, and with trace lengths as short as practical.
3. A 56 Ohm damping resistor for MS_CLK and SD_CLK should be placed near the PCI7411 source.

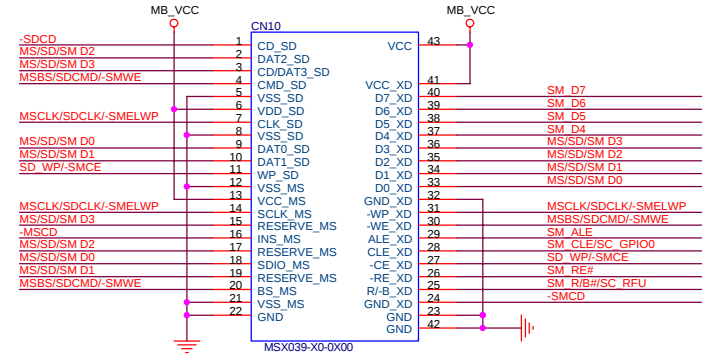
IRLML5103
Rds(on)=0.6



TI Erratum



4 IN1 CARD READER (XD, MMC/SD, MS)



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	PCMCIA Socket & 4-IN-1	E3A
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U27D

1394(FIREWIRE)

PHY PORT 0

TPA0+
TPA0-
TPB0+
TPB0-
TPBIAS0

BIAS CURRENT

R0
R1

CRYSTAL

XO

XI

EEPROM BUS

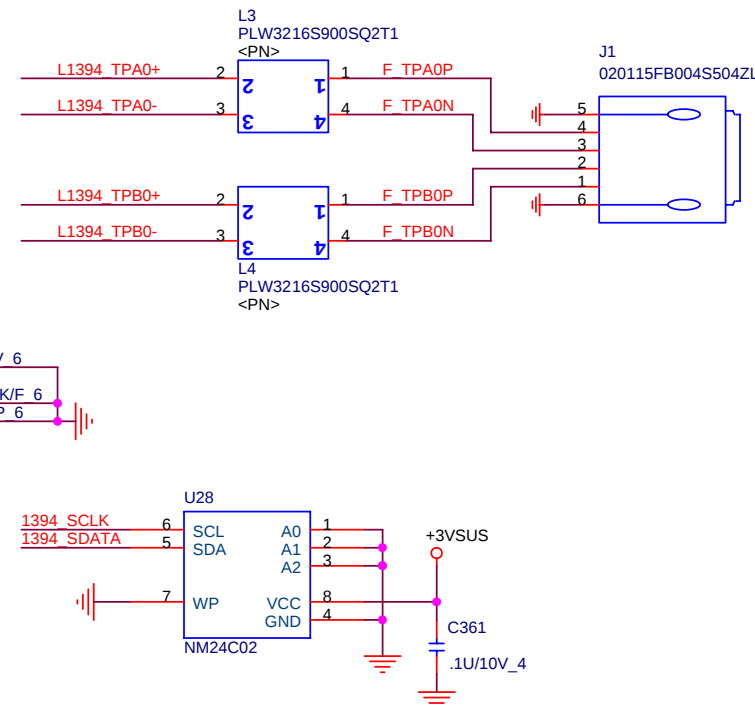
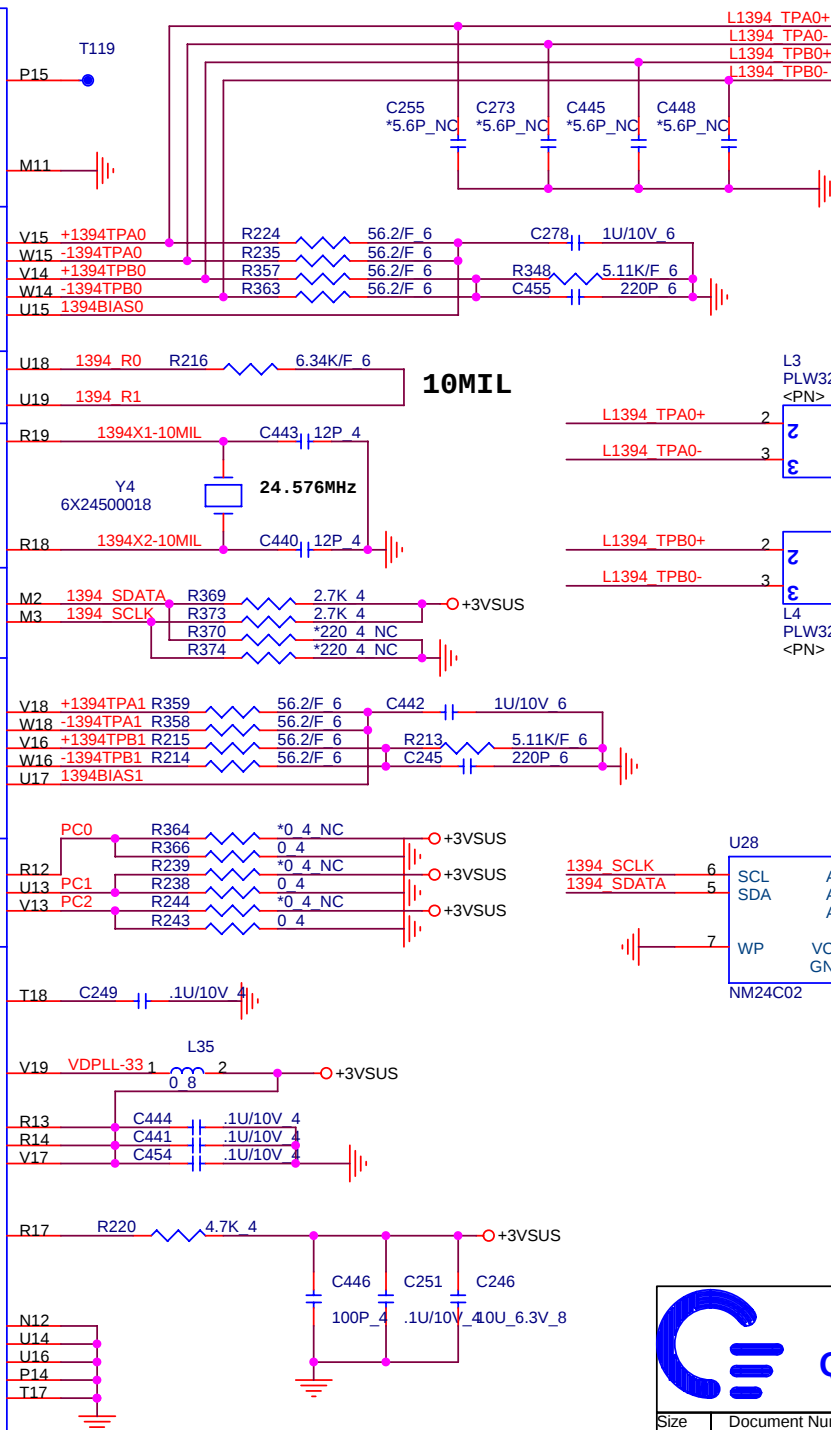
SDA
SCL

PHY PORT 1

TPA1+
TPA1-
TPB1+
TPB1-
TPBIAS1

POWER CLASS

PC0
PC1
PC2



PCI7411

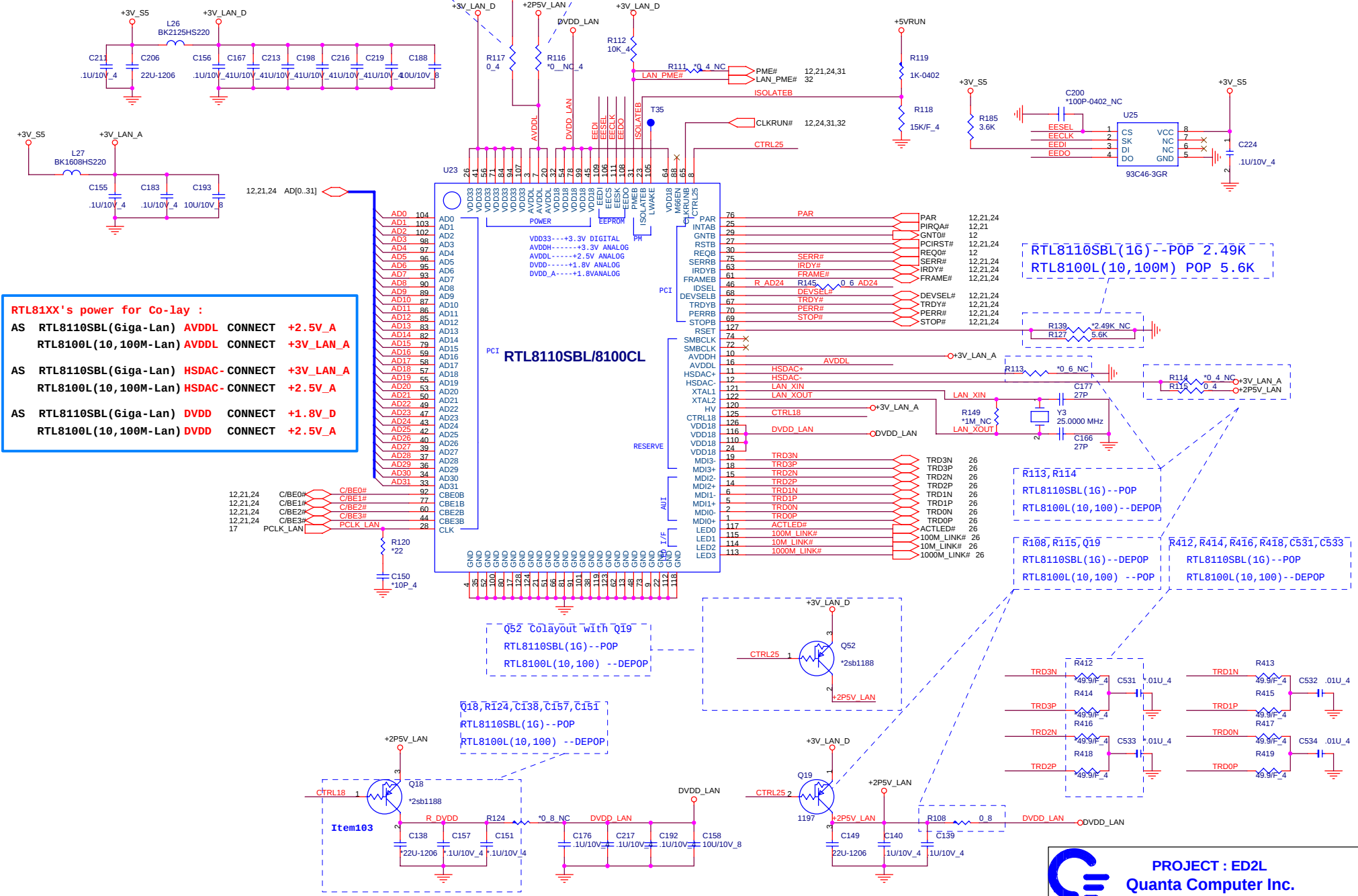


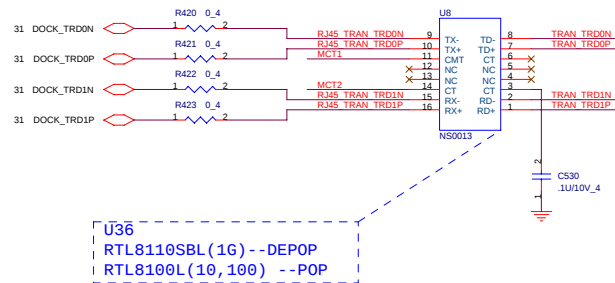
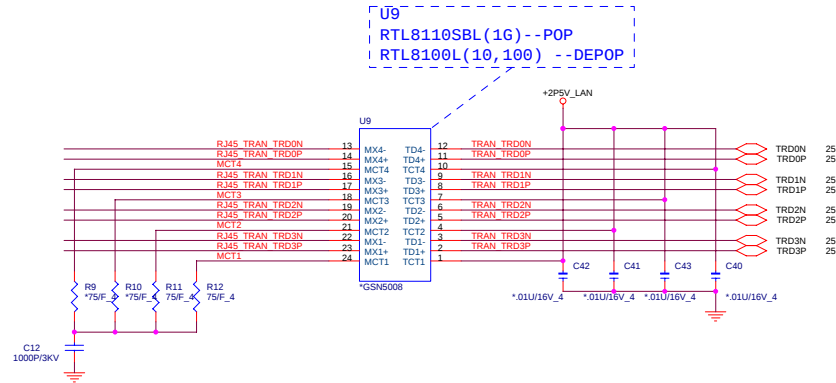
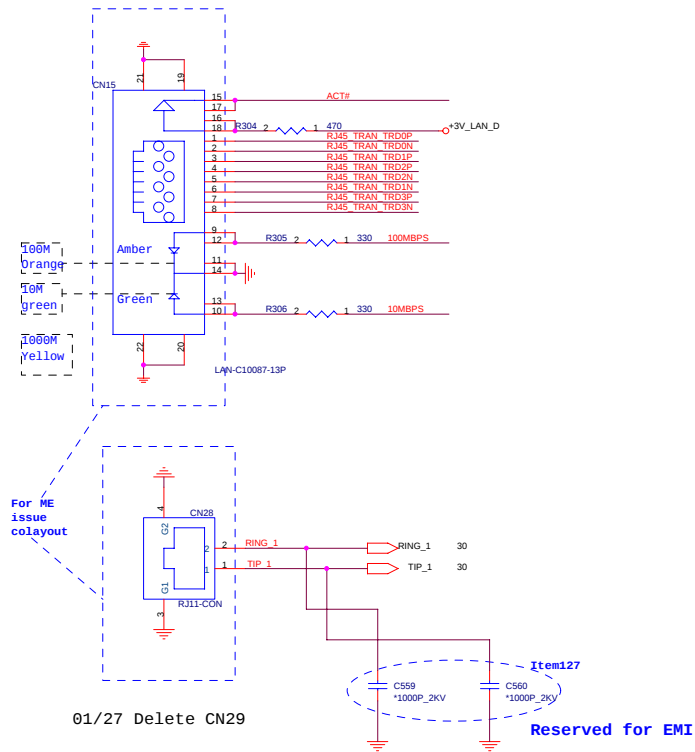
PROJECT : ED2L
Quanta Computer Inc.

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	IEEE1394	E3A
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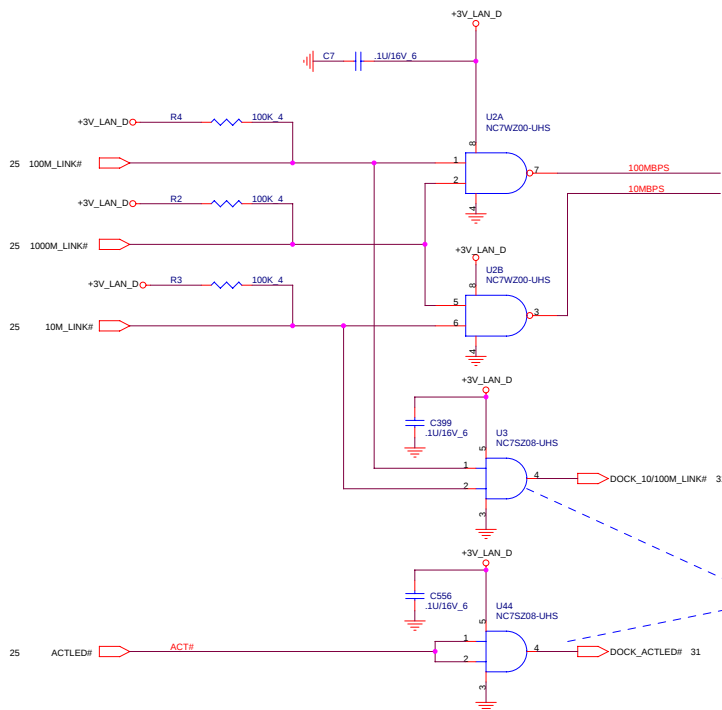
R117
RTL8110SBL (1G) --DEPOP
RTL8100L (10, 100) --POP

R116
RTL8110SBL (1G) --POP
RTL8100L (10, 100) --DEPOP



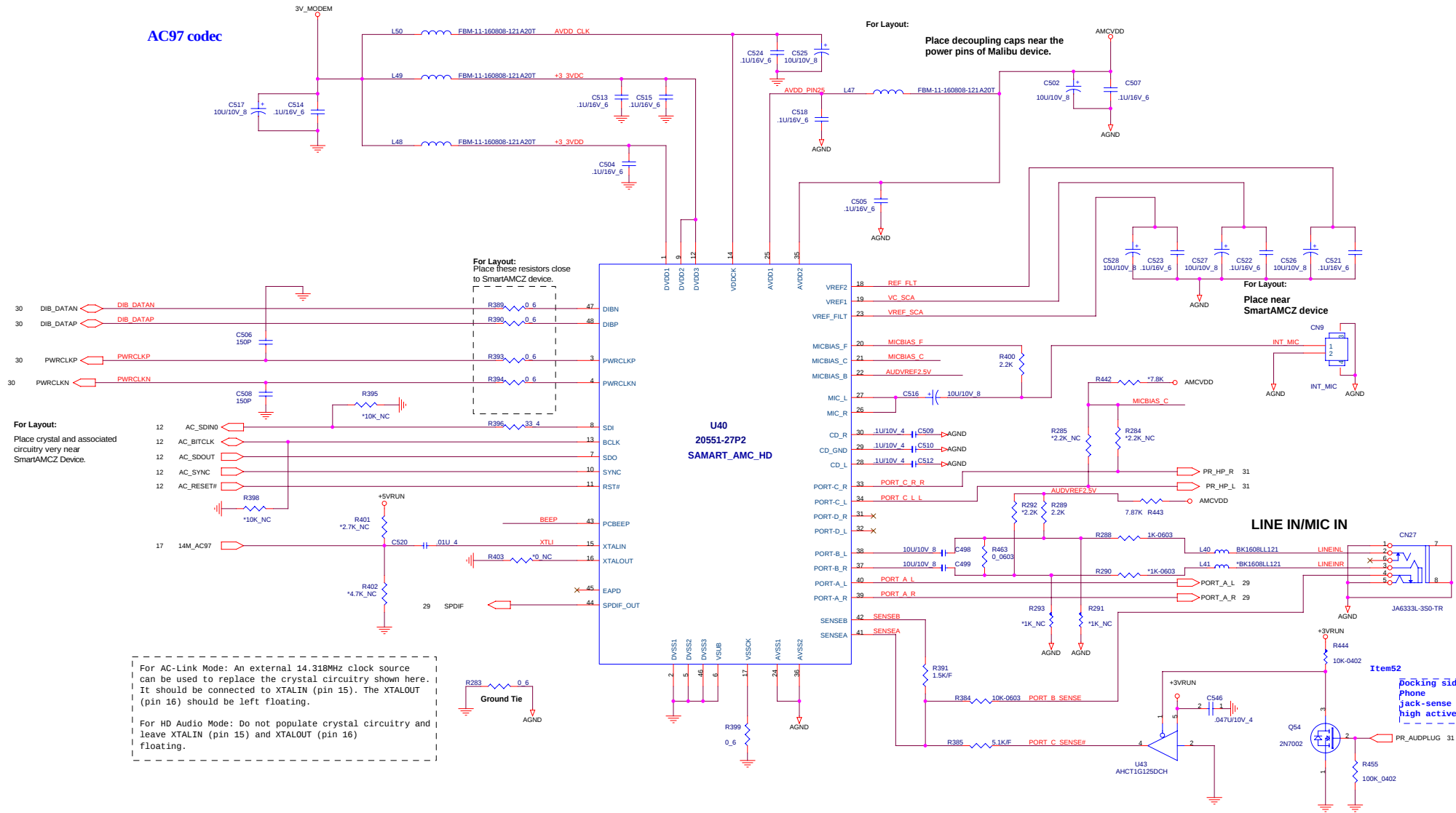


	For RTL8100C	For RTL8110SB
R116	UN-POP	POP
R117	POP	UN-POP
R139	UN-POP	POP
R127	POP	UN-POP
R113	UN-POP	POP
R114	UN-POP	POP
R115	POP	UN-POP
R108	POP	UN-POP
R124	UN-POP	POP
C151	UN-POP	POP
C138	UN-POP	POP
C157	UN-POP	POP
Q52	UN-POP	POP
Q18	UN-POP	POP
Q19	POP	UN-POP
R412	UN-POP	POP
R414	UN-POP	POP
R416	UN-POP	POP
R418	UN-POP	POP
C531	UN-POP	POP
C533	UN-POP	POP
C40	UN-POP	POP
C41	UN-POP	POP
C42	UN-POP	POP
C43	UN-POP	POP
R9	UN-POP	POP
R10	UN-POP	POP
R420	POP	UN-POP
R421	POP	UN-POP
R422	POP	UN-POP
R423	POP	UN-POP
C530	POP	UN-POP
U8	POP	UN-POP
U9	UN-POP	POP
U44	POP	UN-POP
C556	POP	UN-POP
U3	POP	UN-POP
C399	POP	UN-POP

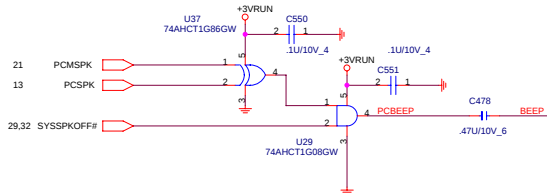


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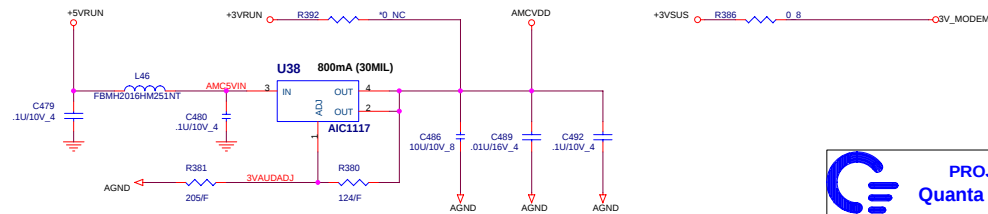
AC97 codec



BEEP



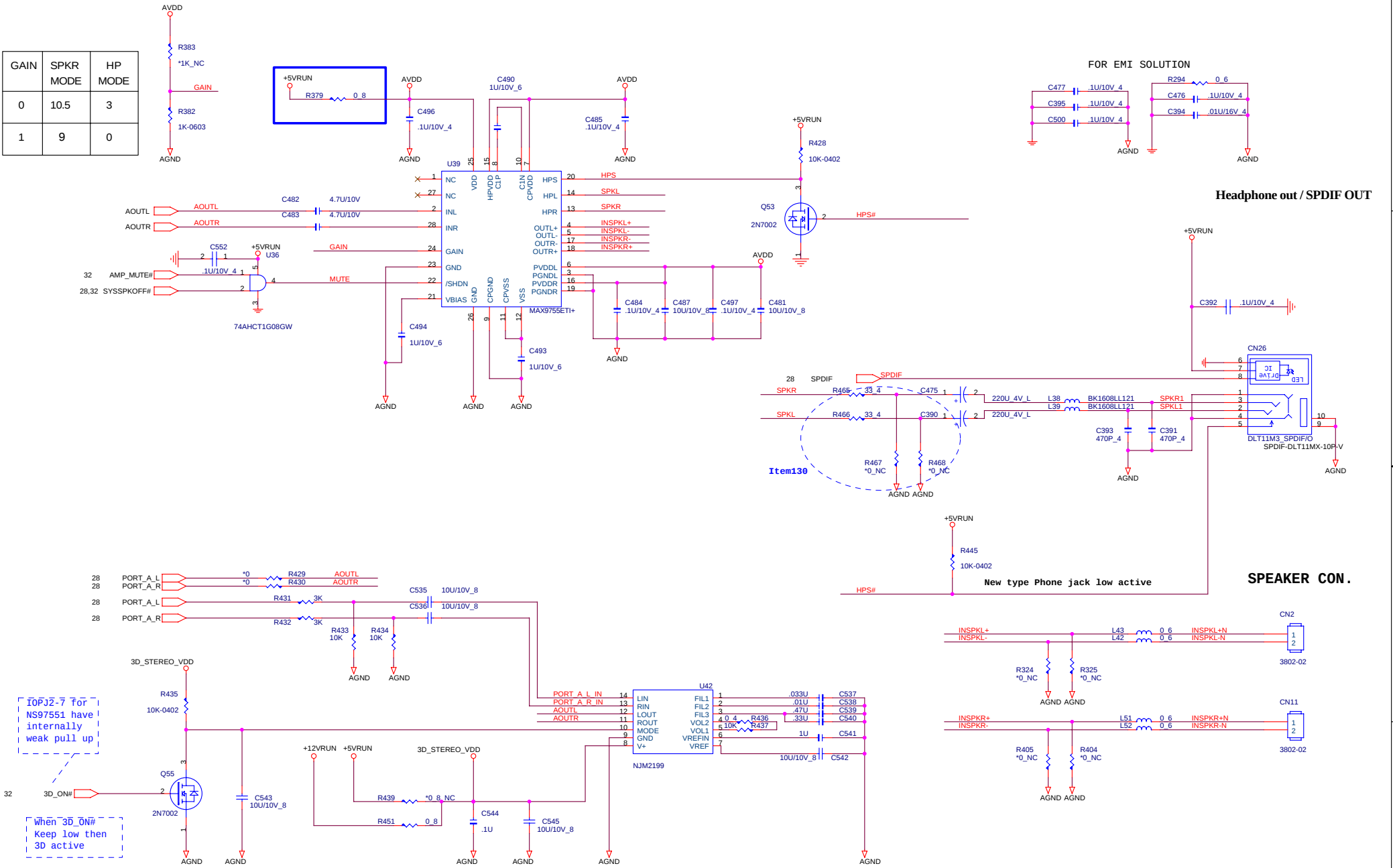
AC97 codec Power

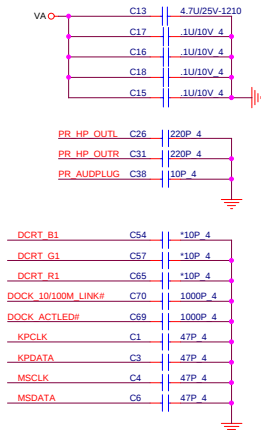
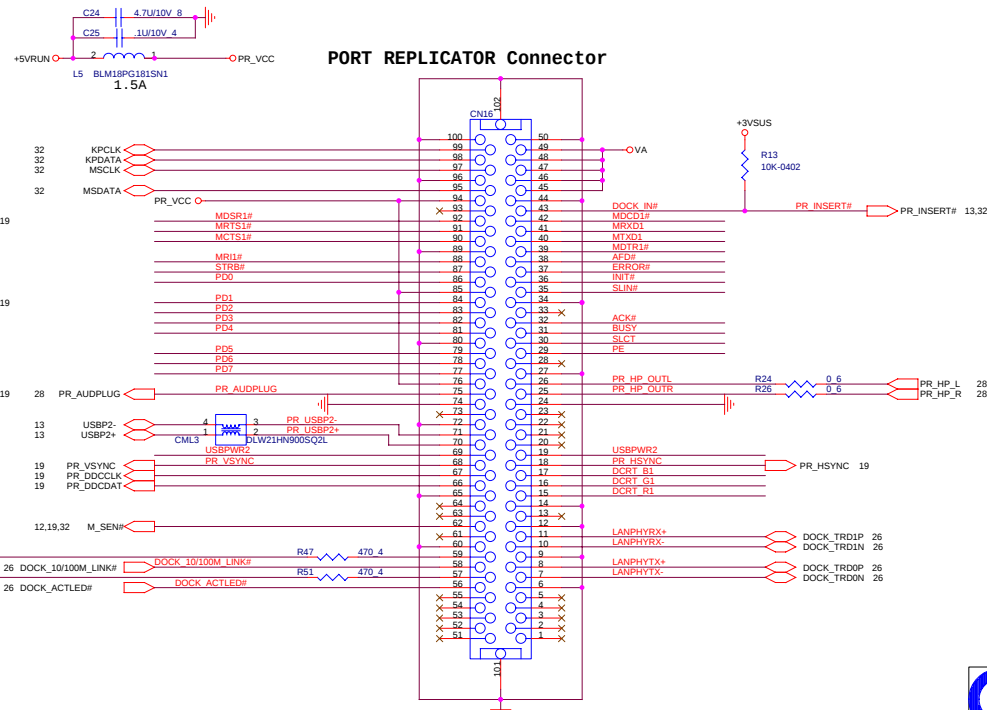
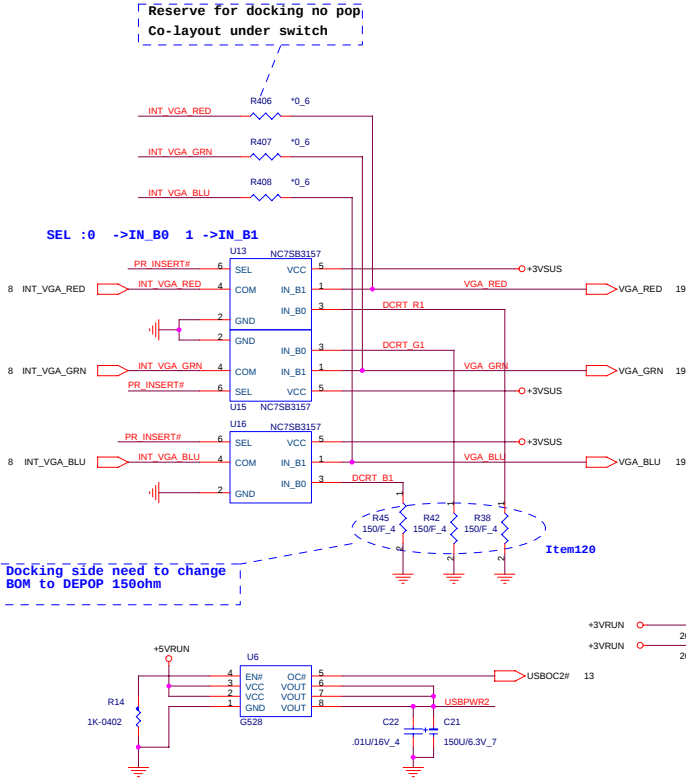
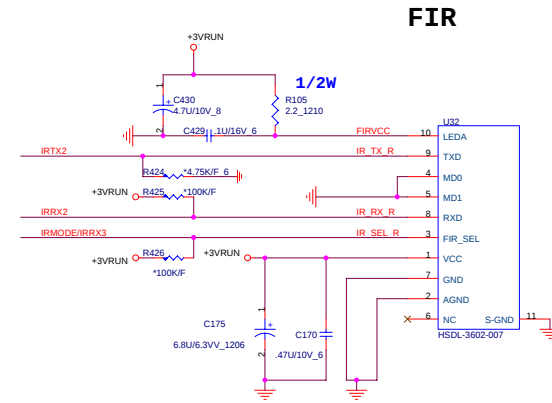
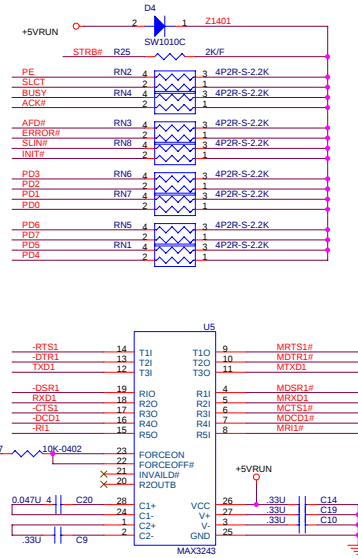
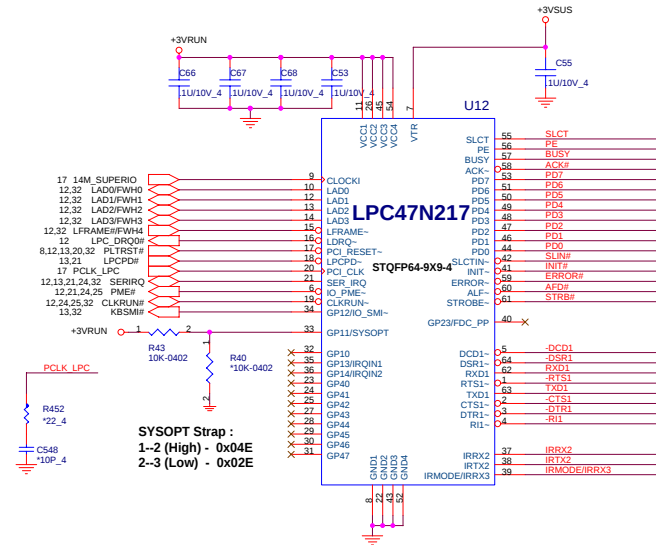


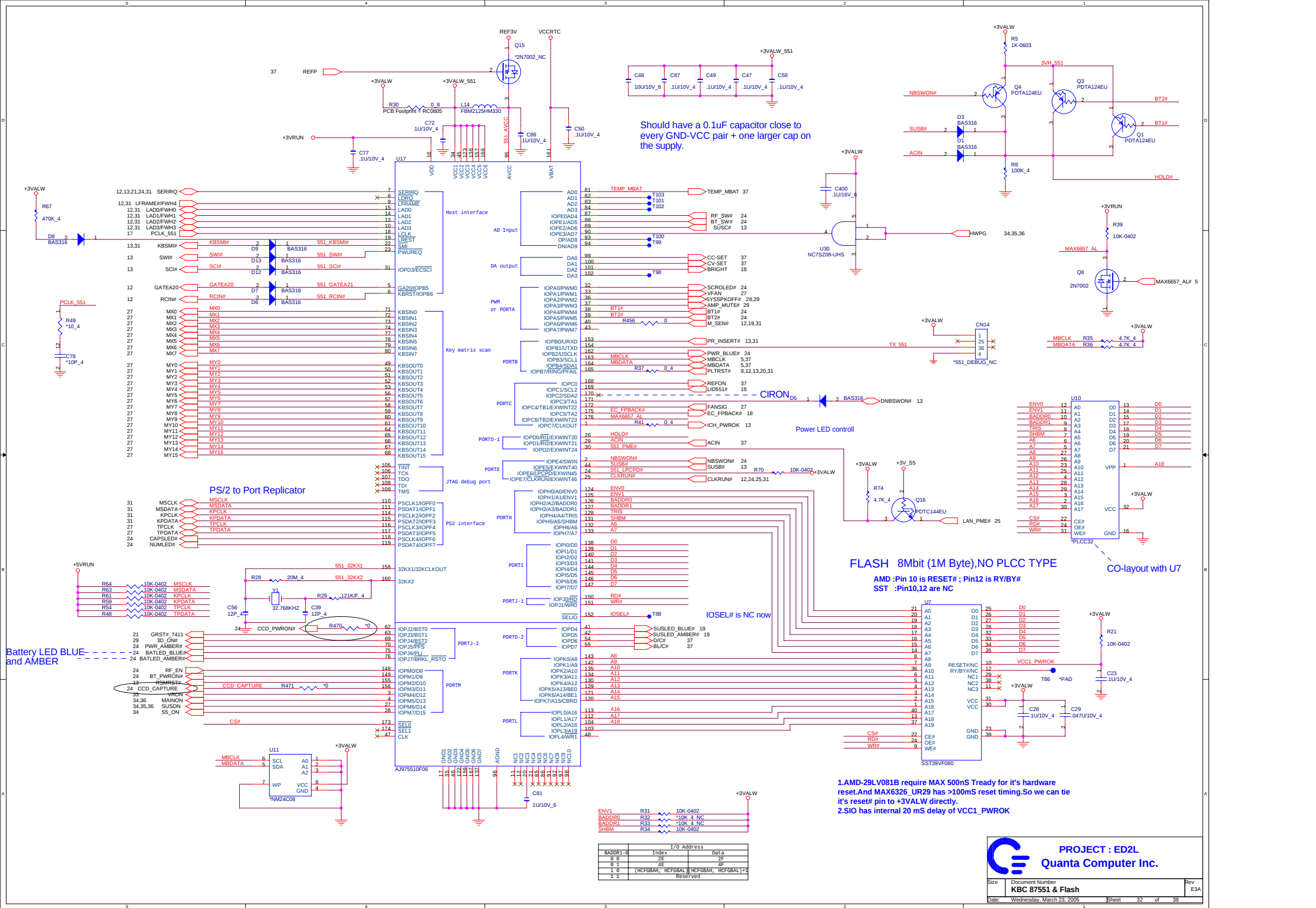
PROJECT : ED2L
Quanta Computer Inc.

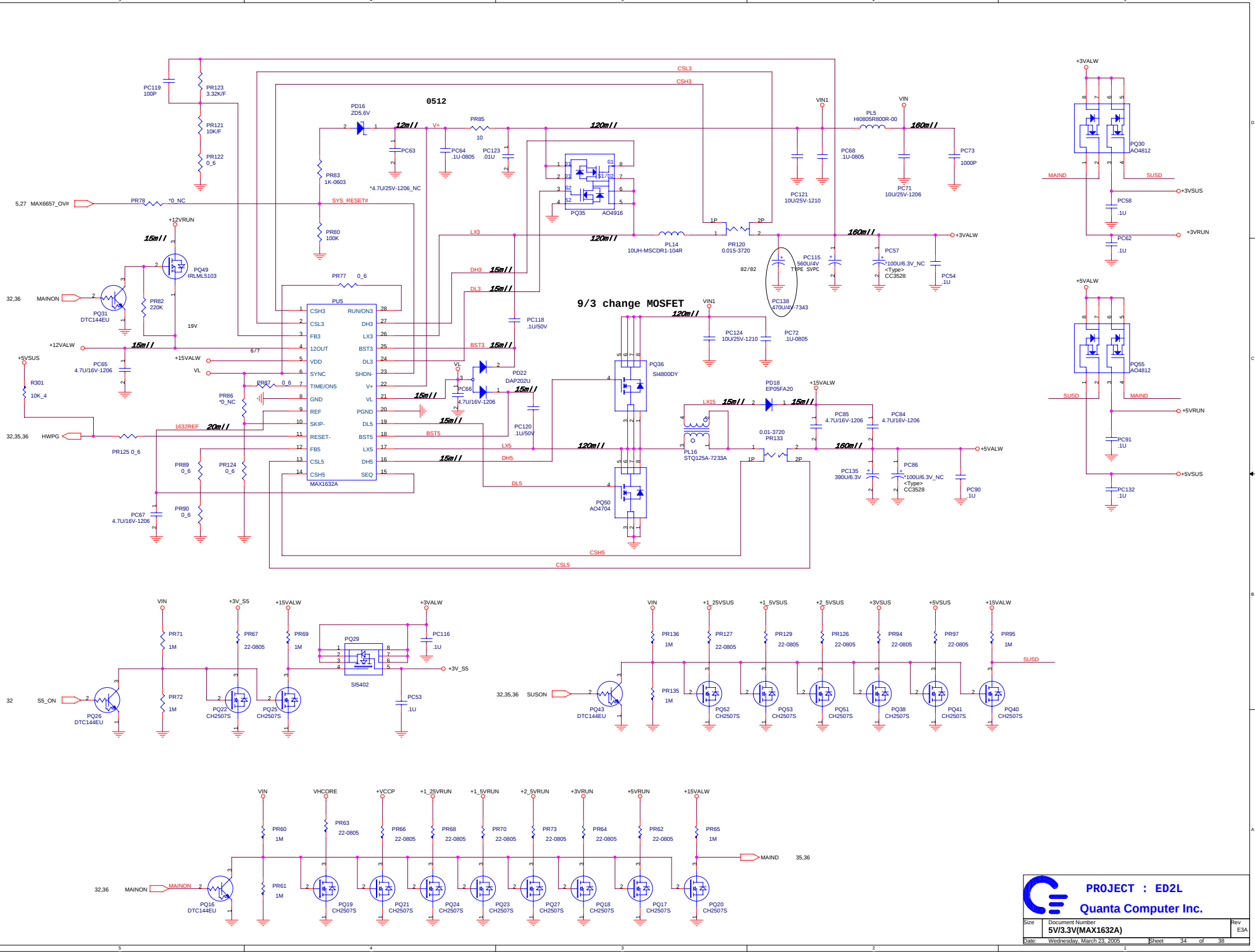
Size	Document Number	Rev
	AC97 CODEC + MODEM	E3A
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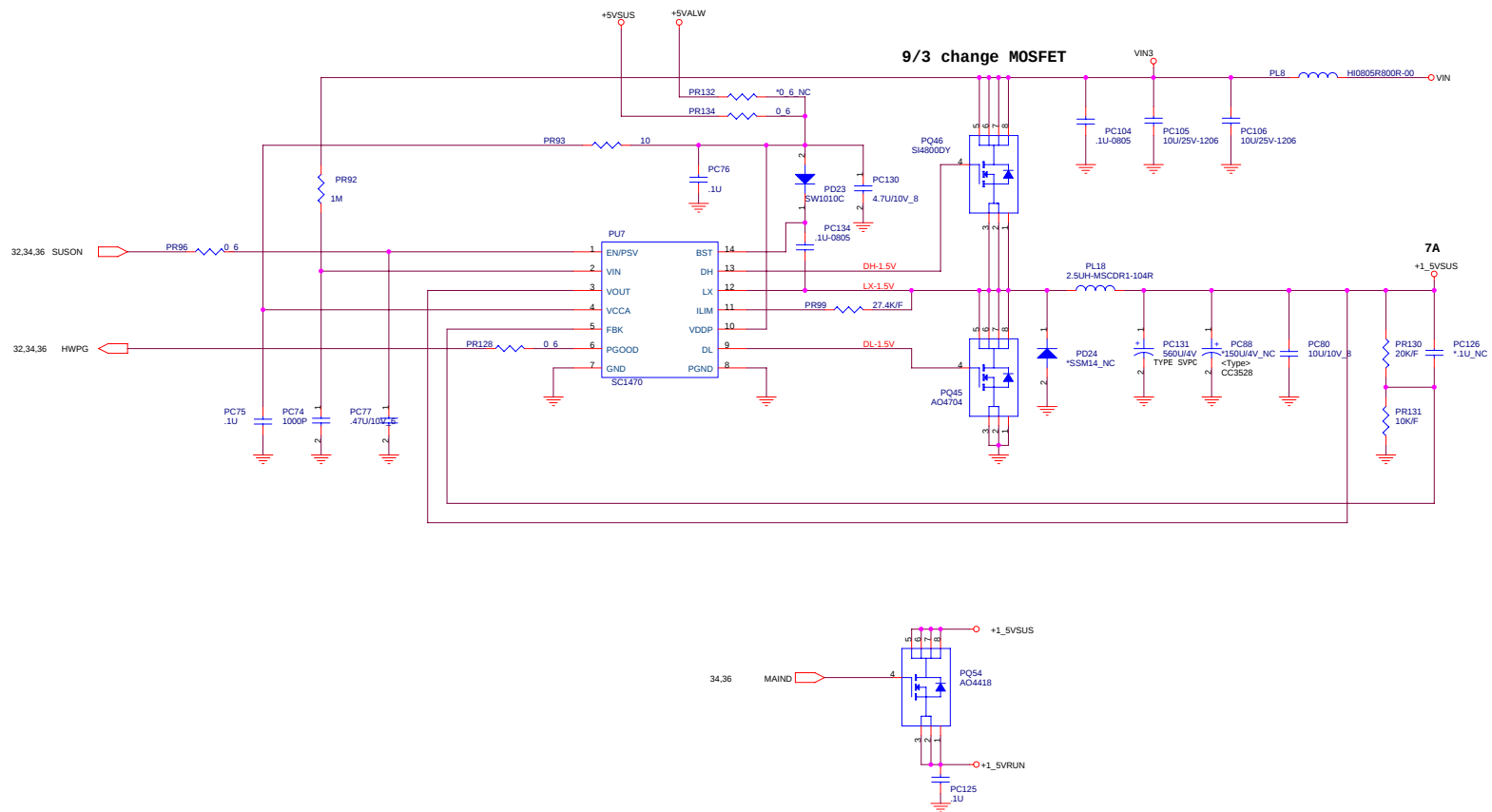
GAIN	SPKR MODE	HP MODE
0	10.5	3
1	9	0

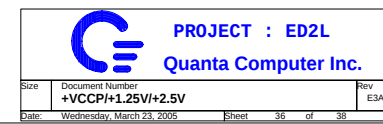


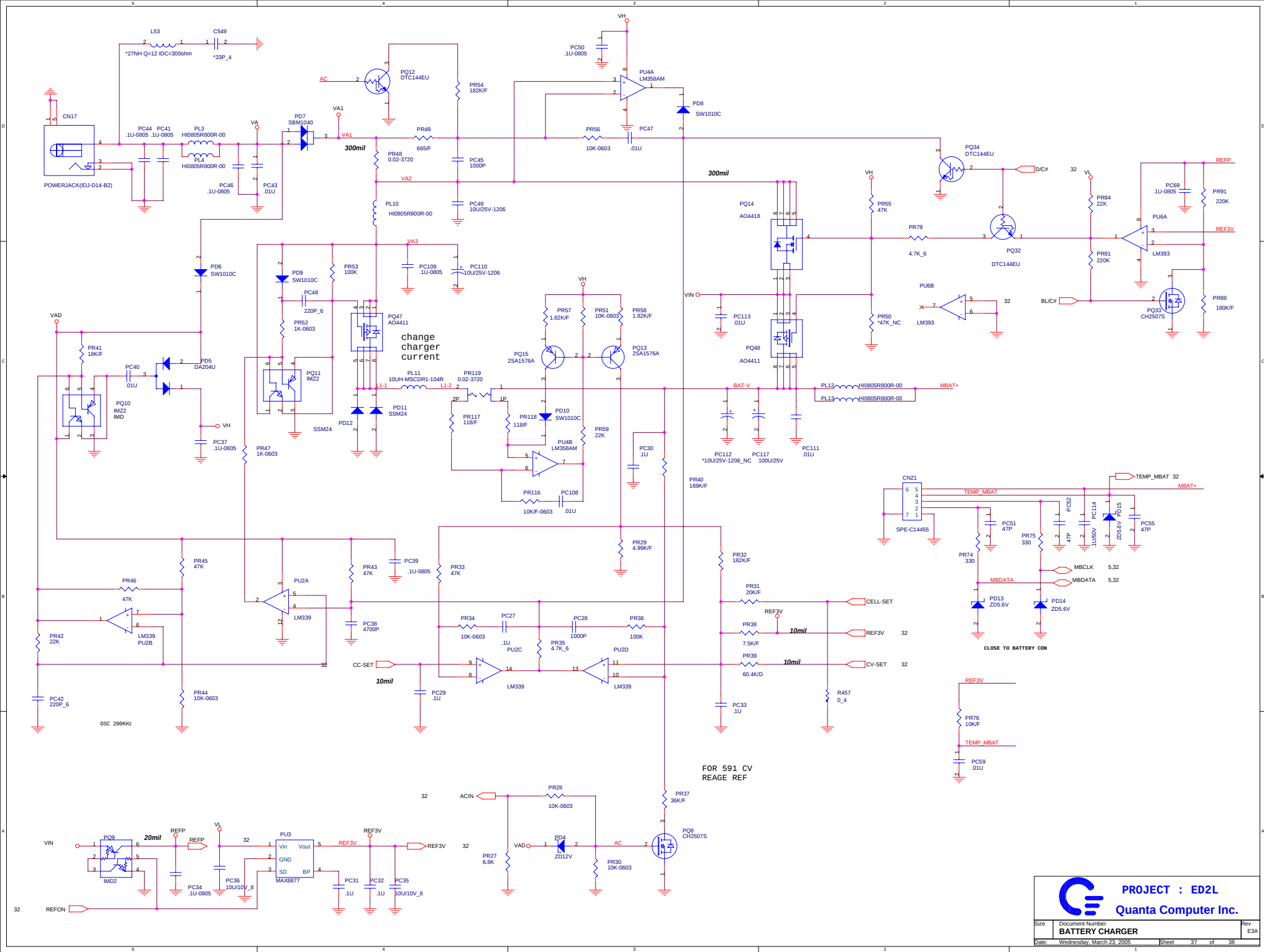












MODEL	REV	CHANGE LIST			Model Page	JM2 MB	
						FM	TO
4 3 2 1	1A	Preliminary Release			1		
					2	1A	
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QUANTA COMPUTER		PROJECT : DM3B	DOC. NO. ???	REV: 1A	ASSY:		
		APPROVED BY :	CHECKED BY:	DRAWN BY :	DATE :	SHEET	1 OF 1