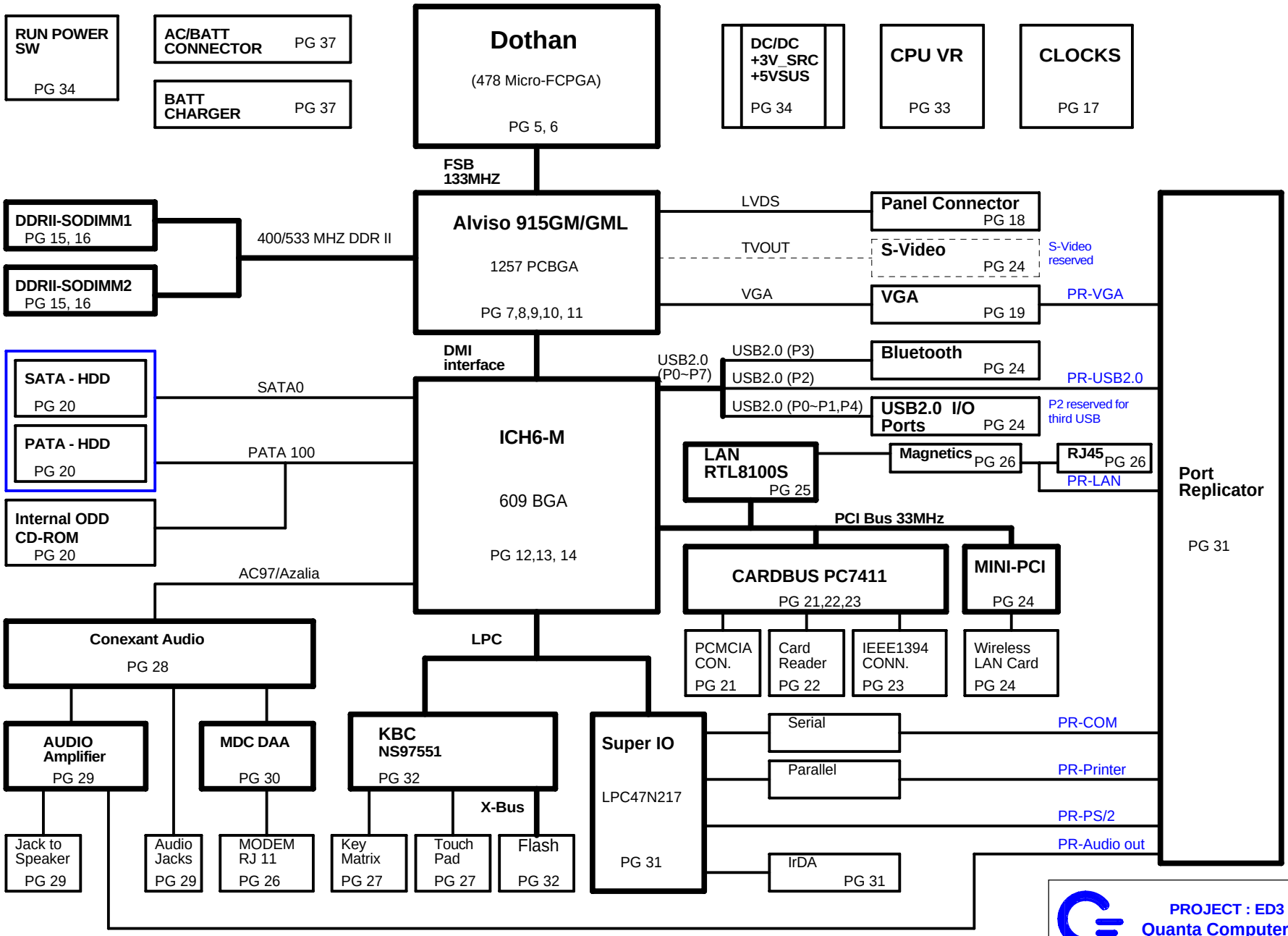
# ED3-UMA DESIGN

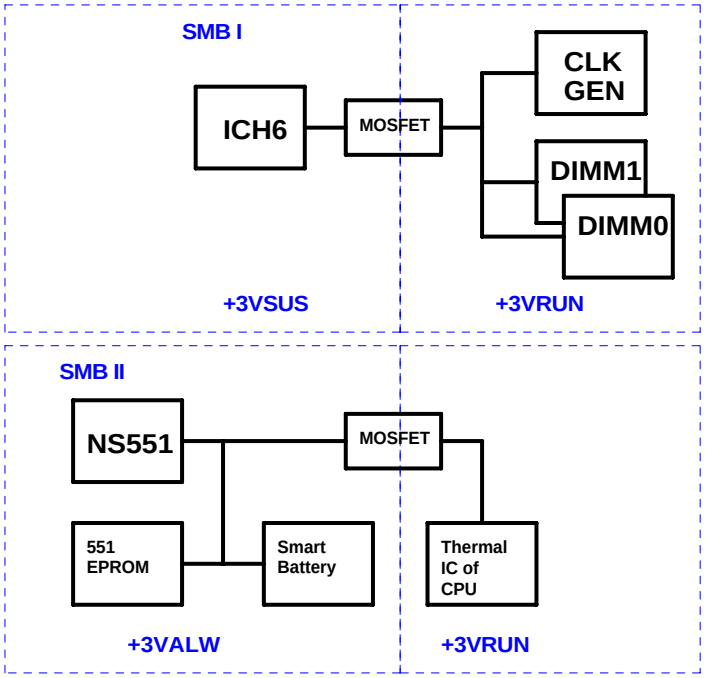
VER : B3B




PROJECT : ED3
Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | Block Diagram 1 | A3A |
| Date: Wednesday, June 22, 2005 | Sheet 1 of 38 | |

| PCI ROUTING TABLE | IDSEL | INTERUPT | DEVICE |
|---|---|---|---|
| REQ0# / GNT0# | AD24 | PIRQA# | RTL8110S |
| REQ2# / GNT2# | AD19 | PIRQB# , PIRQD# | MINI-PCI |
| REQ1# / GNT1# | AD17 | PIRQC#,PIRQD#,PIRQA# | TI 7411 |

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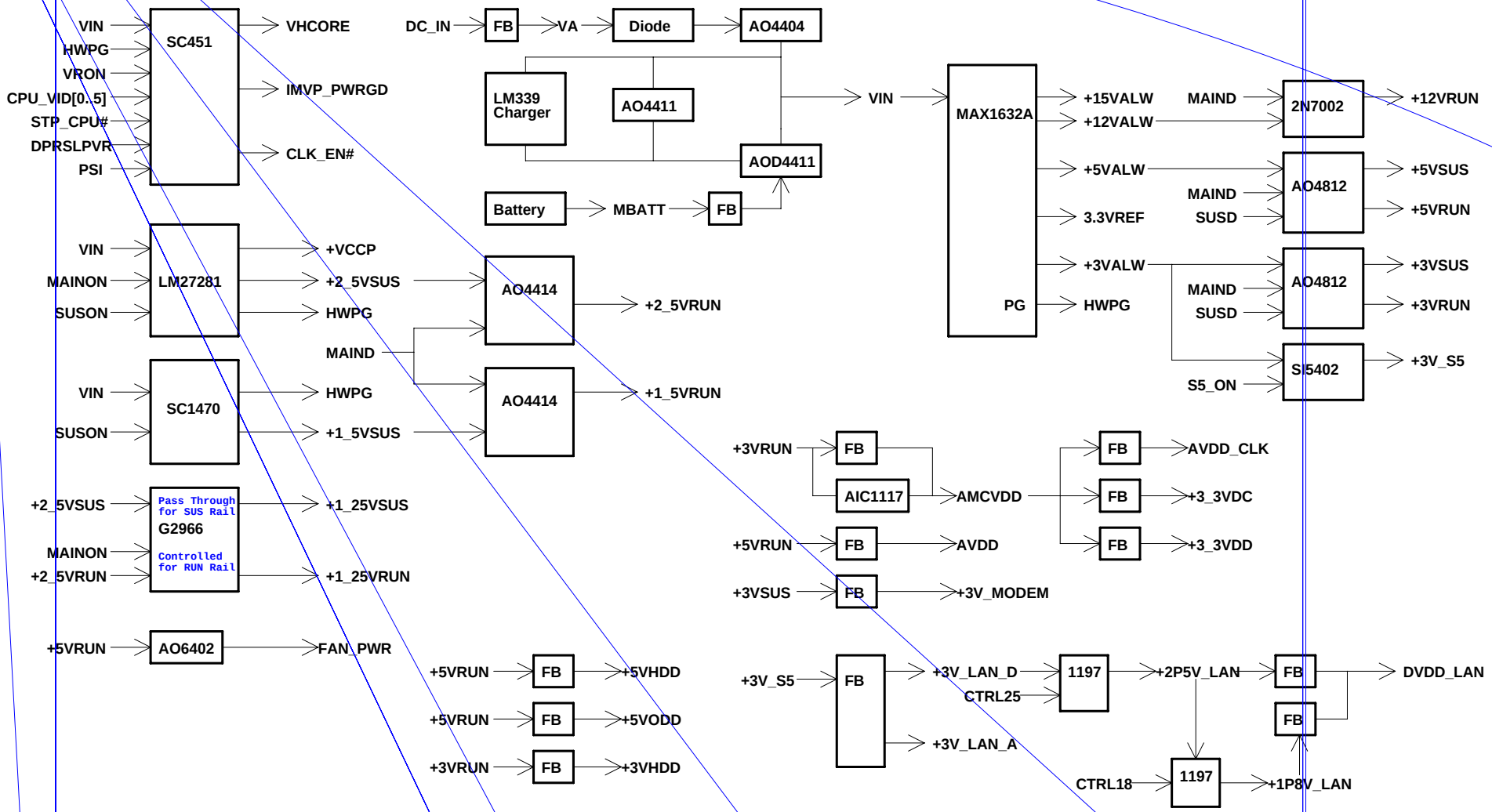
| Size | Document Number | Rev |
|---|---|---|
| | Block Diagram 2 | A3A |
| Date: Tuesday, June 14, 2005 | Sheet 2 of 38 | |

# Power Rail Flow




| Size | Document Number | Rev |
|---|---|---|
| | Block Diagram 3 | A3A |
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## INDEX

| Pg# | Description | DNI LIST |
|---|---|---|
| 1-3 | Schematic Block Diagram | |
| 4 | FRONTPAGE | |
| 5-6 | Dothan/Younah | |
| 7-11 | ALVISO GM | |
| 12-14 | ICH6M | |
| 15-16 | DDRII SO-DIMM(200P) | |
| 17 | CLOCK GENERATOR | |
| 18-19 | LCD CONN & CRT CONN | |
| 20 | SATA & IDE (HDD&CD_ROM) | |
| 21-23 | PCI7411 & CONN & IEEE1394 | |
| 24 | MINI-PCI & MDC CONN | |
| 25-26 | LAN & LAN Conn. | |
| 27 | TOUCH PAD & FAN&KB | |
| 28 | Azilia AC97 CODEC | |
| 29 | Audio Amplifier | |
| 30 | MODEM | |
| 31 | DOCKING & SIO & FIR | |
| 32 | KBC PC97551 | |
| 33 | CPU Power | |
| 34 | 3.3V/5V/12V/15V | |
| 35 | 1.5VSUS/1.5VRUN | |
| 36 | +VCCP/+1.25V/+2.5V | |
| 37 | Battery & Charger | |

## Power and Ground

| New Label | NOTE | Description | Control Signal or Source |
|---|---|---|---|
| VA | | AC ADAPTER (20V) | |
| VIN | | MAIN POWER (10~20V) | |
| MBATT | | MAIN BATTERY + (10~17V) | |
| +15VALW | | +15V ALWAYS | |
| +12VALW | | +12V ALWAYS | |
| +12VRUN | | +12V RUN | MAINON |
| | | | |
| +5VALW | | +5V ALWAYS & KBC POWER | |
| +5V_S5 | NO USE | THIS POWER WILL BE TUNEED OFF IN S5 BATTERY MODE | S5_ON |
| +5VSUS | | +5V S5 CONTROLED POWER | SUSD |
| +5VRUN | | +5V S3 CONTROLED POWER | MAIND |
| +5VHDD | CONNECT TO +5VRUN DIRECTLY | +5V HDD POWER | +5VHDD_EN# |
| +5VODD | CONNECT TO +5VRUN DIRECTLY | +5V ODD POWER | +5VMOD_EN# |
| +5VFDD | NO USE | EXTERNAL FDD POWER (5V) | +5VFDD_EN# |
| FAN_PWR | | FAN POWER (5V) | VFAN, MAX6657_OV# |
| VDDA | | Amplifier Power 5V RUN Plane | +5VRUN |
| AMCVDD | | AC97 Code DAC Power 3VRUN | +3VSUS |
| 3V_MODEM | | MODEM Power 3VSUS | +5VRUN or +3VRUN |
| | | | |
| +3VALW | | 8051 POWER (3V) | |
| +3V_S5 | | THIS POWER WILL BE TUNEED OFF IN S5 BATTERY MODE | S5_ON |
| +3VSUS | | SLP_S5# CTRLD POWER | SUSD |
| +3VRUN | | SLP_S3# CTRLD POWER | MAIND |
| +3VHDD | CONNECT TO +3VRUN DIRECTLY | SATA HDD Power | +3VHDD_EN# |
| | | | |
| +3V_LAN_D | | LAN Digital Power | +3V_S5 |
| +3V_LAN_A | | LAN Analog Power | +3V_S5 |
| +2P5V_LAN | | LAN Analog Power | +3V_LAN_D (+3V_S5) |
| DVDD_LAN | | LAN Digital Power 1.8 or 2.5V | +2P5V_LAN(+3V_S5) |
| RTCVCC | | RTC & PCL POWER | |
| REF3V | | | |
| | | | |
| +2_5VSUS | | | SUSON |
| +2_5VRUN | | | MAIND |
| +1_8VSUS | NO USE | | |
| +1_8VRUN | NO USE | | +2_5VRUN |
| +1_8V_M24 | NO USE | | +1_8VSUS or +1_8VRUN |
| +1_5V_S5 | | THIS POWER WILL BE TUNEED OFF IN S5 BATTERY MODE | S5_ON |
| +1_5VSUS | | | SUSON |
| +1_5VRUN | | AGP I/O POWER | MAIND |
| +1_25VSUS | | SMDDR_VTERM | +2_5VSUS |
| +1_25VRUN | | | MAINON |
| VGA1_2V | NO USE | ATI VGA 1.2V | +2_5VRUN |
| VGACORE | NO USE | ATI VGA CORE 1.0/1.2V | MAINON, POW_SW |
| +VCCP | | AGTL+ POWER (1.05V) | MAINON |
| VHCORE | | CPU CORE POWER (1.25/1.15V) | VR_ON, HWPG |
| | | | |
| GND | ALL PAGES | DIGITAL GROUND | |
| AGND | Page 28,29 | AUDIO GND | |
| GNDP | NO USE | CPU POWER GND | |
| CGNDP | NO USE | CHARGER GND | |
| DC_GND | DC Jcak | DC/DC POWER GND | |
| LANGND | NO USE | COMBO CONN GND | |

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| | Index | A3A |
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CT_0505: Change footprint to BGA479M-SOCKET from L100505 from MPGA479M

U31A

**Dothan**

1 OF 3

Dothan Processor

HA#[3..31] — HD#[0..63]

REQUEST PHASE SIGNALS — DATA PHASE SIGNALS — ERROR SIGNALS — ARBITRATION PHASE SIGNALS — SNOOP PHASE SIGNALS — RESPONSE PHASE SIGNALS — PC COMPATIBILITY SIGNALS — DIAGNOSTIC & TEST SIGNALS — EXECUTION CONTROL SIGNALS — THERMAL DIODE

G1: NC for Dothan and DPRSTP# for Yonah

15 MIL

10 mil trace / 10 mil space

U18 MAX6657

| ITP disable guidelines | | | |
|---|---|---|---|
| Signal | Resistor Value | Connect To | Resistor Placement |
| TDI | 150 ohm +/- 5% | VTT | Within 2.0" of the CPU |
| TMS | 39 ohm +/- 5% | VTT | Within 2.0" of the CPU |
| TRST# | 680 ohm +/- 5% | GND | Within 2.0" of the CPU |
| TCK | 27 ohm +/- 5% | VTT | Within 2.0" of the CPU |
| TDO | Open | VTT | Within 2.0" of the CPU |

Note: Populate R58, R62 when ITP connector is populated.

PROJECT : ED3

Quanta Computer Inc.

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|---|---|---|
| | Dothan (HOST) | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 5 of 38 | |

Place pulldown resistors within 0.5" of COMP pins

**18mils Trace Width of COMP0,2**
**5mils Trace Width of COMP1,3**

Place voltage divider within 0.5" of GTLREF pin

**Trace as Wider as possible.**

Removed +1_8VRUN

## VHCORE

Total caps = 1670 uF > 1430 uF (Intel Recommendation)
ESR = 9m ohm/4 // 5m ohm/35 ---> = 0.1343m ohm

C, mF---------ESR, mW----------ESL, nH
1 x 150 mF-----42 mW (typ) / 2--------2.5 nH / 12
10 x 0.1 mF----16 mW (typ) / 10-------0.6 nH / 10

Dothan 2 OF 3

POWER, GROUND, RESERVED SIGNALS

Dothan Processor

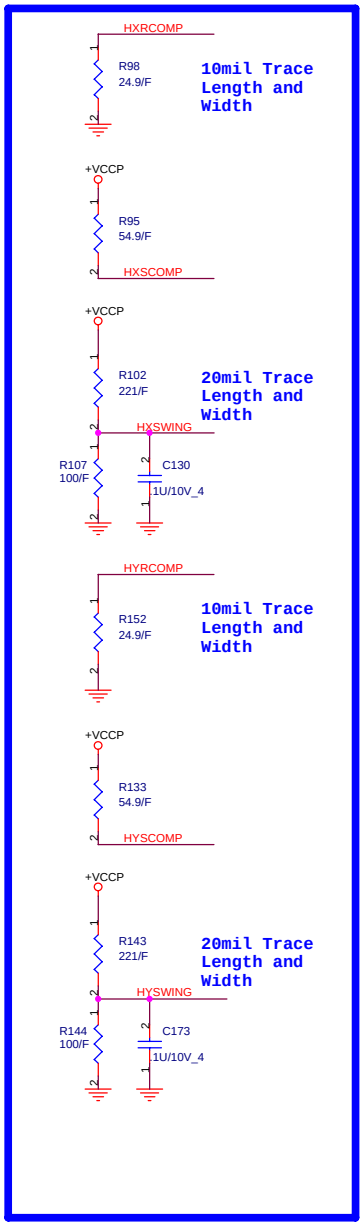
Dothan 3 OF 3

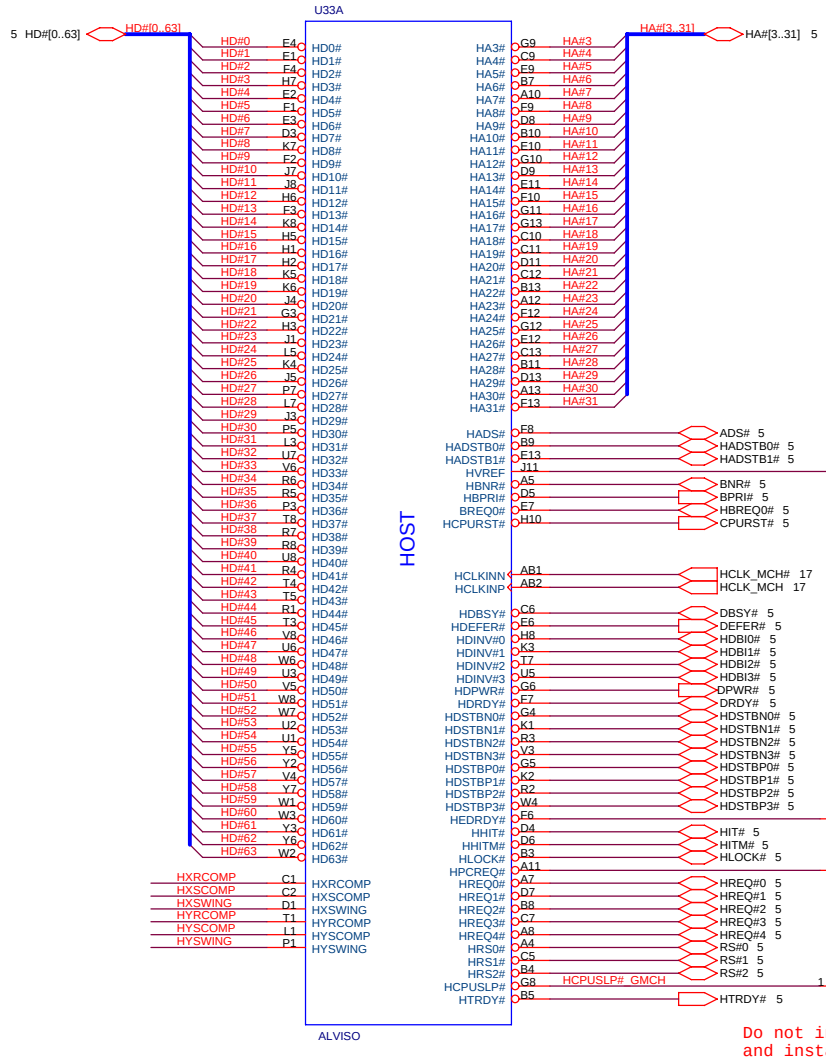
POWER, GROUND AND NC

VID

Dothan Processor

| | |
|---|---|
| PROJECT : | ED3 |
| Quanta Computer Inc. | |
| Size | Document Number: Dothan (Power) — Rev: A3A |
| Date: Wednesday, June 15, 2005 | Sheet 6 of 38 |

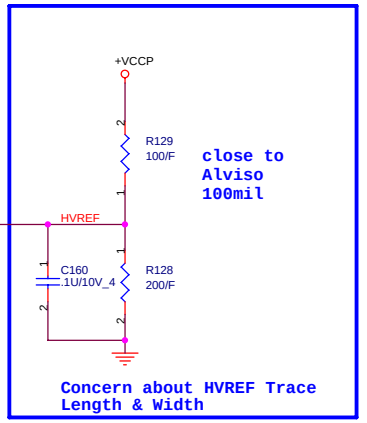



CT_0505: Change footprint to mbga1257-intel-alviso from MBGA-1257

CT_0513: Install R89 0 ohm.

Do not install R89 for Dothan-A and install for Dothan-B




| | | |
|---|---|---|
| PROJECT : ED3 | | |
| Quanta Computer Inc. | | |
| Size | Document Number: Alviso (HOST) | Rev: A3A |
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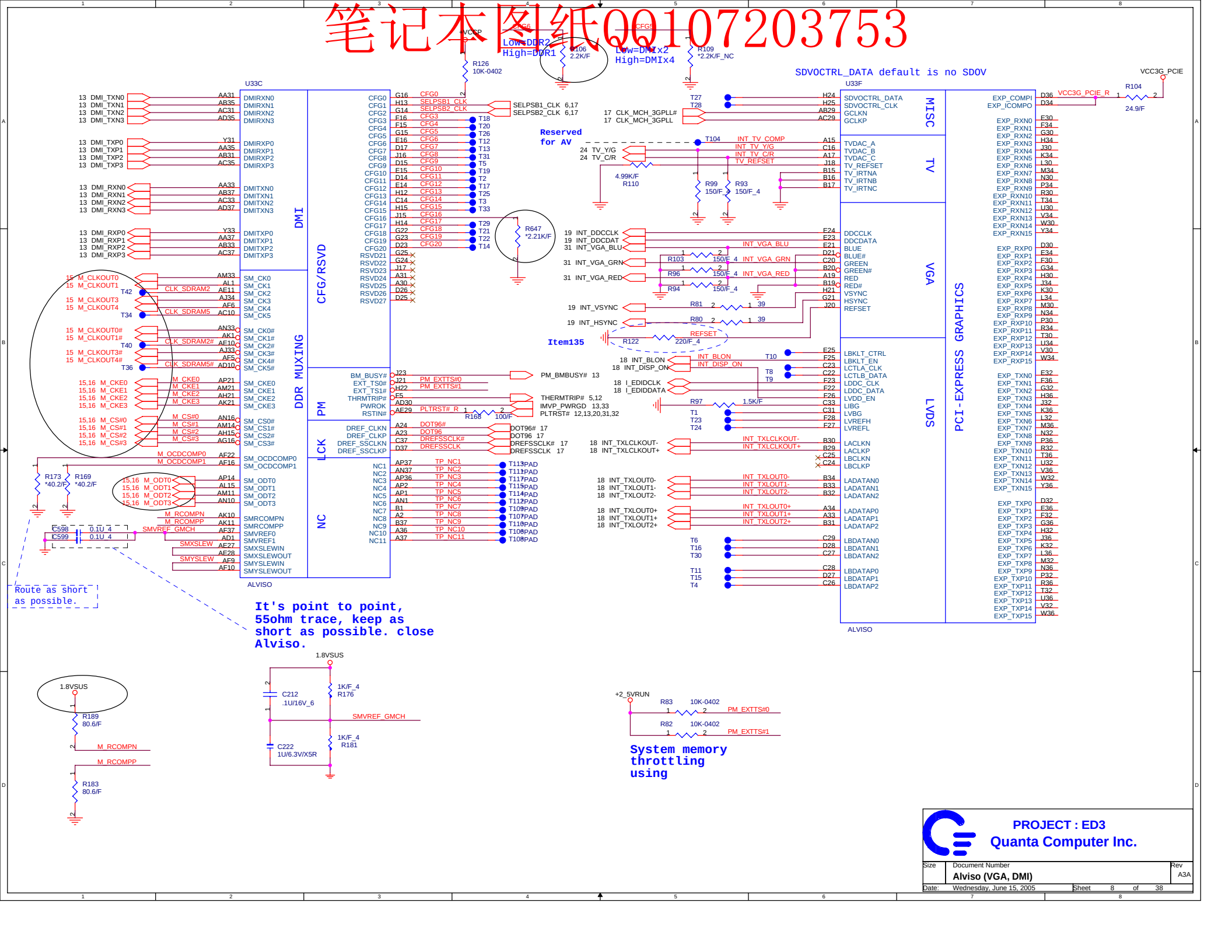

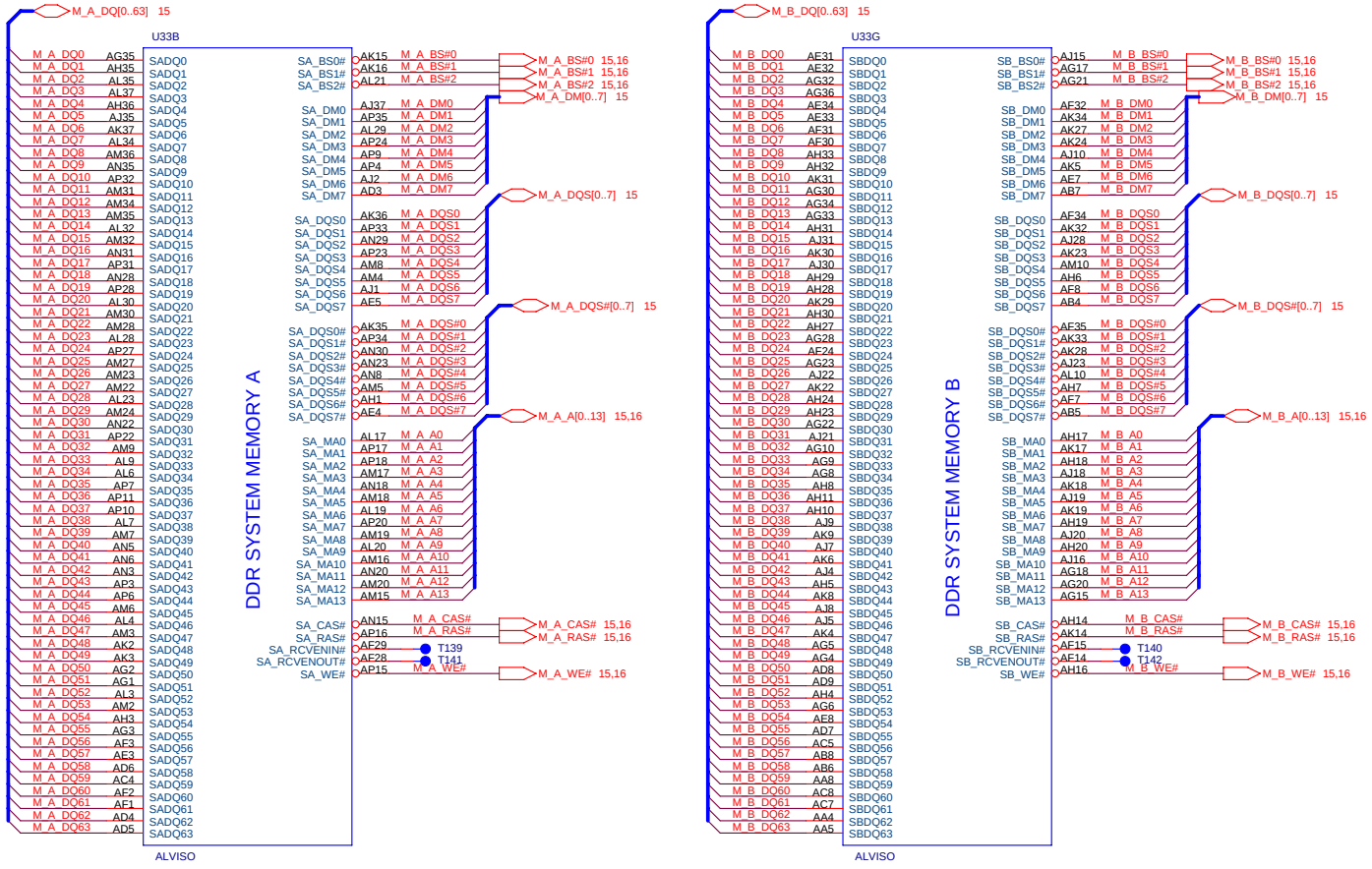


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| Size | Document Number | Rev |
|---|---|---|
| | Alviso (DDR) | A3A |
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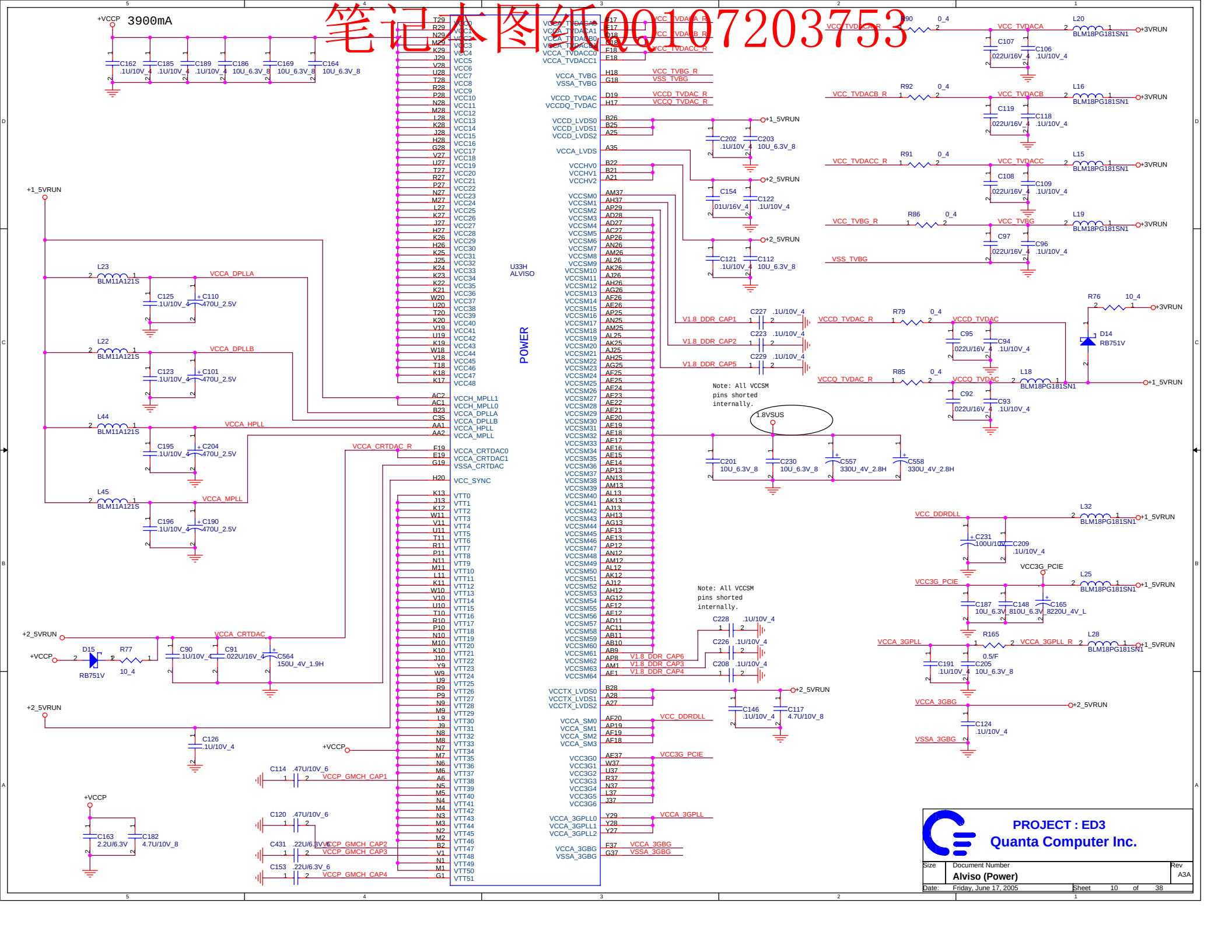

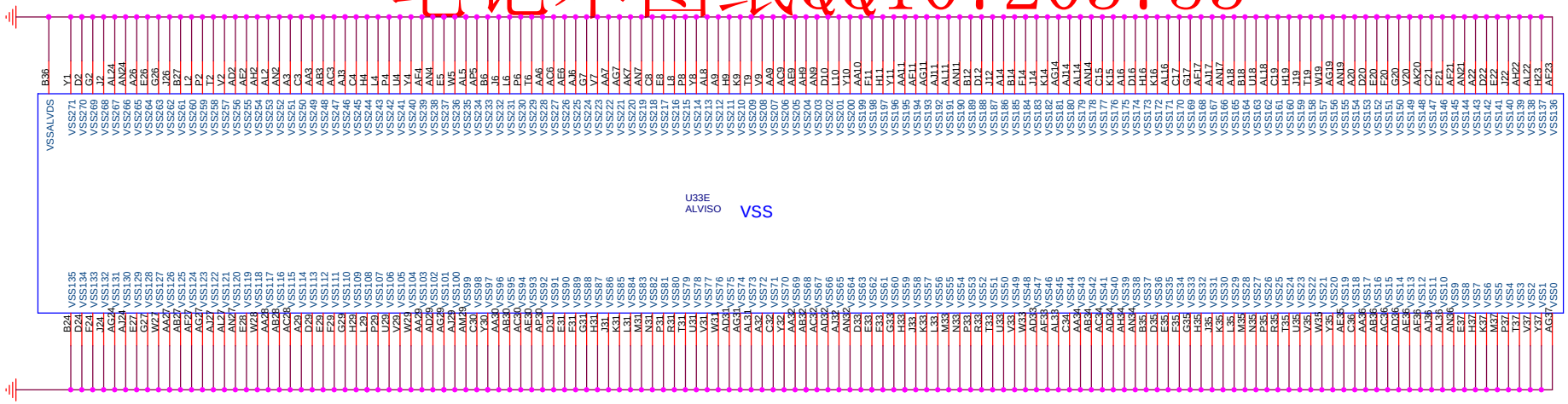


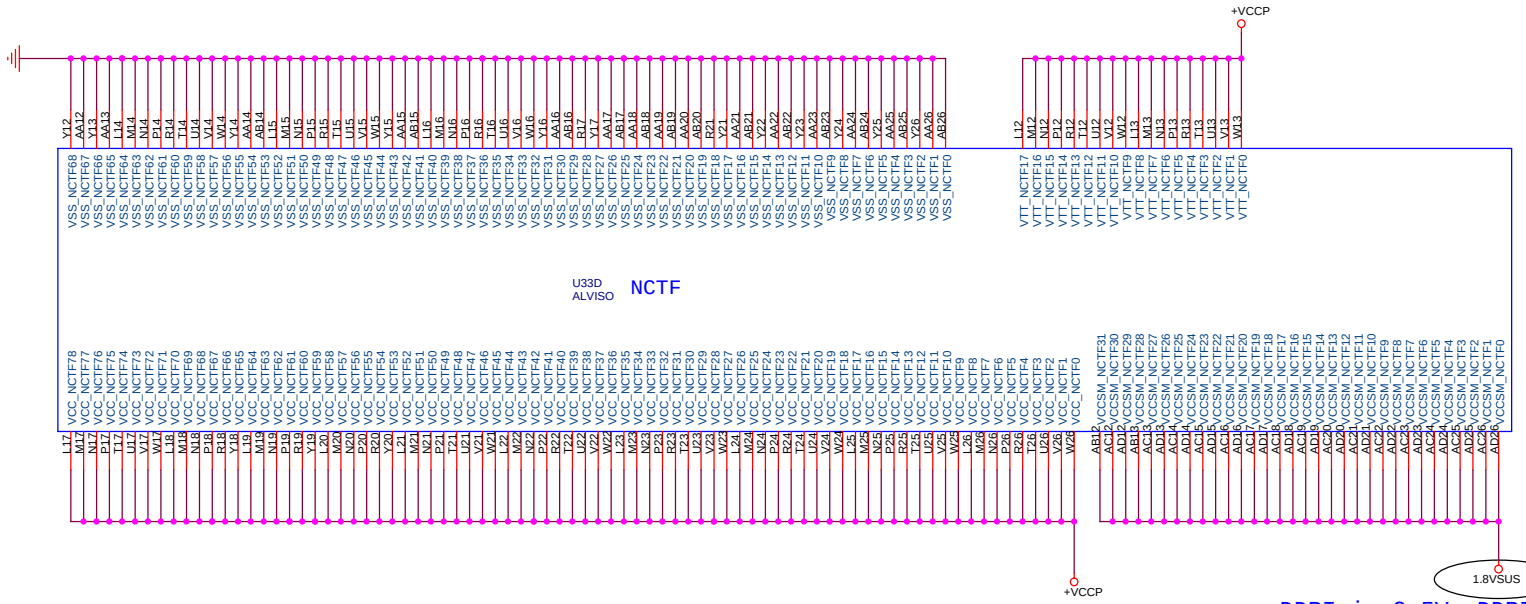


DDRI is 2.5V, DDRII is 1.8V.

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| | Alviso (VSS, NCTF0 | A3A |
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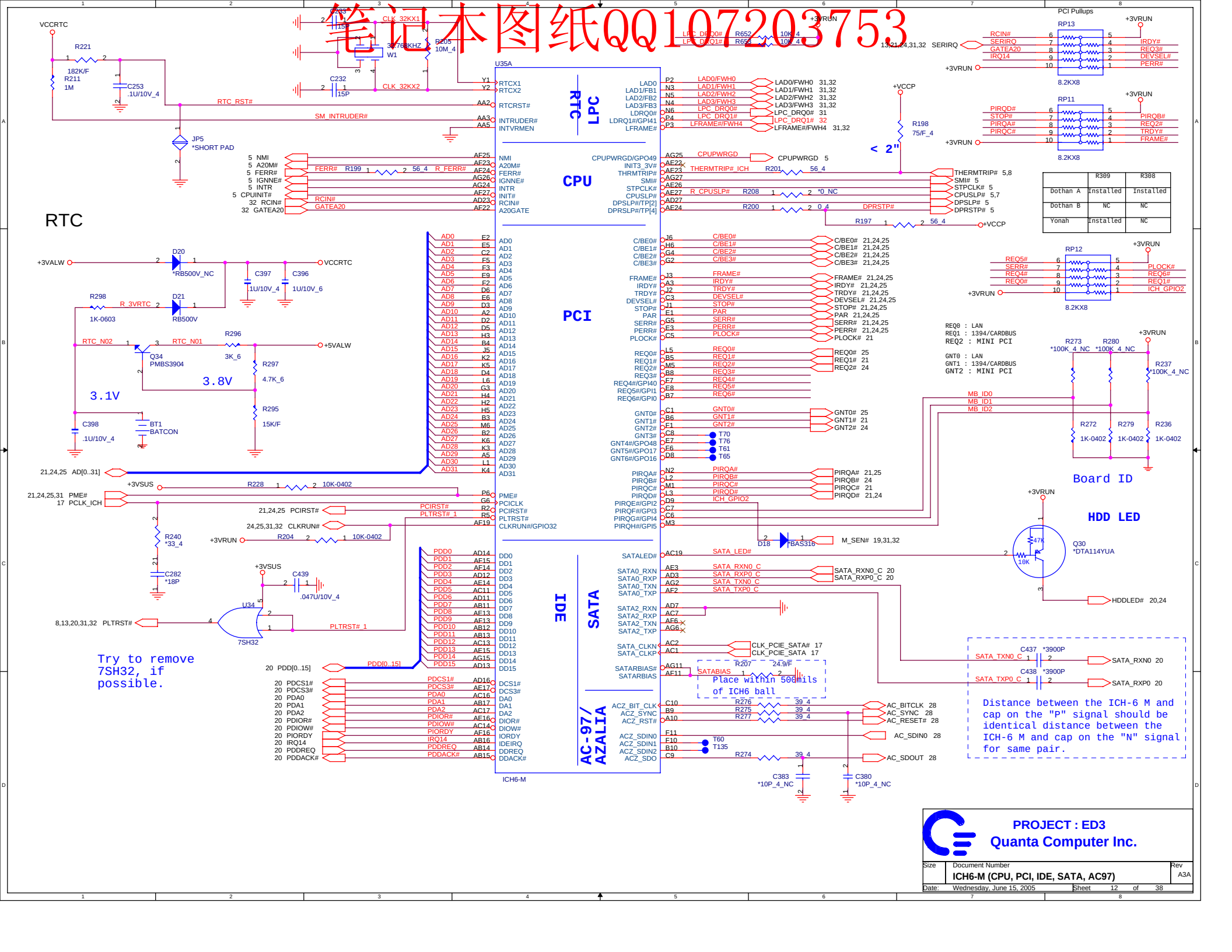

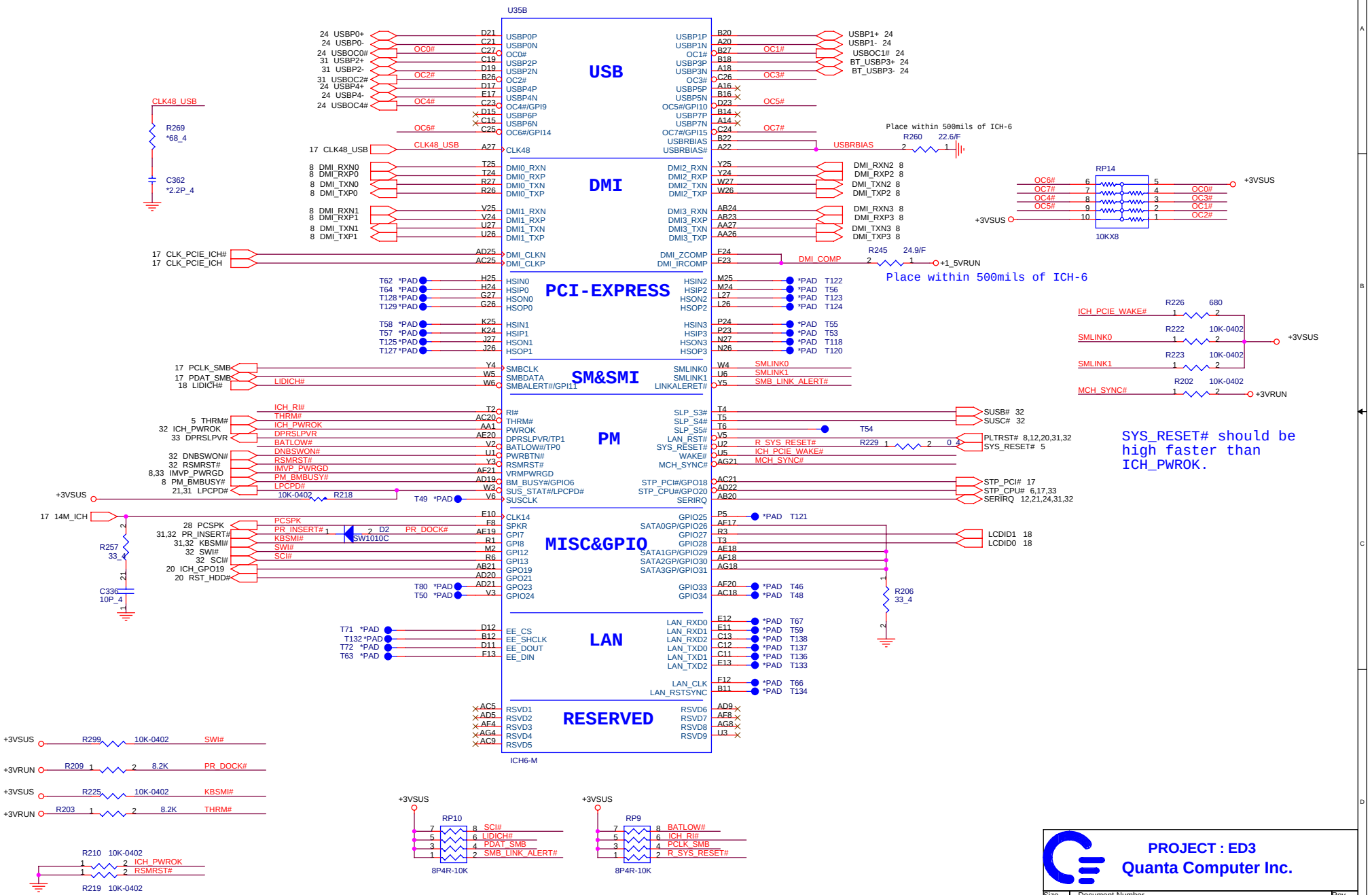


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| Size | Document Number | Rev |
|---|---|---|
| | ICH6-M (USB, DMI, LPC) | A3A |
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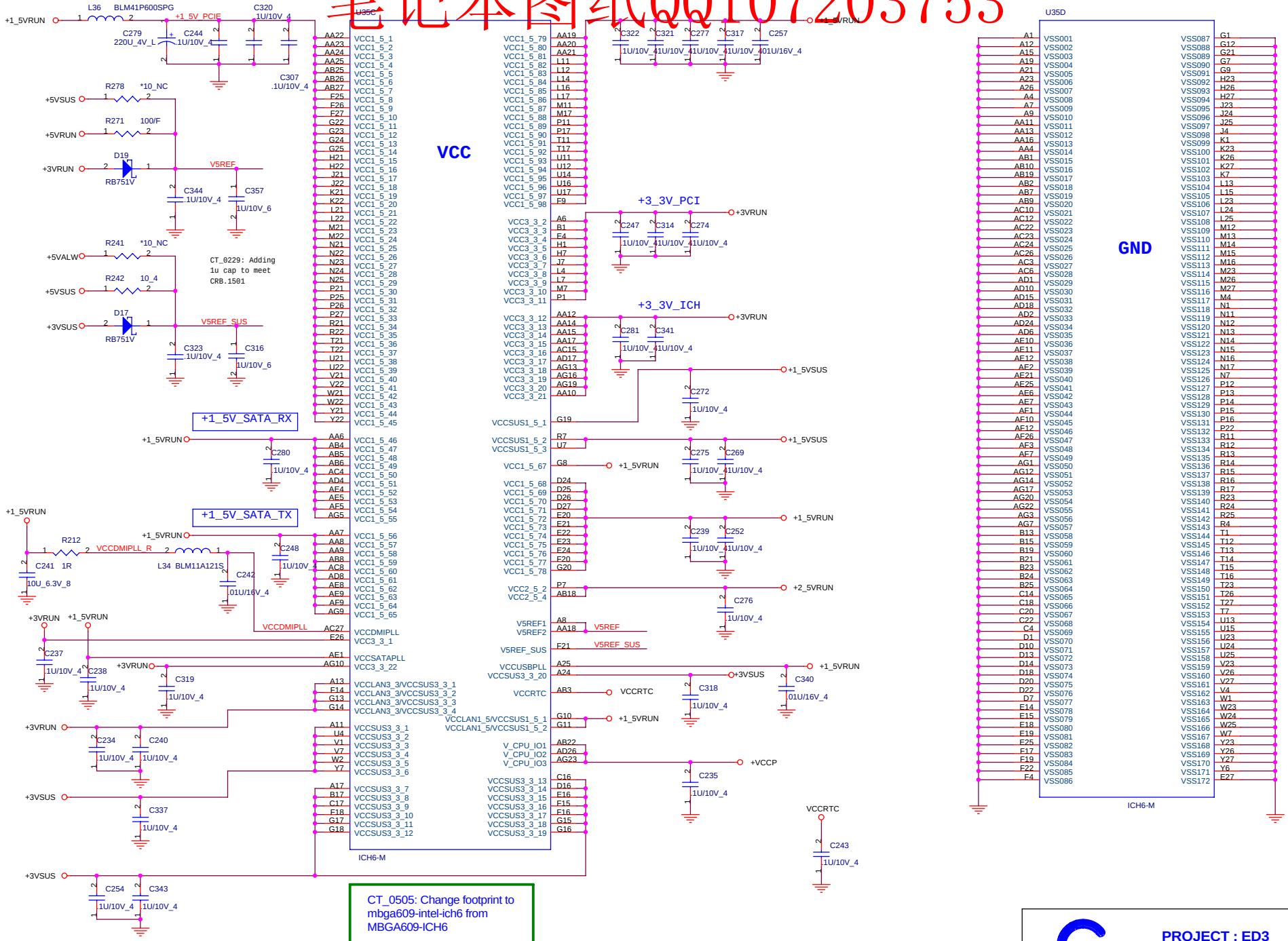


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| Size | Document Number | Rev |
|---|---|---|
| | ICH6-M (Power & GND) | A3A |
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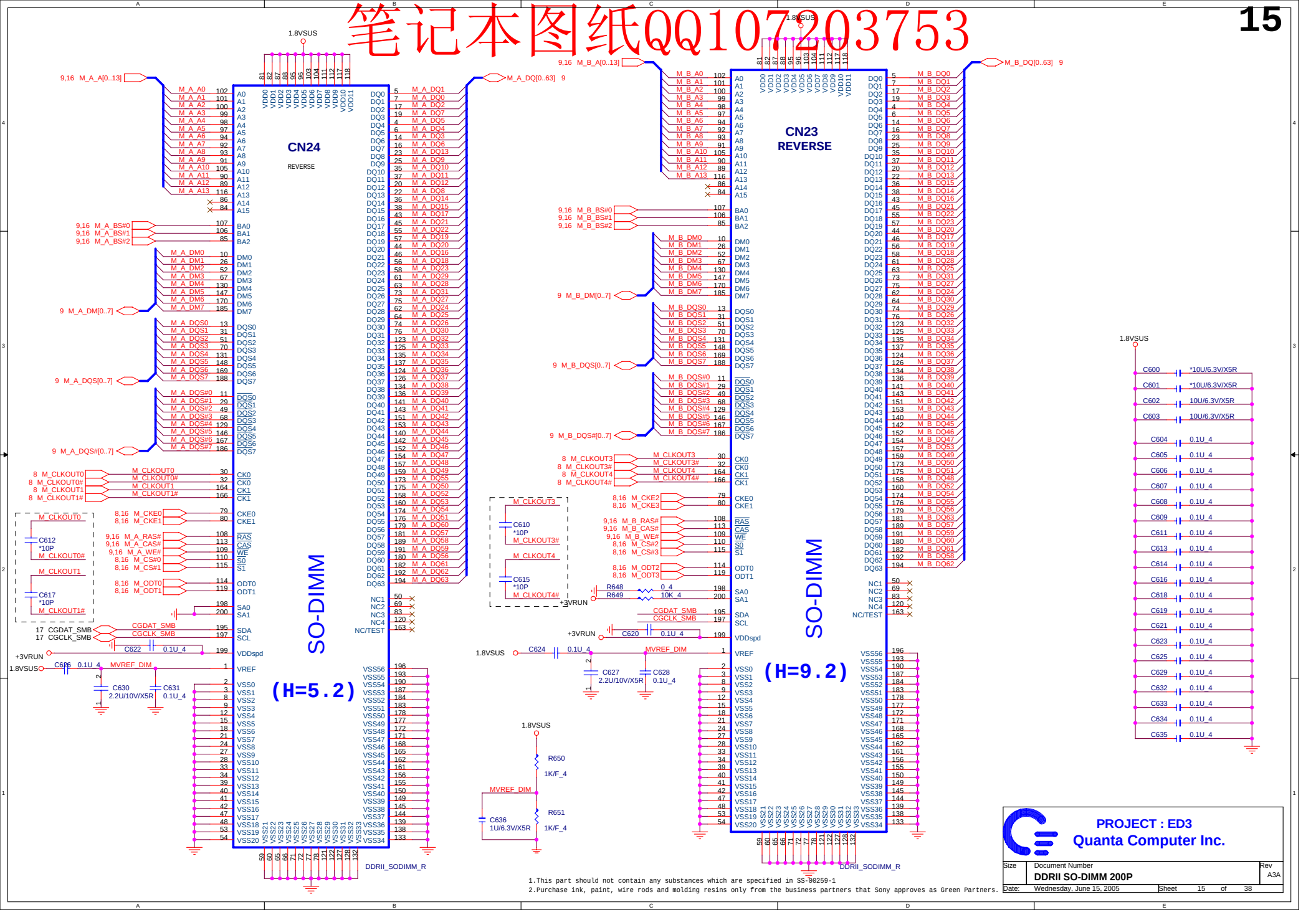


1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

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| Size | Document Number | Rev |
|---|---|---|
| | DDRII SO-DIMM 200P | A3A |
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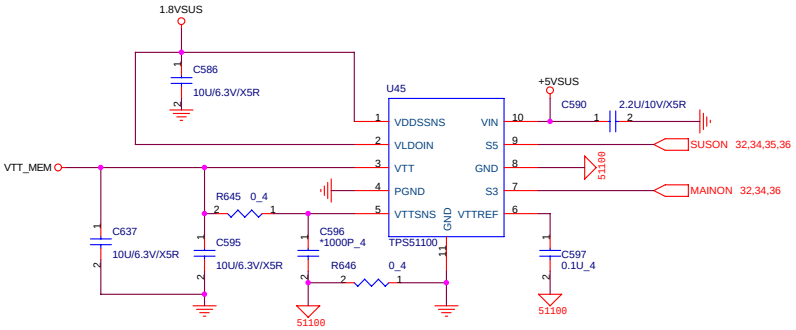


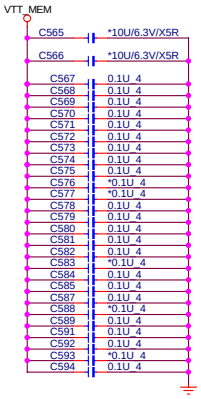


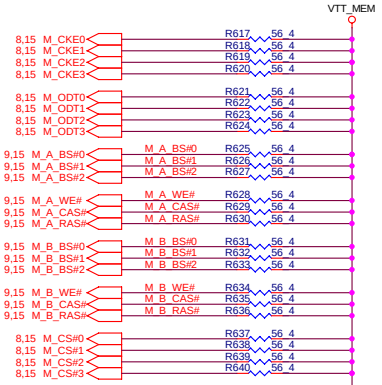


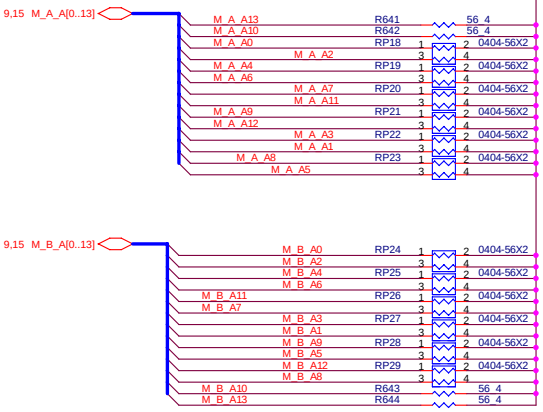


PROJECT : ED2
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| Size | Document Number | Rev |
|---|---|---|
| | DDR TERMINATION | A3A |

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| FSC | FSB | FSA | CPU | SRC | PCI |
|---|---|---|---|---|---|
| 1 | 0 | 1 | 100 | 100 | 33 |
| 0 | 0 | 1 | 133 | 100 | 33 |
| 0 | 1 | 1 | 166 | 100 | 33 |
| 0 | 1 | 0 | 200 | 100 | 33 |
| 0 | 0 | 0 | 266 | 100 | 33 |
| 1 | 0 | 0 | 333 | 100 | 33 |
| 1 | 1 | 0 | 400 | 100 | 33 |
| 1 | 1 | 1 | RSVD | 100 | 33 |

| | DothanA | DothanB |
|---|---|---|
| R137 | Install | NC |
| R330 | NC | NC |

Place these termination to close CK410M. Cause those Pin-out is for Current-Mode.

U22 CK-410M

XTAL_IN, XTAL_OUT, VTT_PWRGD#/PD#, PCI_STOP#, CPU_STOP#, SCLK, SDATA, FSA/USB_48, FSB/TEST_MODE, FSC/TEST_SEL, VDD_REF, VDD_CPU, VDD_PCI_1, VDD_PCI_2, VDD_SRC0, VDD_SRC1, VDD_SRC2, VDD_48, IREF, DOT96, DOT96#, VDDA, VSSA, REF, CPU0, CPU0#, CPU1, CPU1#, CPU2_ITP/SRC7, CPU2#_ITP/SRC7#, SRC6, SRC6#, SRC5, SRC5#, SRC4, SRC4#, SRC3, SRC3#, SRC2, SRC2#, SRC1, SRC1#, SRC0, SRC0#, PCI5, PCI4, PCI3, PCI2, PCIF1, PCIF0/ITP_EN, GND_48, GND_REF, GND_PCI_1, GND_PCI_2, GND_SRC, GND_CPU

ICS954206/CY284XX
250mA ( MAX. )

SMbus address D2

FSB and FSC are directly controlled by Dothan-B

Iref=5mA, Ioh=4*Iref

CK-410M PIN 35,36 Strap Pin. for ITP or PCIE using.

Tie to VCC (Logic 1) is for ITP using.
Tie to GND (Logic 0) is for PCIE using.

CT_0505: Change footprint to TSSOP56-8_1-5 from TSSOP56-240

Connect ICH6 SMB

Connect DDR Module's SMB

CT_0229: Change MOS to RHU002N06 due to layout concern.

These are for backdrive issue

Bypass CAPs need to follow Bypass CAP. Routing Rule, no vias between CAP to CHIPSET VCC Pin or GND.

Place these termination to close CK410M. Cause those Pin-out is for Current-Mode.

PROJECT : ED3
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| Size | Document Number | Rev |
|---|---|---|
| | CLOCK Generator | A3A |
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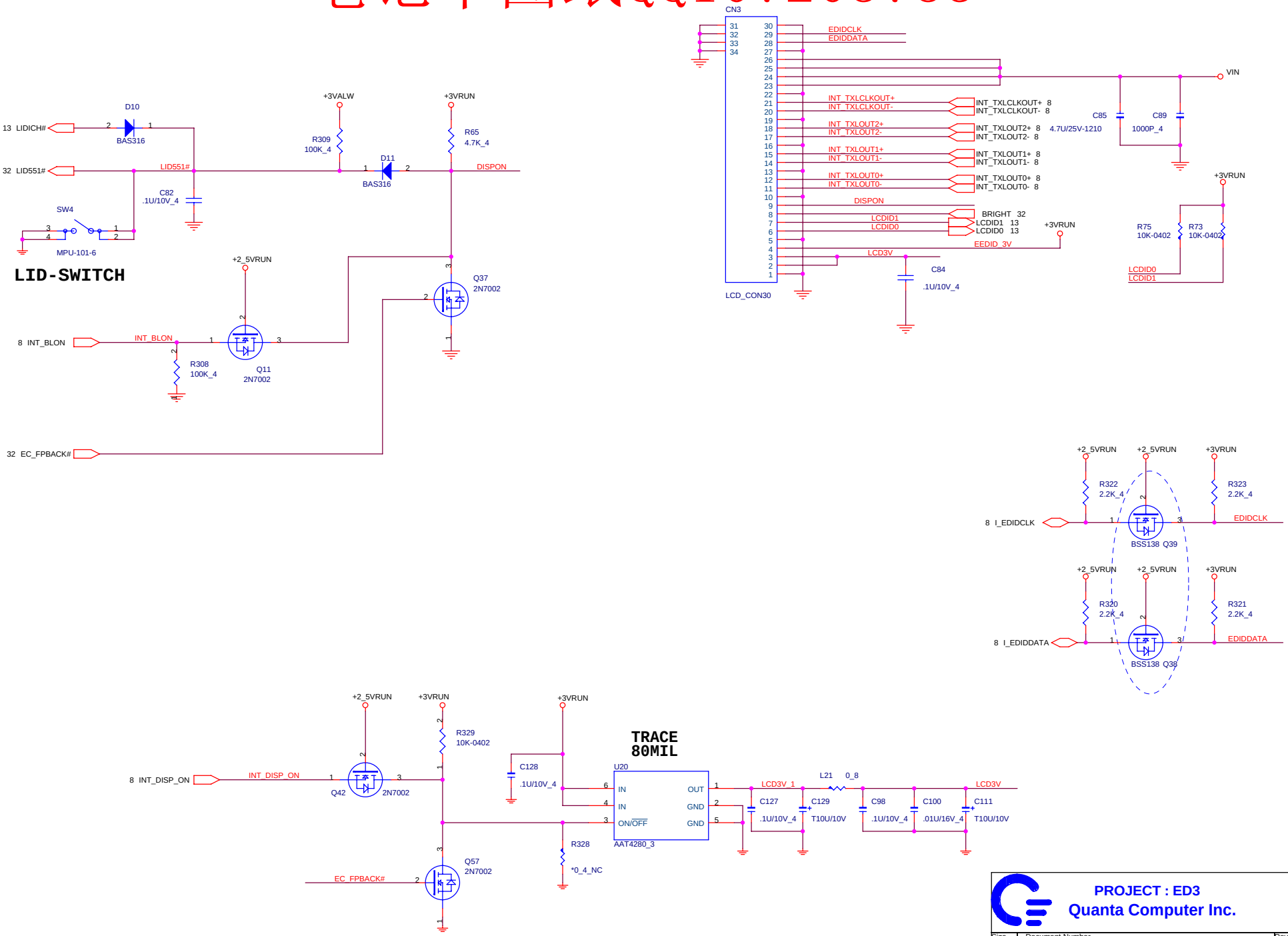


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| Size | Document Number | Rev |
|---|---|---|
| | LCD Connector | A3A |
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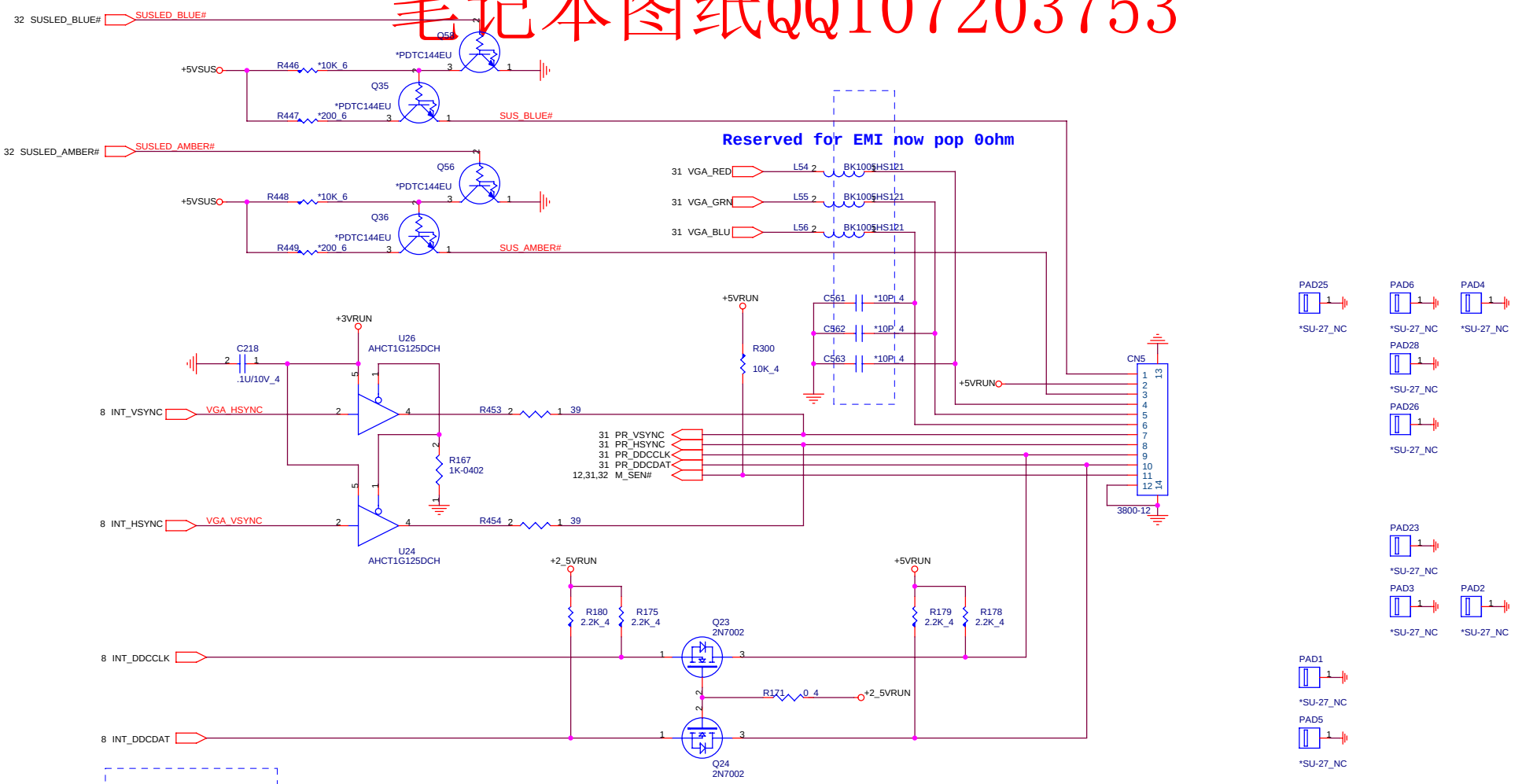
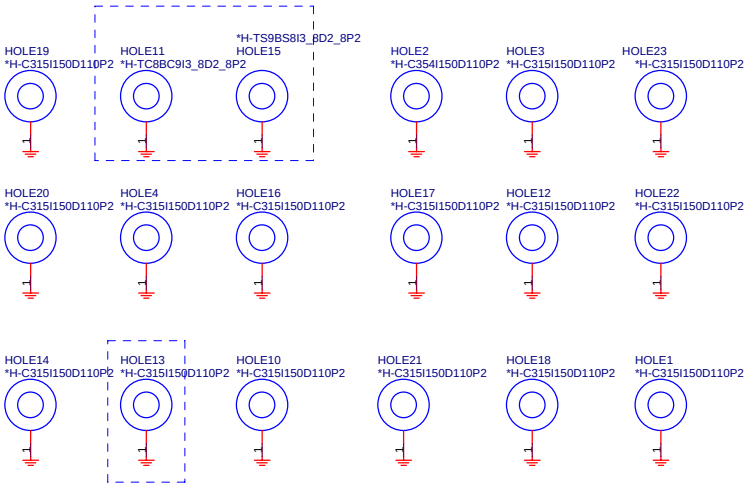
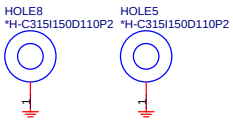
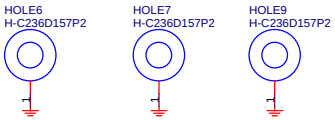



TV-Board fixing Nut




CPU SOCKET



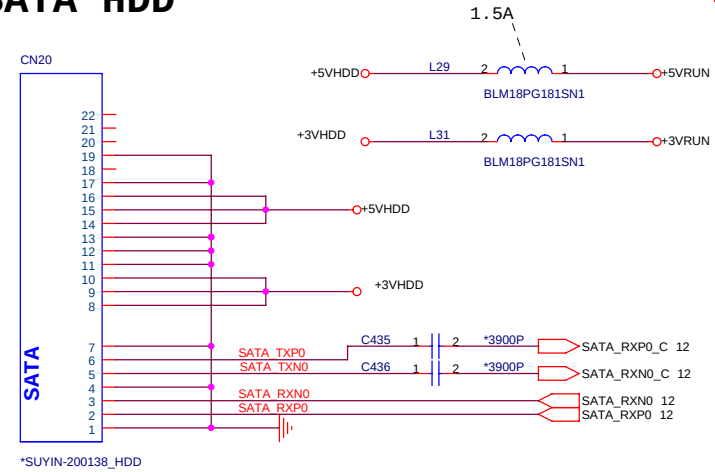




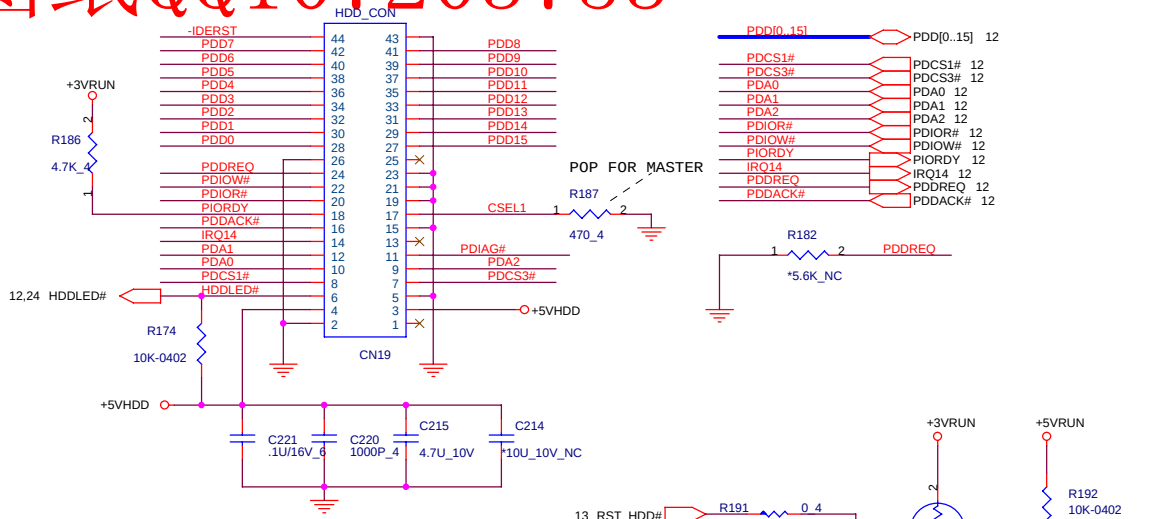
**PROJECT : ED3**
**Quanta Computer Inc.**

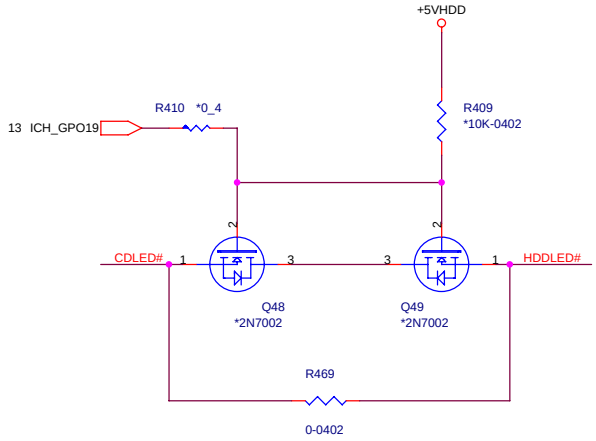
| Size | Document Number | Rev |
|---|---|---|
| | CRT & TV Connector | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 19 of 38 | |

# SATA HDD

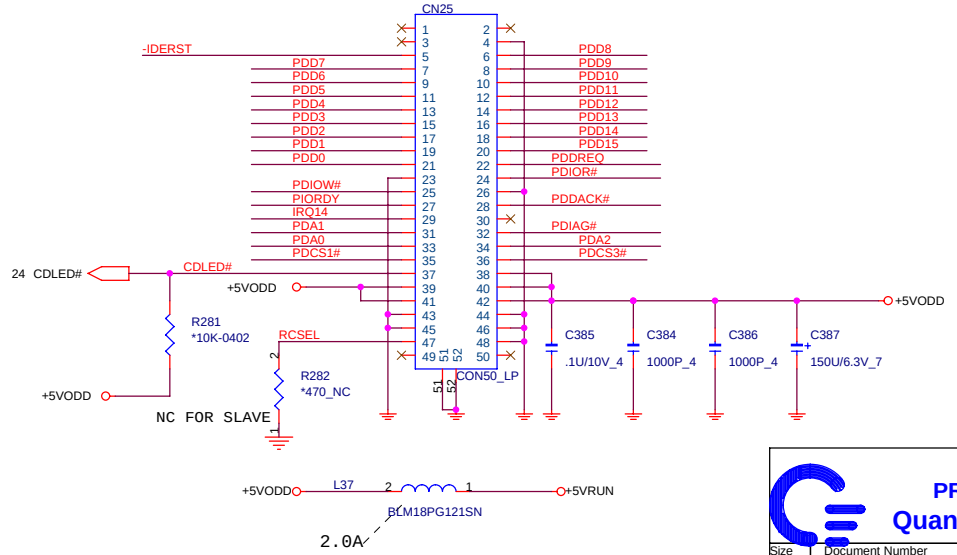



# PATA HDD




# ODD




PROJECT : ED3

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|---|---|---|
| | HDD & CDROM Connector | A3A |
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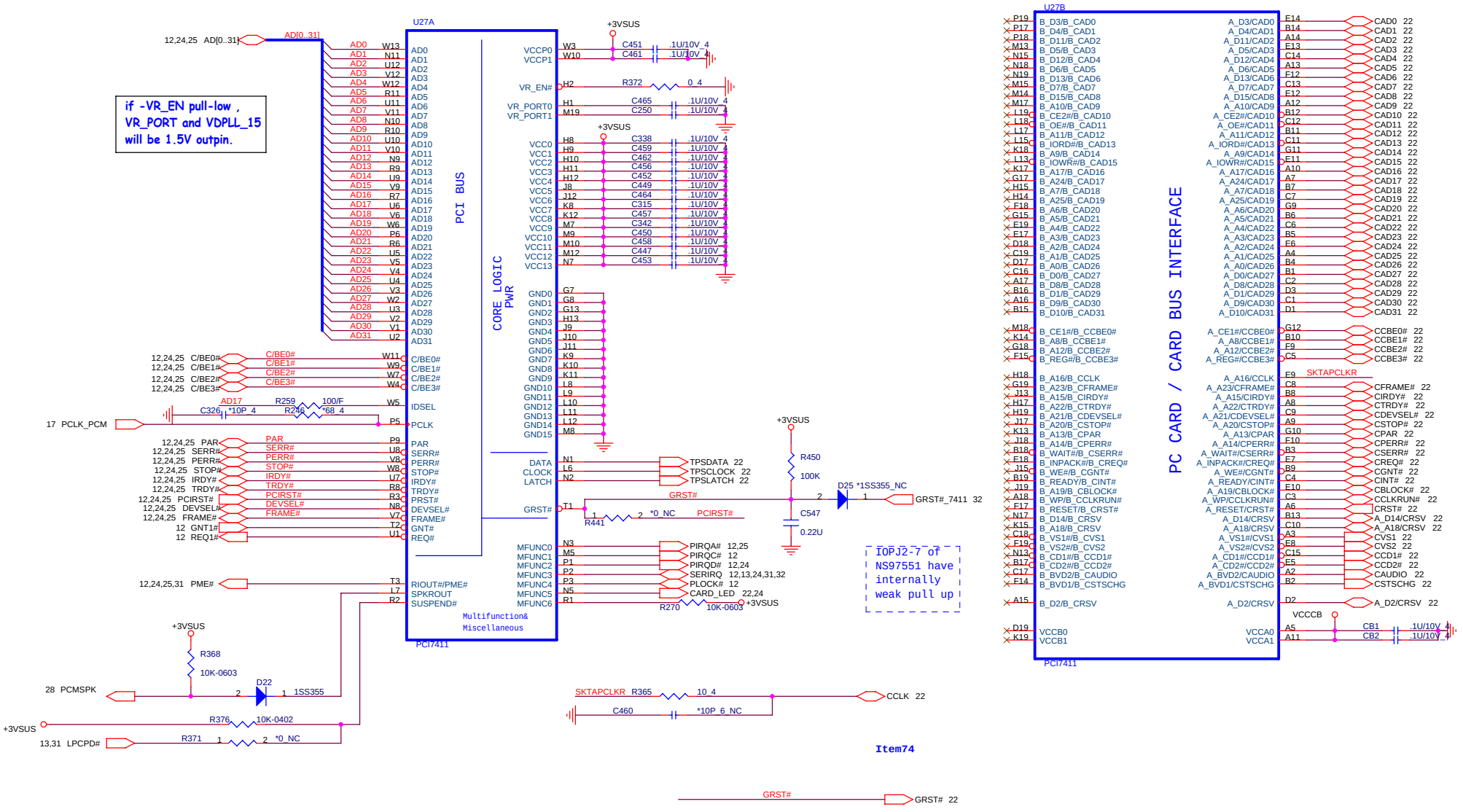



| | | |
|---|---|---|
| PROJECT : ED2 | | |
| Quanta Computer Inc. | | |
| Size | Document Number: PCMCIA TI7411 | Rev: A3A |
| Date: Wednesday, June 15, 2005 | Sheet 21 of 38 | |

# PCMCIA SOCKET

CN4

SANTA-1306-68P

U21

TPS2220A (DB)

U27C

PCI7411

Six For 1 Card

MC_PWR_CTR low active(default) , or change register to high active

Flash Media Layout Guidelines:

1. Signal traces should be 60 Ohm +/- 10%.
2. All signal traces should be routed with equal propagation delay, and with trace lengths as short as practical.
3. A 56 Ohm damping resistor for MS_CLK and SD_CLK should be placed near the PCI7411 source.

**IRLML5103**
**Rds(on)=0.6**

Q46 IRLML5103

Q47 *IRLML5103

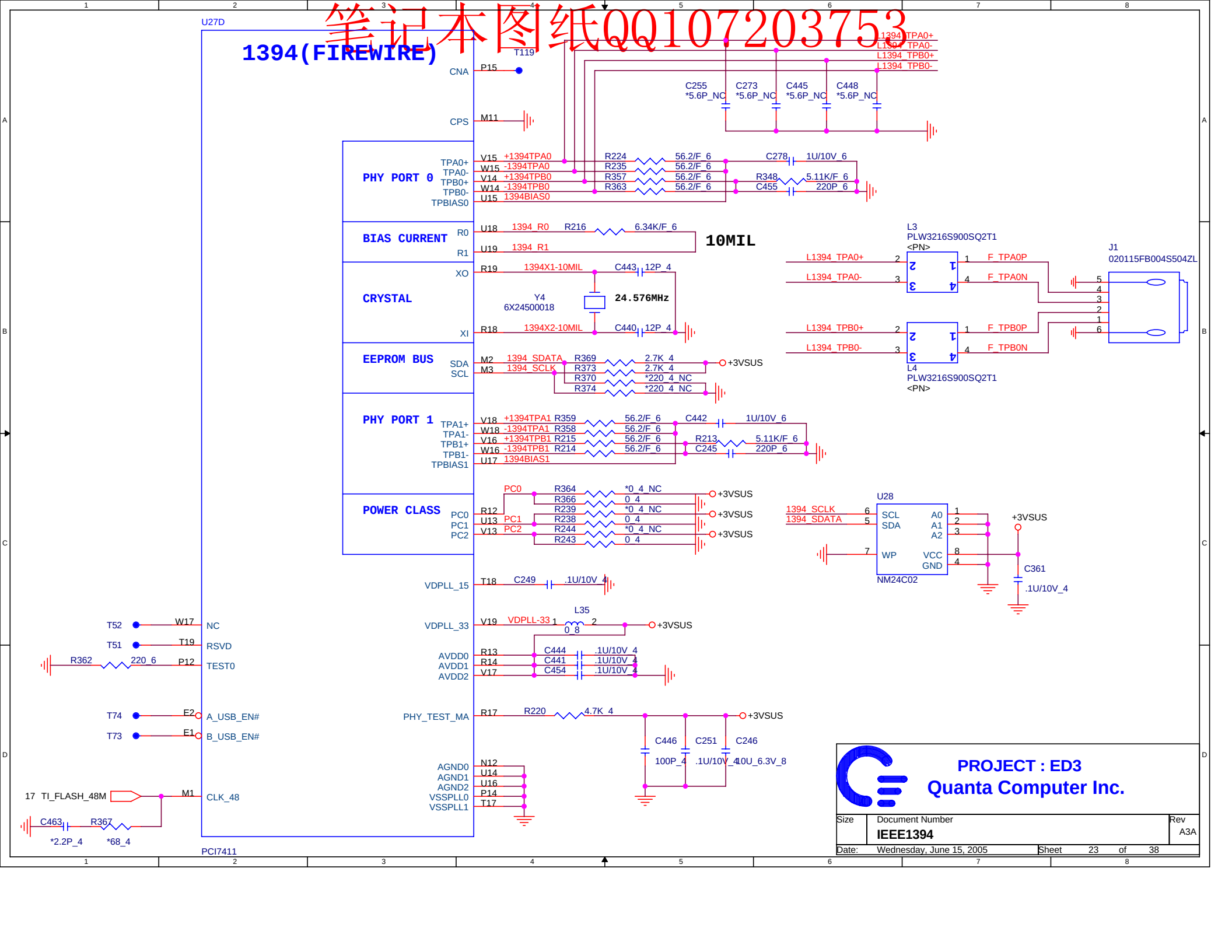
TI Erratum

## 4 IN1 CARD READER (XD,MMC/SD,MS)

CN10

MSX039-X0-0X00

| | | |
|---|---|---|
| PROJECT : ED3 | | |
| Quanta Computer Inc. | | |
| Size | Document Number | Rev |
| | PCMCIA Socket & 4-IN-1 | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 22 of 38 | |



ID Select : AD19
Interrupt Pin : INTB# , INTD#
Request Indicate : REQ2#
Grant Indicate : GNT2#

MINI-PCI

CN22

MINIPCI_TYPE_III

Each channel is 1A

TV Board R3 ,R4 need to change BOM to 150ohm

OPTION (DOCKING ON OR S-VEDIO Board ON)

Item136

Orange Forward Voltage 2.0~2.4
Blue Forward Voltage 3.4~3.8
DC Forward Current 20mA

BLUE_LED
Forward Voltage 2.8~3.05
DC Forward Current 20mA

PROJECT : ED2
Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | Mini PCI, T/P, USB, LED | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 24 of 38 | |

R117
RTL8110SBL(1G)--DEPOP
RTL8100L(10,100)--POP

R116
RTL8110SBL(1G)--POP
RTL8100L(10,100)--DEPOP

**RTL81XX's power for Co-lay :**

| | | | | |
|---|---|---|---|---|
| AS | RTL8110SBL(Giga-Lan) | AVDDL | CONNECT | +2.5V_A |
| | RTL8100L(10,100M-Lan) | AVDDL | CONNECT | +3V_LAN_A |
| AS | RTL8110SBL(Giga-Lan) | HSDAC- | CONNECT | +3V_LAN_A |
| | RTL8100L(10,100M-Lan) | HSDAC- | CONNECT | +2.5V_A |
| AS | RTL8110SBL(Giga-Lan) | DVDD | CONNECT | +1.8V_D |
| | RTL8100L(10,100M-Lan) | DVDD | CONNECT | +2.5V_A |

RTL8110SBL(1G)--POP 2.49K
RTL8100L(10,100M) POP 5.6K

R113,R114
RTL8110SBL(1G)--POP
RTL8100L(10,100)--DEPOP

R108,R115,Q19
RTL8110SBL(1G)--DEPOP
RTL8100L(10,100) --POP

R412,R414,R416,R418,C531,C533
RTL8110SBL(1G)--POP
RTL8100L(10,100)--DEPOP

Q52 Colayout with Q19
RTL8110SBL(1G)--POP
RTL8100L(10,100) --DEPOP

Q18,R124,C138,C157,C151
RTL8110SBL(1G)--POP
RTL8100L(10,100) --DEPOP

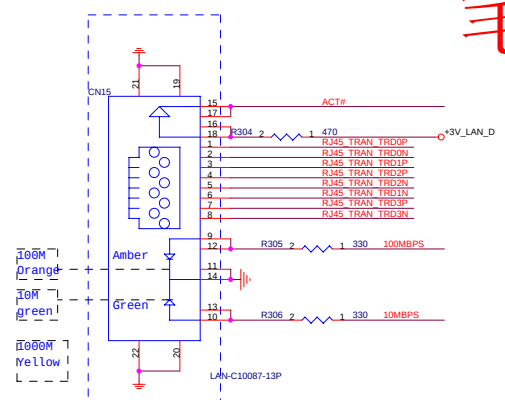
Item103

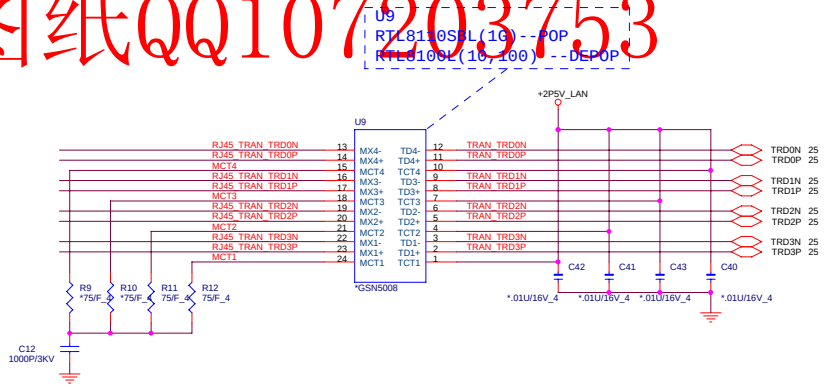
**RTL8110SBL/8100CL**

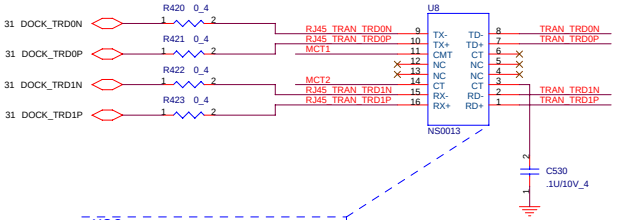
PROJECT : ED2
Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | LAN RTL8110S/8100CL | A3A |

Date: Wednesday, June 15, 2005 Sheet 25 of 38

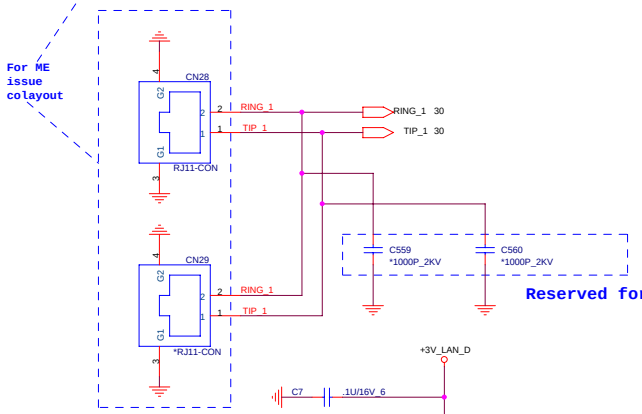


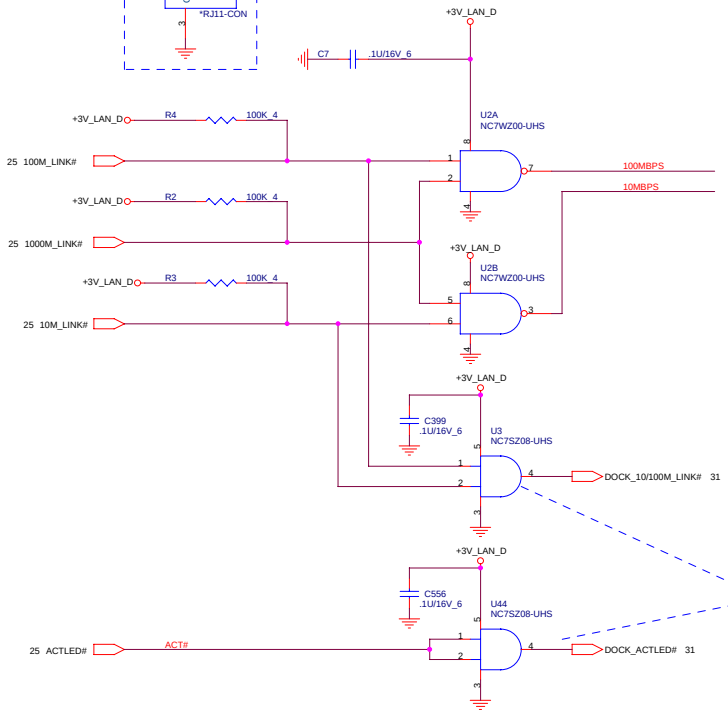




U36
RTL8110SBL(1G)--DEPOP
RTL8100L(10,100) --POP

For ME issue colayout




Reserved for EMI

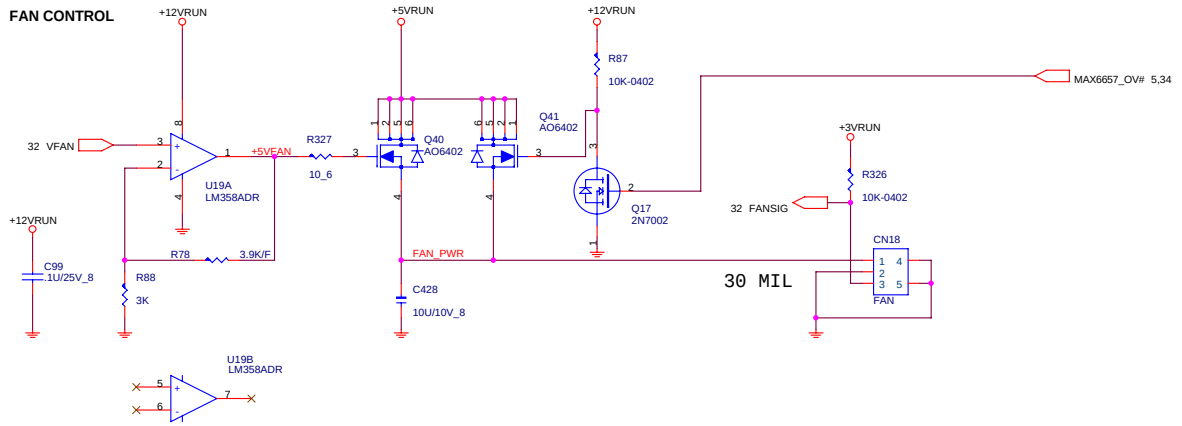


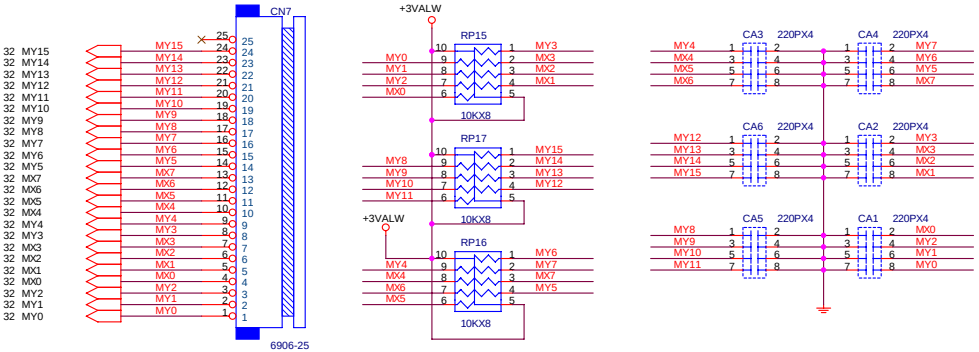

U44,U3,C399,C556
RTL8110SBL(1G)--DEPOP
RTL8100L(10,100) --POP

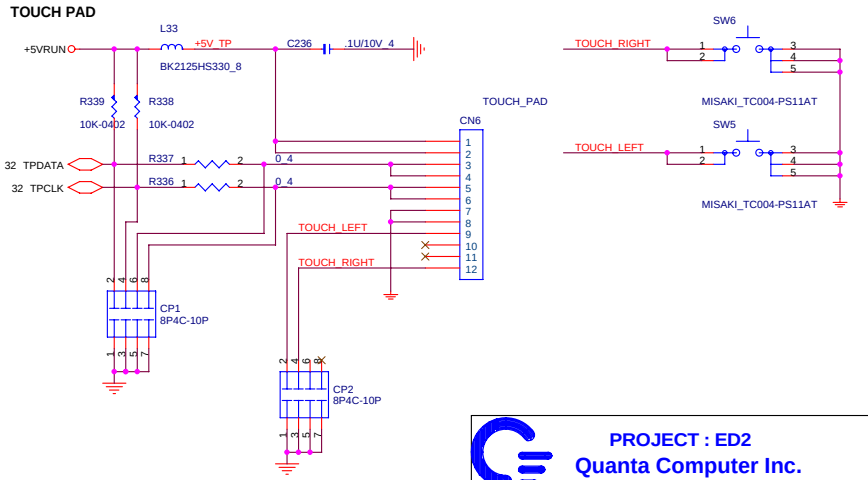
| | For RTL8100C | For RTL8110SB |
|---|---|---|
| R116 | UN-POP | POP |
| R117 | POP | UN-POP |
| R139 | UN-POP | POP |
| R127 | POP | UN-POP |
| R113 | UN-POP | POP |
| R114 | UN-POP | POP |
| R115 | POP | UN-POP |
| R108 | POP | UN-POP |
| R124 | UN-POP | POP |
| C151 | UN-POP | POP |
| C138 | UN-POP | POP |
| C157 | UN-POP | POP |
| Q52 | UN-POP | POP |
| Q18 | UN-POP | POP |
| Q19 | POP | UN-POP |
| R412 | UN-POP | POP |
| R414 | UN-POP | POP |
| R416 | UN-POP | POP |
| R418 | UN-POP | POP |
| C531 | UN-POP | POP |
| C533 | UN-POP | POP |
| C40 | UN-POP | POP |
| C41 | UN-POP | POP |
| C42 | UN-POP | POP |
| C43 | UN-POP | POP |
| R9 | UN-POP | POP |
| R10 | UN-POP | POP |
| R420 | POP | UN-POP |
| R421 | POP | UN-POP |
| R422 | POP | UN-POP |
| R423 | POP | UN-POP |
| C530 | POP | UN-POP |
| U8 | POP | UN-POP |
| U9 | UN-POP | POP |
| U44 | POP | UN-POP |
| C556 | POP | UN-POP |
| U3 | POP | UN-POP |
| C399 | POP | UN-POP |

PROJECT : ED3
Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | LAN Switch & Connector | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 26 of 38 | |

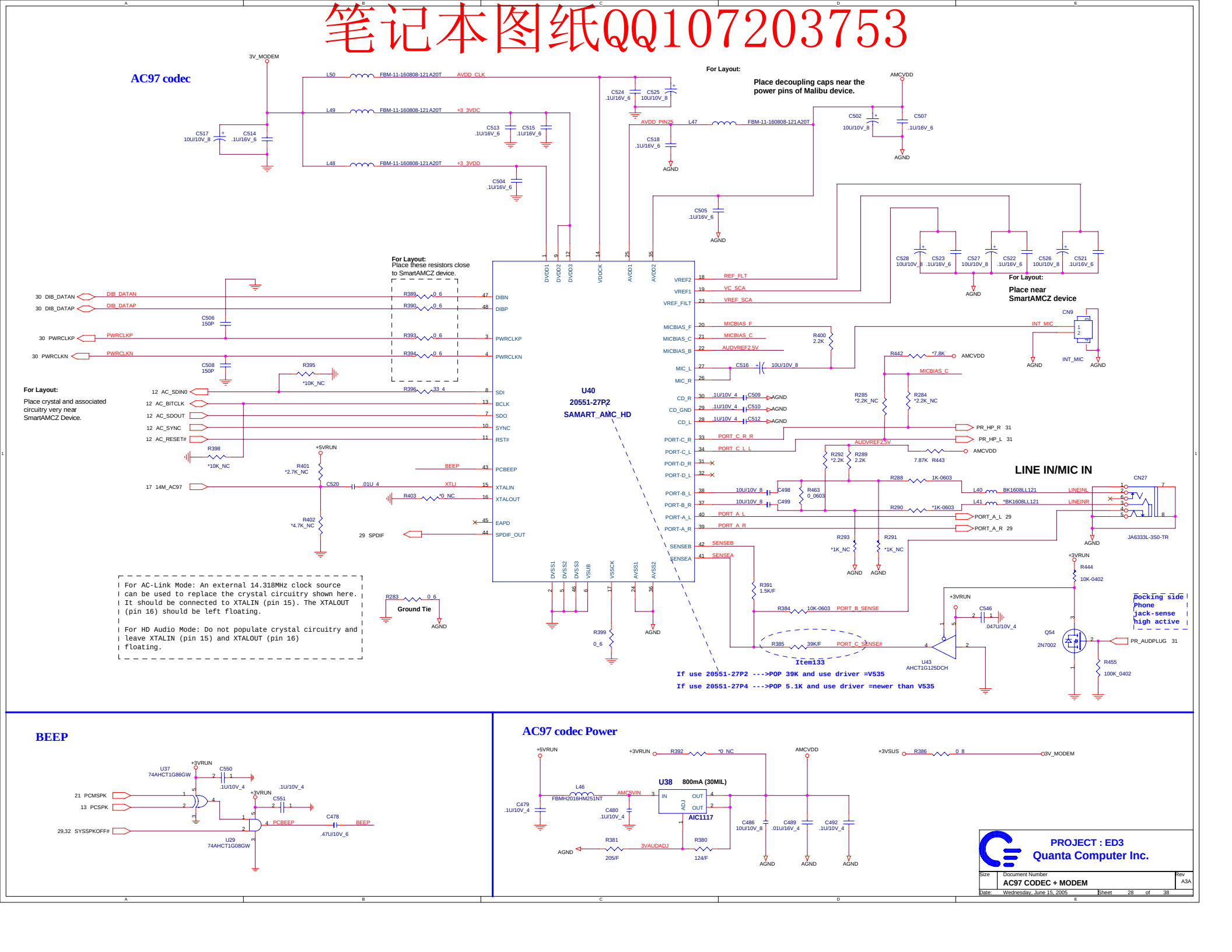



PROJECT : ED2
Quanta Computer Inc.

| Size | Document Number | Rev |
|---|---|---|
| | T/P, FAN, Switch, K/B | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 27 of 38 | |



| GAIN | SPKR MODE | HP MODE |
|---|---|---|
| 0 | 10.5 | 3 |
| 1 | 9 | 0 |

FOR EMI SOLUTION

Headphone out / SPDIF OUT

SPEAKER CON.

New type Phone jack low active

IOPJ2-7 for NS97551 have internally weak pull up

When 3D_ON# Keep low then 3D active

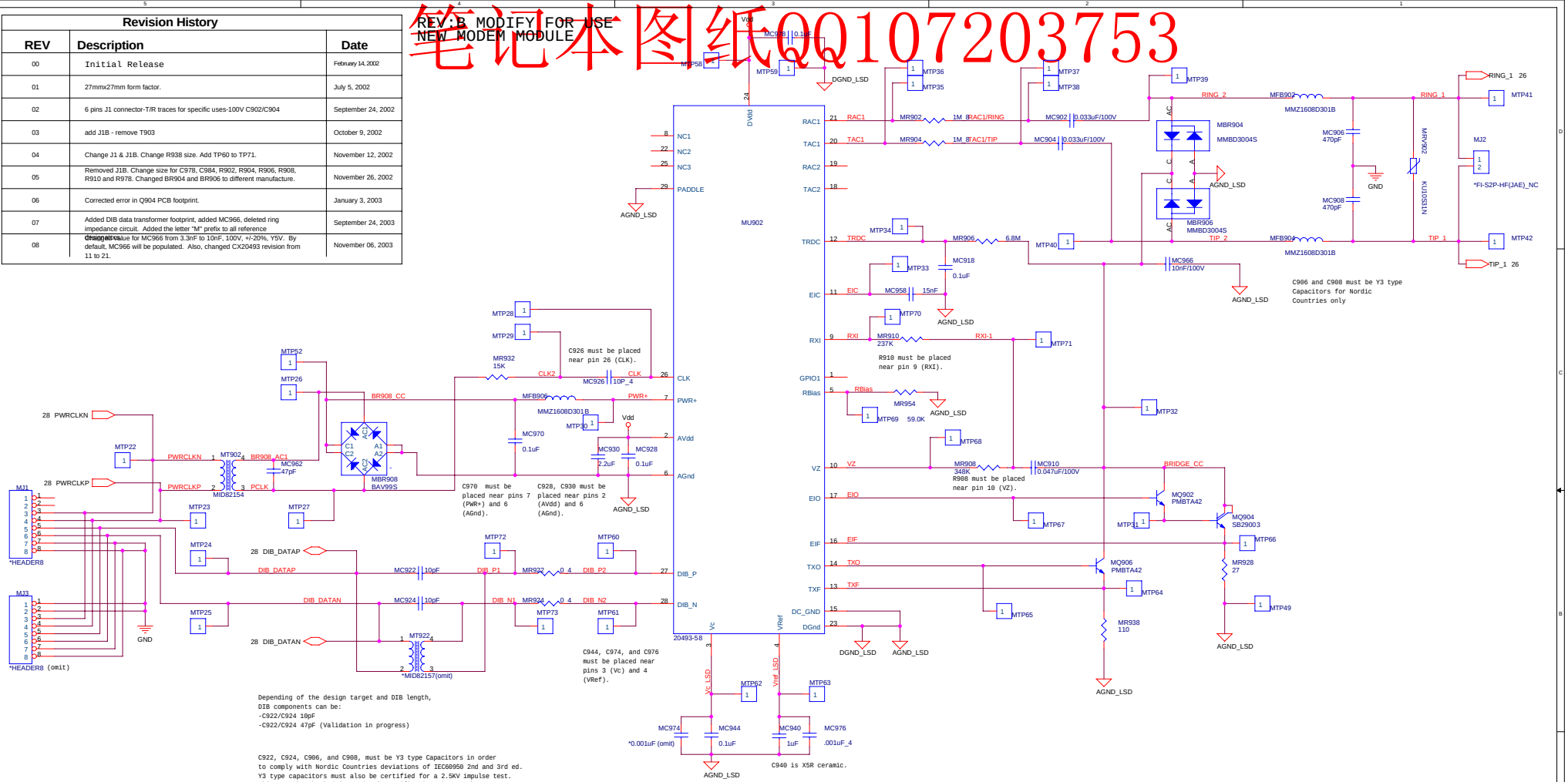
| | | | |
|---|---|---|---|
| PROJECT : ED2 Quanta Computer Inc. | | | |
| Size | Document Number: Audio Amplifier & Jack | | Rev: A3A |
| Date: Wednesday, June 15, 2005 | Sheet 29 of 38 | | |

## Revision History

| REV | Description | Date |
|---|---|---|
| 00 | Initial Release | February 14, 2002 |
| 01 | 27mmx27mm form factor. | July 5, 2002 |
| 02 | 6 pins J1 connector-T/R traces for specific uses-100V C902/C904 | September 24, 2002 |
| 03 | add J1B - remove T903 | October 9, 2002 |
| 04 | Change J1 & J1B. Change R938 size. Add TP60 to TP71. | November 12, 2002 |
| 05 | Removed J1B. Change size for C978, C984, R902, R904, R906, R908, R910 and R978. Changed BR904 and BR906 to different manufacture. | November 26, 2002 |
| 06 | Corrected error in Q904 PCB footprint. | January 3, 2003 |
| 07 | Added DIB data transformer footprint, added MC966, deleted ring impedance circuit. Added the letter "M" prefix to all reference designators. | September 24, 2003 |
| 08 | Changed value for MC966 from 3.3nF to 10nF, 100V, +/-20%, Y5V. By default, MC966 will be populated. Also, changed CX20493 revision from 11 to 21. | November 06, 2003 |

REV:B MODIFY FOR USE NEW MODEM MODULE





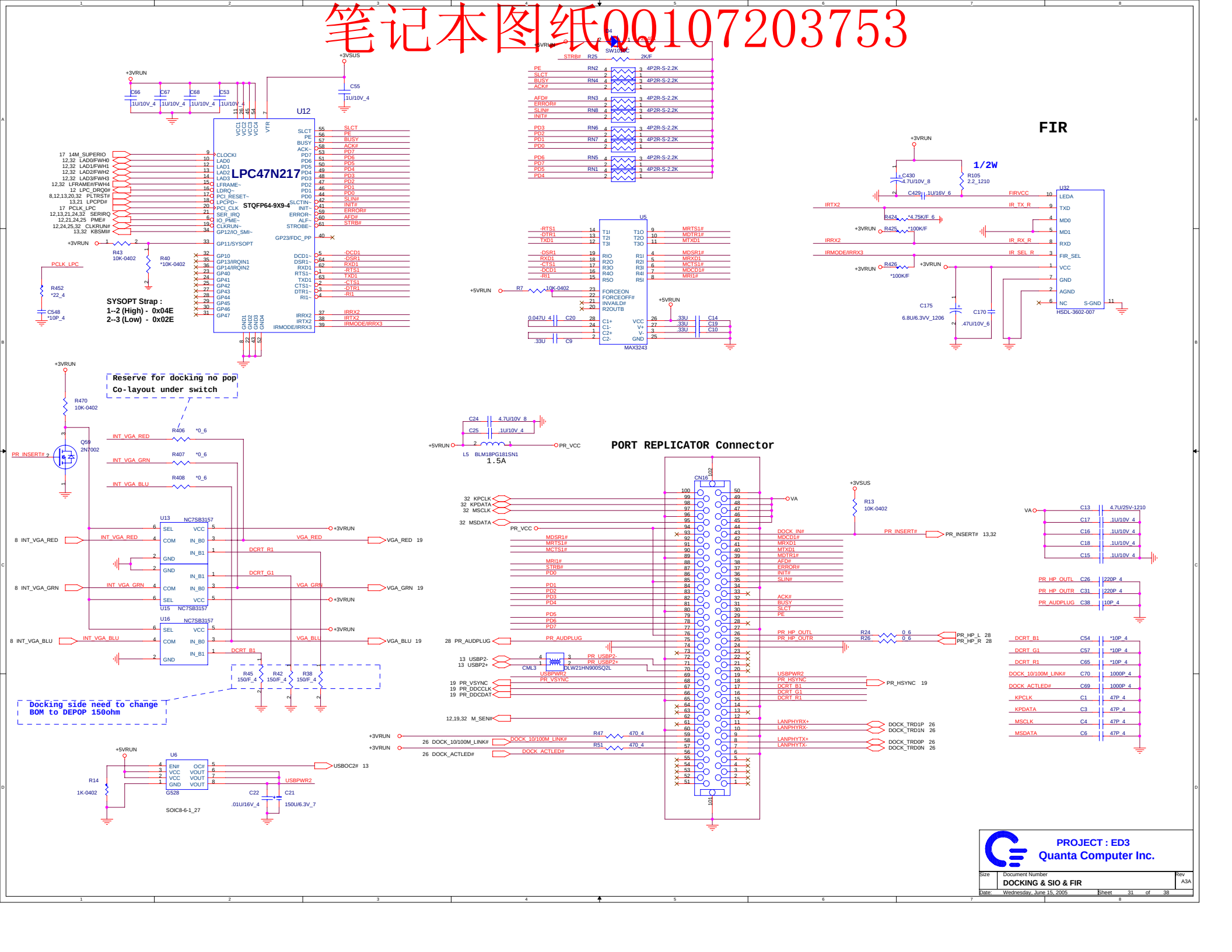

Depending of the design target and DIB length, DIB components can be:
-C922/C924 10pF
-C922/C924 47pF (Validation in progress)

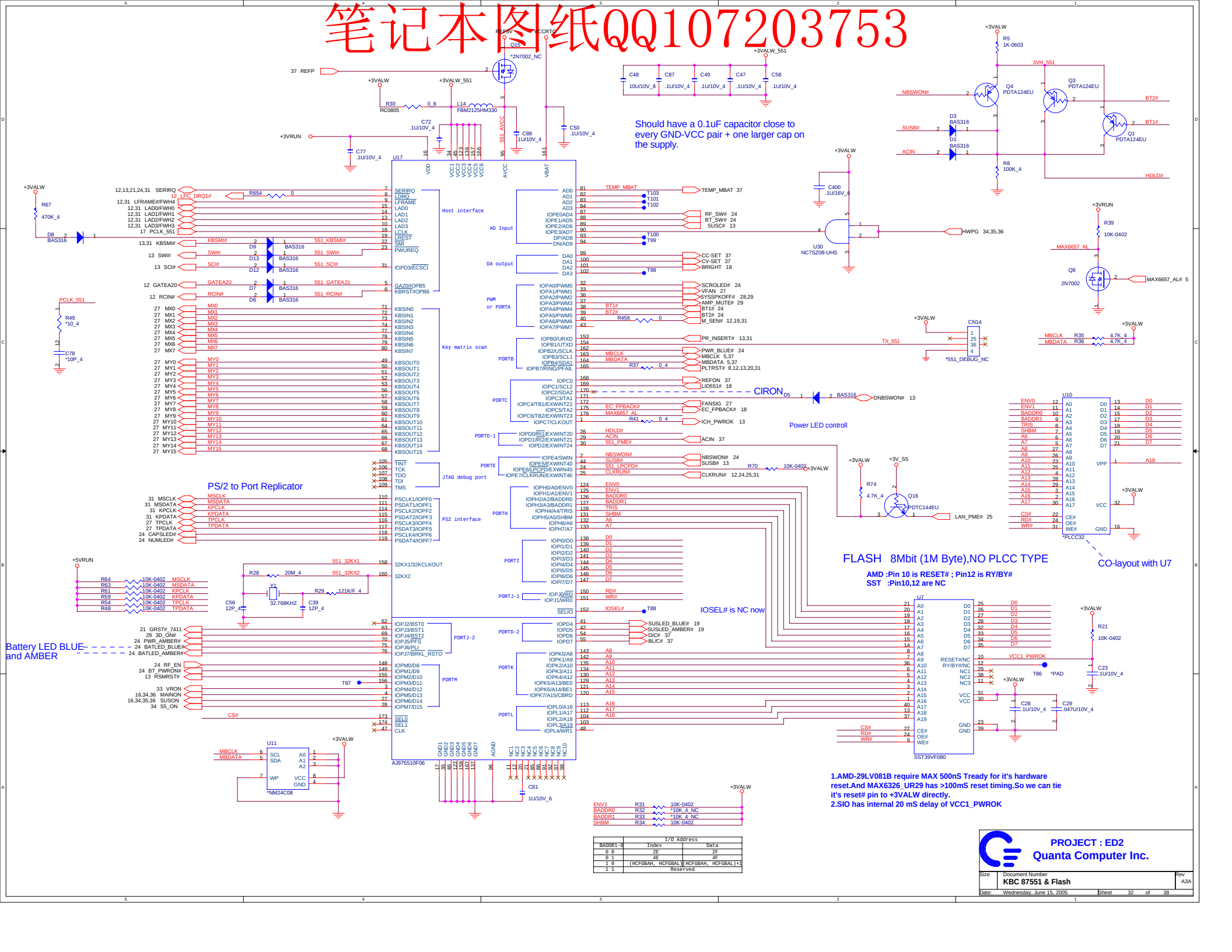
C922, C924, C906, and C908, must be Y3 type Capacitors in order to comply with Nordic Countries deviations of IEC60950 2nd and 3rd ed. Y3 type capacitors must also be certified for a 2.5KV impulse test. This must be checked in vendors' specifications (see AVL).

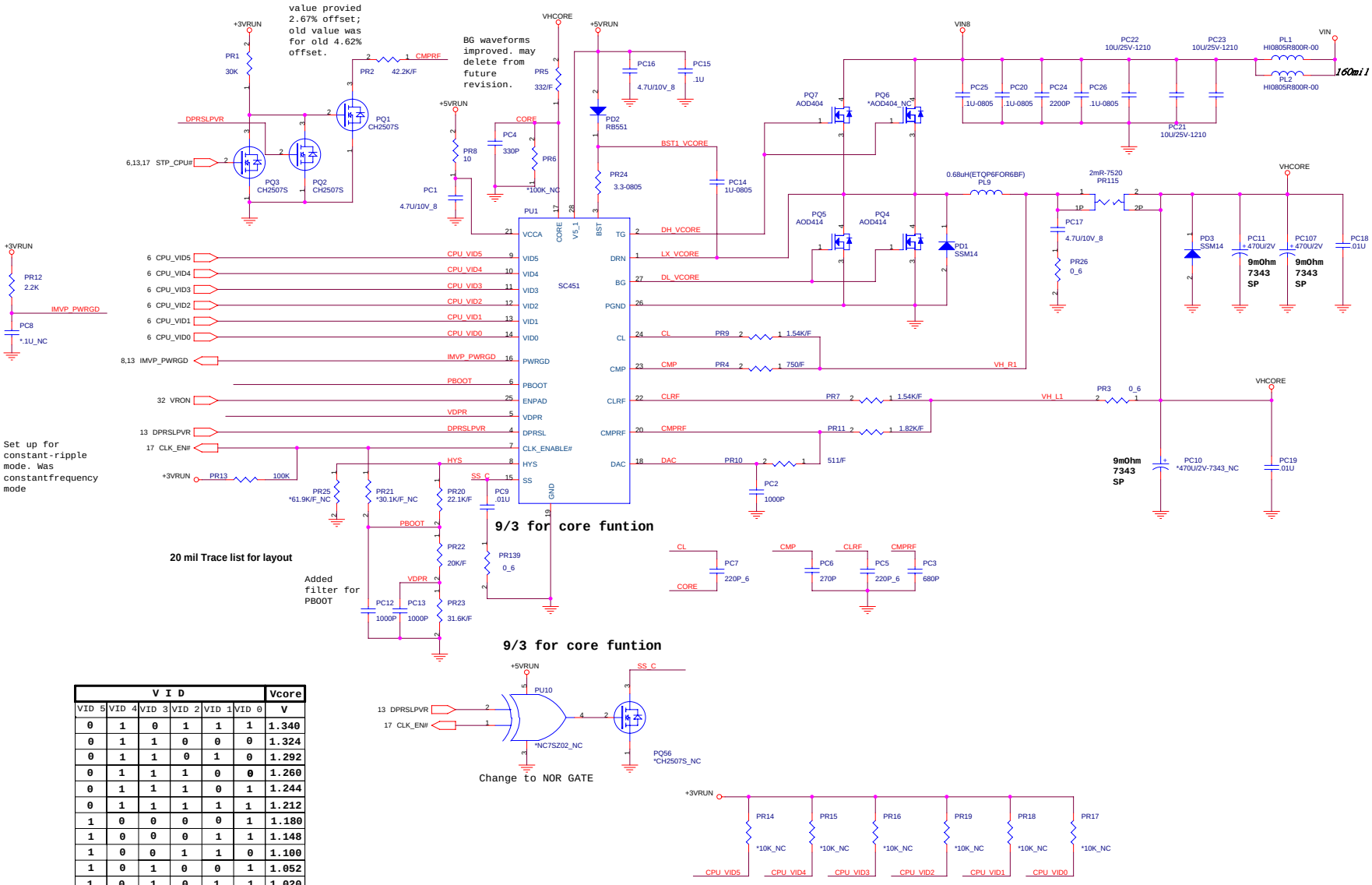
Circuit traces for C922 and C924 should be less than 2 inches.

**PROJECT : ED2**
**Quanta Computer Inc.**

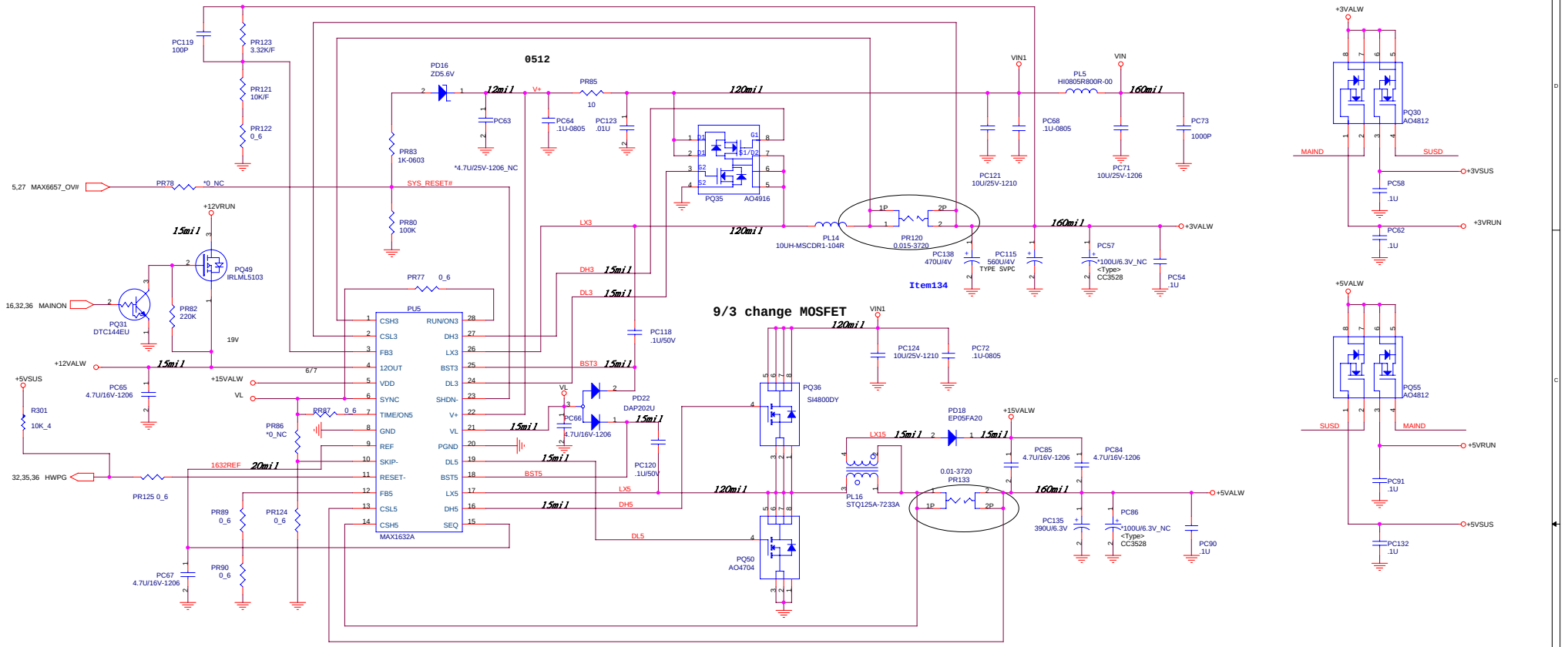
| Size | Document Number | Rev |
|---|---|---|
| | MODEM DAA | A3A |
| Date: Wednesday, June 15, 2005 | Sheet 30 of 38 | |

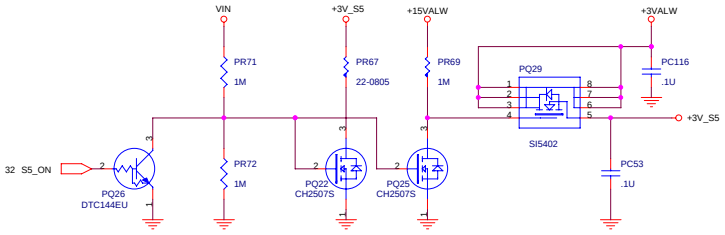


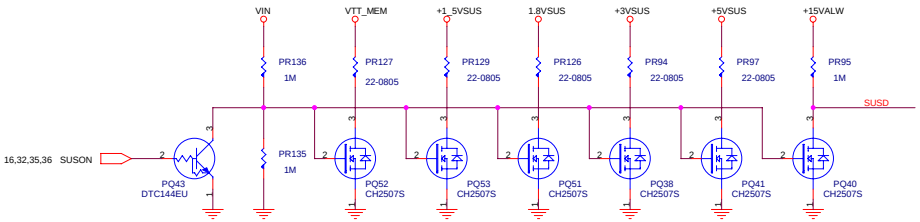


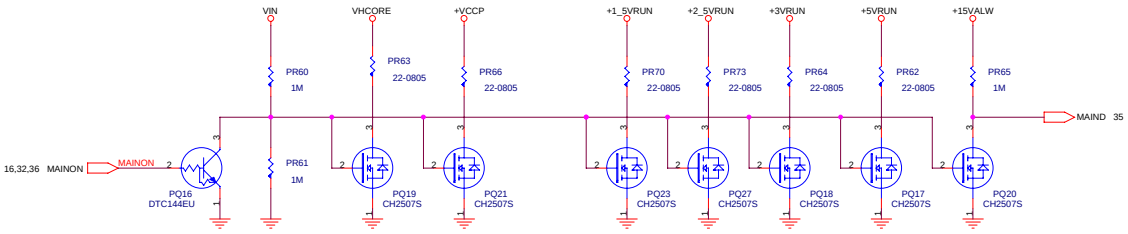



| V I D | | | | | | Vcore |
|---|---|---|---|---|---|---|
| VID 5 | VID 4 | VID 3 | VID 2 | VID 1 | VID 0 | V |
| 0 | 1 | 0 | 1 | 1 | 1 | 1.340 |
| 0 | 1 | 1 | 0 | 0 | 0 | 1.324 |
| 0 | 1 | 1 | 0 | 1 | 0 | 1.292 |
| 0 | 1 | 1 | 1 | 0 | 0 | 1.260 |
| 0 | 1 | 1 | 1 | 0 | 1 | 1.244 |
| 0 | 1 | 1 | 1 | 1 | 1 | 1.212 |
| 1 | 0 | 0 | 0 | 0 | 1 | 1.180 |
| 1 | 0 | 0 | 0 | 1 | 1 | 1.148 |
| 1 | 0 | 0 | 1 | 1 | 0 | 1.100 |
| 1 | 0 | 1 | 0 | 0 | 1 | 1.052 |
| 1 | 0 | 1 | 0 | 1 | 1 | 1.020 |
| 1 | 0 | 1 | 1 | 1 | 0 | 0.972 |
| 1 | 1 | 0 | 0 | 0 | 0 | 0.940 |

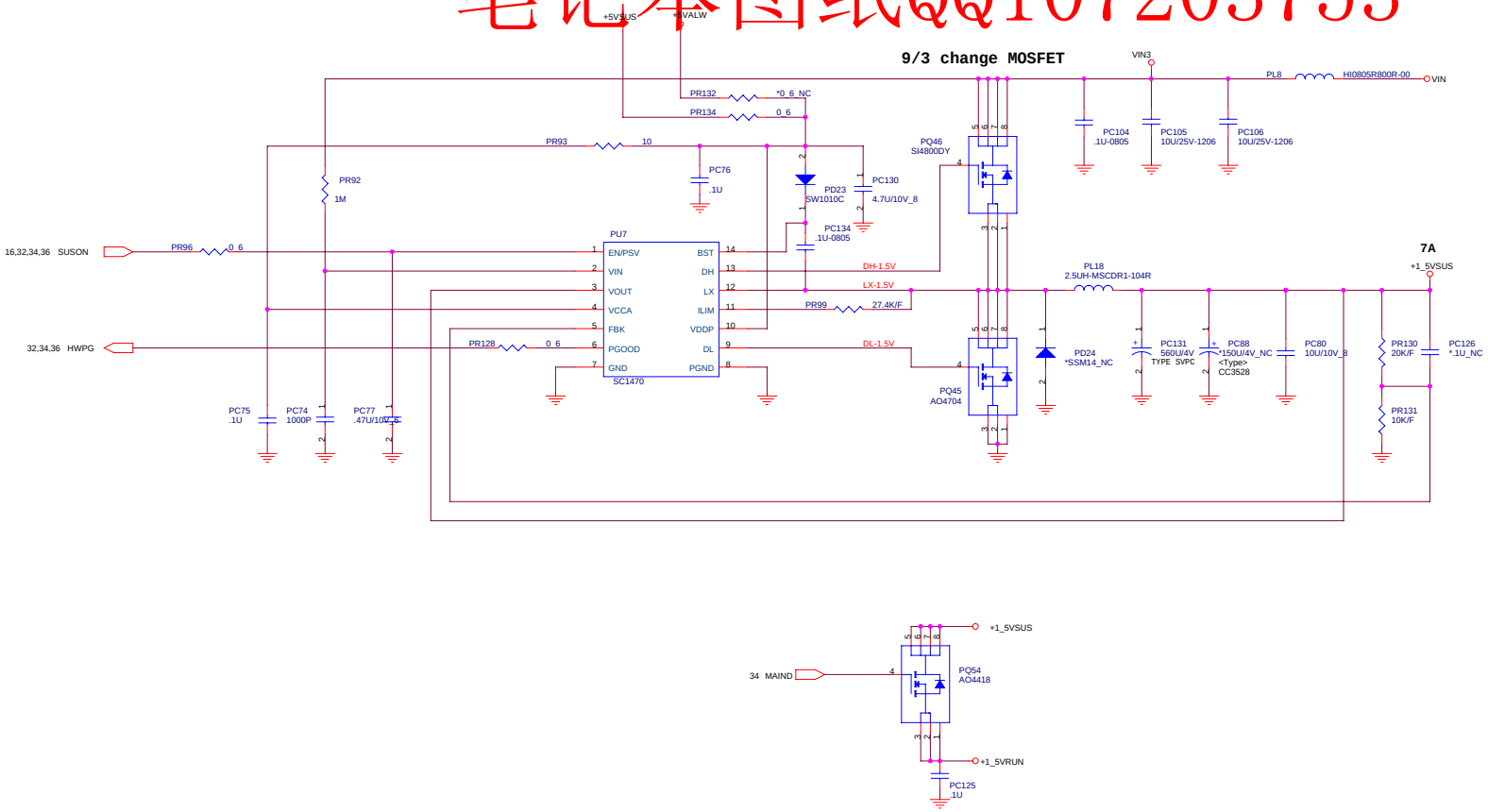


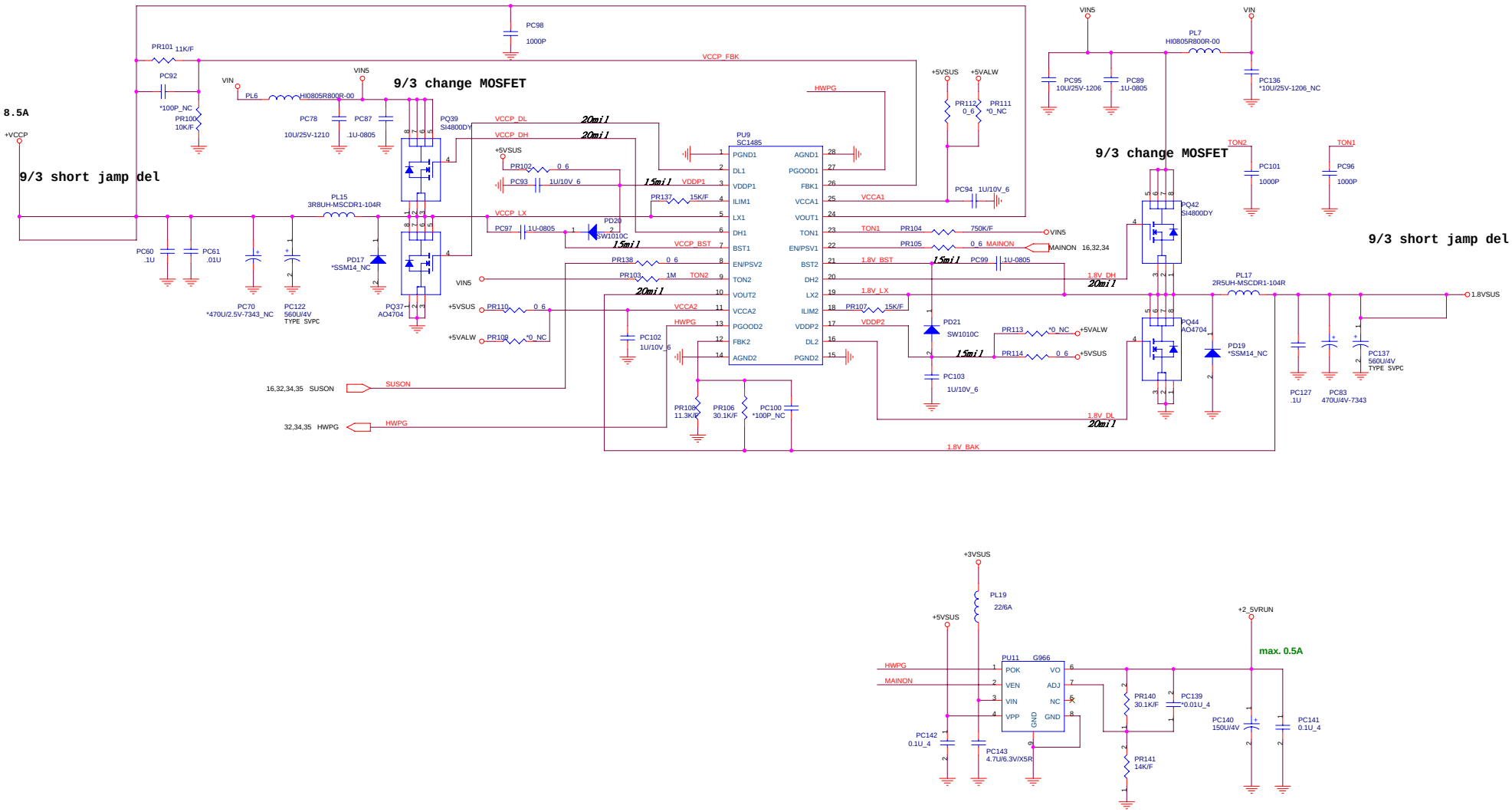


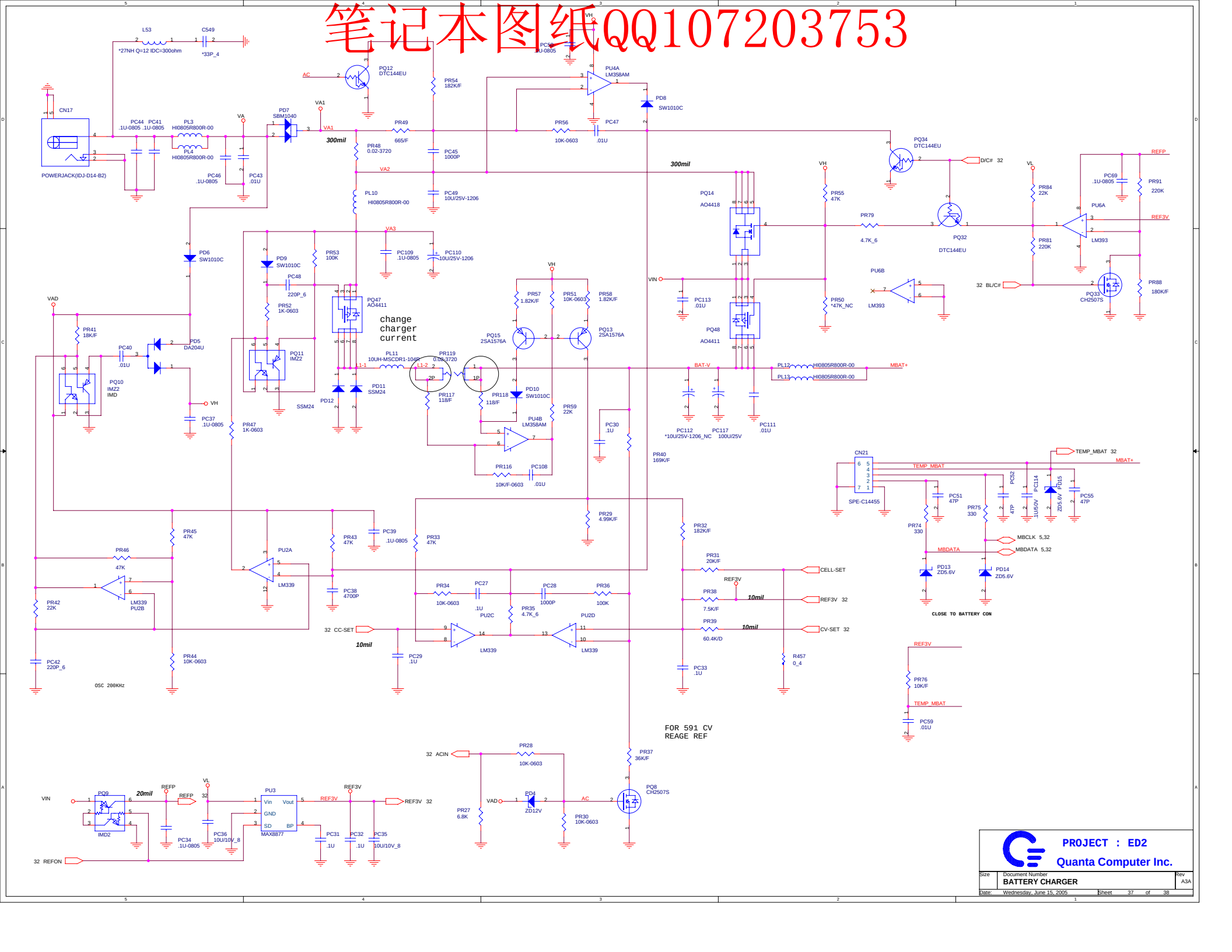


| | | |
|---|---|---|
| PROJECT : ED2 | | |
| Quanta Computer Inc. | | |
| Size | Document Number: 5V/3.3V(MAX1632A) | Rev: A3A |
| Date: Wednesday, June 15, 2005 | Sheet 34 of 38 | |




| | | |
|---|---|---|
| | PROJECT : ED2 | |
| | Quanta Computer Inc. | |
| Size | Document Number: 2.5VSUS / +1.25TERM | Rev: A3A |
| Date: Wednesday, June 15, 2005 | Sheet 35 of 38 | |





| MODEL | REV | CHANGE LIST | Model / Page | JM2 MB FM | JM2 MB TO |
|---|---|---|---|---|---|
| | 1A | Preliminary Release | 1 | | |
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QUANTA COMPUTER

| | | | | |
|---|---|---|---|---|
| PROJECT : DM3B | DOC. NO. ??? | REV: 1A | ASSY: | |
| APPROVED BY : | CHECKED BY: | DRAWN BY : | DATE : | SHEET 1 OF 1 |