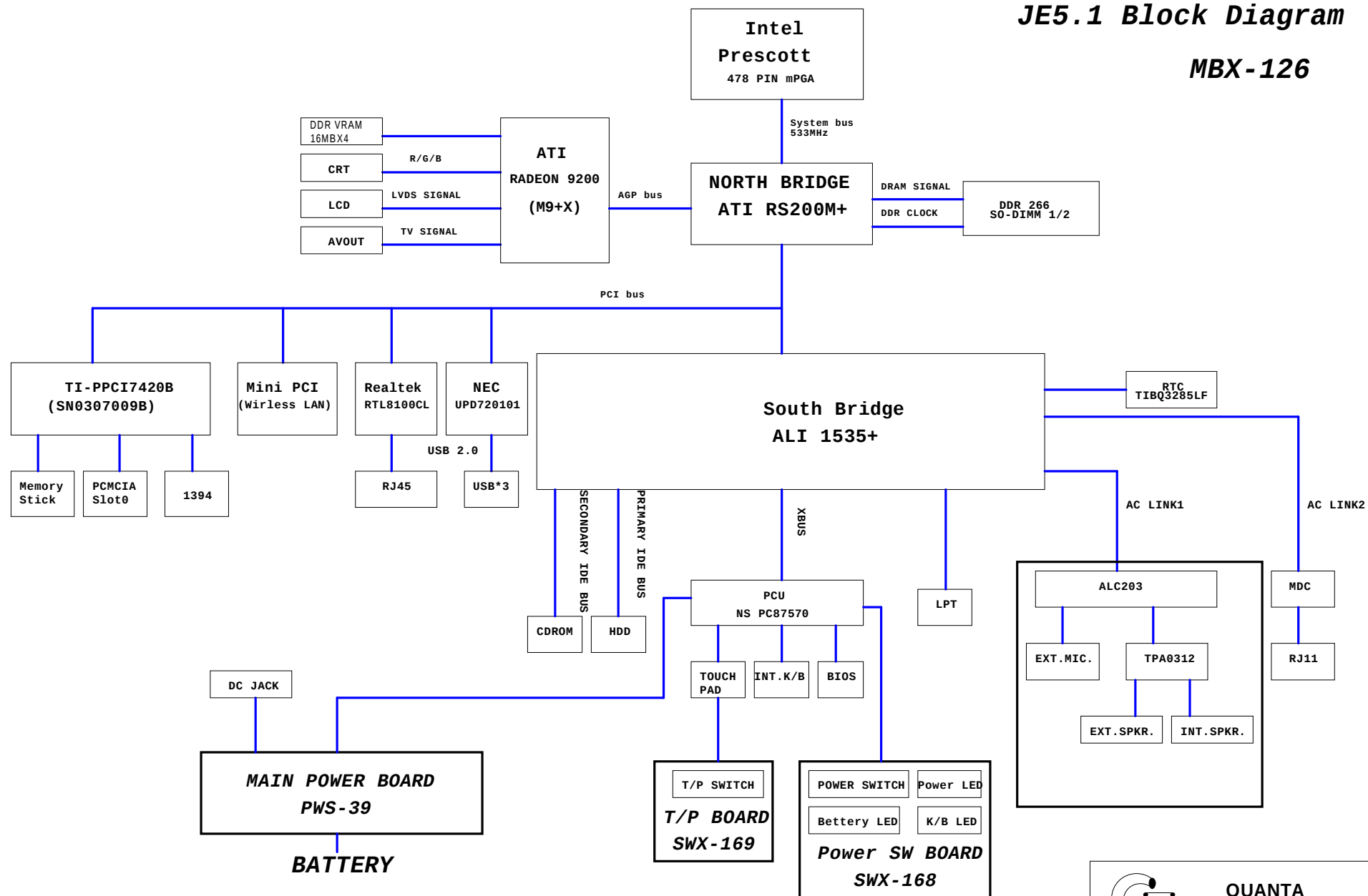


JE5.1 Block Diagram**MBX-126**

Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



**QUANTA
COMPUTER**

Page List

01 Block Diagram
02 Page List
03 Prescott CPU-1
04 Prescott CPU-2
05 RS200M-AGTL+

06 RS200M-DDR I/F
07 RS200M-PCI & AGP I/F
08 RS200M-VIDEO I/F & CLKGEN
09 SYSTEM CONFIGURATION
10 DDR Terminator

11 DDR SODIMMx2
12 CRT PORT/TV
13 ALI M1535+ (PCI,ISA)-1/2
14 ALI M1535+ (IDE)-2/2
15 USB2.0 Controller (NEC-uPD720101)

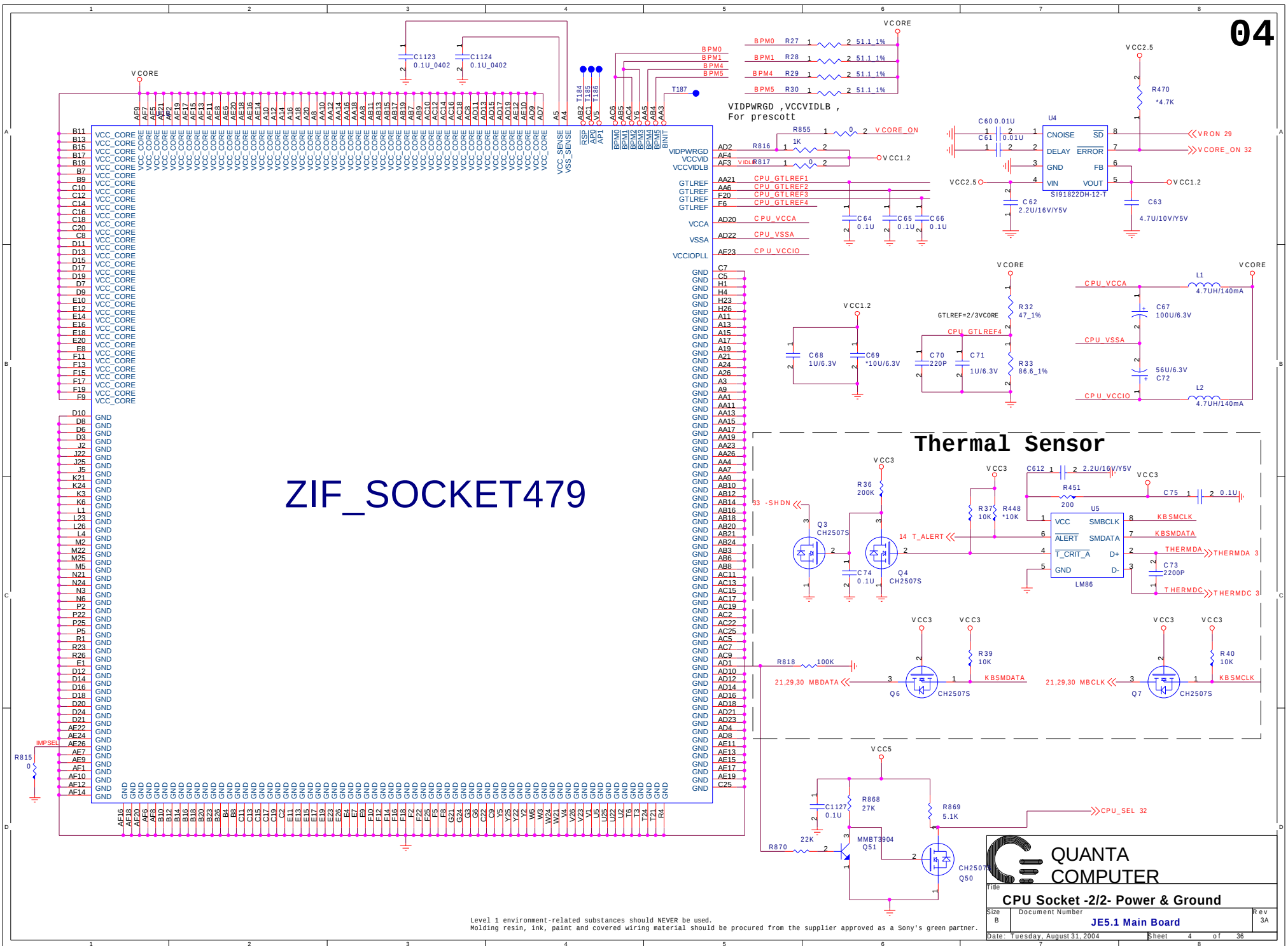
16 ATI M9+ 1/3
17 ATI M9+ 2/3
18 ATI M9+ 3/3
19 VGA DDR VRAMX2 1/2
20 VGA DDR VRAMX2 2/2

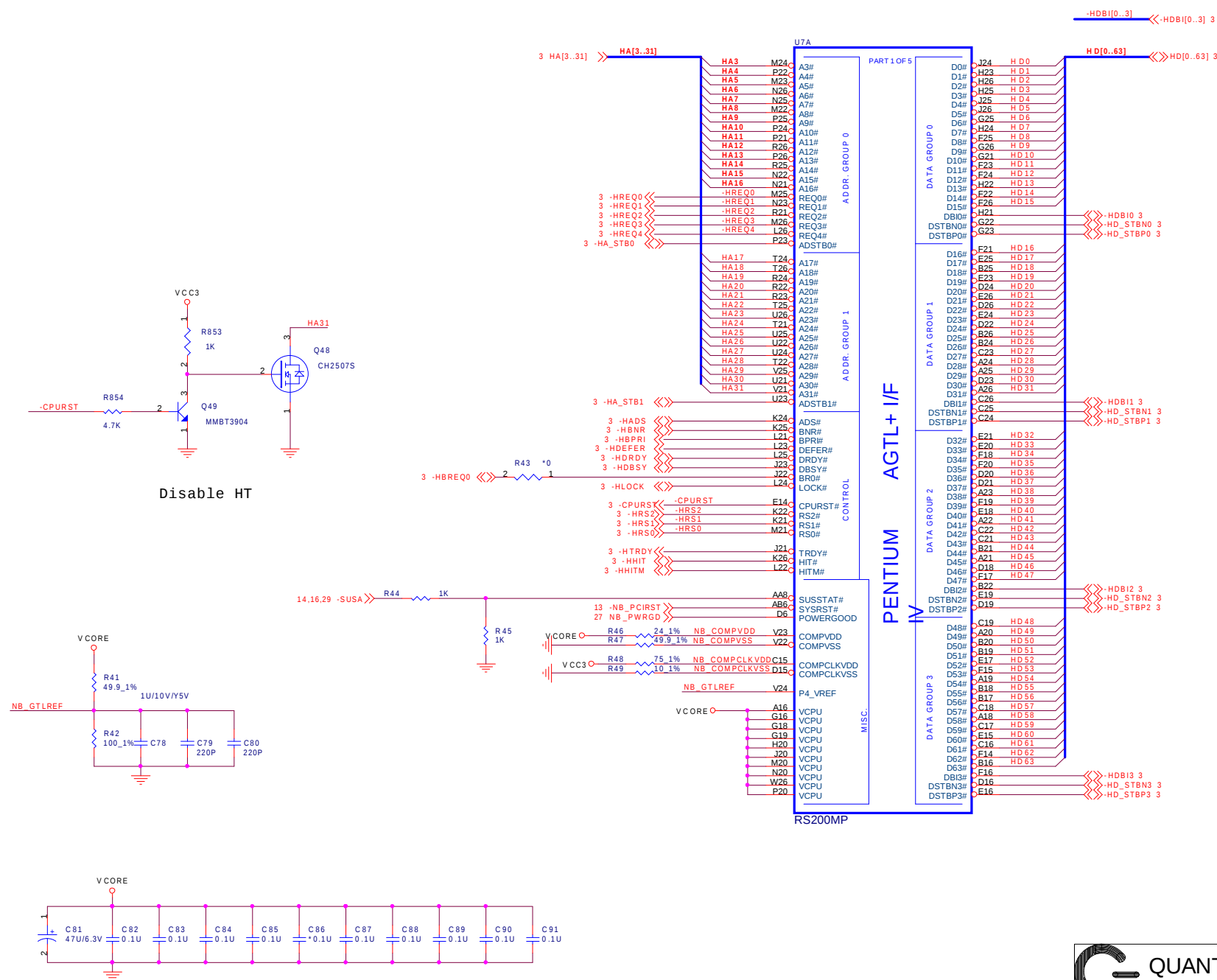
21 AGP SSC
22 TI7420-A (CARDBUS+1394+MS)
23 TI7420-B (CARDBUS+1394+MS)
24 Cardbus slot
25 LAN interface RTL8100CL

26 MiniPCI & MDC
27 RTC/POWER GOOD LOGIC
28 IDE/HDD/LPT Connector
29 PCU NS 87570
30 Int. Keyboard/Bios/FAN

31 VCORE POWER 1
32 MAX1546(VCORE POWER 2)
33 MAX1632(5V/3V/12V)
34 MAX1844(2.5V/1.8V/1.25V)
35 MAX1844(VCC1.5)
36 Load Switch

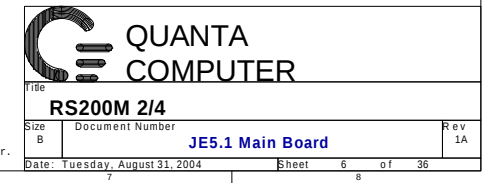
ZIF_SOCKET479

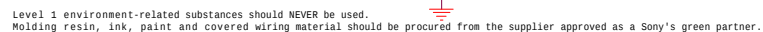


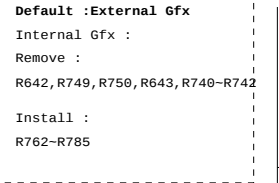


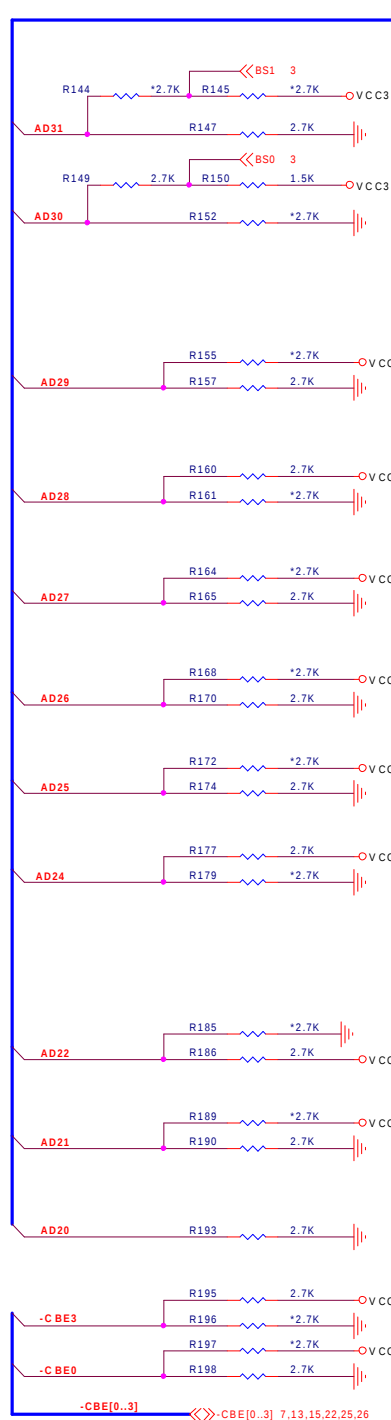
Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

06









AD[31..30] : CLK SPEED
DEFAULT: 01

00: 100 MHZ
01: 133 MHZ
10: 166MHZ
11: 66 MHZ

AD29: RS200M FULL CONFIGURATION
DEFAULT: 1

0: FULL CONFIGURATION
1: USES DEFAULT VALUES

AD28: SPREAD SPECTRUM ENABLE
DEFAULT: 1

0: DISABLE
1: ENABLE

AD27: MCERR OBSERVATION
DEFAULT: 0

0: ENABLE
1: DISABLE

AD26: BUS PARKING
DEFAULT: 0

0: ENABLE
1: DISABLE

AD25: BINIT# OBSERVATION
DEFAULT: 0

0: ENABLE
1: DISABLE

AD24: IOQ ENABLE
DEFAULT: 1

0: SET IOQ TO 1
1: SET IOQ TO 12

AD22: REF27 ENABLE
DEFAULT: 1

0: PCICLK5 OUTPUT
1: 27 MHz REFERENCE CLOCK INPUT

AD21: CLK_BYPASS (TEST ONLY)
DEFAULT: 0

0: NORMAL
1: TEST MODE

AD20: PCICLK EXPANSION
DEFAULT: 0

1: PCI_REQ#3, PCI_GNT#3 USED AS PCICLK
0: PCI_REQ#3, PCI_GNT#3 USED AS REQ/INT

CBE#3: PROD TEST
DEFAULT: 1

0: SHORT TIMERS
1: NORMAL OPERATION

CBE#0: RESERVE

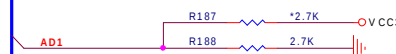
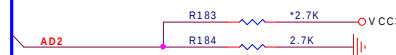
AD[0..31] <<<>>> AD[0..31] 7,13,15,22,25,26

AD14: AGTL+ VDDQ

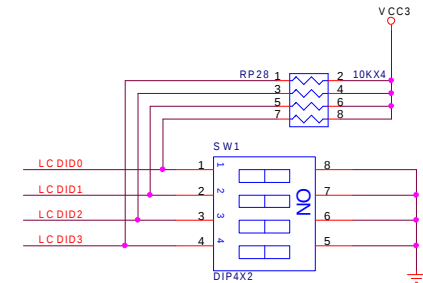
0: 1.2V MOBILE PART
1: 1.7V DESKTOP PART

AD[12] : FLAT PANEL ID MSB

AD[7..4] : FLAT PANEL ID



The AD0 signal strapping pull high for external AGP .
The AD0 signal strapping pull low for internal AGP .



AD3: A_LINK ENABLE
DEFAULT: 0
0: PCI- 33MHz, MULTI POINT CONNECTION
1: A_LINK, POINT TO POINT INTERCONNECT

AD2: AUTO CALIBRATION ENABLE
DEFAULT: 0
0: AUTO CALIBRATION ENABLE
1: AUTO CALIBRATION DISABLE

AD1: FrcShortReset
DEFAULT: 0
0: NORMAL OPERATION
1: TEST MODE

AD0: PCI/AGP CONFIG.
DEFAULT: 1
1: EXTERNAL AGP 66 MHz
0: INTERNAL AGP 33 MHz

PCI_PREQAT# : INTERNAL CLOCK GEN.
DEFAULT: 1

0: DISABLE
1: ENABLE



SYSTEM CONFIG

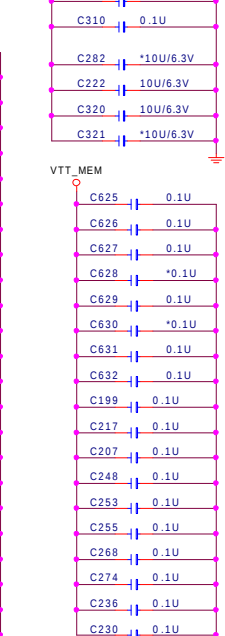
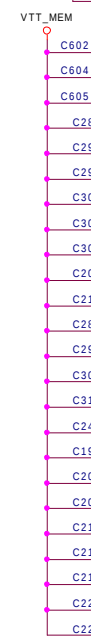
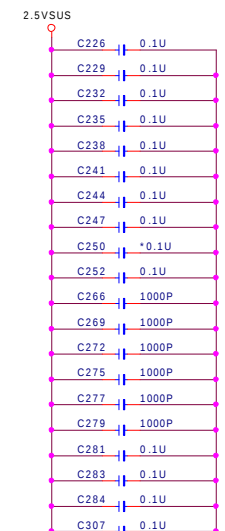
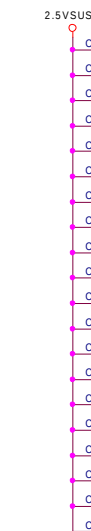
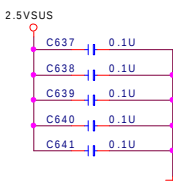
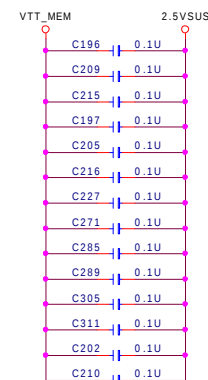
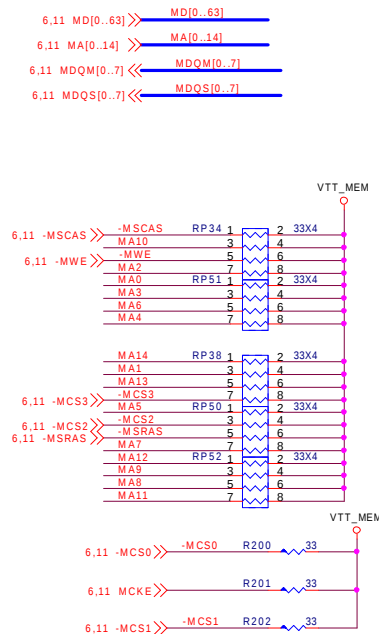
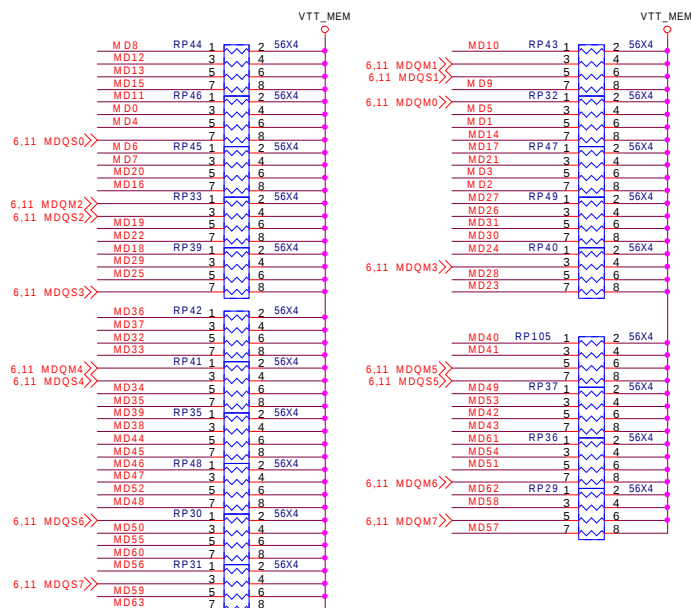
Size B	Document Number	Rev 1A
Date: Tuesday, August 31, 2004	Sheet 9 of 36	

JE5.1 Main Board

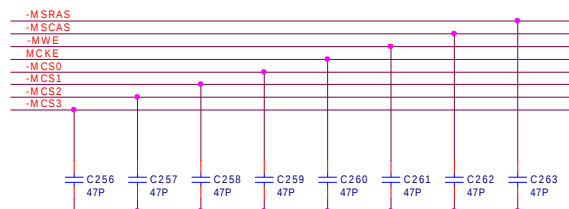
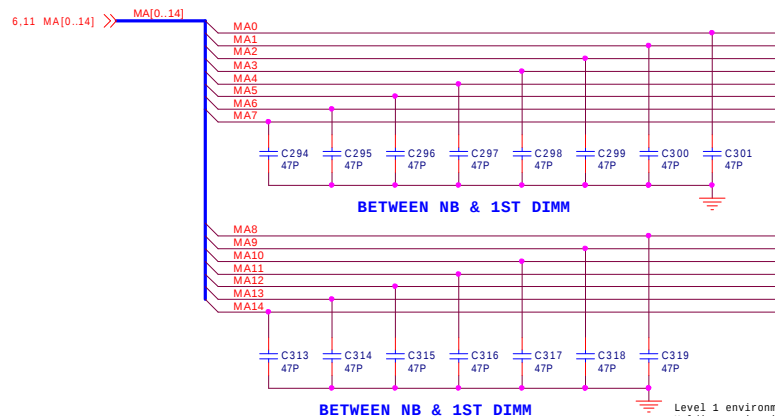
Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

DDR TERMINATOR

10



MA Terminator



Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

QUANTA
COMPUTER

File
Memory -1/2- Terminator

Size B Document Number **JE5.1 Main Board** Rev 1A

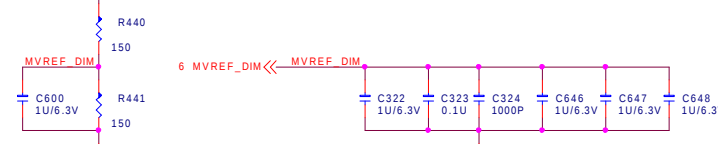
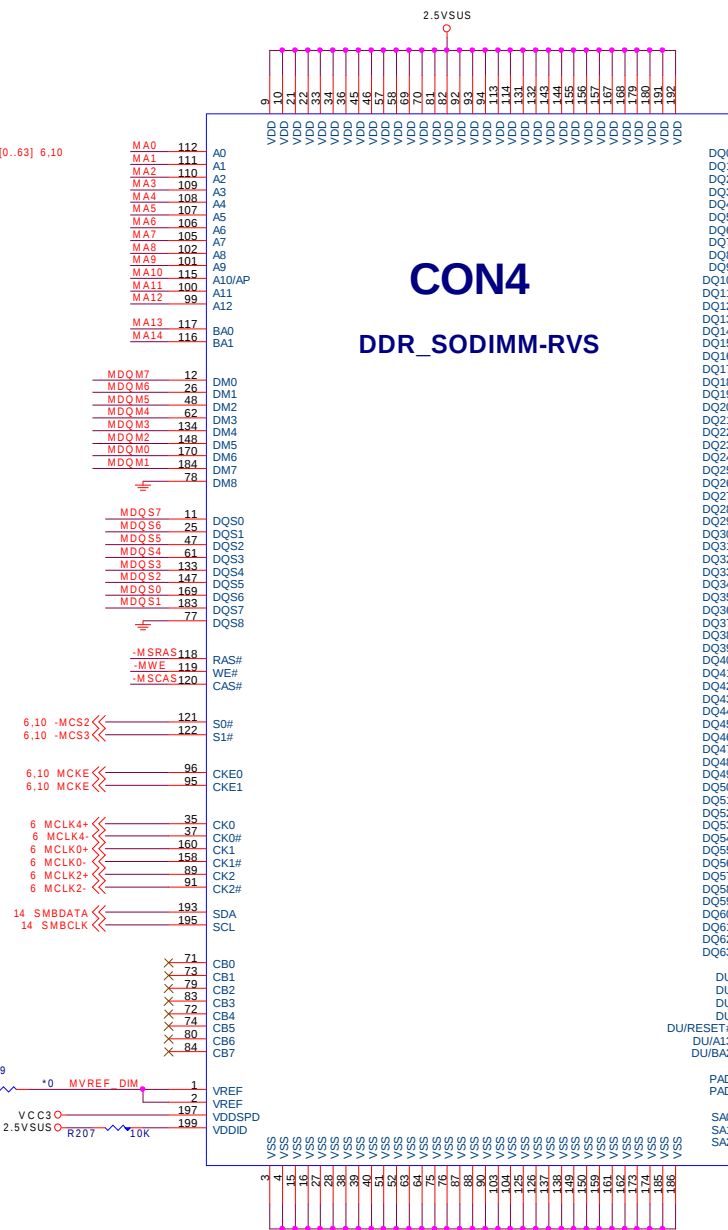
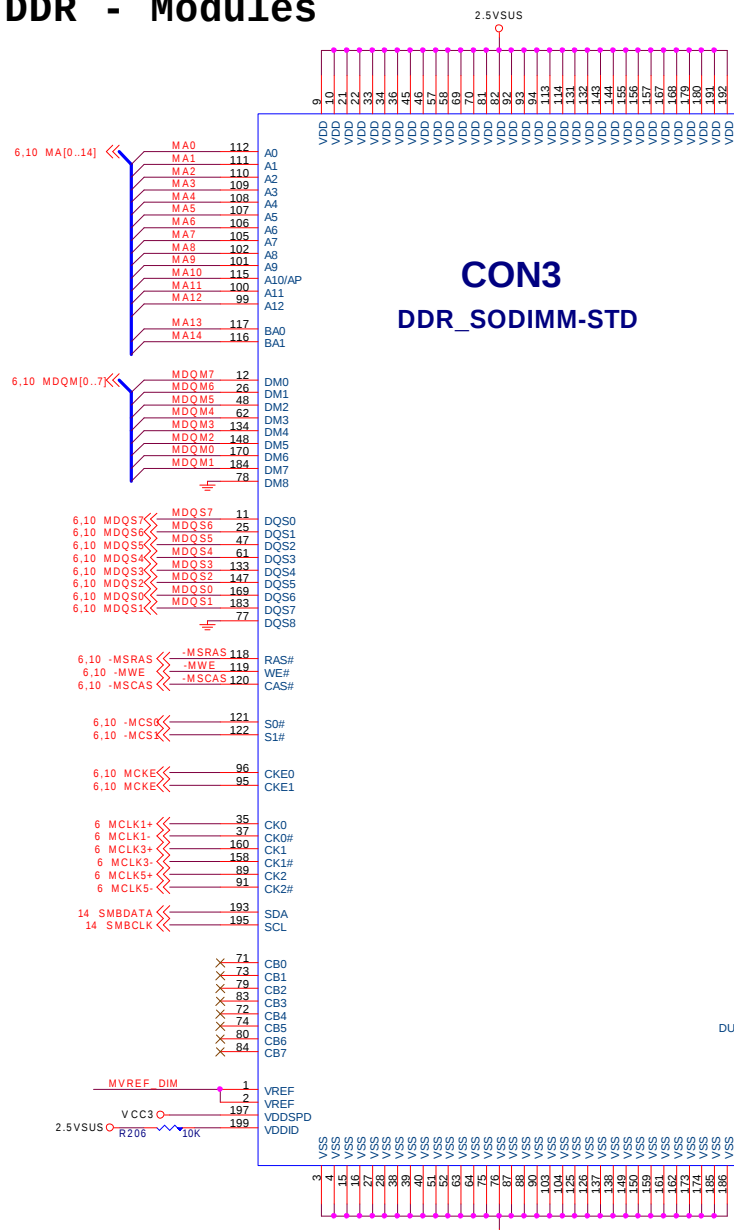
Date: Tuesday, August 31, 2004 Sheet 10 of 36

DDR - Modules

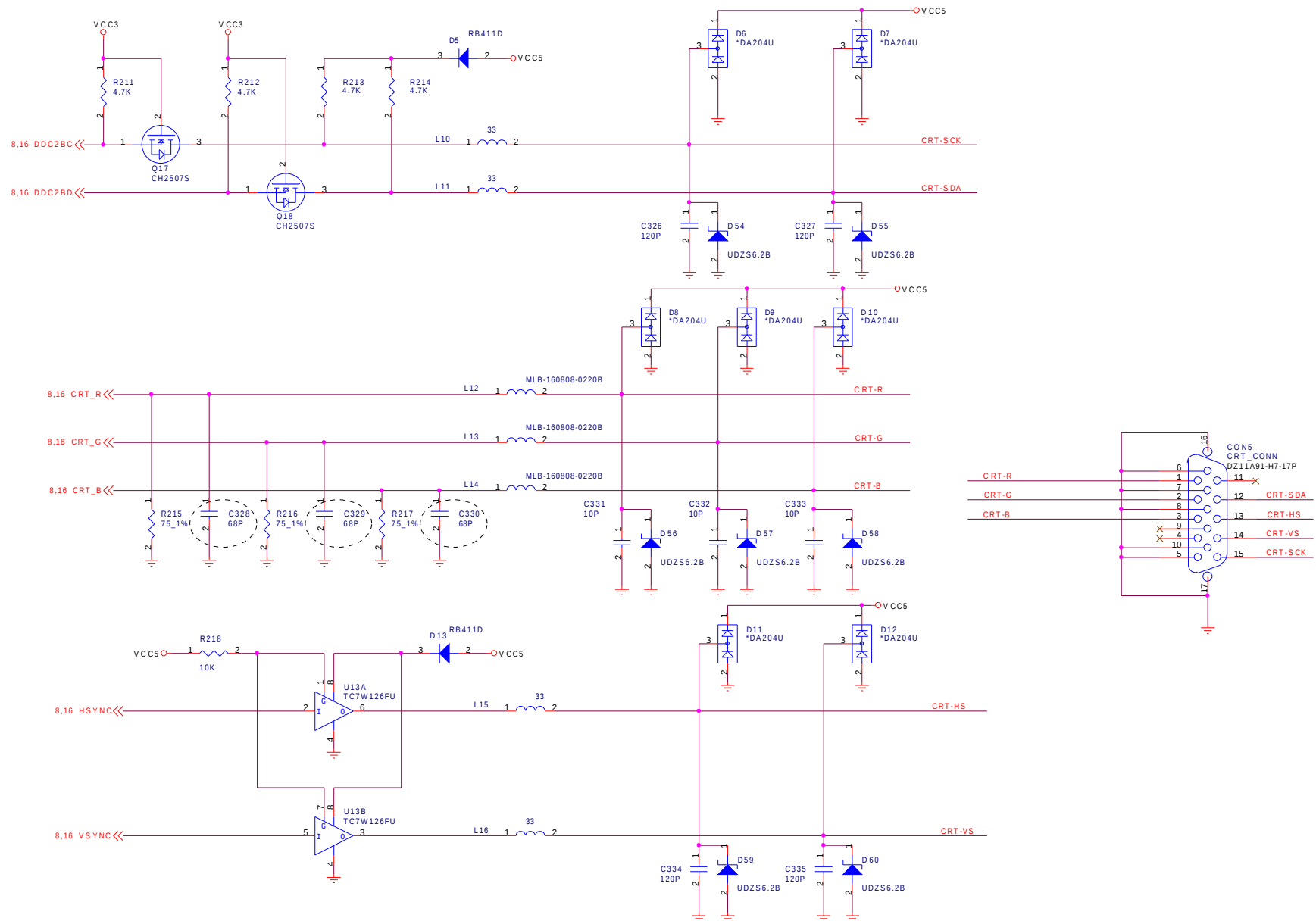
11

CON3
DDR_SODIMM-STD

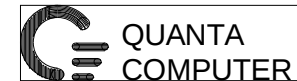
CON4
DDR_SODIMM-RVS



Level 1 environment-related substances should NEVER be used.
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

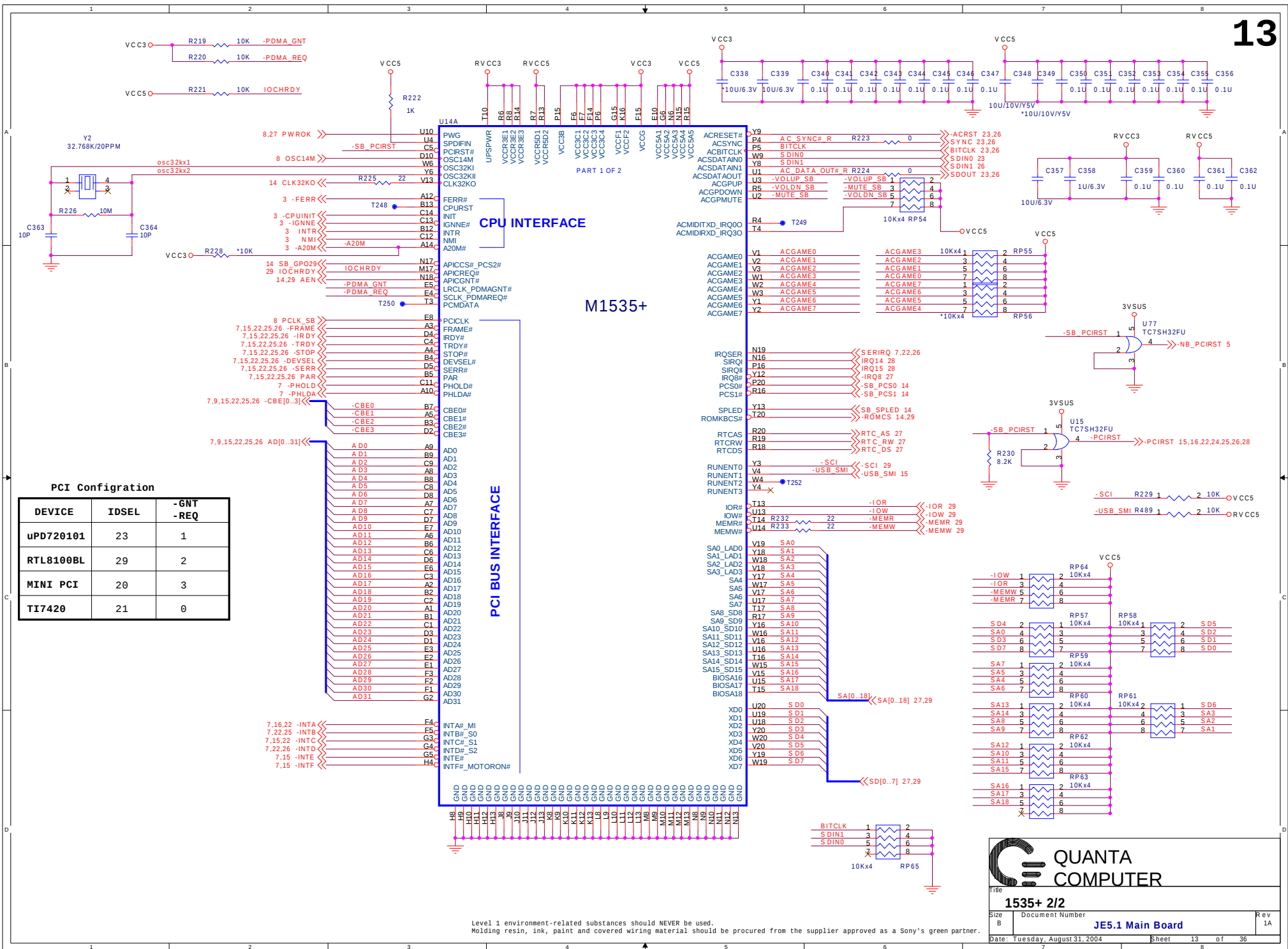


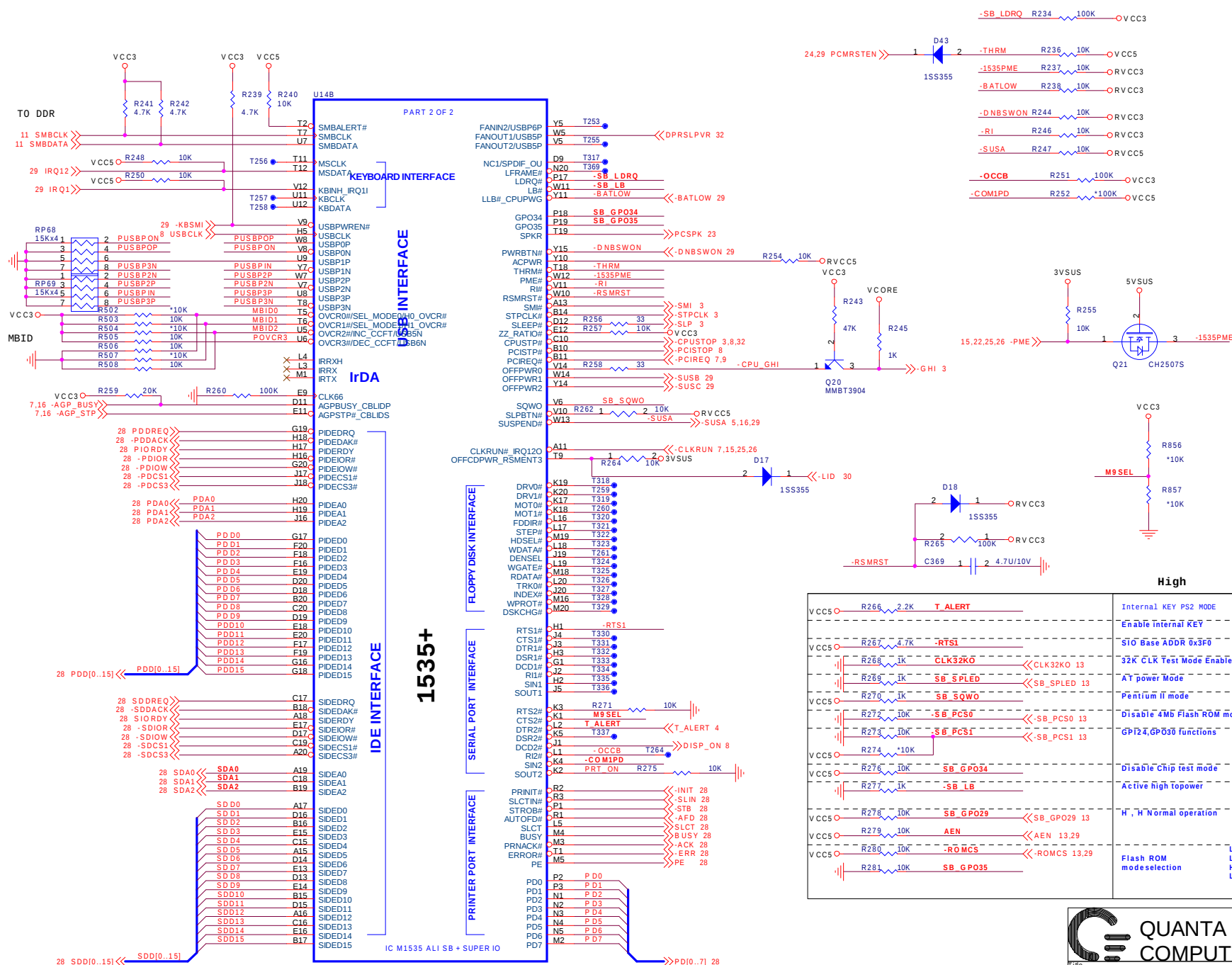
Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



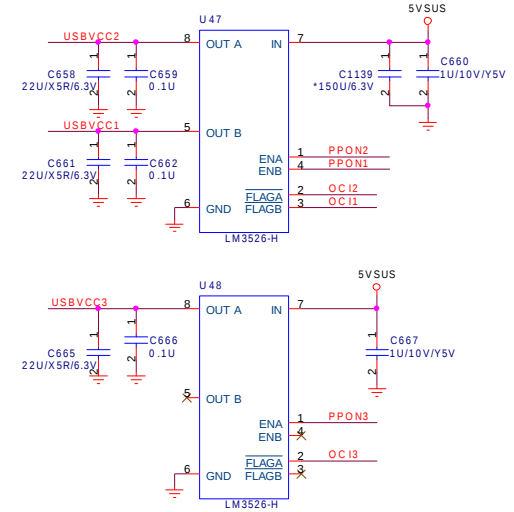
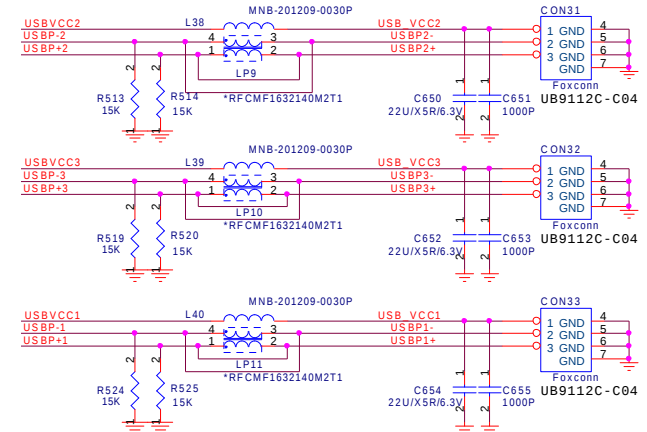
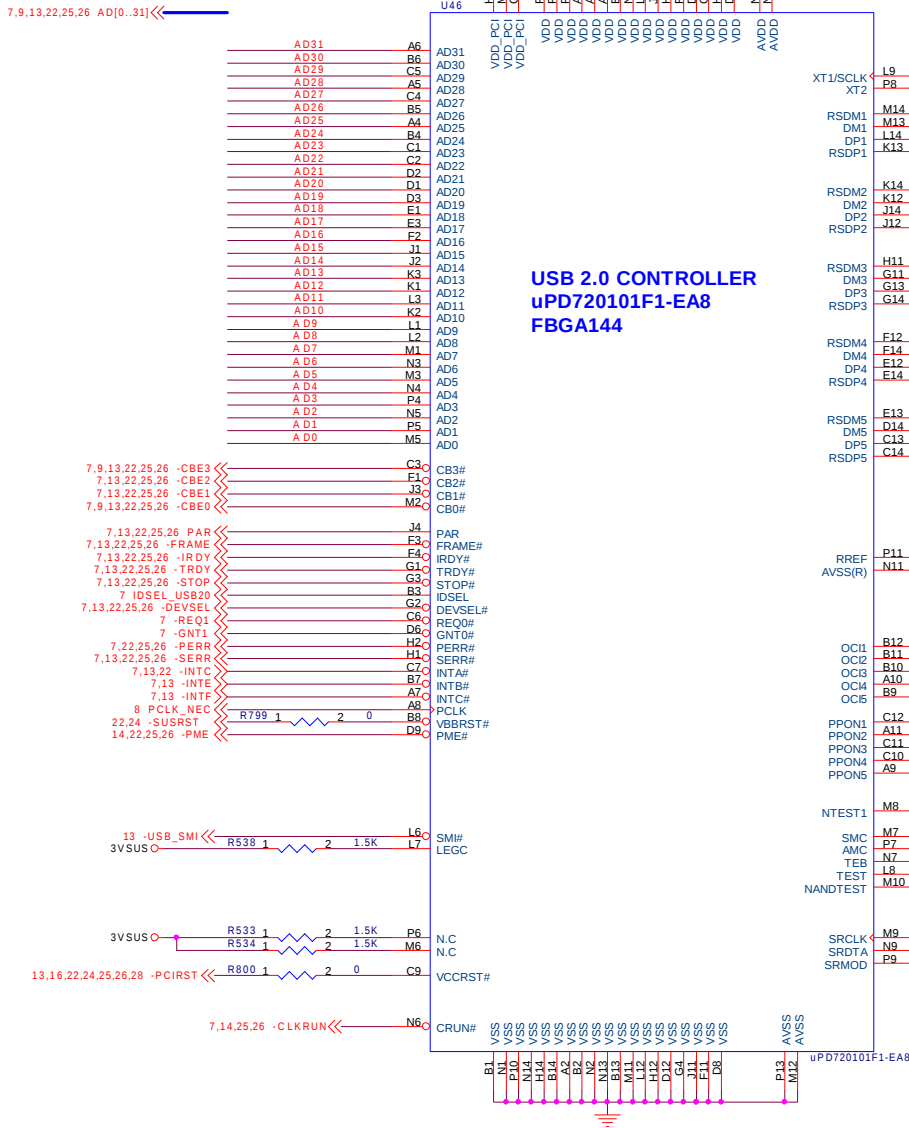
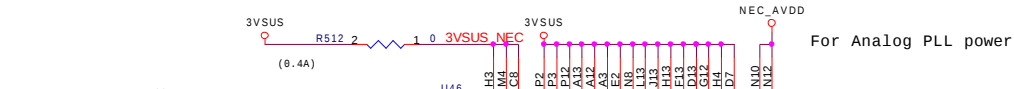
File
CRT PORT/TV
Size B Document Number **JE5.1 Main Board** Rev 1A

Date: Tuesday, August 31, 2004 Sheet 12 of 36

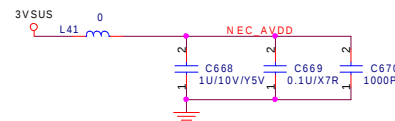
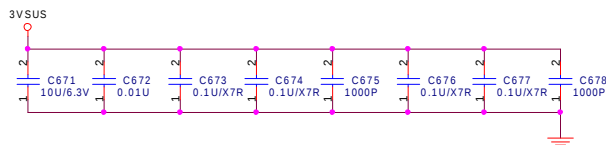
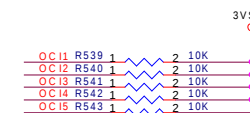




Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



The chip is power switch for USB power .



Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

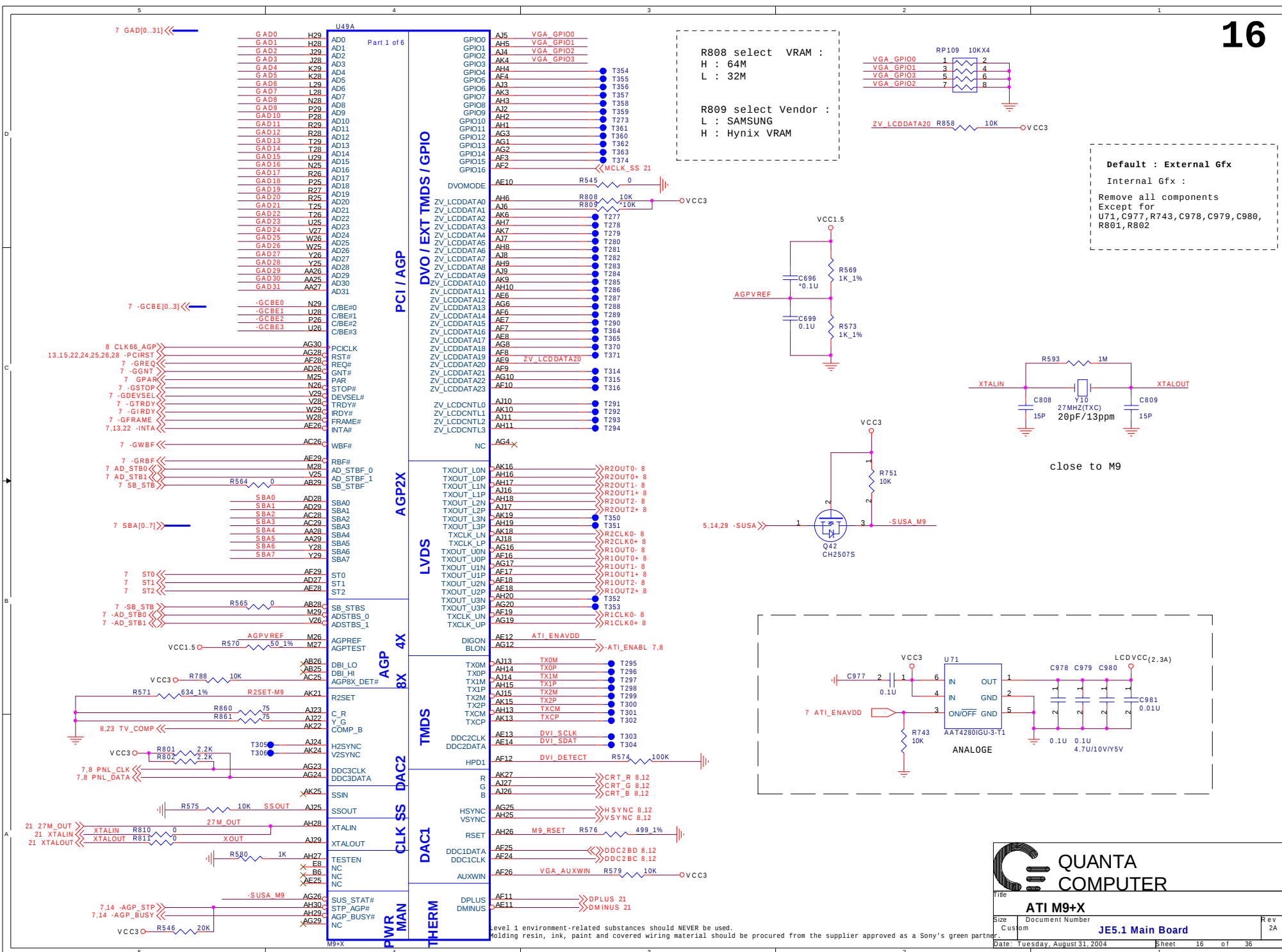
QUANTA COMPUTER

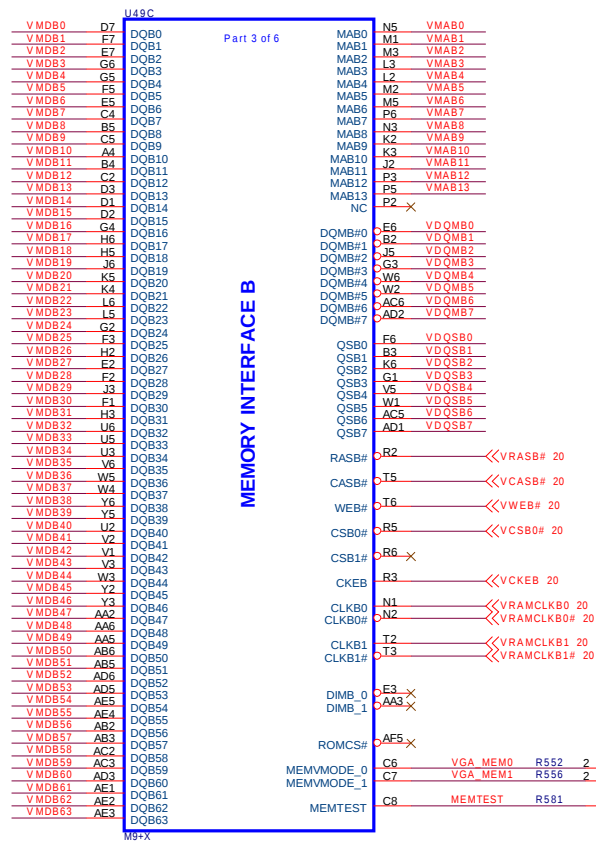
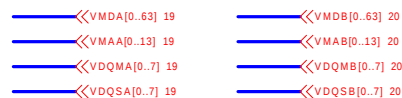
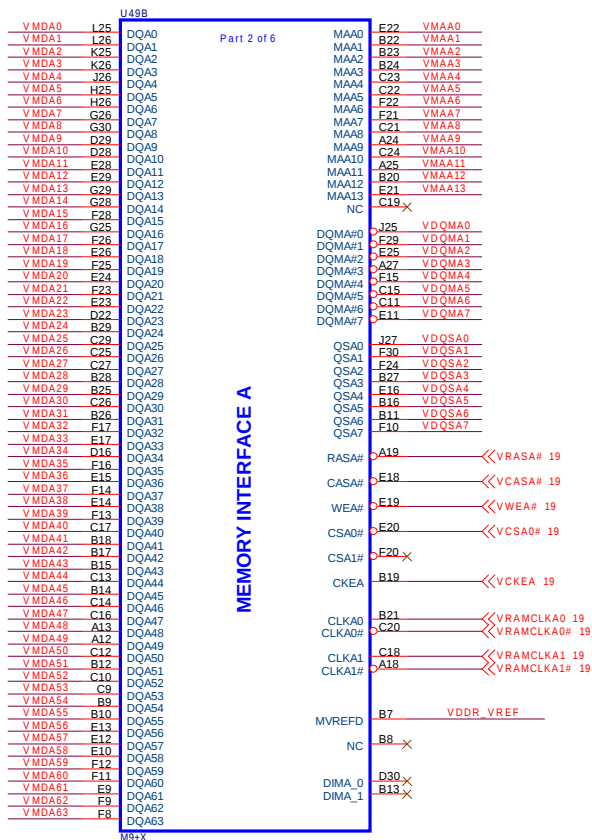
File

USB Controller NEC-uPD720101

Size Custom Document Number **JE5.1 Main Board** Rev 3A

Date: Tuesday, August 31, 2004 Sheet 15 of 36

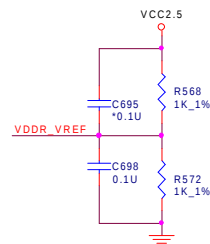




Default : External Gfx

Internal Gfx :

Remove all components .



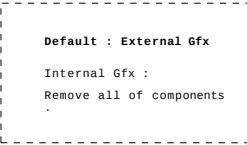
ATI M9+X

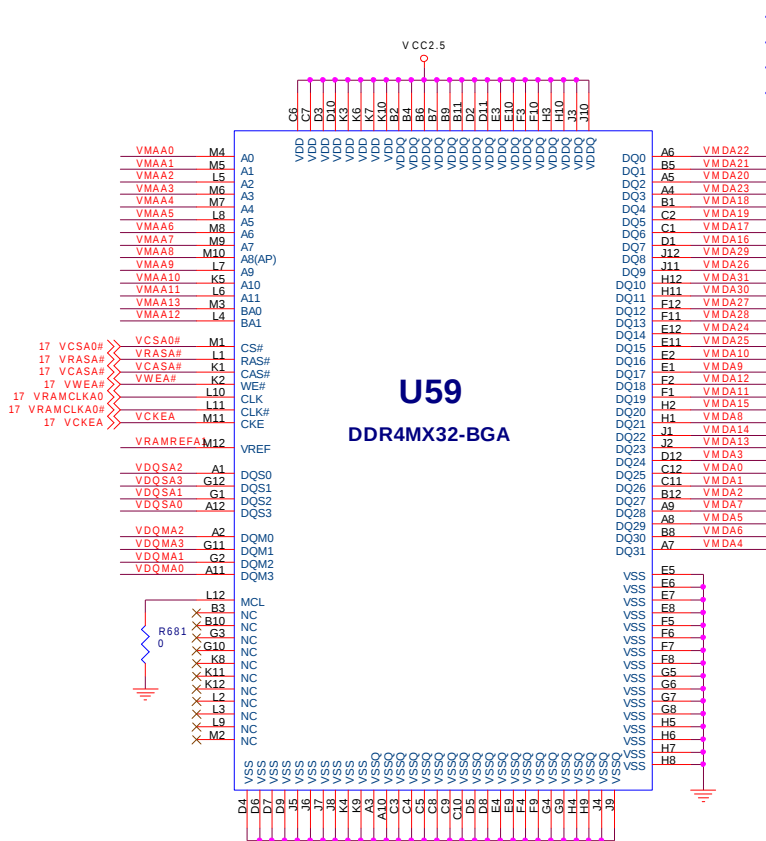
Size Custom Document Number JE5.1 Main Board

Rev 1A

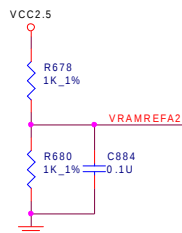
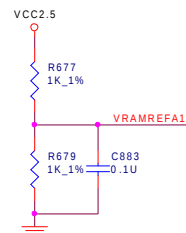
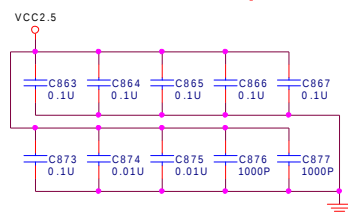
Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

Date: Tuesday, August 31, 2004 Sheet 17 of 36

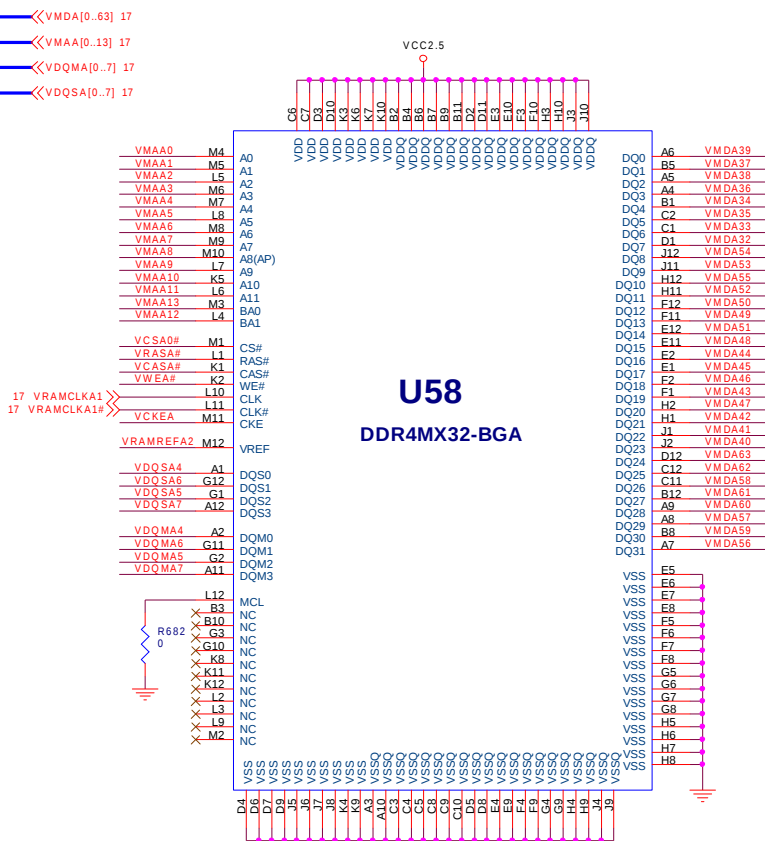




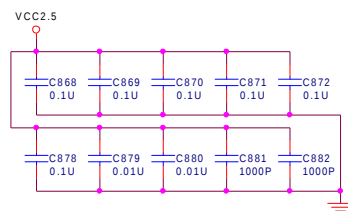
Samsung K4D263238E-VC33
(Hynix -HY5DU28322AF(P))



Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



Samsung K4D263238E-VC33
(Hynix -HY5DU28322AF(P))



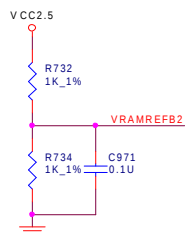
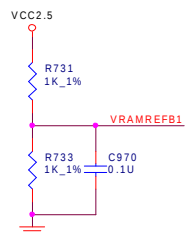
QUANTA
COMPUTER

VGA DDR VRAMx2 1/2

Size	Document Number	Rev
Custom	JE5.1 Main Board	1A
Date: Tuesday, August 31, 2004	Sheet 19 of 36	

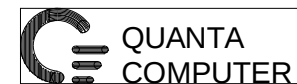
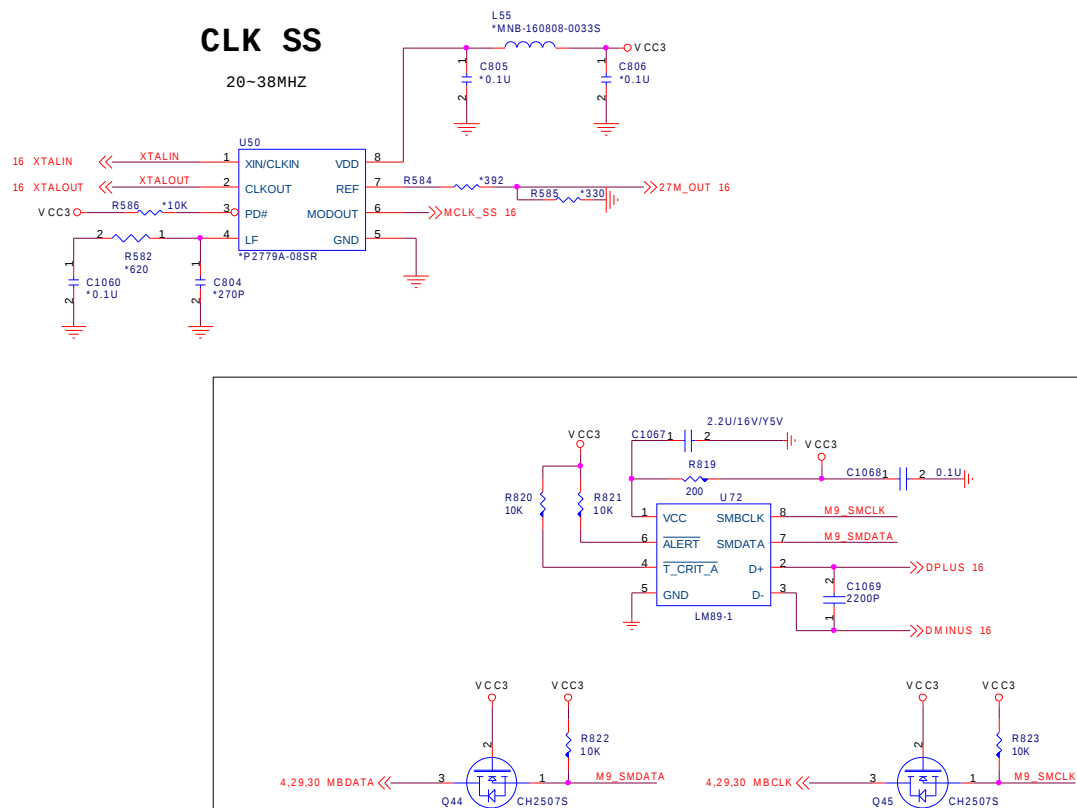


```
Internal Gfx :
Remove all components .
```



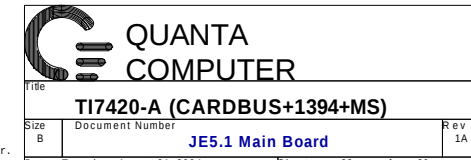
Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

MCLK (230MHZ) SPREAD SPECTRUM LOGIC

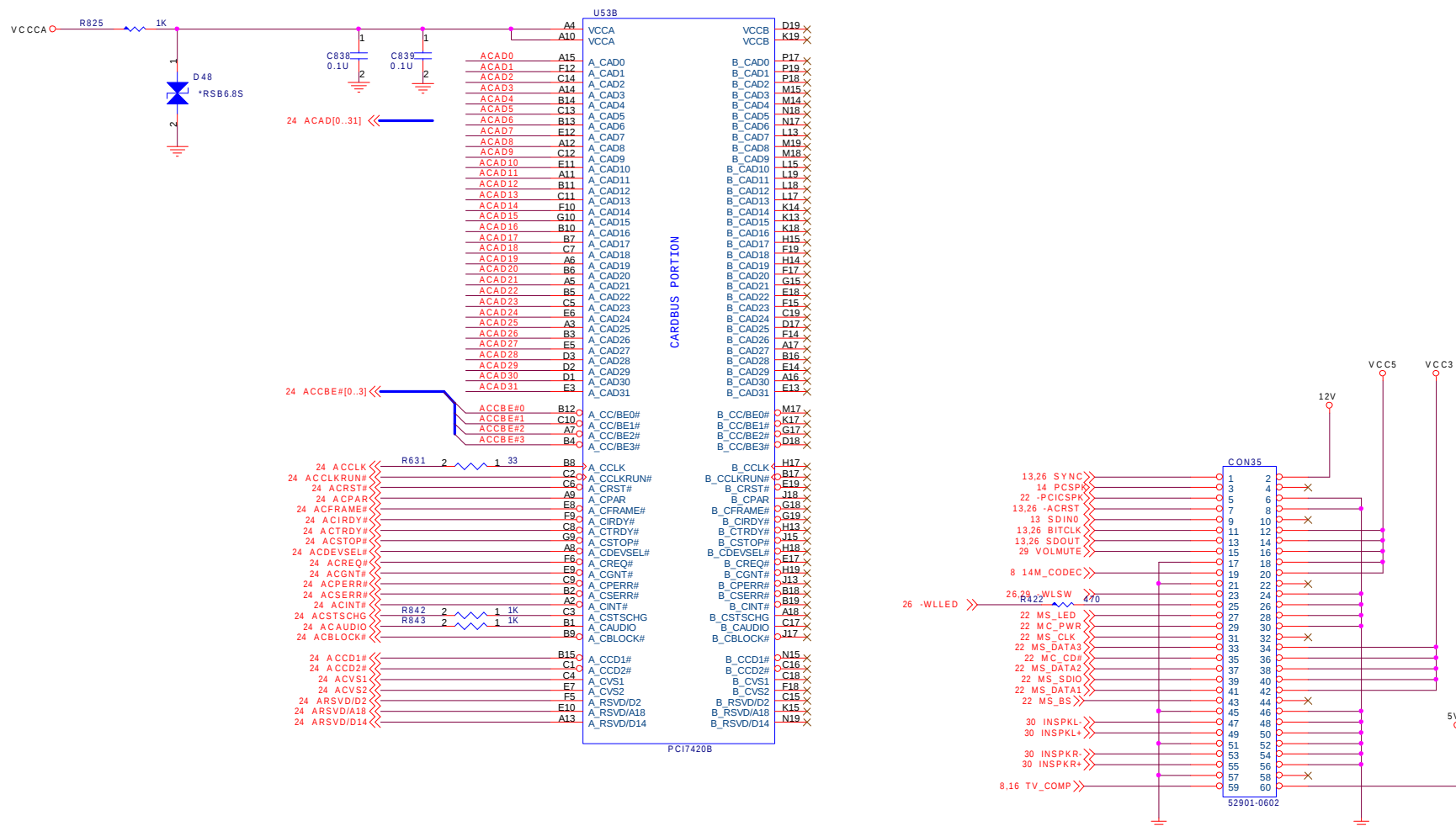


File			AGP SSC	Rev 1A
Size	Custom	Document Number	JE5.1 Main Board	
Date: Tuesday, August 31, 2004			Sheet 21 of 36	

Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

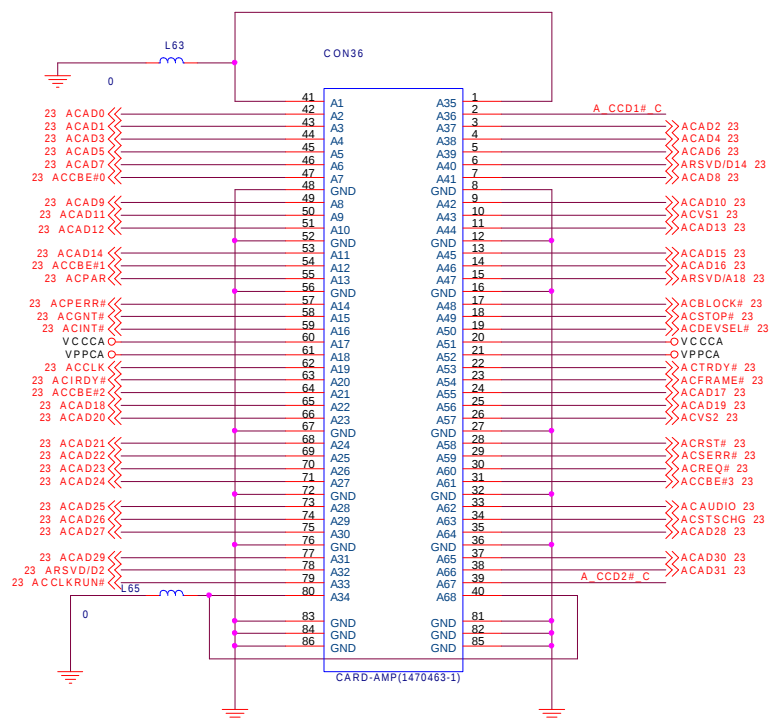


Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

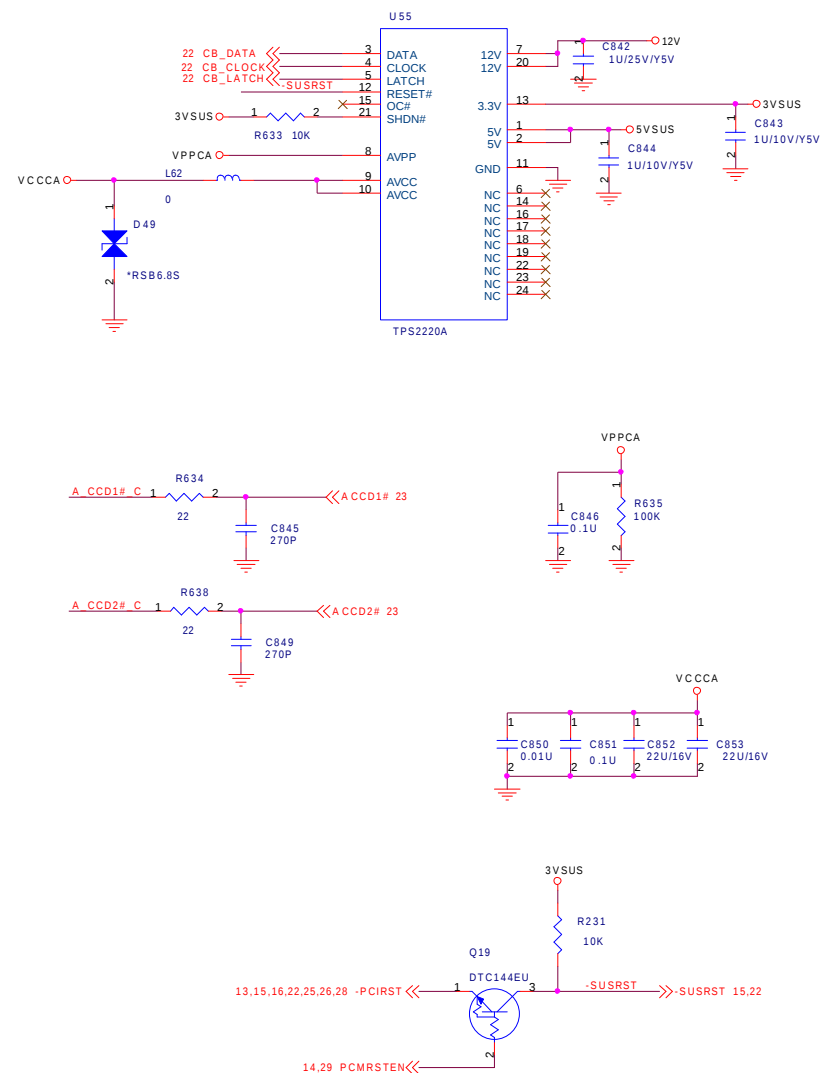


Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

23 ACAD[0..31] << 
 23 ACCBE#[0..3] << 

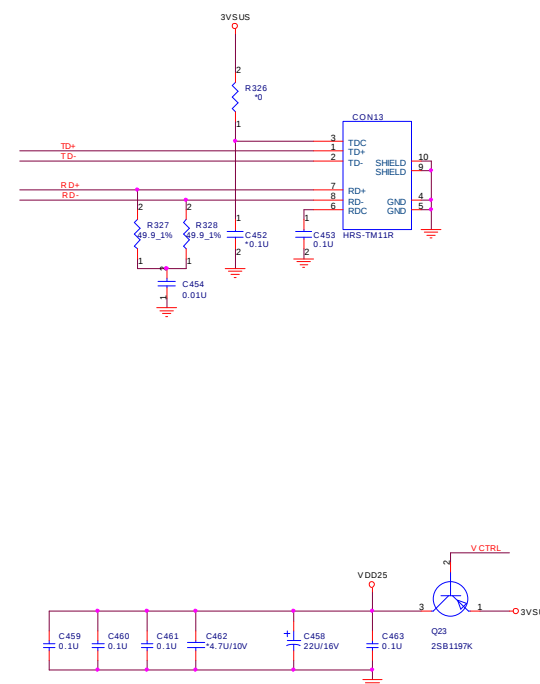


CardBus power switch chip



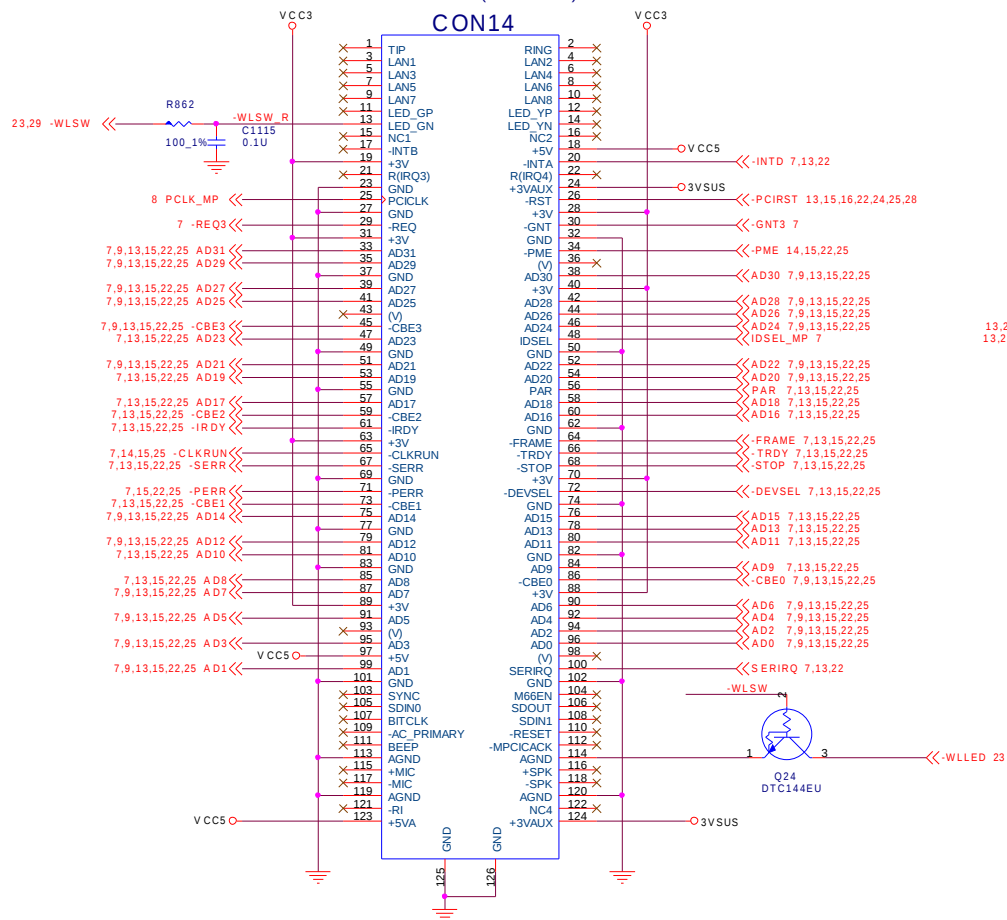
Level 1 environment-related substances should NEVER be used.
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

 QUANTA COMPUTER		
CARDBUS SLOT		
Size B	Document Number	Rev 1A
JE5.1 Main Board		
Date: Tuesday, August 31, 2004	Sheet 24 of 36	

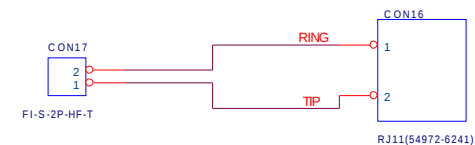
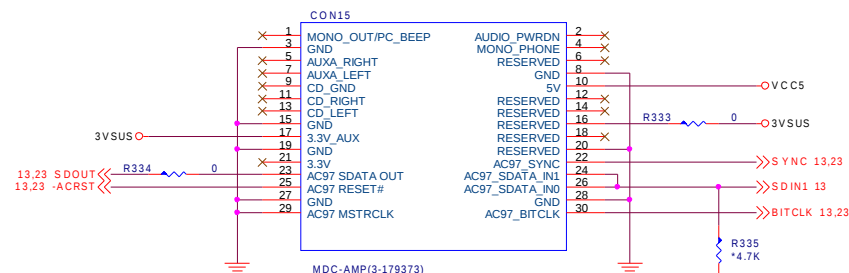


MINIPCI-AMP(440073-124)

CON14



MDC (Height 4.4mm)

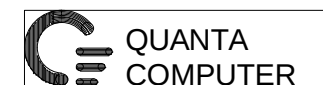


Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

QUANTA
COMPUTER

MiniPCI & MDC

Size	Document Number	Rev
B	JE5.1 Main Board	2A
Date:	Tuesday, August 31, 2004	Sheet 26 of 36



Size B	Document Number JE5.1 Main Board
-----------	--

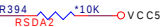
Date: Tuesday, August 31, 2004 Sheet 27 of 36

Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

28



(Hight 13mm)



CON22

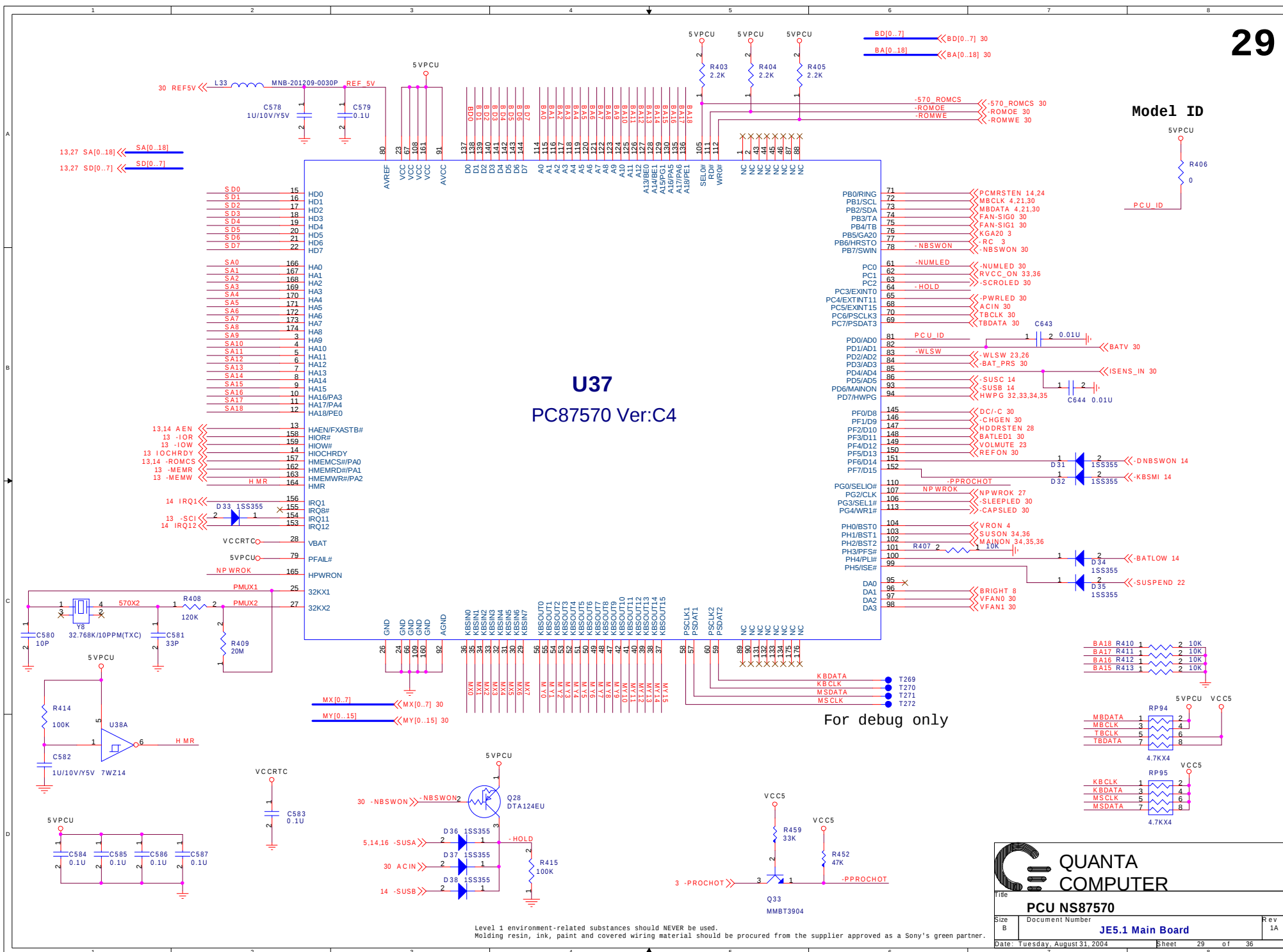


QUANTA
COMPUTER

IDE/HDD/LPT Connector

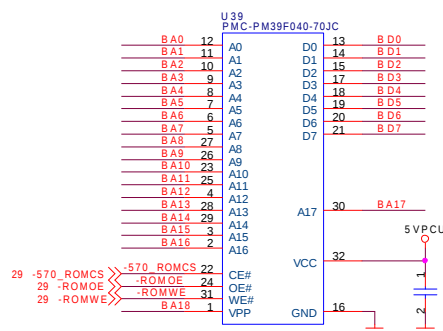
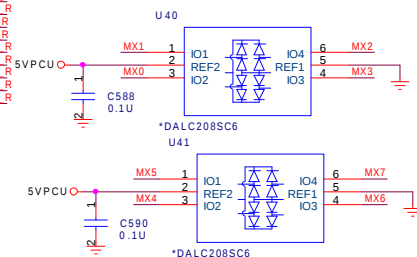
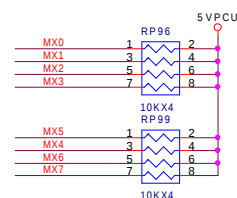
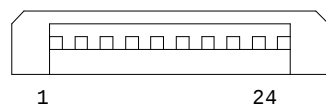
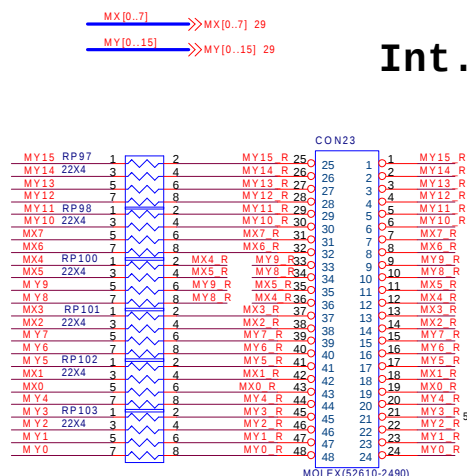
Size B	Document Number JE5.1 Main Board
-----------	--

Date: Tuesday, August 31, 2004 Sheet 28 of 36

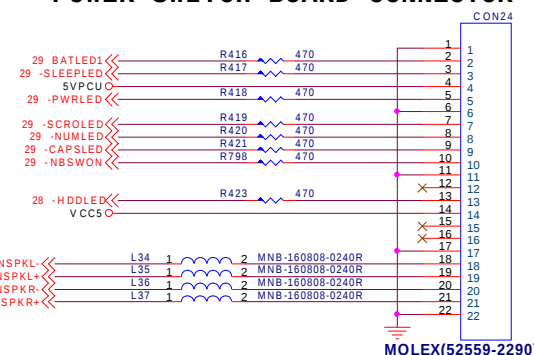


Int. keyboard

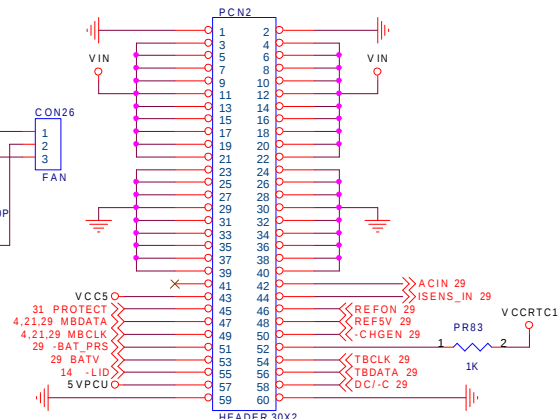
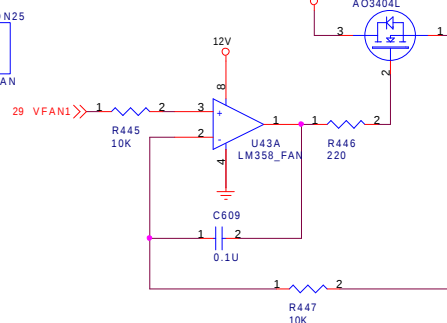
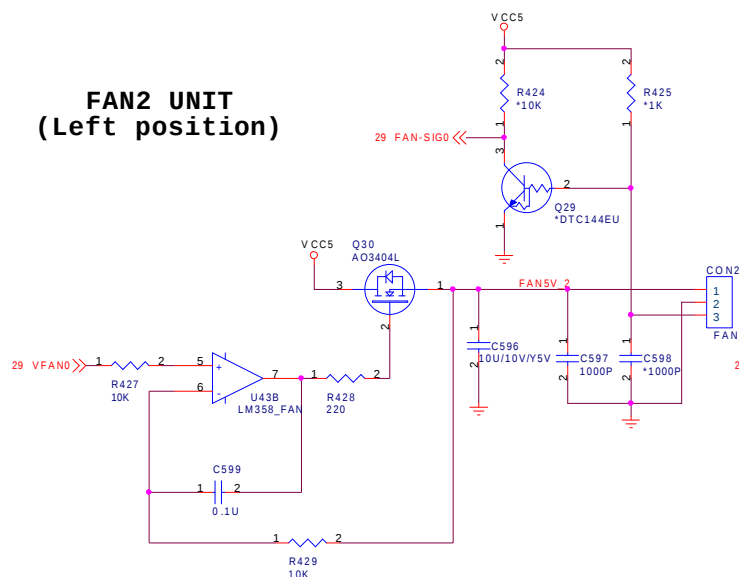
BIOS



POWER SWITCH BOARD CONNECTOR

FAN2 UNIT
(Left position)FAN1 UNIT
(Middle position)

CHARGER BOARD CONNECTOR



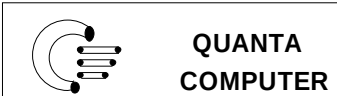
QUANTA
COMPUTER

INT. KEYBOARD/FAN

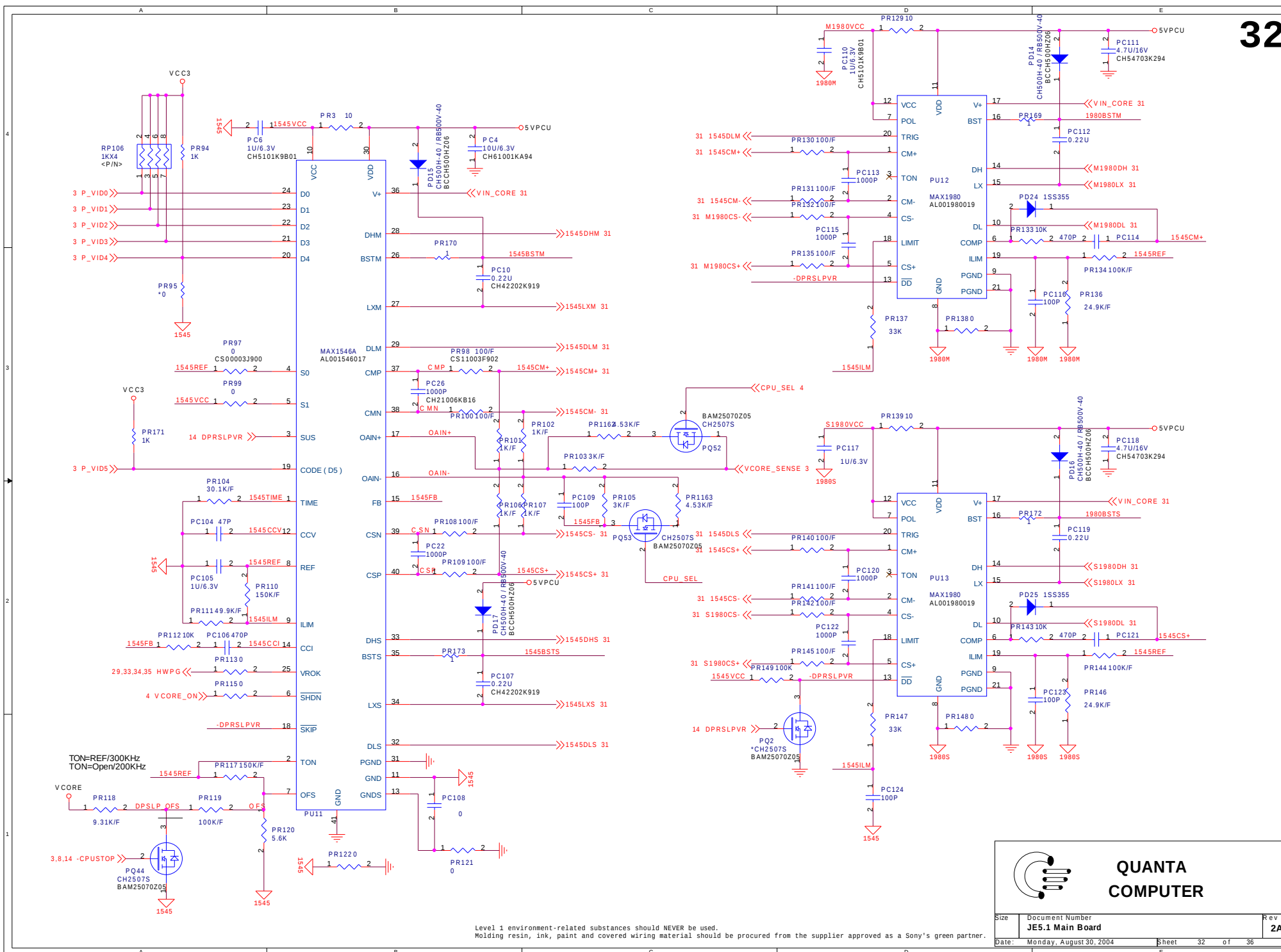
Size Custom Document Number JE5.1 Main Board Rev 1A

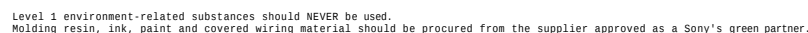
Date: Tuesday, August 31, 2004 Sheet 30 of 36

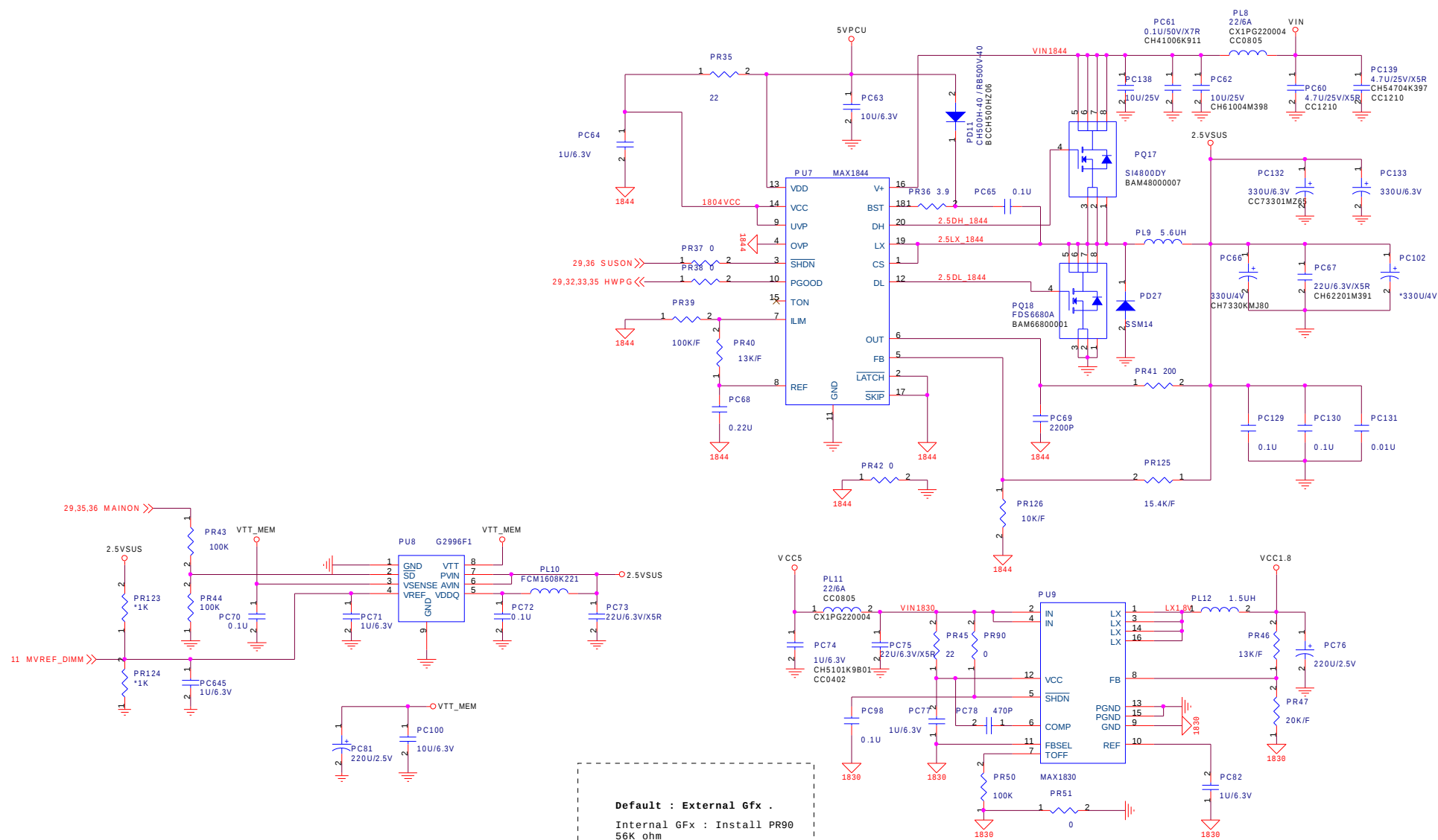
Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



Size	Document Number	Rev
	JE5.1 Main Board	2A
Date:	Monday, August 30, 2004	Sheet 31 of 36



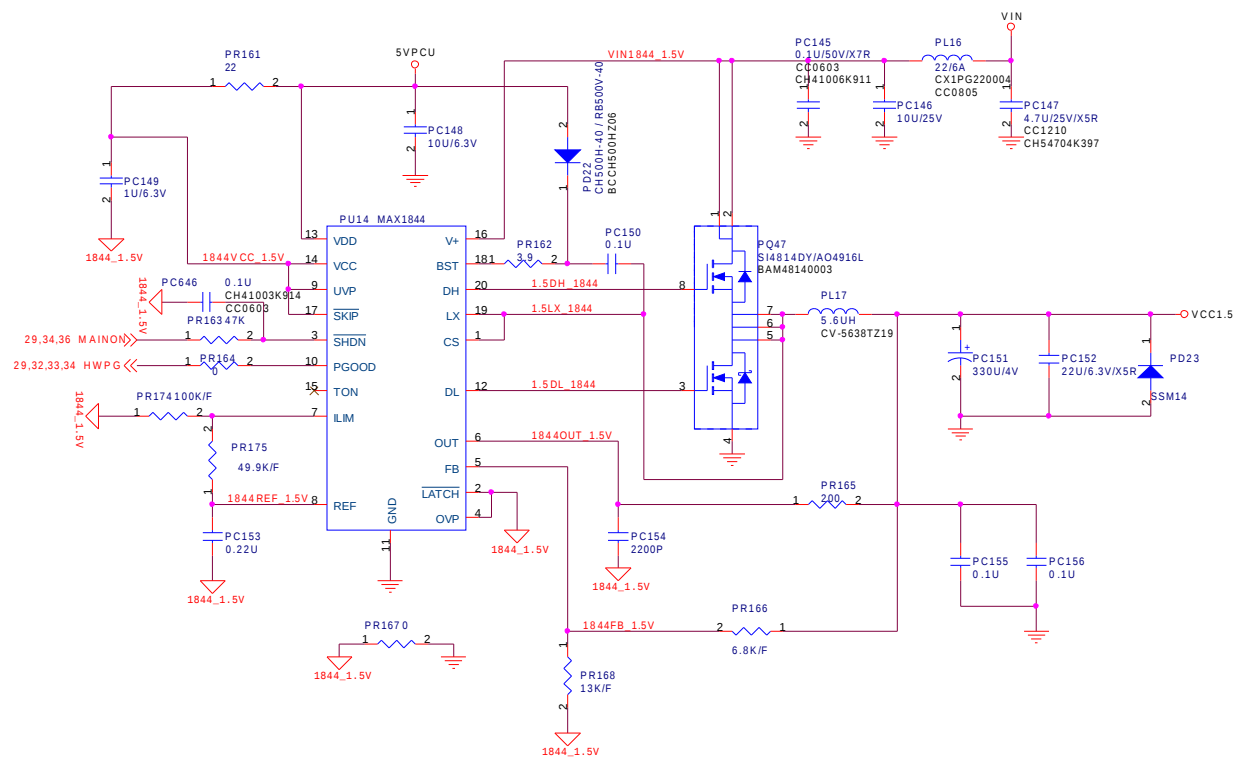




**QUANTA
COMPUTER**

Level 1 environment-related substances should NEVER be used.
 Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.

Size	Document Number	Rev
	JE5.1 Main Board	2A
Date:	Monday, August 30, 2004	Sheet 34 of 36



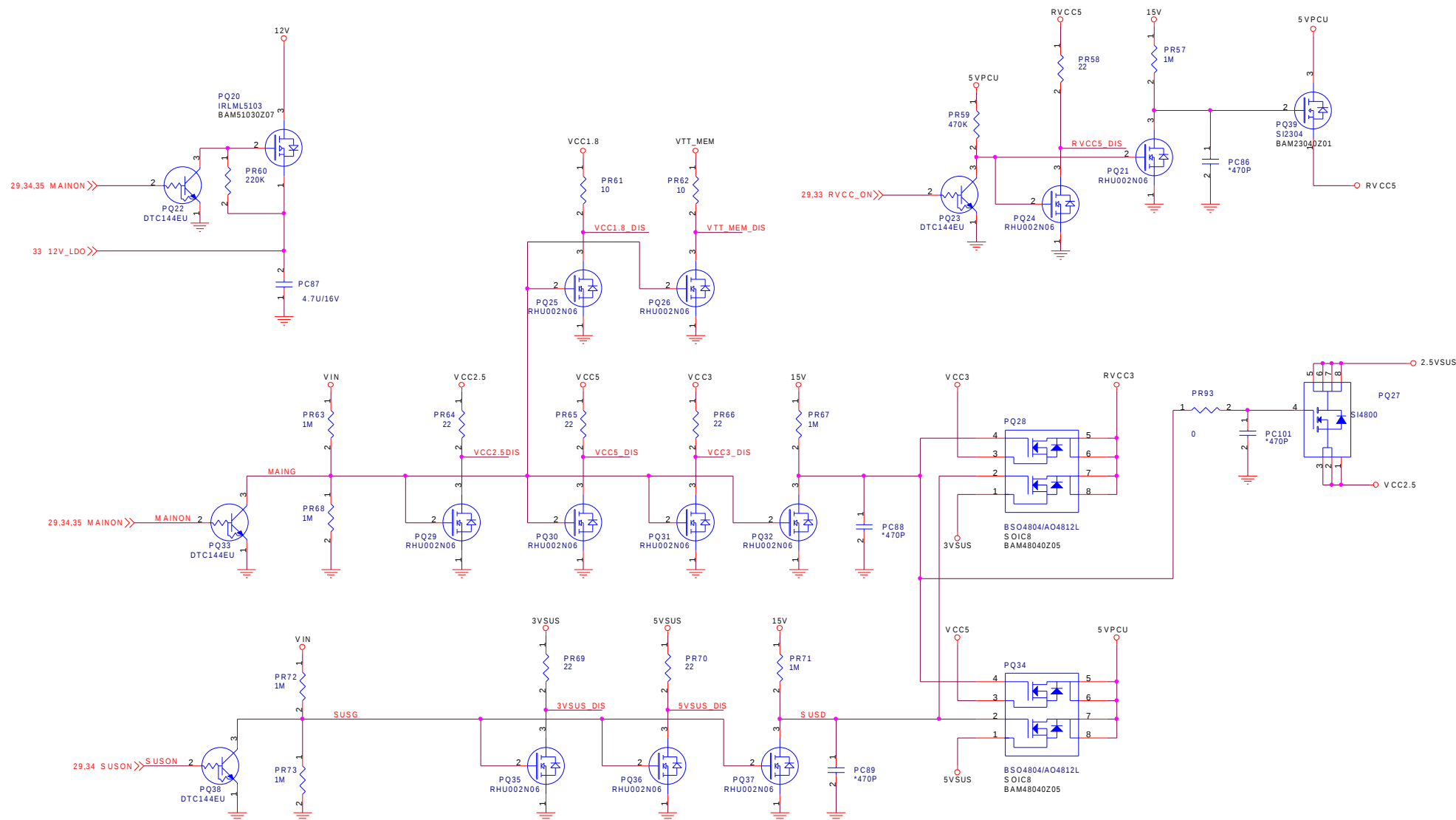
Default : External Gfx

Internal Gfx :

Remove all of components



QUANTA
COMPUTER



Level 1 environment-related substances should NEVER be used.
Molding resin, ink, paint and covered wiring material should be procured from the supplier approved as a Sony's green partner.



QUANTA
COMPUTER

Size	Document Number JE5.1 Main Board	Rev	1
Date:	Monday, August 30, 2004	Sheet	36 of 36