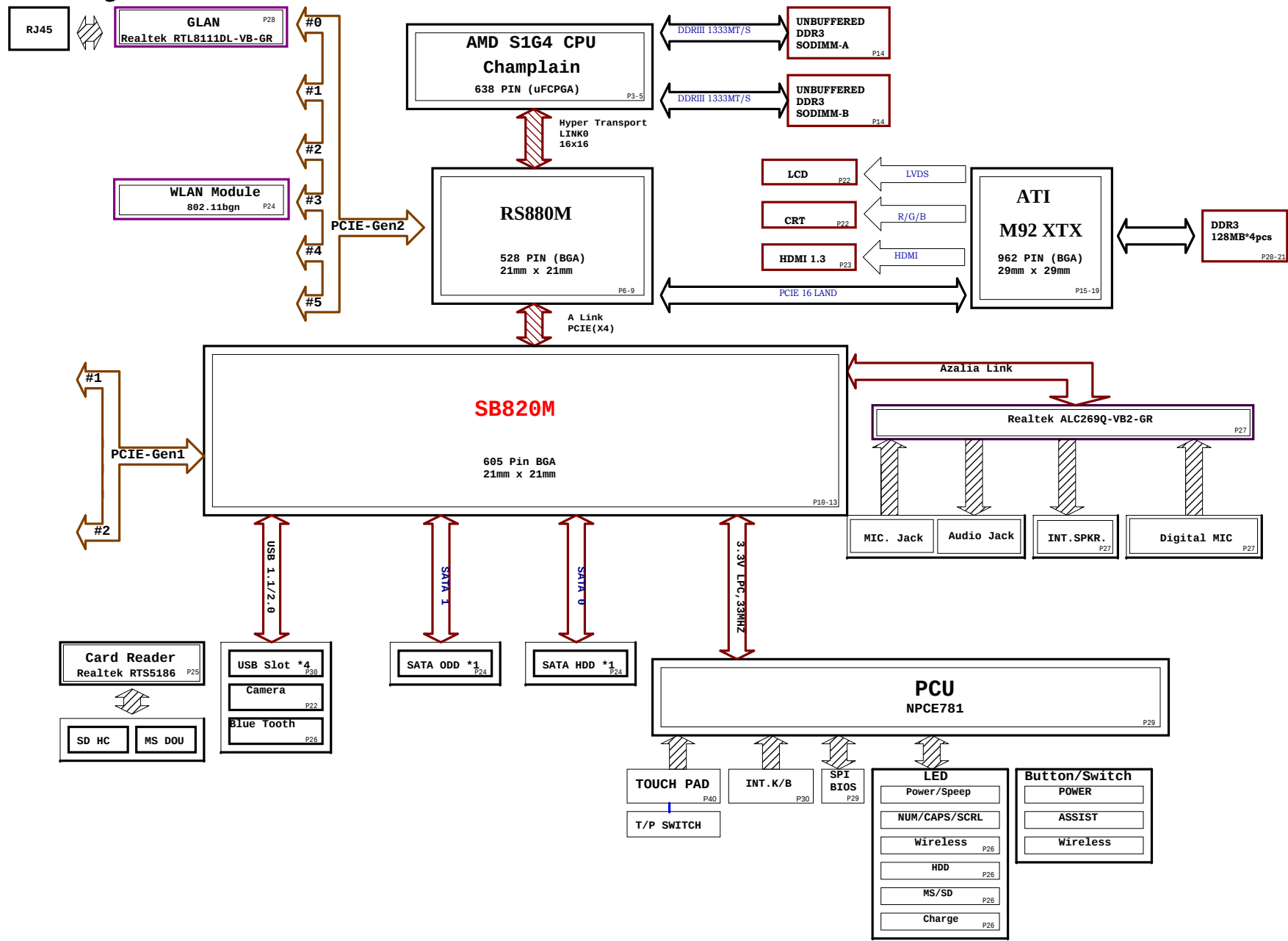


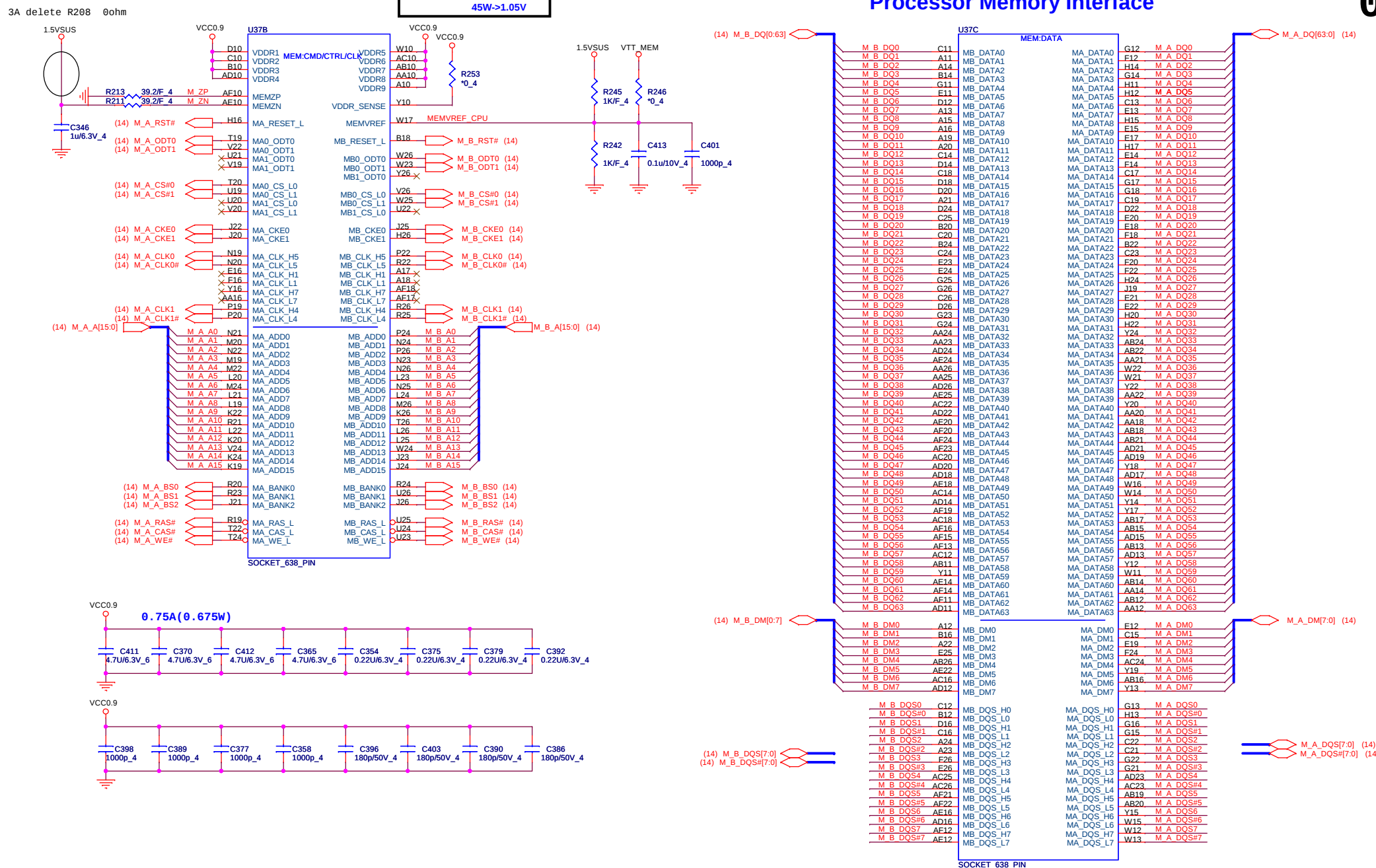
Page	Title of schematic page	Rev.	Date
01	Page List	3A	03/15
02	Block Diagram	3A	
03	CPU-Champlain HT/CONTROL (1/3)	3A	03/15
04	CPU-Champlain MEMORY (2/3)	3A	03/15
05	CPU-Champlain POWER/GND (3/3)	3A	
06	NB-RS880M HT/SP-MEM (1/4)	3A	03/15
07	NB-RS880M GFX/PCIE (2/4)	3A	
08	NB-RS880M SYSTEM (3/4)	3A	03/15
09	NB-RS880M POWER (4/4)	3A	03/15
10	SB-SB820M PCI/CLK/LPC (1/4)	3A	03/15
11	SB-SB820M AUDIO/USB (2/4)	3A	03/15
12	SB-SB820M SATA (3/4)	3A	
13	SB-SB820M POWER (4/4)	3A	03/15
14	DDR3 SODIMM* 2 (5.2mm+9.2mm)	3A	
15	ATI-M92 XTX PCIE/LVDS (1/5)	3A	
16	ATI-M92 XTX Main I/O (2/5)	3A	03/15
17	ATI-M92 XTX Power/GND (3/5)	3A	03/15
18	ATI-M92 XTX Display POWER (4/5)	3A	03/15
19	ATI-M92 XTX MEM I/F (5/5)	3A	03/15
20	ATI-M92 XTX VRAM-B DDR3	3A	
21	CRT/LVDS	3A	03/15
22	HDMI	3A	03/15

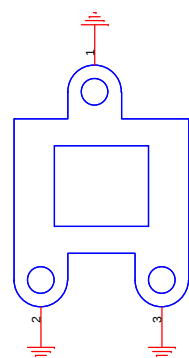
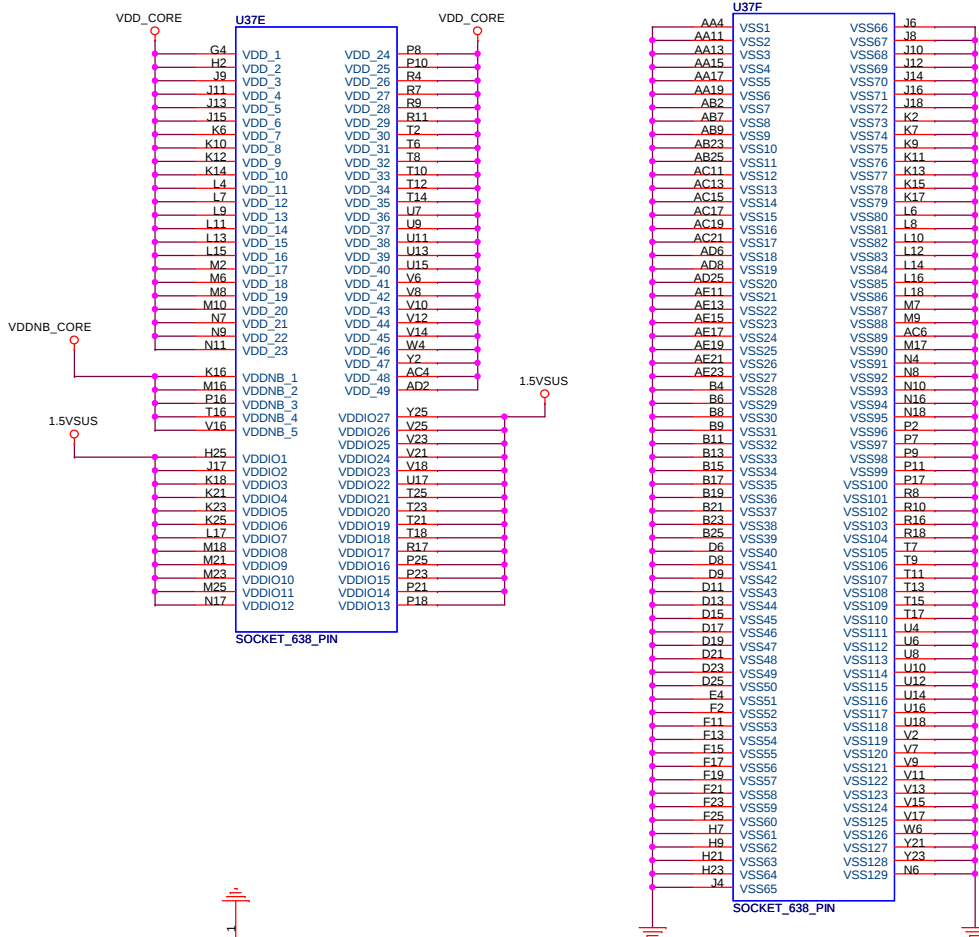
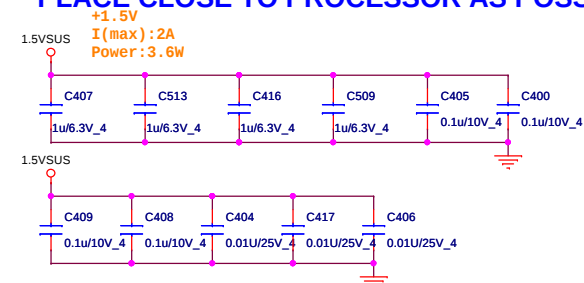
Page	Title of schematic page	Rev.	Date
23	WLAN/HDD/ODD	3A	03/15
24	Card Reader RTS5186	3a	03/15
25	Express Card/BT/LED	3A	03/15
26	Codec-ALC269Q VB5 GR	3A	03/15
27	LAN RTL8111E	3A	03/15
28	NPCE781	3A	03/15
29	KB/USB/FAN/PS	3A	
30	VCORE(ISL6265A)	3A	03/15
31	3VPCU&5VPCU(PM6686)	3A	03/15
32	1.5VSUS/VTT_MEM	3A	03/15
33	DYN_VCC1.1(OZ8116LN)	3A	03/15
34	VCC1.1(OZ8116LN)-7A	3A	03/15
35	VGA_CORE(OZ8116)-16A	3A	03/15
36	VCC1.8/0.9/2.5/1.0	3A	03/15
37	POWER(BAT IN / ADA IN/ UL)	3A	03/15
38	CHARGER (ISL6252A)	3A	03/15
39	Small board connector	3A	03/15
40	Decoupling(Cap)	3A	03/15
41	Power Sequence	3A	
42	CHANGE LIST	3A	03/15

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

 Quanta Computer Inc. PROJECT : NE7		Rev 3A
AMD Danube		
Date:	Monday, March 22, 2010	Sheet 1 of 42







BK1
CPU BKT/SECC/M2.5/NUT=3/3P

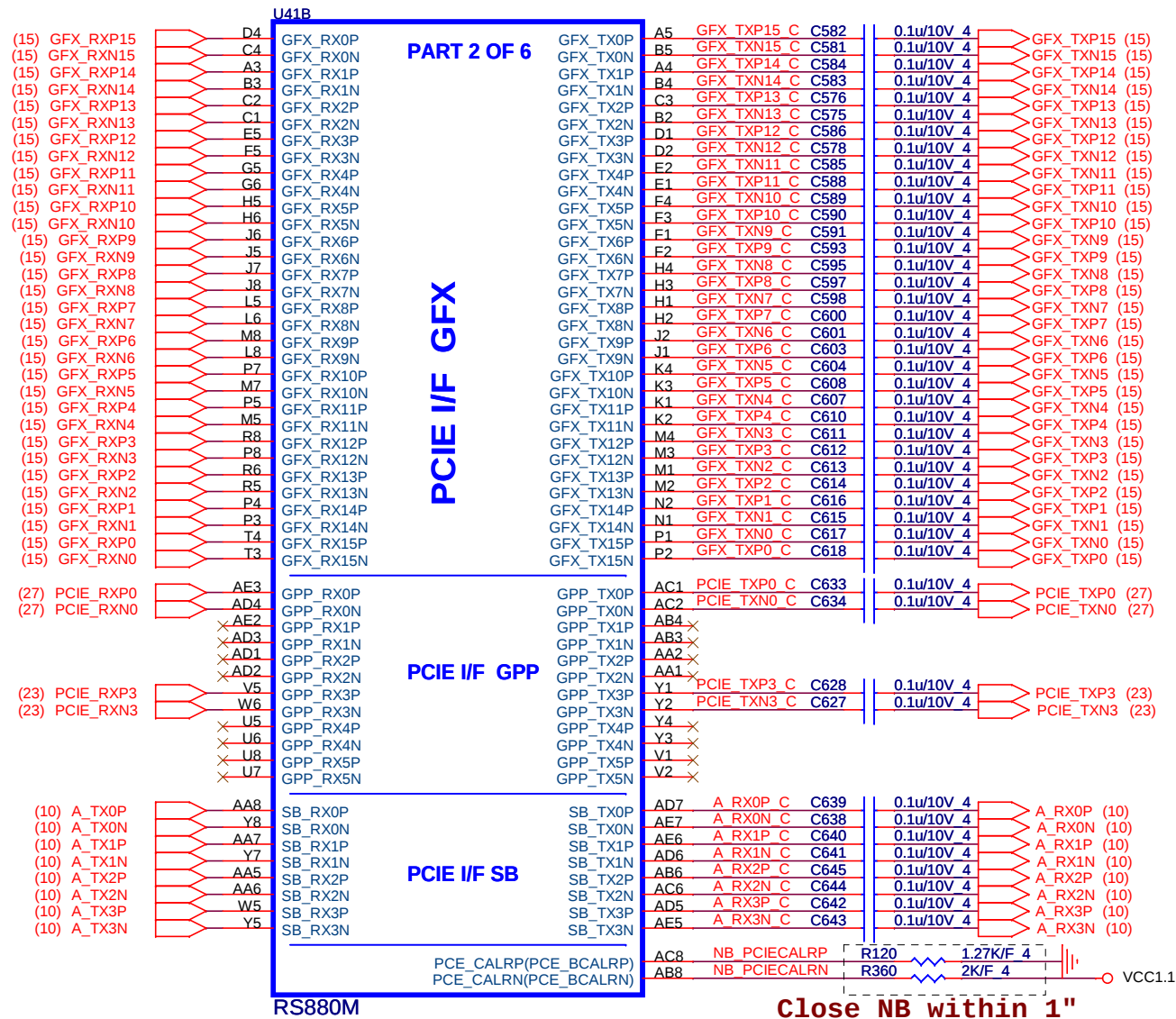
U10D	PAR 4 OF 6	
A612	MEM_A0Q(NC)	MEM_DQ0/DV0_VSYNC(NC)
A613	MEM_A1Q(NC)	MEM_DQ1/DV1_HSYNC(NC)
A614	MEM_A2Q(NC)	MEM_DQ2/DV2_DE(NC)
A615	MEM_A3Q(NC)	MEM_DQ3/DV3_DQ(NC)
A616	MEM_A4Q(NC)	MEM_DQ4Q(NC)
A617	MEM_A5Q(NC)	MEM_DQ5/DV0_D1(NC)
A618	MEM_A6Q(NC)	MEM_DQ6/DV0_D2(NC)
A619	MEM_A7Q(NC)	MEM_DQ7/DV0_D3(NC)
A620	MEM_A8Q(NC)	MEM_DQ8/DV0_D3(NC)
A621	MEM_A9Q(NC)	MEM_DQ9/DV0_D5(NC)
A622	MEM_A10Q(NC)	MEM_DQ10/DV0_D6(NC)
A623	MEM_A11Q(NC)	MEM_DQ11/DV0_D1(NC)
A624	MEM_A12(NC)	MEM_DQ12(NC)
Y4	MEM_A13(NC)	MEM_DQ13/DV0_D8(NC)
A626	MEM_BA0(NC)	MEM_DQ14/DV0_D10(NC)
A627	MEM_BA1(NC)	MEM_DQ15/DV0_D11(NC)
A628	MEM_BA2(NC)	MEM_DQ0SDP/DV0_IDCKP(NC)
		MEM_DQ0SDP/DV0_IDCKP(NC)
W12	MEM_RAS(NC)	MEM_DQ0SP(NC)
W12	MEM_CAS(NC)	MEM_DQ0S1(NC)
W12	MEM_WB(NC)	
A630	MEM_CSQ(NC)	MEM_DMQ(NC)
A631	MEM_CKE(NC)	MEM_DM1/DV0_D8(NC)
W4	MEM_ODT(NC)	
W15	MEM_CKP(NC)	IOPLLVD018(NC)
W14	MEM_CKN(NC)	IOPLLVD019(NC)
A634	MEM_COMP(NC)	IOPLLVSS(NC)
A635	MEM_COMP(NC)	
A636		MEM_VREF(NC)

PART 1 OF 6

HYPER TRANSPORT CPU I/F



2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



Layout Swap
 TX11, TX10, TX9,
 TX8, TX7, TX6, TX5,
 TX4, TX3, TX2, TX0

GLAN

WLAN

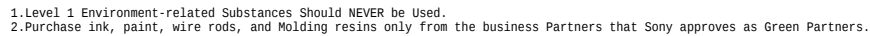


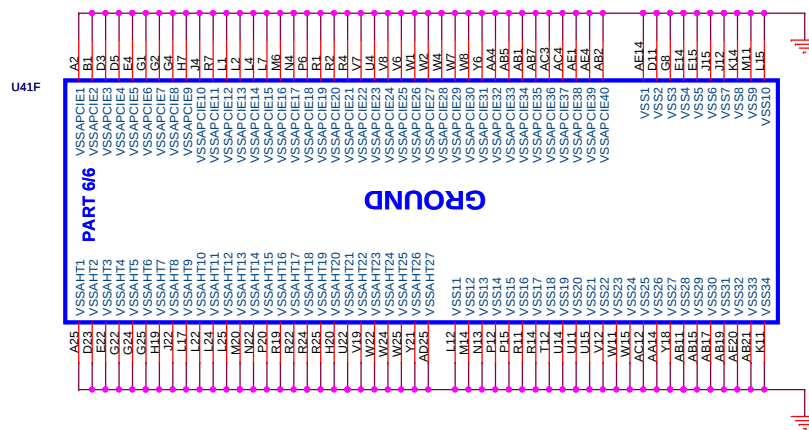
Quanta Computer Inc.

PROJECT : NE7

Size	Document Number	Rev
	RS880M GFX/PCIE(2/4)	3A
Date:	Monday, March 22, 2010	Sheet 7 of 42

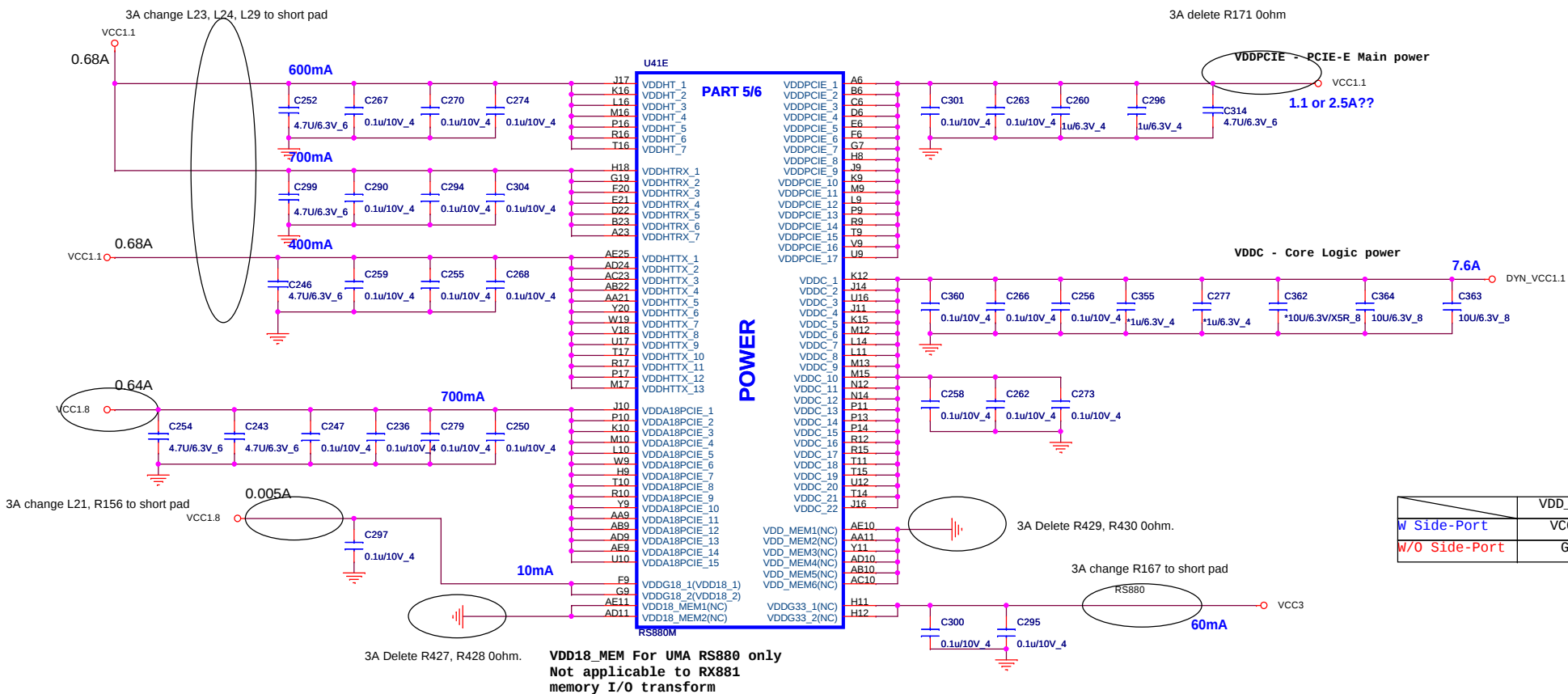
- Level 1 Environment-related Substances Should NEVER be Used.
- Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.





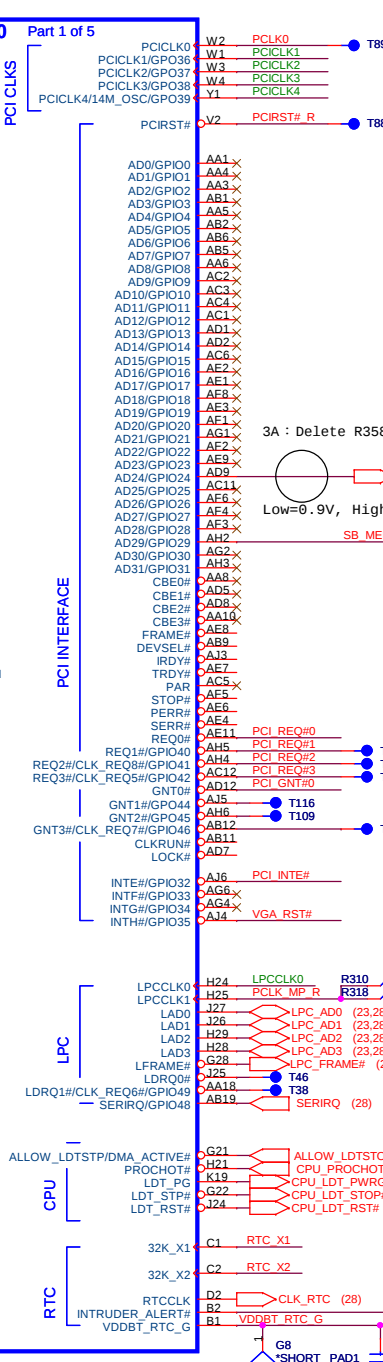
RS880M/RX881 POWER DIFFERENCE TABLE

PIN NAME	RX881	RS880M	PIN NAME	RX881	RS880M
VDDHT	+1.1V	+1.1V	IOPLLVDD	NC	+1.1V
VDDHTRX	+1.1V	+1.1V	AVDD	NC	+3.3V
VDDHTTX	+1.1V	+1.1V	AVDDDI	NC	+1.8V
VDDA18PCIE	+1.8V	+1.8V	AVDDQ	NC	+1.8V
VDDG18	+1.8V	+1.8V	PLLVD	NC	+1.1V
VDD18_MEM	NC	+1.8V	PLLVD18	NC	+1.8V
VDDPCIE	+1.1V	+1.1V	VDDA18PCIEPLL	+1.8V	+1.8V
VDDC	+1.1V	+1.1V	VDDA18HTPLL	+1.8V	+1.8V
VDD_MEM	NC	+1.5V	VDDLT18	NC	+1.8V
VDDG33	NC	+3.3V	VDDLT18	NC	+1.8V
IOPLLVDD18	NC	+1.8V	VDDLT33	NC	NC

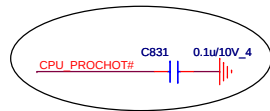
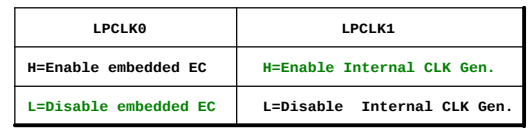


	VDD18_MEM
w Side-Port	VCC1.8
w/o Side-Port	GND

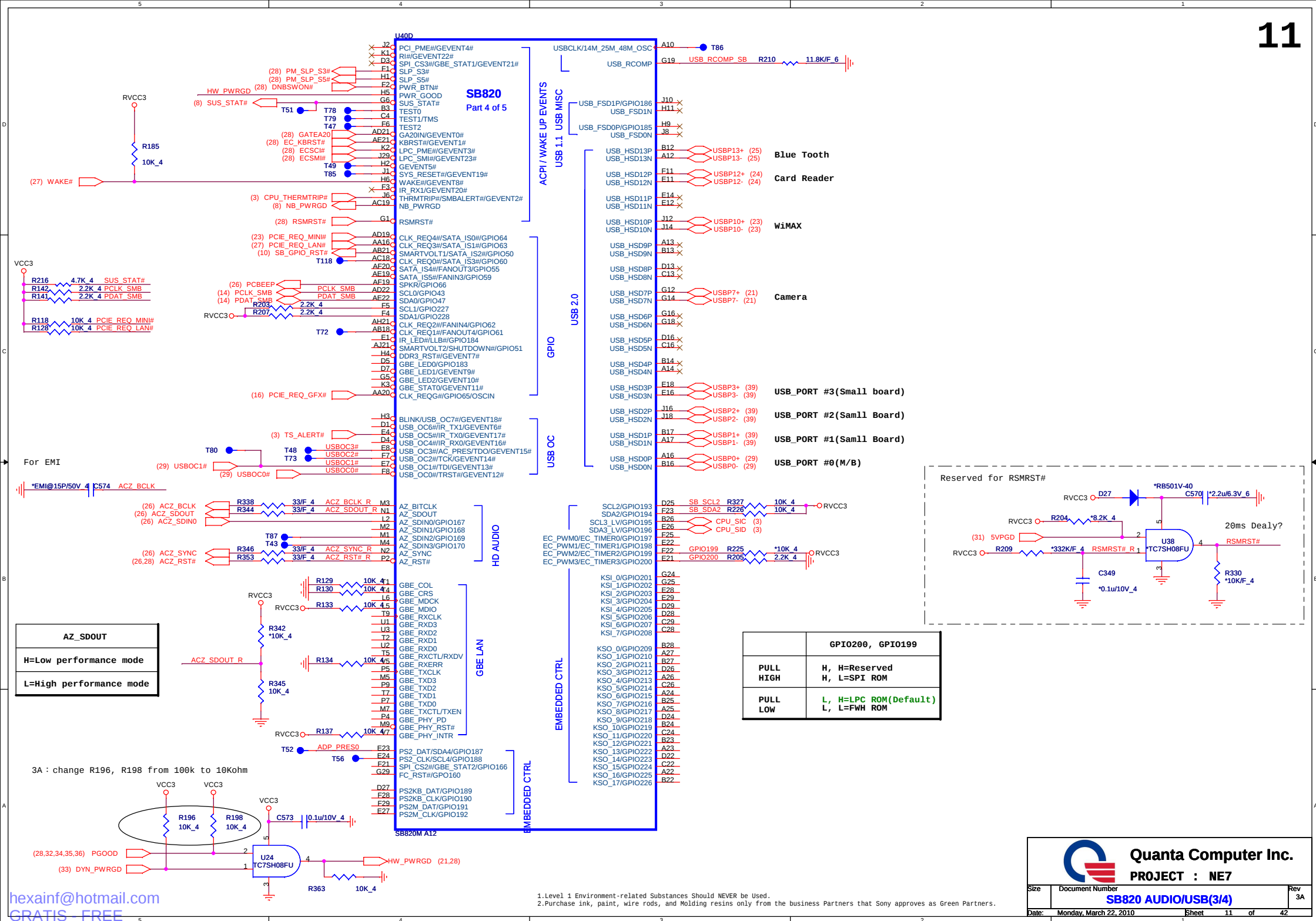
	VDD_MEM
w Side-Port	VCC1.5
w/o Side-Port	GND



RTC

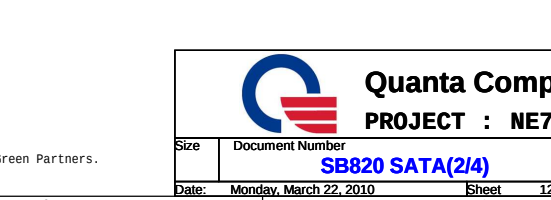


 **Quanta Computer Inc.**
PROJECT : NE7





	NON	SAMSUNG	HYNIX	
SP ID0	0	1	0	1
SP ID1	0	0	1	1



VCC3

C283

10uF/6.3V_8

C292

0.1u/10V_4

C306

0.1u/10V_4

C322

0.1u/10V_4

131mA

U40C

SB820

Part 3 of 5

CORE 50

VDDIO_33 PC1GP 1

VDDIO_33 PC1GP 2

VDDIO_33 PC1GP 3

VDDIO_33 PC1GP 4

VDDIO_33 PC1GP 5

VDDIO_33 PC1GP 6

VDDIO_33 PC1GP 7

VDDIO_33 PC1GP 8

VDDIO_33 PC1GP 9

VDDIO_33 PC1GP 10

VDDIO_33 PC1GP 11

VDDIO_33 PC1GP 12

PC1GHO I/O

VDDCR_11_1

VDDCR_11_2

VDDCR_11_3

VDDCR_11_4

VDDCR_11_5

VDDCR_11_6

VDDCR_11_7

VDDCR_11_8

VDDCR_11_9

VDDAN_11_CLK_1

Part 3 of 5

CORE_S0

- VDDCR_11_1
- VDDCR_11_2
- VDDCR_11_3
- VDDCR_11_4
- VDDCR_11_5
- VDDCR_11_6
- VDDCR_11_7
- VDDCR_11_8
- VDDCR_11_9

VDDIO

- VDDAN_11_CLK_1
- VDDAN_11_CLK_2
- VDDAN_11_CLK_3
- VDDAN_11_CLK_4
- VDDAN_11_CLK_5
- VDDAN_11_CLK_6
- VDDAN_11_CLK_7
- VDDAN_11_CLK_8

VDDIO_GBE_S

- VDDIO_11_GBE_S_1
- VDDIO_11_GBE_S_2
- VDDIO_GBE_S_1
- VDDIO_GBE_S_2

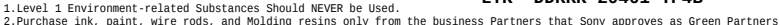
510mA

SB820 without GBE: Connected to GND plane.

U40E			
SB820			
VSSIO_SATA_1	VSS_1	A32	
VSSIO_SATA_2	VSS_2	A28	
VSSIO_SATA_3	VSS_3	A2	
VSSIO_SATA_4	VSS_4	E5	
VSSIO_SATA_5	VSS_5	D23	
VSSIO_SATA_6	VSS_6	E25	
VSSIO_SATA_7	VSS_7	E6	
VSSIO_SATA_8	VSS_8	F24	
VSSIO_SATA_9	VSS_9	N15	
VSSIO_SATA_10	VSS_10	R13	
VSSIO_SATA_11	VSS_11	R17	
VSSIO_SATA_12	VSS_12	T10	
VSSIO_SATA_13	VSS_13	P10	
VSSIO_SATA_14	VSS_14	V11	
VSSIO_SATA_15	VSS_15	U15	
VSSIO_SATA_16	VSS_16	M18	
VSSIO_SATA_17	VSS_17	V19	
VSSIO_SATA_18	VSS_18	M11	
VSSIO_SATA_19	VSS_19	L12	
VSSIO_USB_1	VSS_20	L18	
VSSIO_USB_2	VSS_21	J7	
VSSIO_USB_3	VSS_22	P3	
VSSIO_USB_4	VSS_23	V4	
VSSIO_USB_5	VSS_24	AD6	
VSSIO_USB_6	VSS_25	AD4	
VSSIO_USB_7	VSS_26	AB7	
VSSIO_USB_8	VSS_27	AC9	
VSSIO_USB_9	VSS_28	W9	
VSSIO_USB_10	VSS_29	W10	
VSSIO_USB_11	VSS_30	AJ28	
VSSIO_USB_12	VSS_31	B29	
VSSIO_USB_13	VSS_32	U4	
VSSIO_USB_14	VSS_33	Y18	
VSSIO_USB_15	VSS_34	U4	
VSSIO_USB_16	VSS_35	Y12	
VSSIO_USB_17	VSS_36	Y11	
VSSIO_USB_18	VSS_37	AA11	
VSSIO_USB_19	VSS_38	AA12	
VSSIO_USB_20	VSS_39	G4	
VSSIO_USB_21	VSS_40	J4	
VSSIO_USB_22	VSS_41	G8	
VSSIO_USB_23	VSS_42	G9	
VSSIO_USB_24	VSS_43	M12	
VSSIO_USB_25	VSS_44	AF25	
VSSIO_USB_26	VSS_45	H7	
VSSIO_USB_27	VSS_46	AH29	
VSSIO_USB_28	VSS_47	V10	
	VSS_48	F6	
	VSS_49	N4	
	VSS_50	L4	
EFUSE	VSS_51	L8	
	VSS_52		
VSSAN_HWM			
VSSXL	VSSPL_SYS	M20	
VSSIO_PCIECLK_1	VSSIO_PCIECLK_14	H26	
VSSIO_PCIECLK_2	VSSIO_PCIECLK_15	Z20	
VSSIO_PCIECLK_3	VSSIO_PCIECLK_16	AA23	
VSSIO_PCIECLK_4	VSSIO_PCIECLK_17	AB23	
VSSIO_PCIECLK_5	VSSIO_PCIECLK_18	AD23	
VSSIO_PCIECLK_6	VSSIO_PCIECLK_19	AA26	
VSSIO_PCIECLK_7	VSSIO_PCIECLK_20	AC26	
VSSIO_PCIECLK_8	VSSIO_PCIECLK_21		
VSSIO_PCIECLK_9	VSSIO_PCIECLK_22	W21	
VSSIO_PCIECLK_10	VSSIO_PCIECLK_23	W20	
VSSIO_PCIECLK_11	VSSIO_PCIECLK_24	AF26	
VSSIO_PCIECLK_12	VSSIO_PCIECLK_25	L21	
VSSIO_PCIECLK_13	VSSIO_PCIECLK_26	K20	
	VSSIO_PCIECLK_27		
Part 5 of 5			

Part 5 of 5

SB820M A12





Dual channel support

Single channel support

Madison M2



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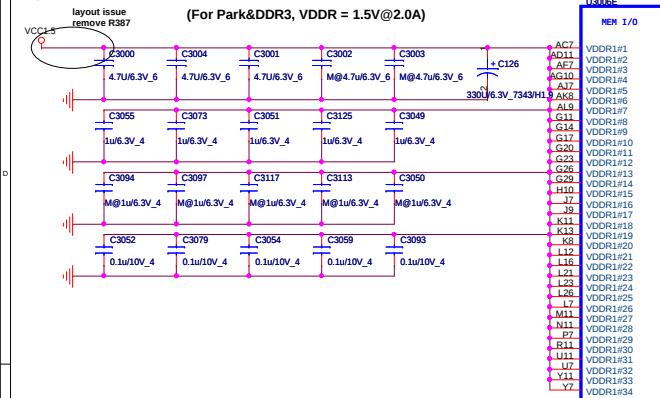
PROJECT : NE7

Size	Document Number	Rev
	ATI PCIE/LVDS	3A
Date:	Monday, March 22, 2010	Sheet 15 of 42

1.Level 1 Environment-related Substances Should NEVER be Used.
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M@ for Madison level

(For Park&DDR3, VDDR = 1.5V@2.0A)



	M92	Manhattan
VDDRHA	X	X
VSSRHA	X	X
VDDRHB	0	X
VSSRHB	0	X

LEVEL TRANSLATION

T/0

VDDRHA

VSSRHA

VDDRHB

VSSRHB

VDDRHA

VSSRHA

VDDRHB

VSSRHB

VDDRHA

VSSRHA

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POWER

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VDDRHB

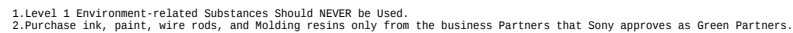
VSSRHB

VDDRHA

VSSRHA

VDDRHB

VSSRHB

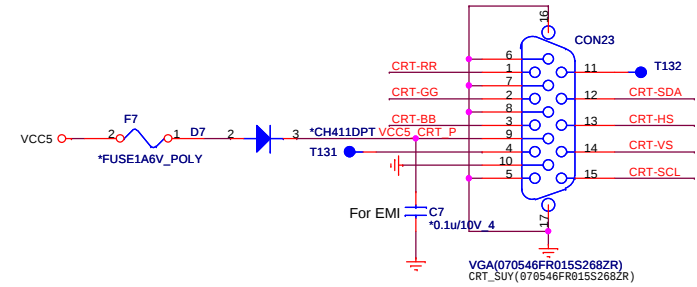
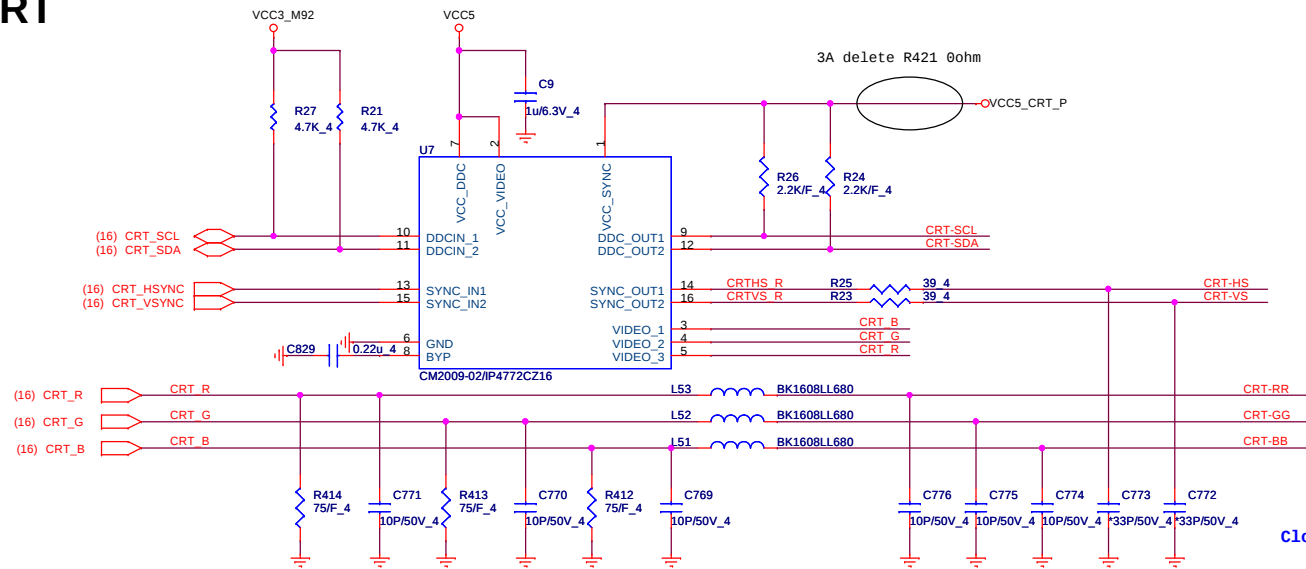


Size	Document Number	Rev
	ATI MEM/IF/DP	3A
Date:	Monday, March 22, 2010	Sheet 18 of 42

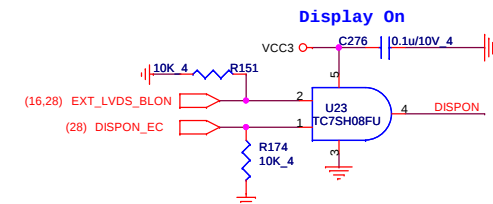
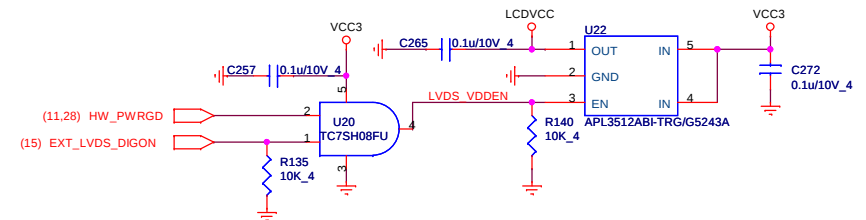
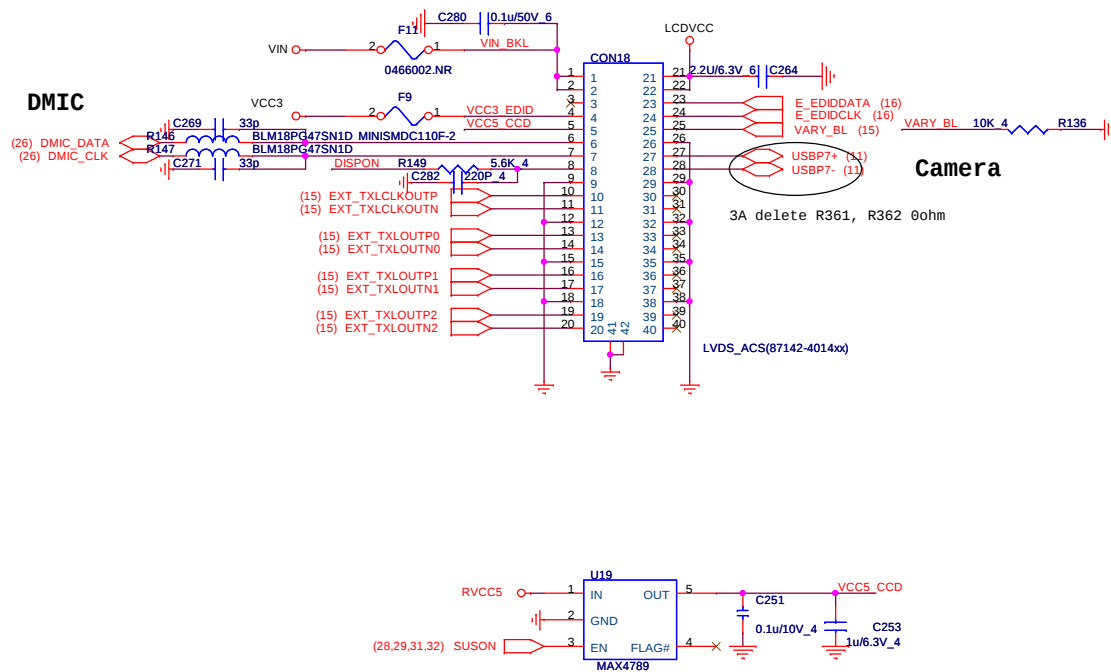


A vertical bar is divided into four segments labeled A, B, C, and D from bottom to top. An arrow points to the boundary between segments B and C.

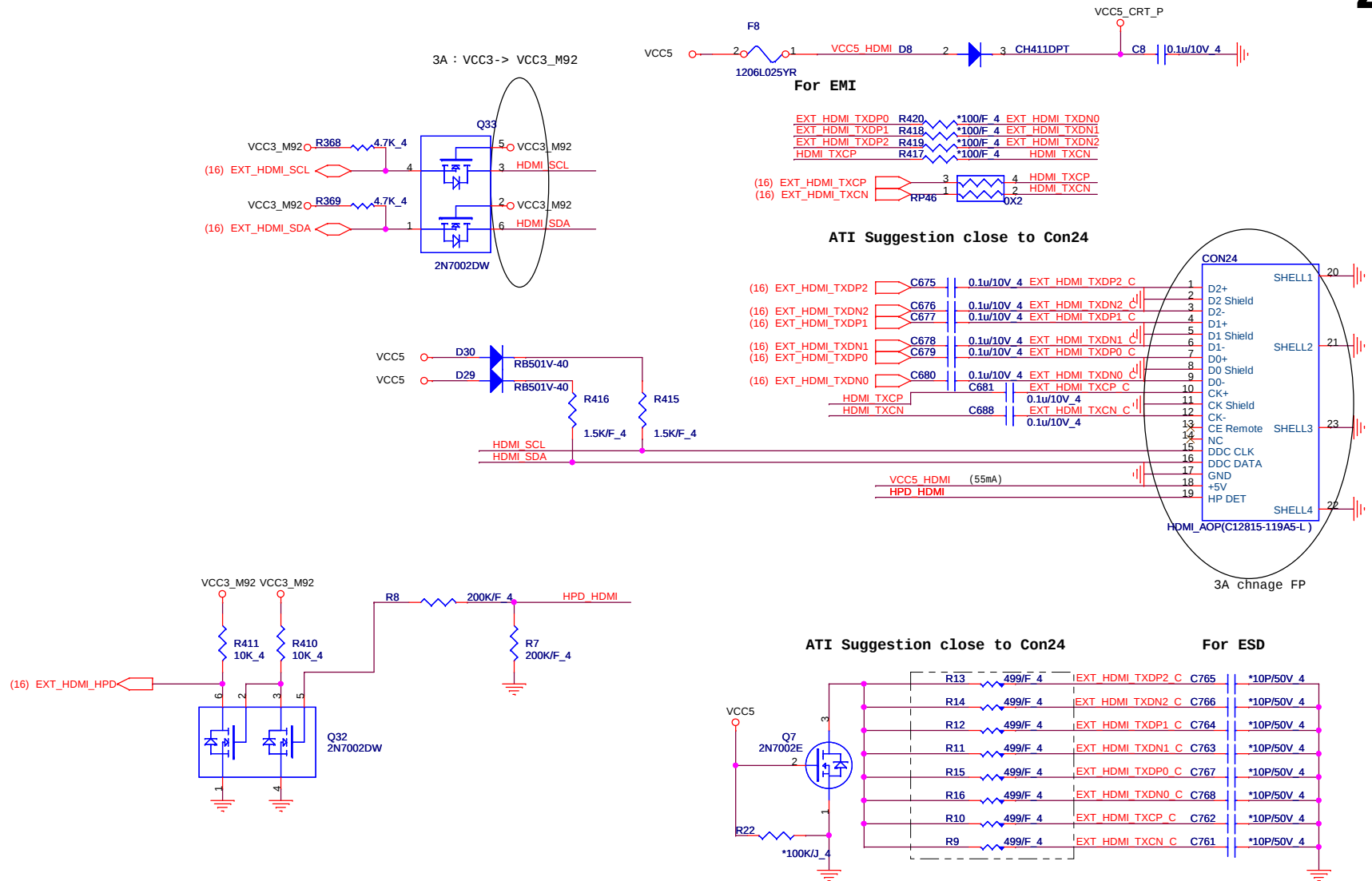




LVDS

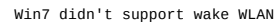


Display On



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			HDMI	3A
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1440mA Peak, 636mA Avg

VCC3

C458 0.1u/10V_4

C457 0.1u/10V_4

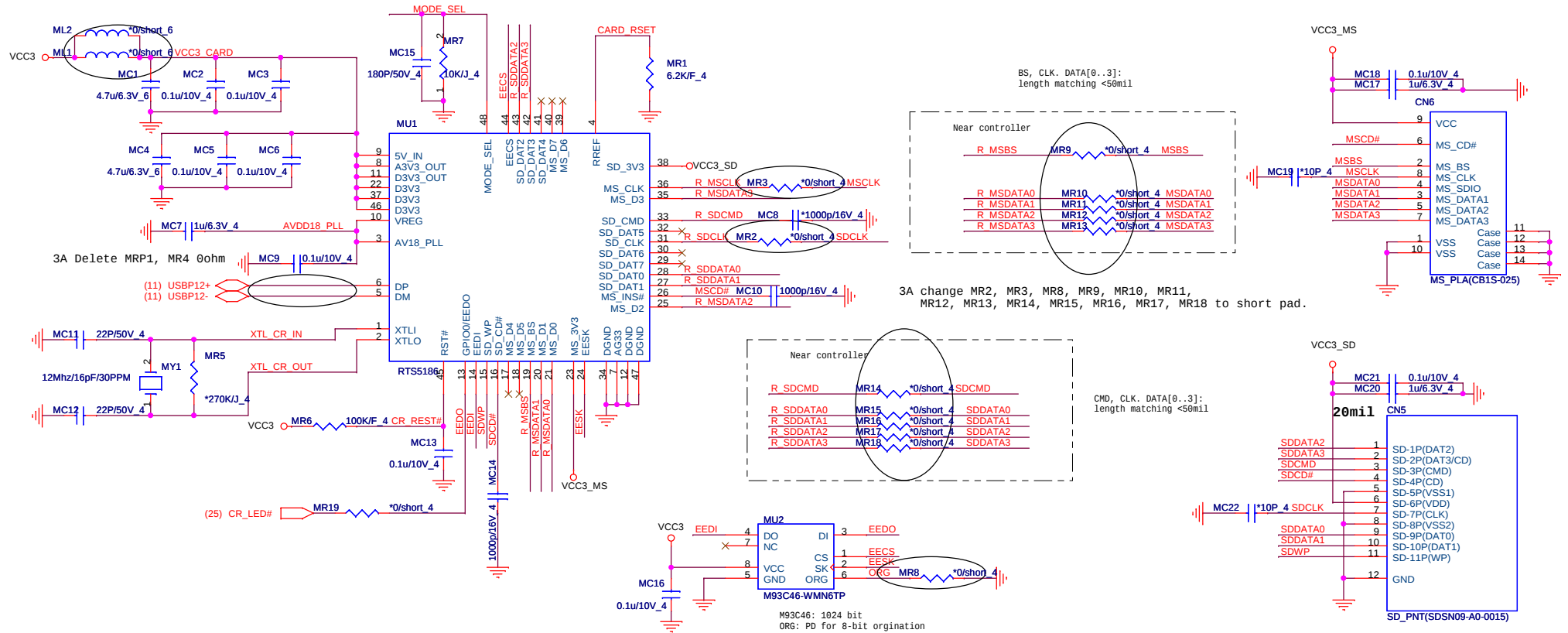
C444 0.1u/10V_4

C454 1u/6.3V_4

C463 10u/6.3V_8

C443 *22u/6.3V_8

3A change ML1 to short pad



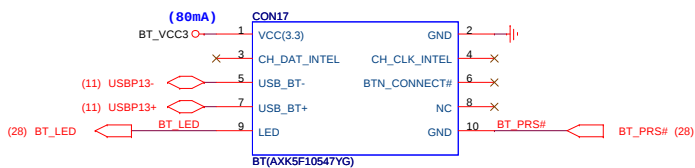
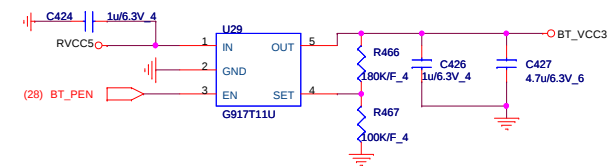


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PROJECT : NE7

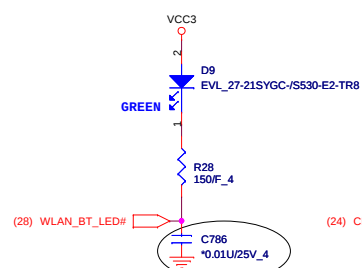
Size	Document Number	Rev 3A
CARD Reader		
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- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

Bluetooth

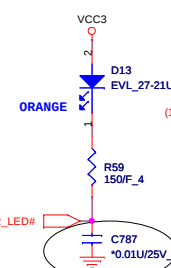


RF LED

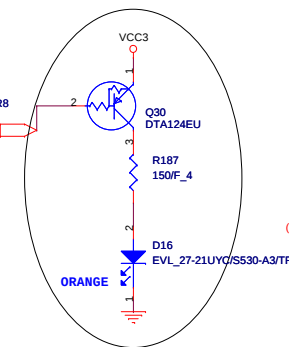


3A : Reverse C786, C787, C788 FP for ESD

Card LED

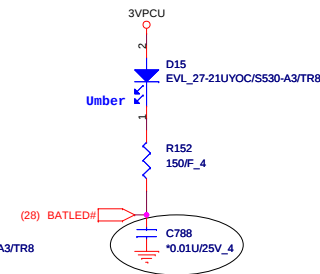


HDD LED

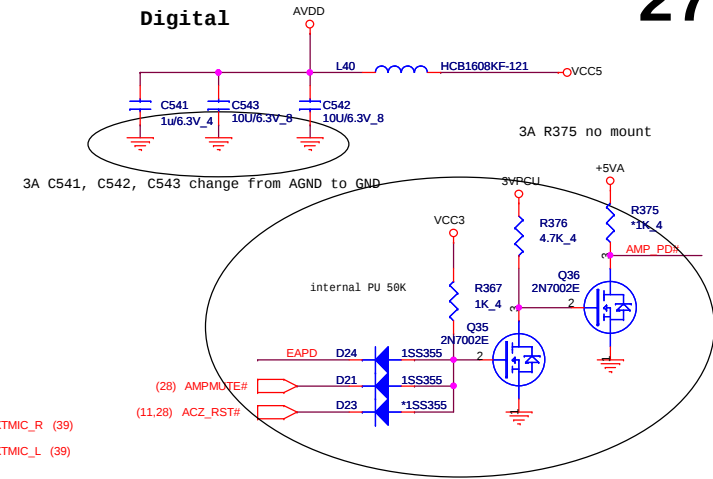


3A : adding Q30 for ESD.

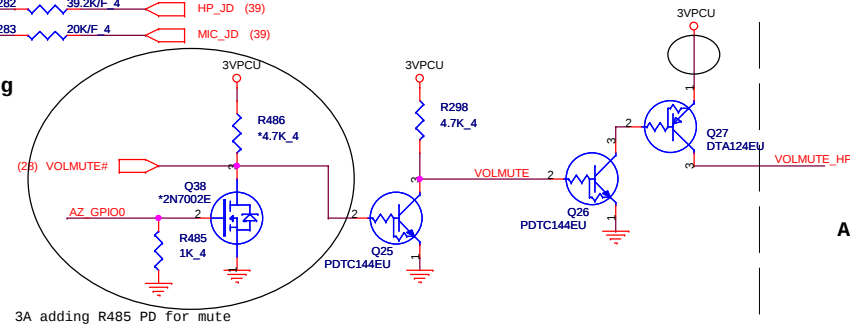
Battery LED



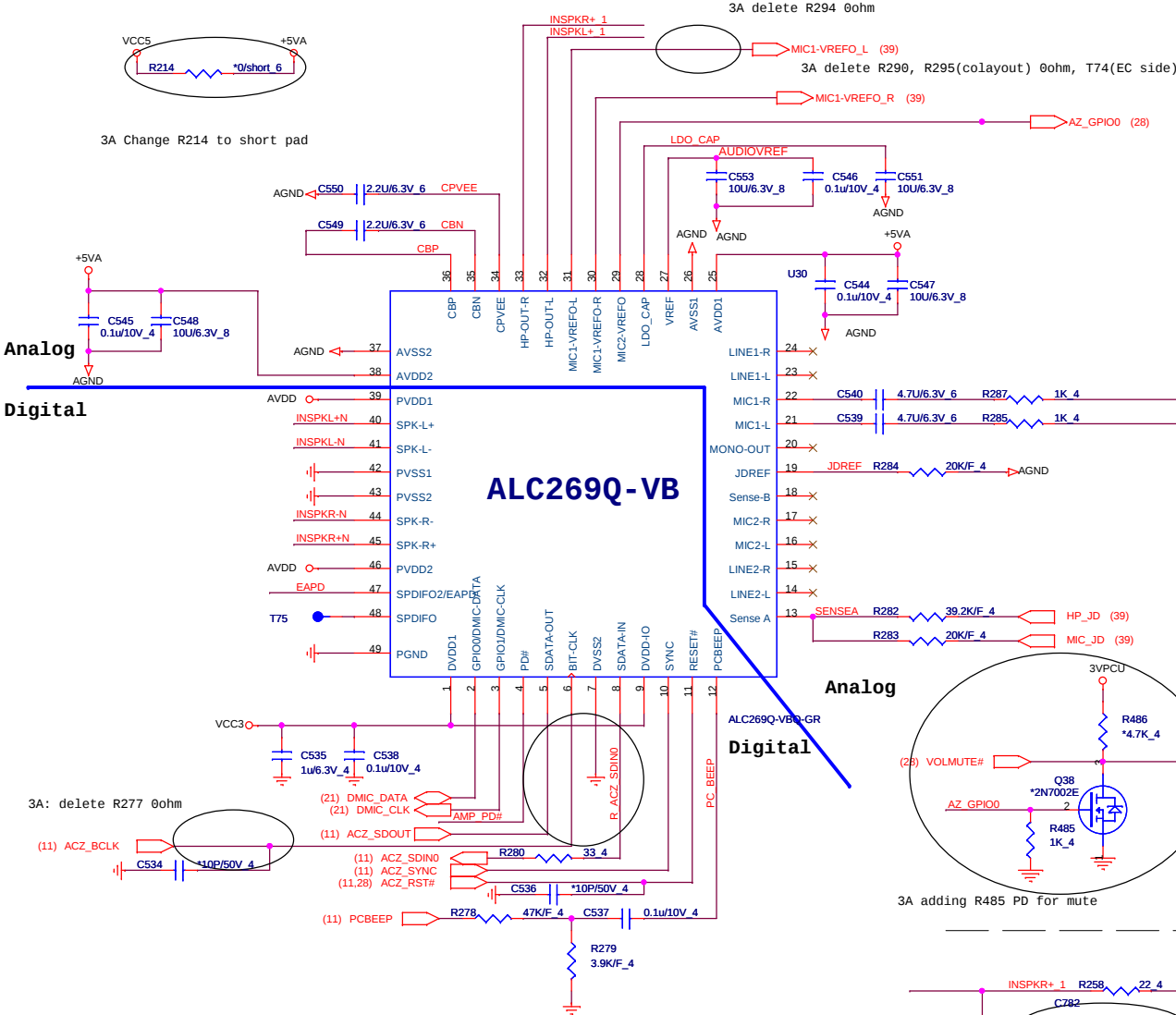
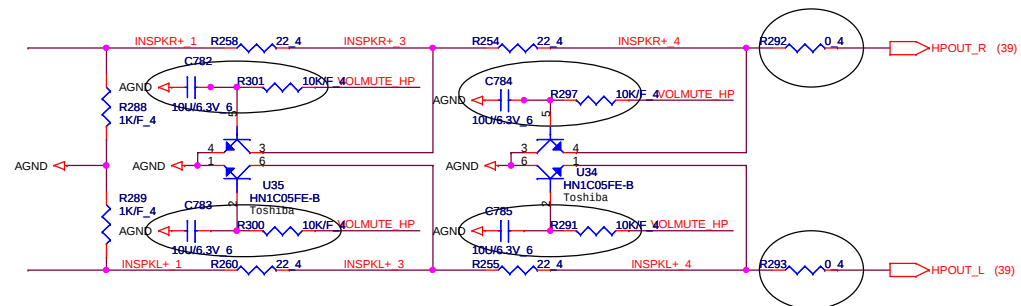
Digital



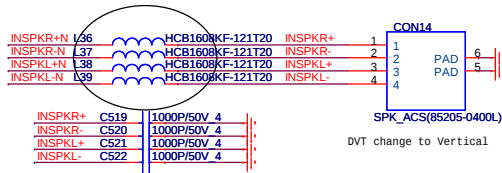
3A delete R299 4.7k ohm



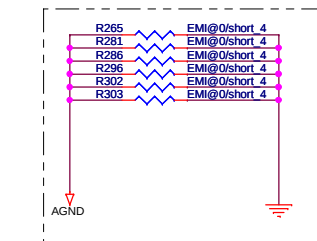
Analog

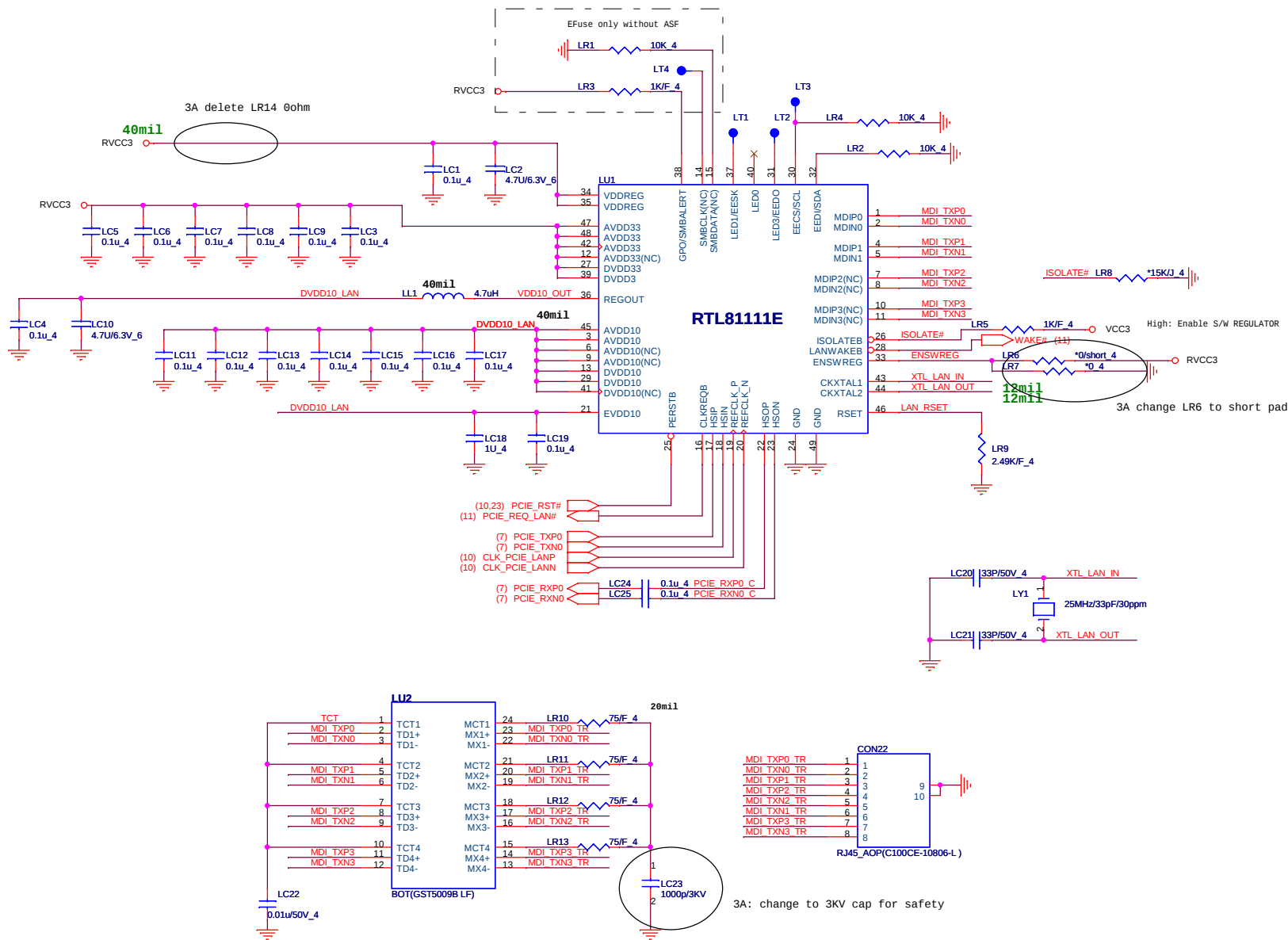


SPEAKER CON.



For EMI



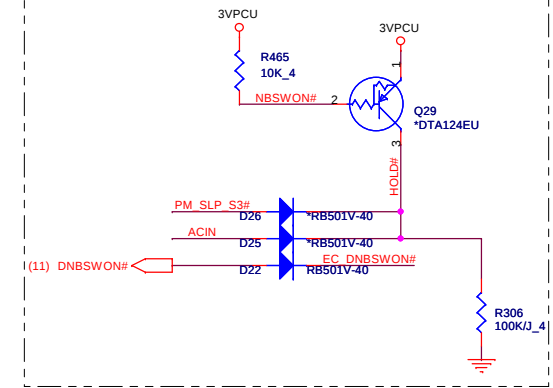


I/O Address		
BADDR1-0	Index	Data
1 0	2E	2F
1 1	4E	4F
0 0	(HCFGBAH, HCFGBAL) (HCFGBAH, HCFGBAL)+1	
0 1	XOR-TREE TEST	

SHMB: SHMB(If = 0 Enable share host BIOS memory)

DOCK_RST# : BADDR0
T7: BADDR1

Reserved for more EC wake up function



Issue after RVCCON 20-25ms

Power step3
Power step1

3A add R489 10 k PD

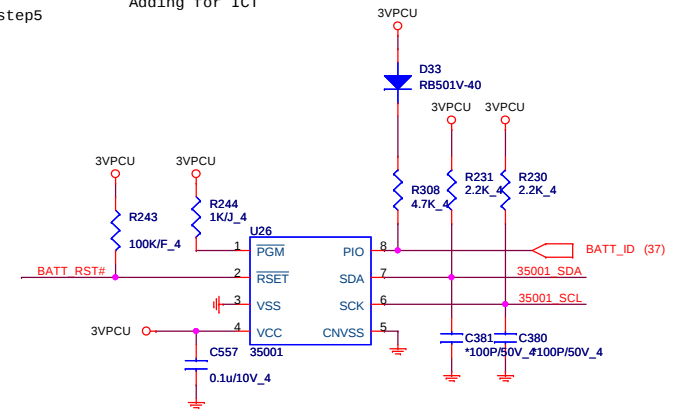
3A change TP to cpu_prochot# in EC.

BATT_SCK R236 4.7K 4
BATT_SDA R307 4.7K 4
FUN_ASSIST# R304 10K 4

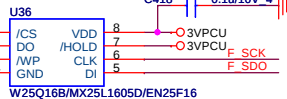
Power step4

Power step5

Adding for ICT



EC SDI-> EEprom D0
EC SDO-> EEprom D1

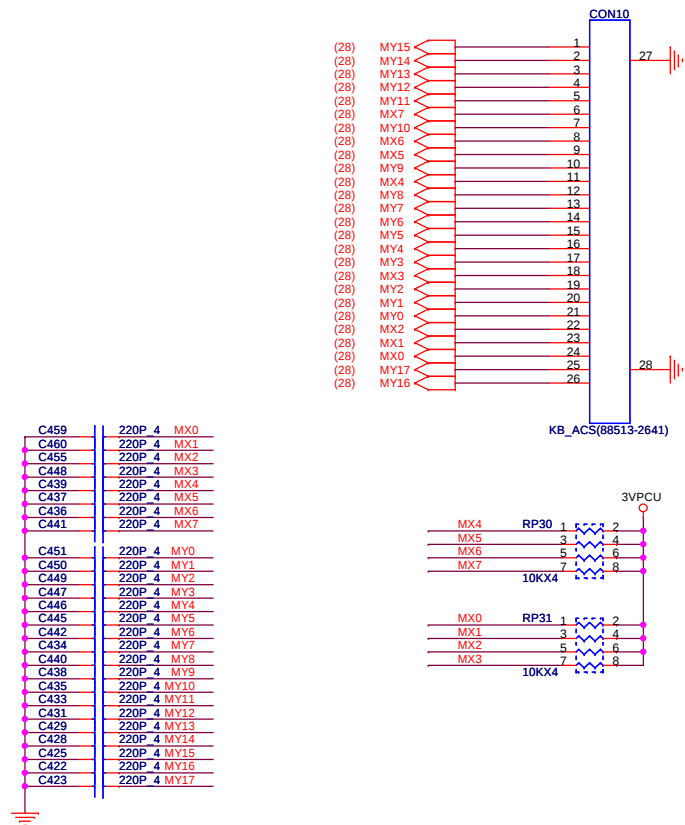


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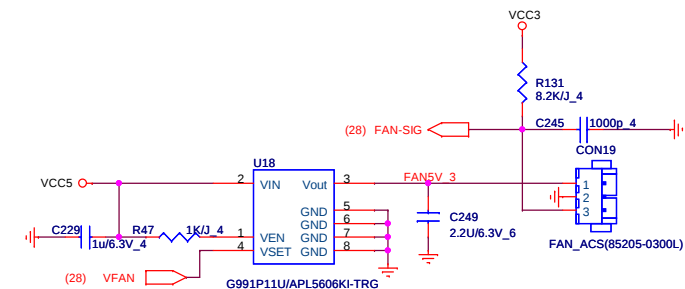
Size	Document Number	Rev
	NPCE781 & FLASH	3A
Date:	Monday, March 22, 2010	Sheet 28 of 42

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

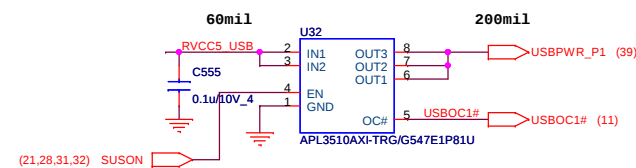
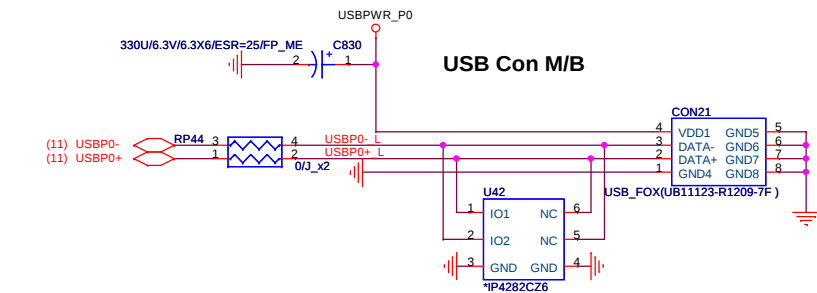
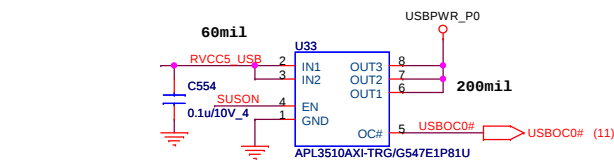
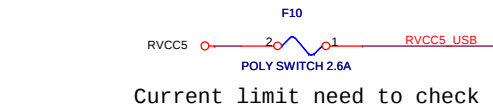
KEYBOARD



FAN



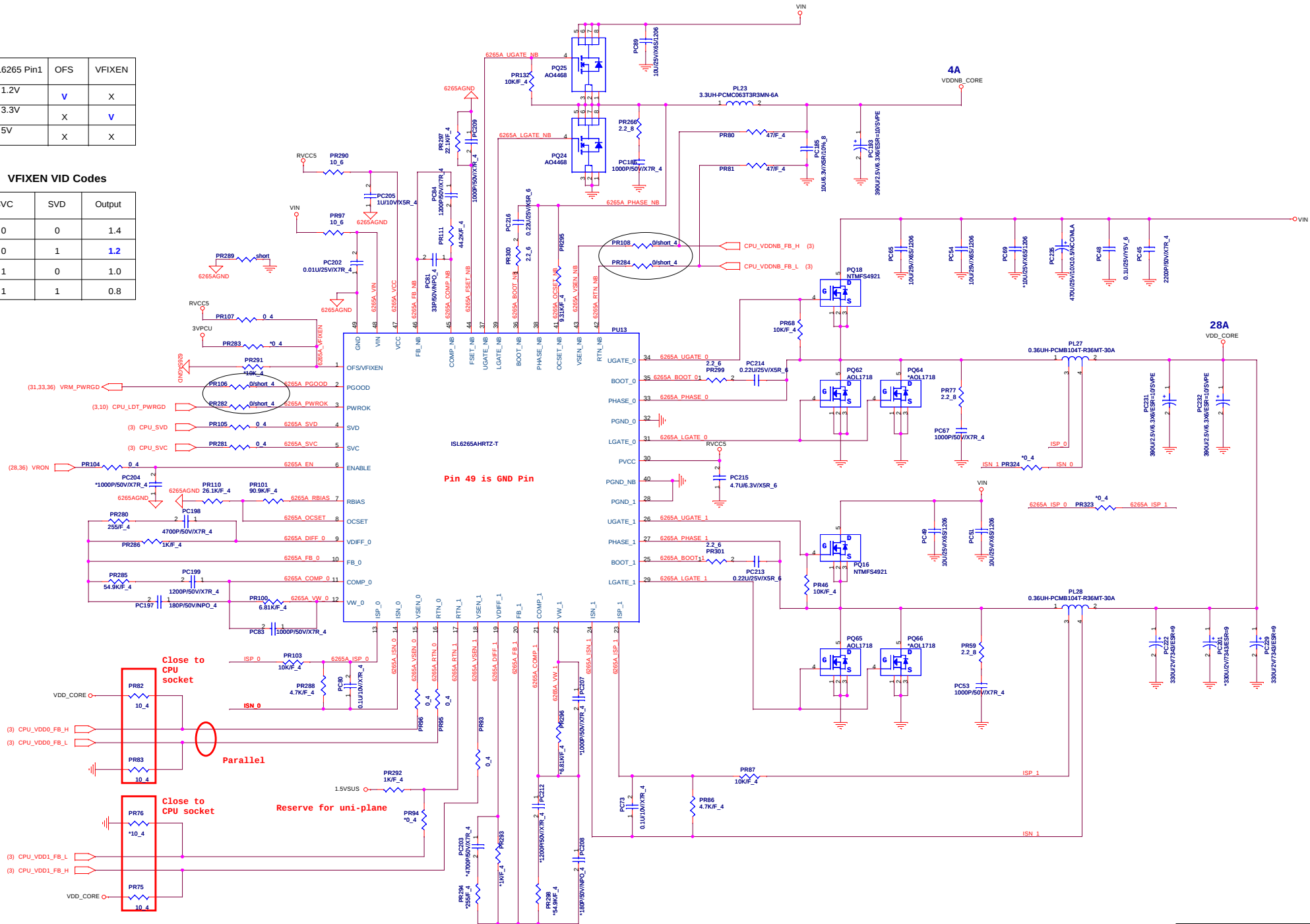
USB Port + e-SATA(USB)



ISL6265 Pin1	OFS	VFIXEN
1.2V	V	X
3.3V	X	V
5V	X	X

VFIXEN VID Codes

SVC	SVD	Output
0	0	1.4
0	1	1.2
1	0	1.0
1	1	0.8



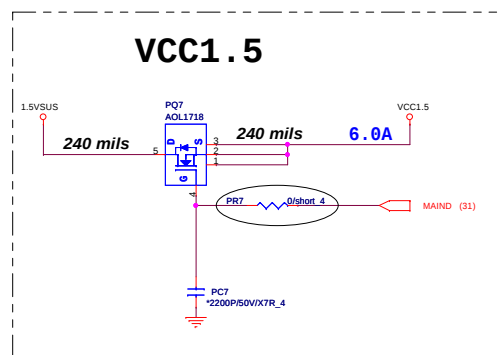
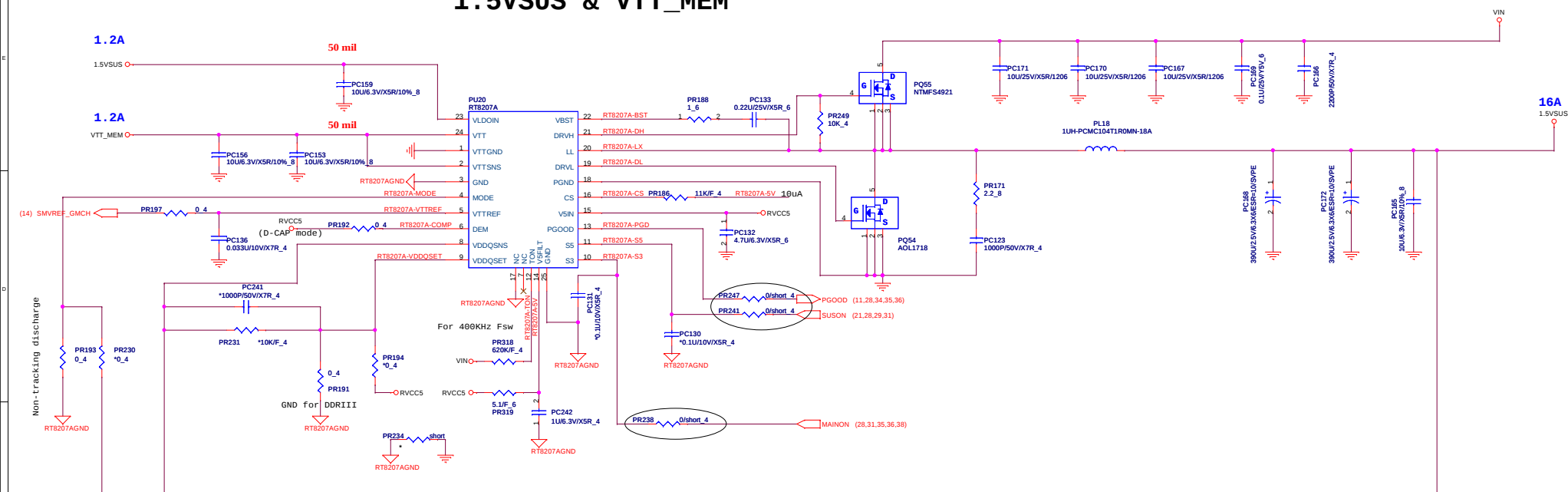
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3.3V & 5V

 Quanta Computer Inc. PROJECT : NE7		Rev 3
Size	Document Number	
	3VPCU&5VPCU(PM6686)	
Date:	Monday, March 22, 2010	Sheet 31 of 42

2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners

1.5VSUS & VTT_MEM



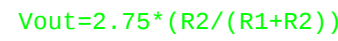
MODE	DISCHARGE MODE
+5V	No discharge
+1.8V	Tracking discharge
GND	Non-tracking discharge

VDDQSET	VDDQ(V)	VTTREF & VTT	NOTE
GND	1.5 Fixed	VDDQSNS/2	DDR3
5V	1.8 Fixed	VDDQSNS/2	DDR2
FB-Resistor	Adjustable	VDDQSNS/2	1.5V<VDDQ<3V

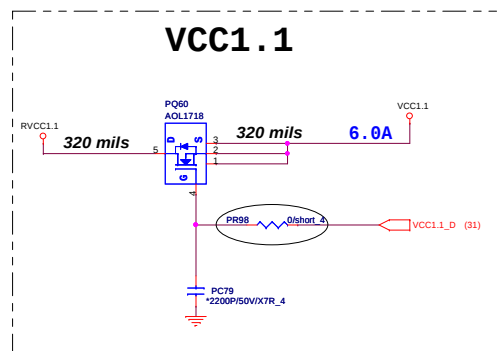
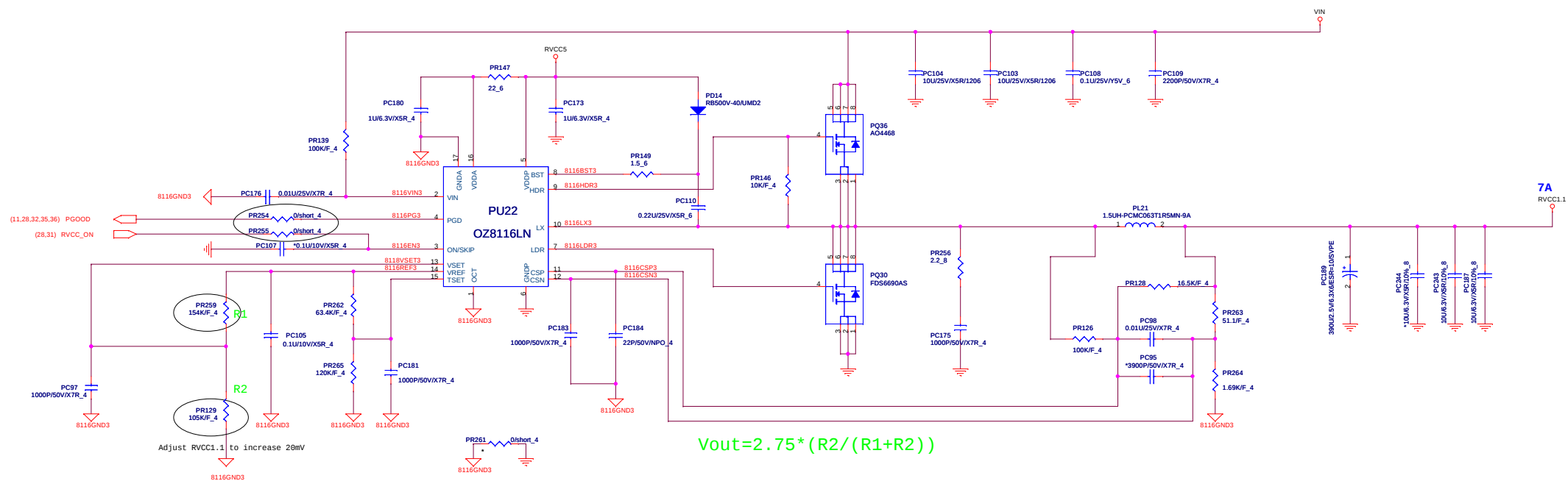
VTT = VTTREF = VDDQSNS/2 = 0.75V

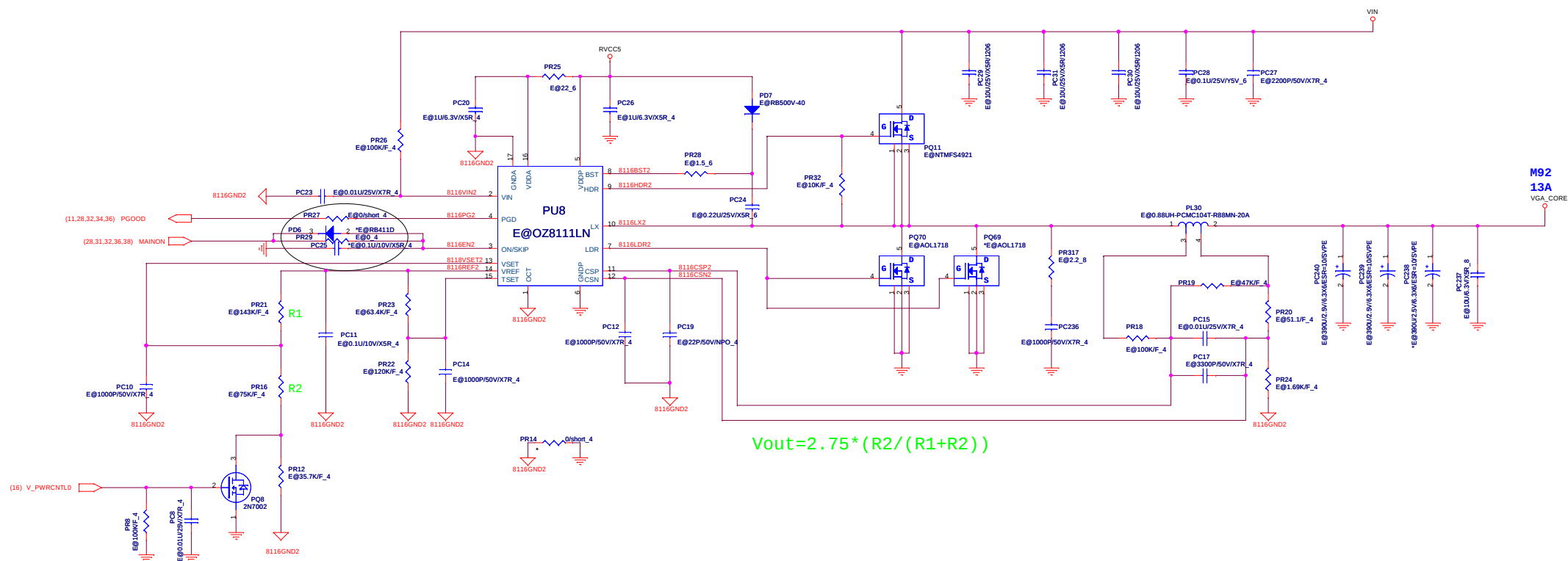
STATE	S3	S5	1.5VSUS	VTTREF	VTT
S0	1	1	on	on	on
S3	0	1	on	on	off
S4/S5	0	0	off	off	off

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DYN_PWR_EN	High	Low
DYN_VCC1.1	0.95V	1.1V

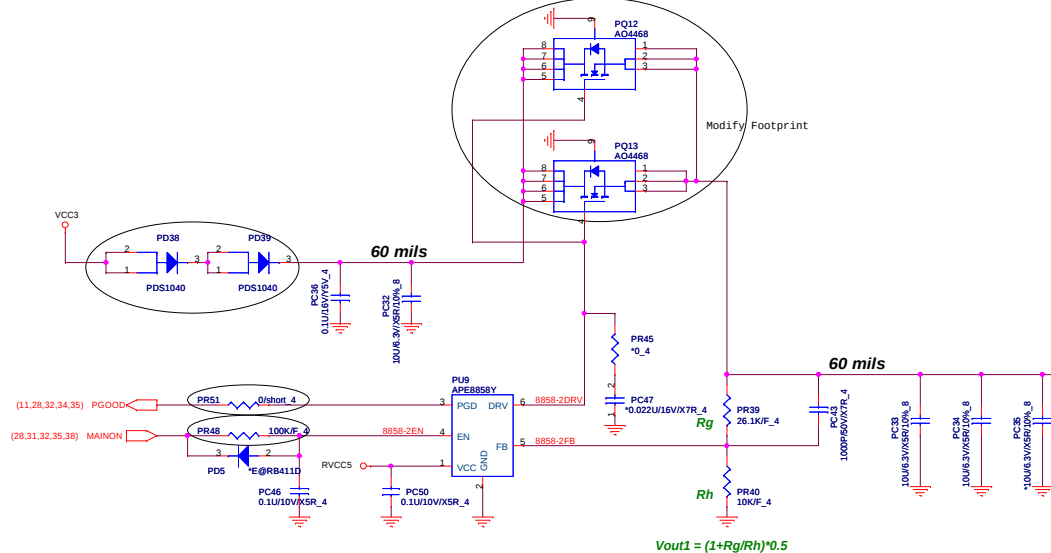


**M92**

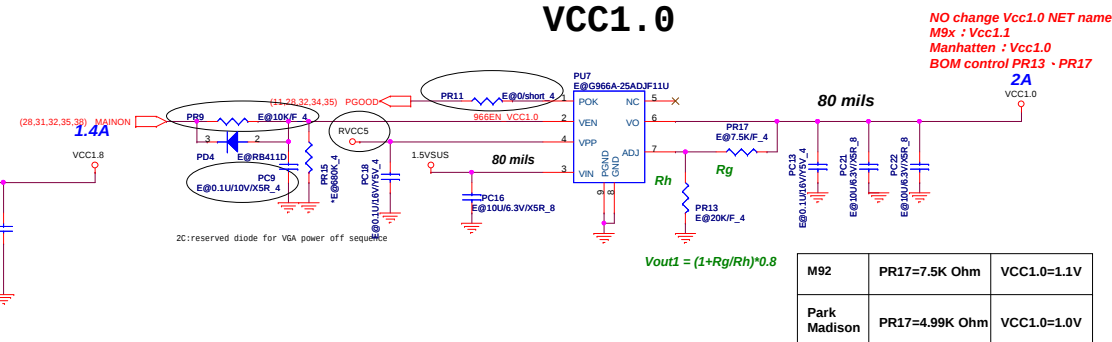
External VGA_CORE Voltage Setting:

V_PWRCNTL0	VGA_CORE
0	1.20V
1	0.95V

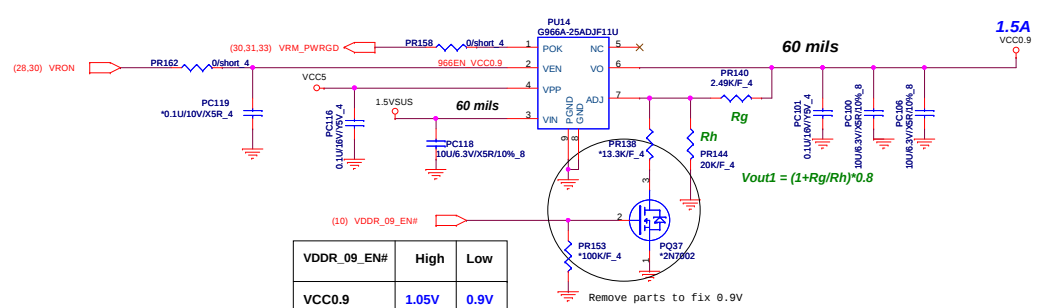
VCC1.8



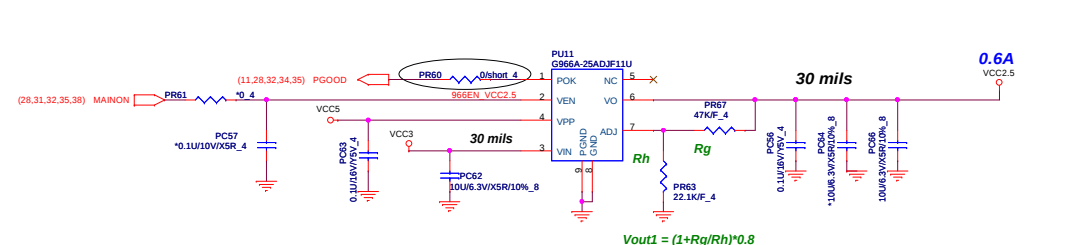
VCC1.0



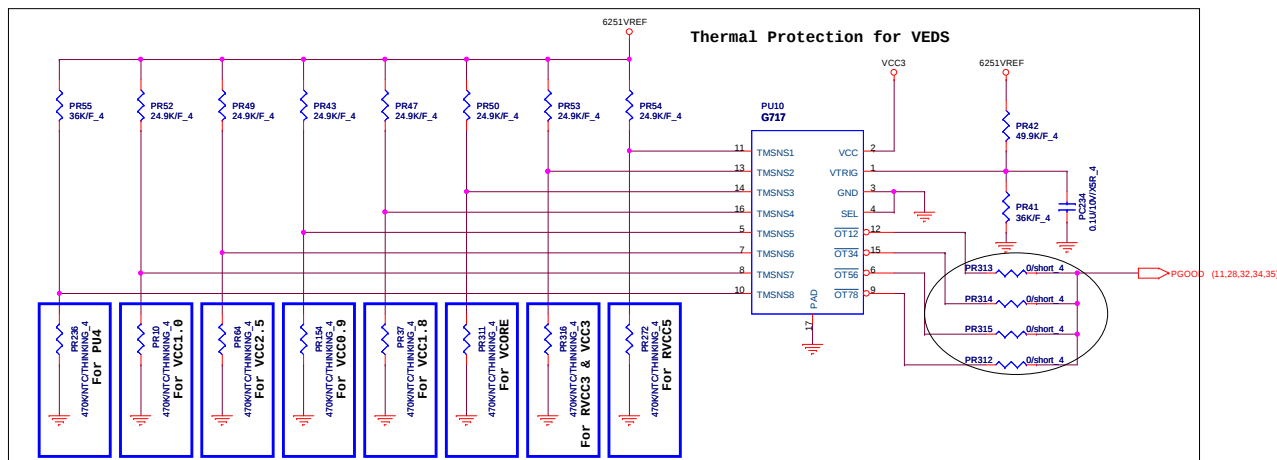
VCC0.9

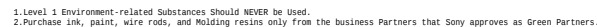


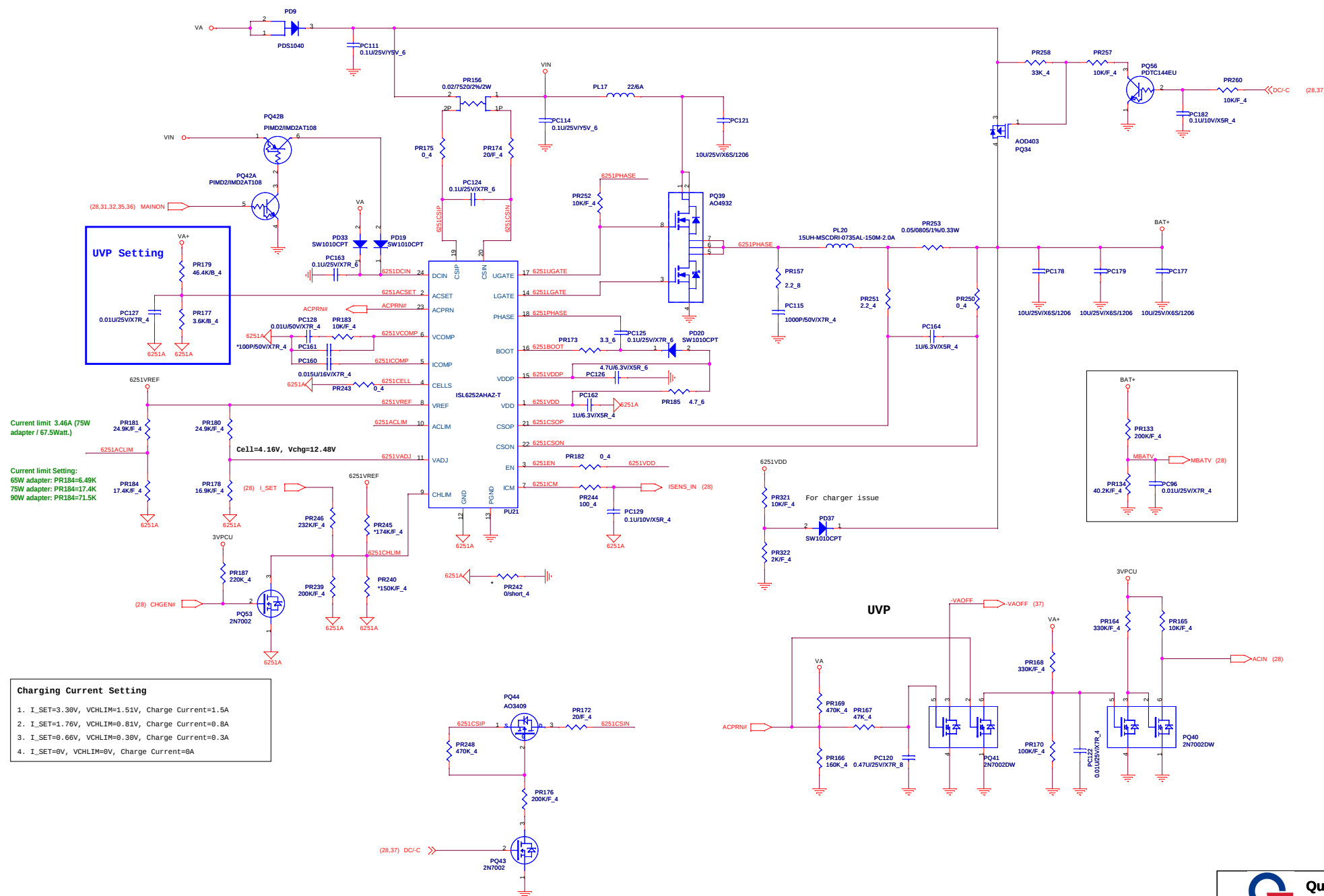
VCC2.5



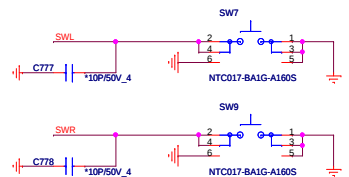
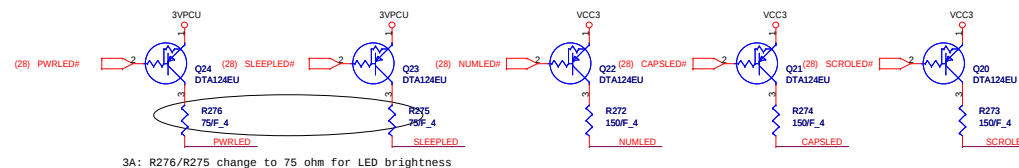
Thermal Protection for VEDS



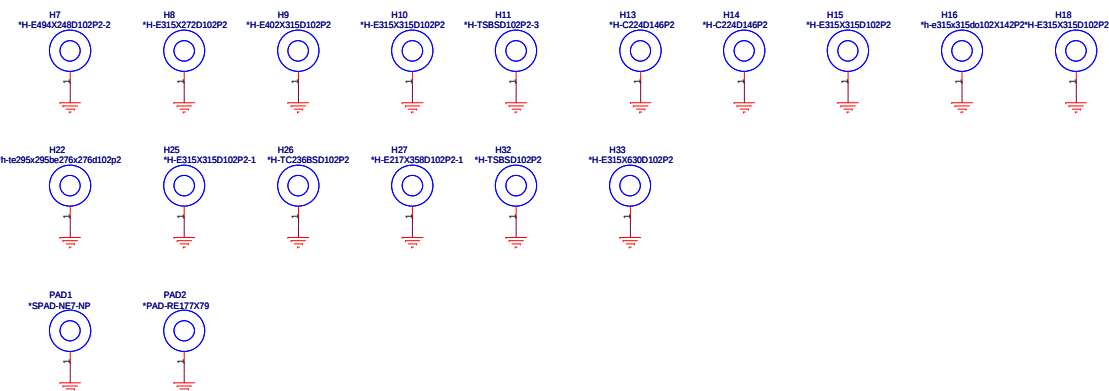
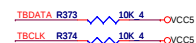
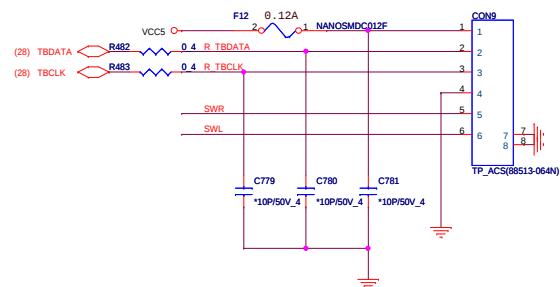




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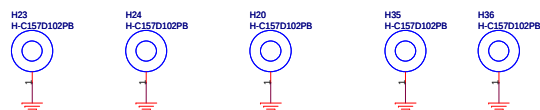
MB to TP connector



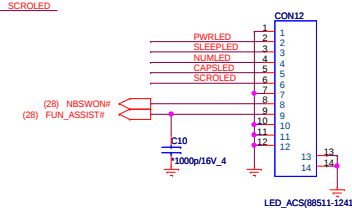
WLAN

BT

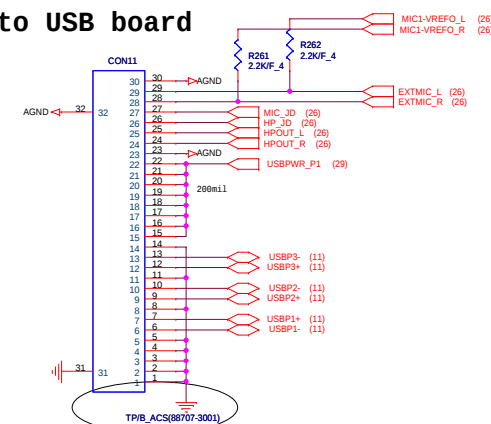
1.8V MOS



MB to LED board



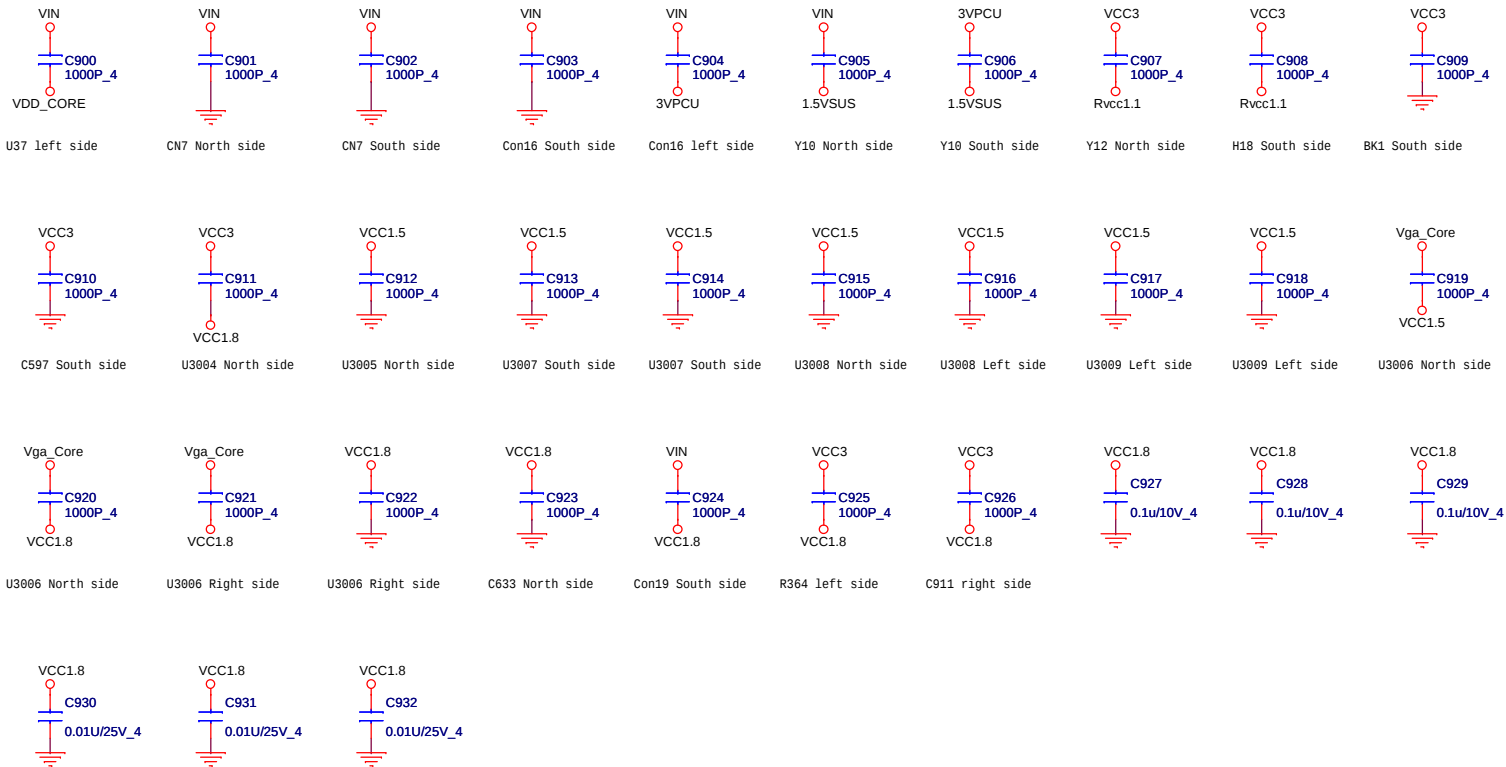
MB to USB board



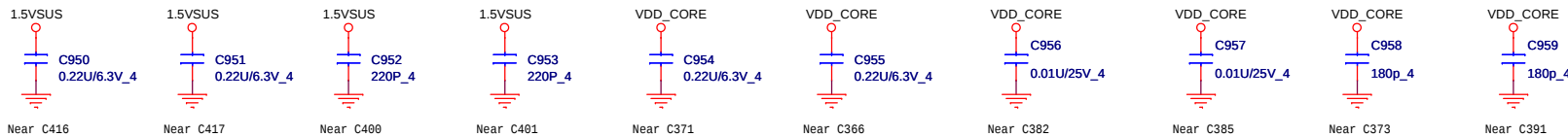
3A change from 88511-3001 to 88707-3001

Decoupling Cap

40



Power ripple



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	Decoupling Cap	3A
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2A Adding R159 (10K) for shut down issue.

3A Rename for location(R464, C452)

3A Delete AMD debug connector

3A change R169, R155, R162, R340, R163, R161, R168, R341, R337 to short pad

3A Delete R332, R249, R181, R333 0ohm

3A change L23, L24, L29, L21, R156, R171, R167 to short pad

3A Delete R427, R428, R429, R430 0ohm.

3A delete RP36, RP42, RP38, RP26, RP43, RP41, RP35 0ohm

3A : Delete R358 0ohm

3A : change R247, R305 from 100k to 10Kohm

3A : change R196, R198 from 100k to 10Kohm

3A change R150, L27, L26, L30 to short pad

3A change R172, L42, L28, L25 to short pad

3A change R309, R184, L31, L34, L41 to short pad

3A change L32, L33, L35, R212 to short pad

3A Adding GPIO3 `4 for VGA thermal sensor

3A : change L3004, L3019 to short pad

3A change L3006, L3010 to short pad

3A delete L3007, L3012, L3013, R3061 0ohm

3A delete R3049 0ohm

3A change R3112 to short pad

3A delete R3113 0ohm

3A change R252 to short pad.

3A change MR2, MR3, MR8, MR9, MR10, MR11, MR12, MR13, MR14, MR15, MR16, MR17, MR18, ML1 to short pad.

3A : adding Q30 for ESD.

3A Change R214 to short pad

3A: delete R277, R294 0ohm

3A: Change L36, L37, L38, L39 to short pad

3A delete R290, R295(colayout) 0ohm, T74(EC side)

3A C541, C542, C543 change from AGND to GND

3A R375 no mount

3A delete LR14 0ohm

3A change LR6 to short pad

3A change R478 to short pad

3A adding R485 PD for mute

Remove short pad: R422,R423