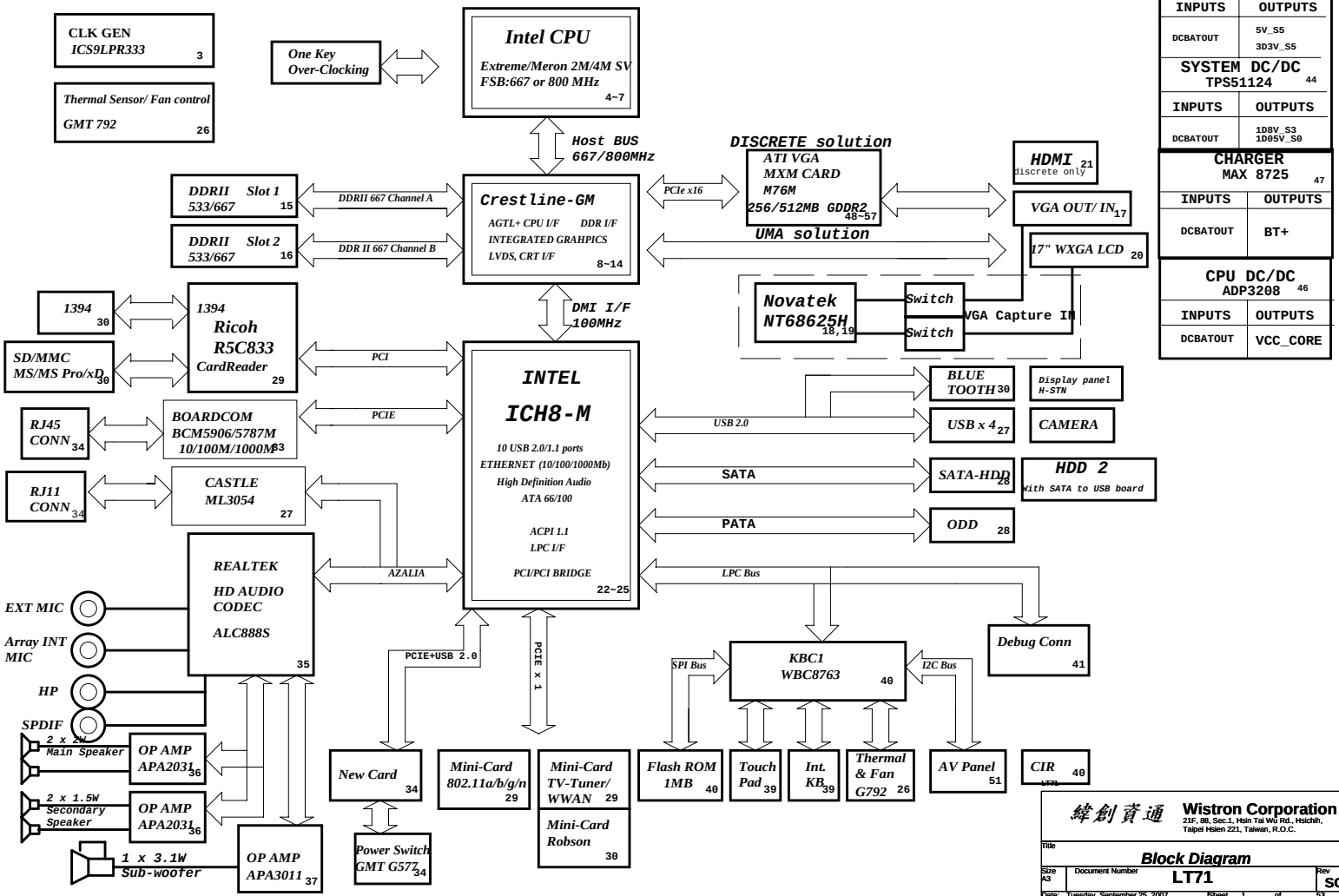
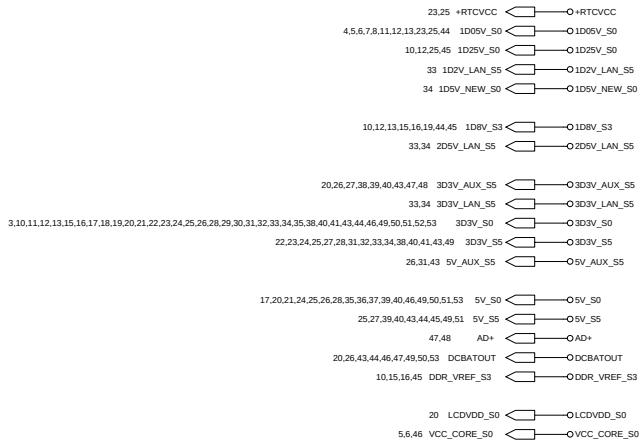
LT-71 Block Diagram





LT71

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***Table of Content***

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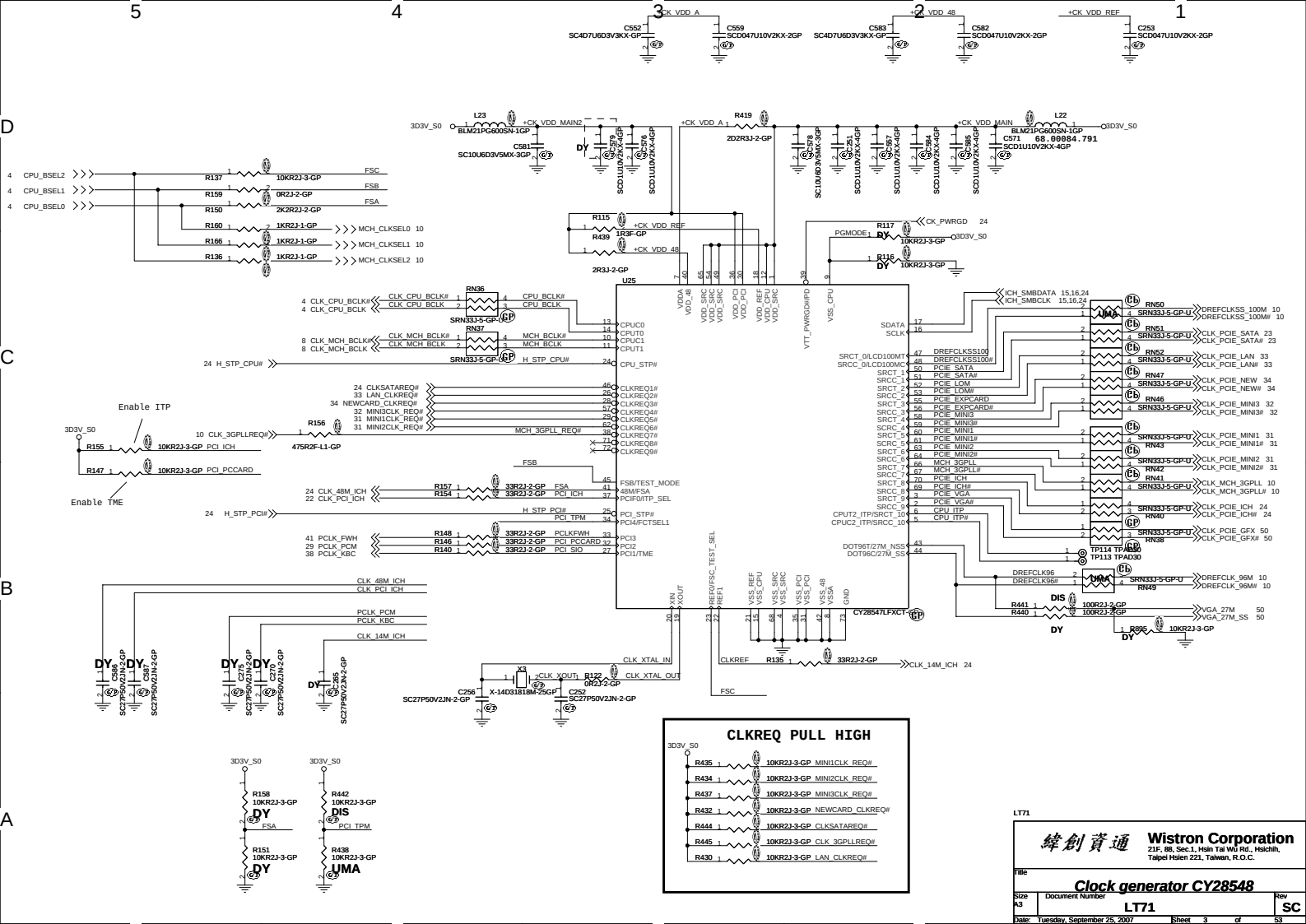
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4
3
2
1
D
C
B
A
U25
CY28547LFXCT
CLK_14M_ICH
CLK_XTAL_IN
CLK_XTAL_OUT
X-14D31818M-25GP
CLK_48M_ICH
CLK_PCI_ICH
PCLK_PCM
PCLK_KBC
FSC
FSA
FSB
PCI_TPM
PCI_PCCARD
PCI_SIO
MCH_3GPLL_REQ#
CLKREF
CLKREQ PULL HIGH
Clock generator CY28548
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
LT71
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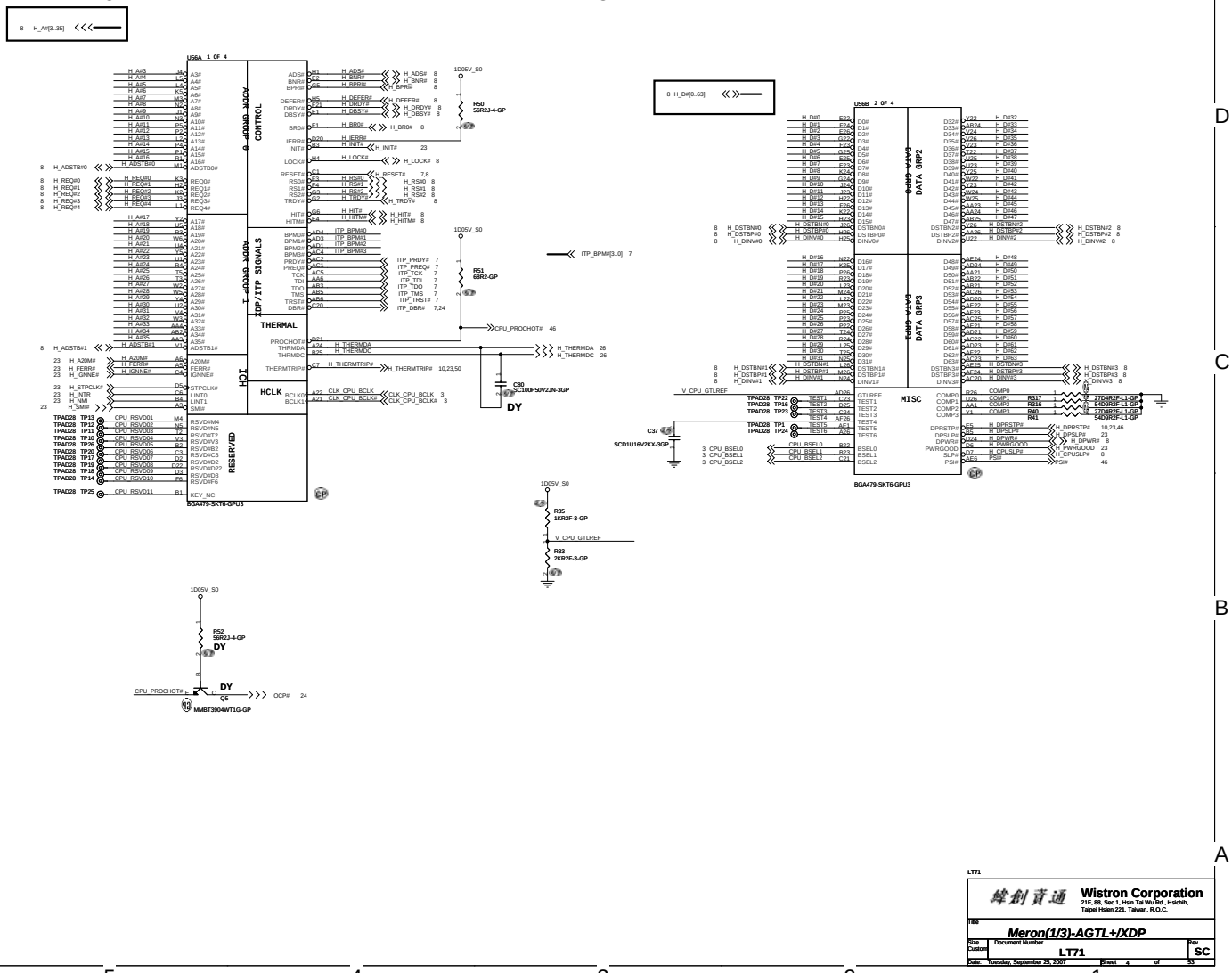

LT71

蜂創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***Meron(1/3)-AGTL+/XDP***

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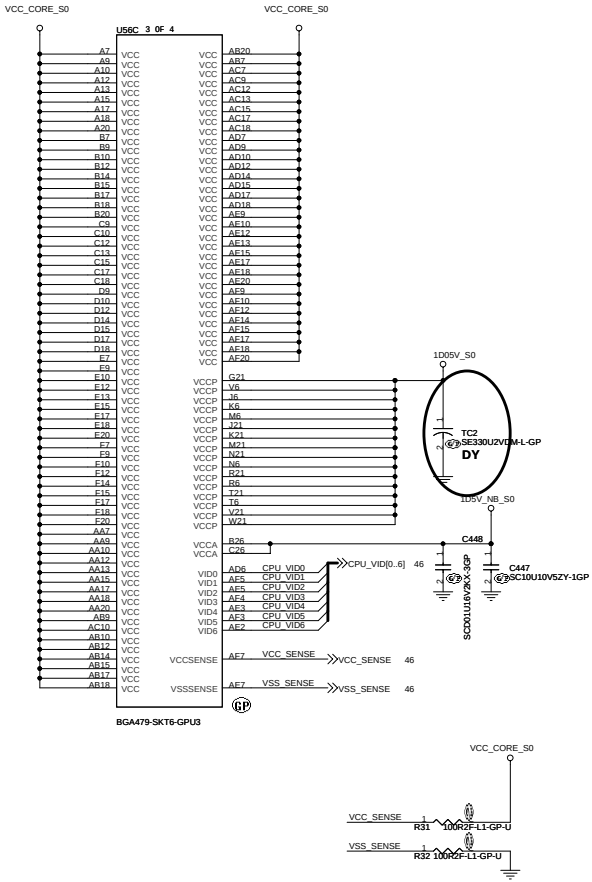

LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***Meron(2/3)-AGTL+/PWR***	
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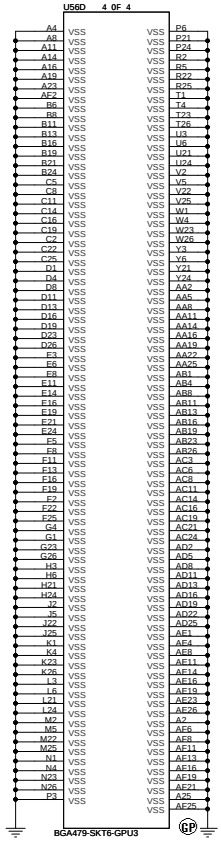

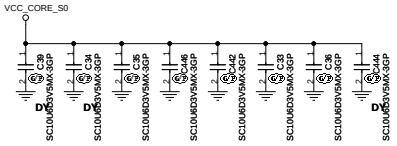

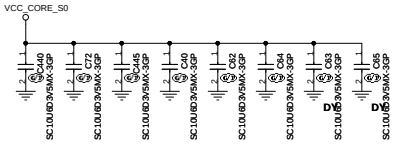

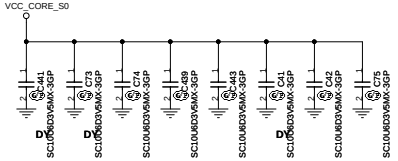

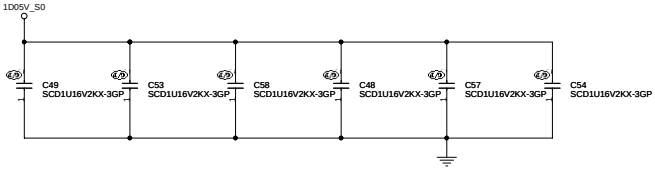

LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***Meron(3/3)-GND&Bypass***	
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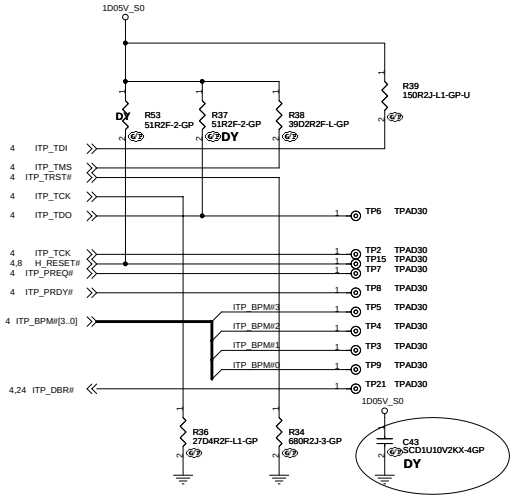
ITP Connector



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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: *Meron(3/3)-GND&Bypass*		
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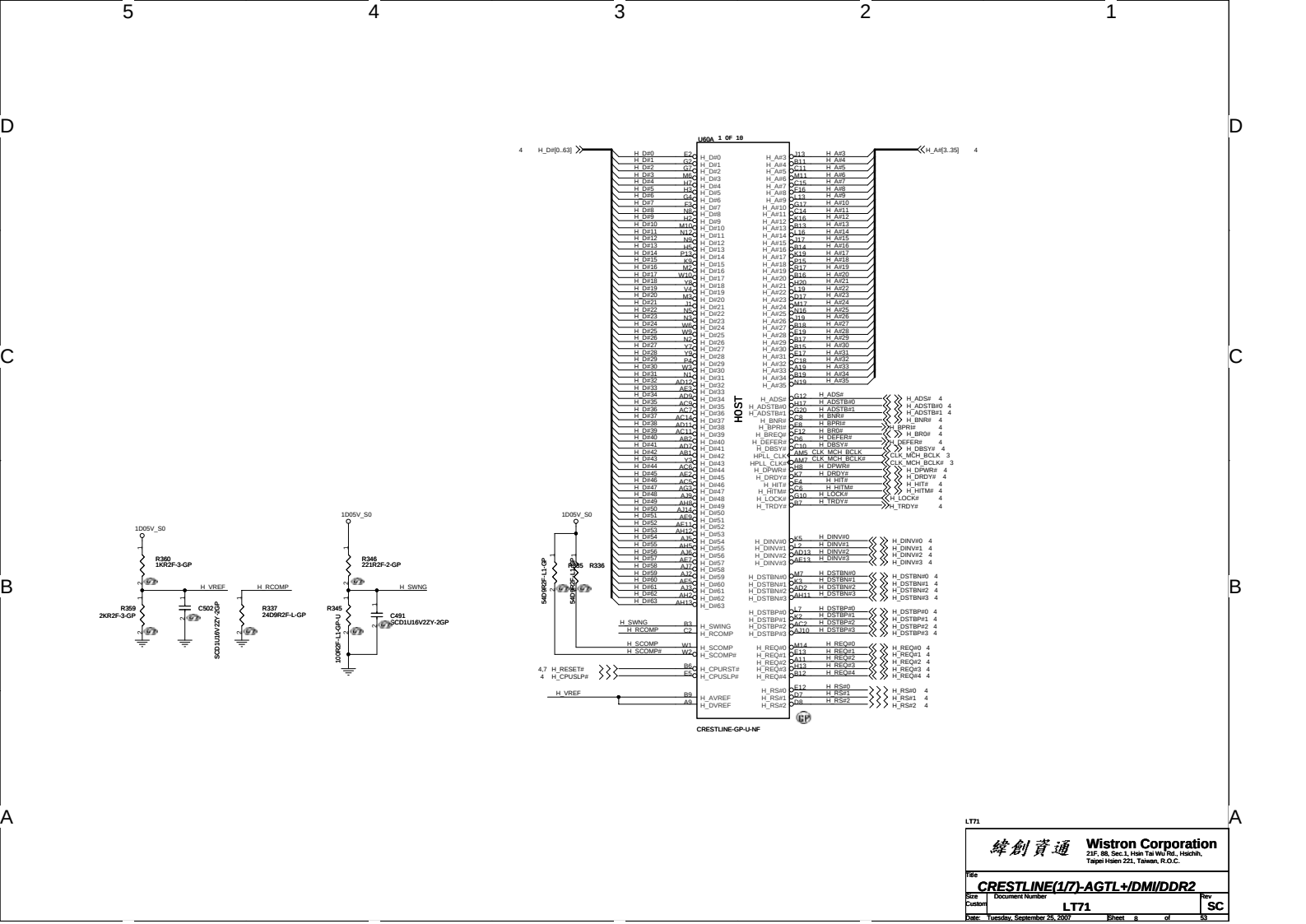

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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	***CRESTLINE(1/7)-AGTL+/DMI/DDR2***
Size: Custom / Document Number: LT71 / Rev: SC	
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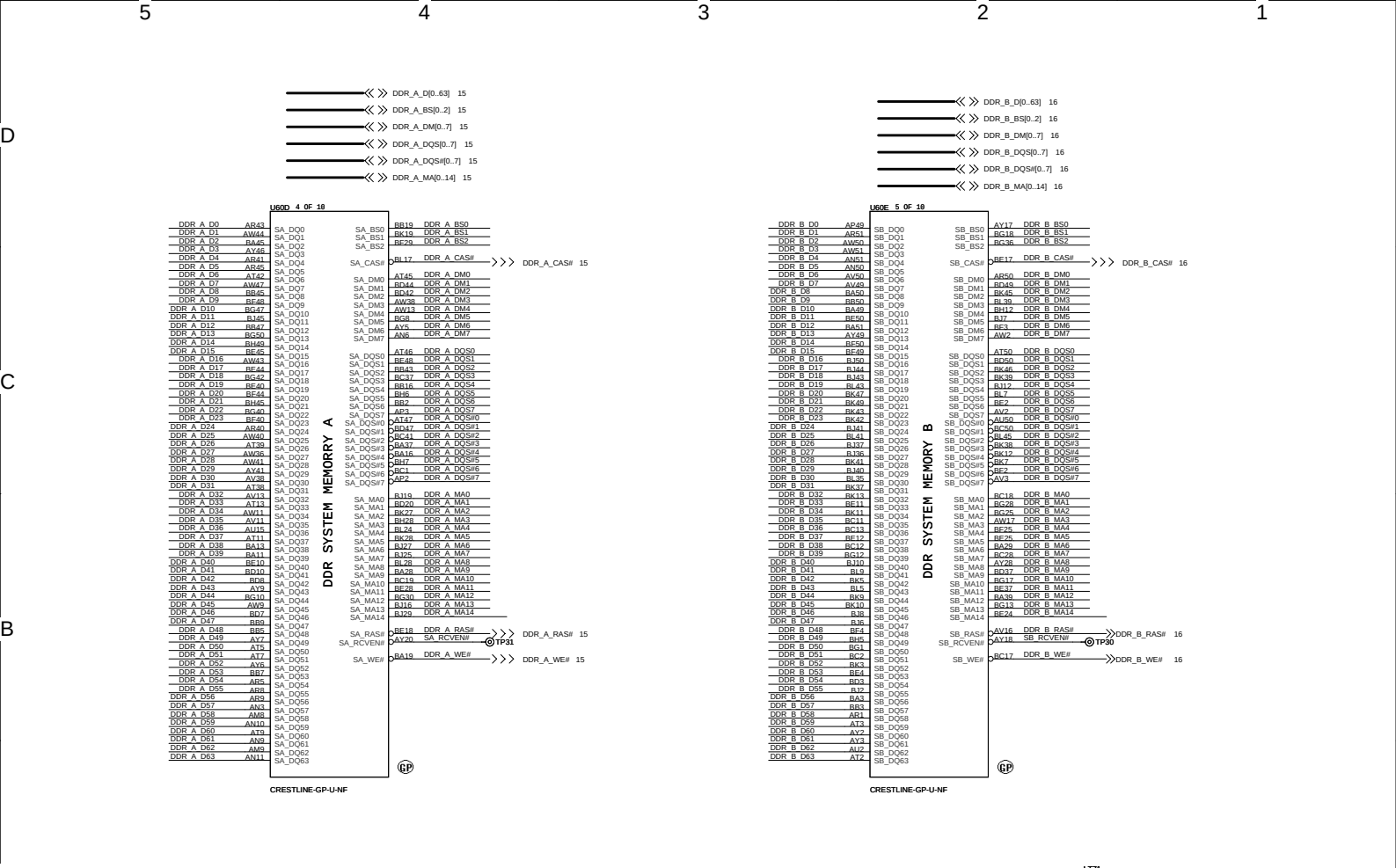

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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***CRESTLINE(2/7)-DDR2 A/B CH***	
Size A3	Document Number **LT71**	Rev **SC**
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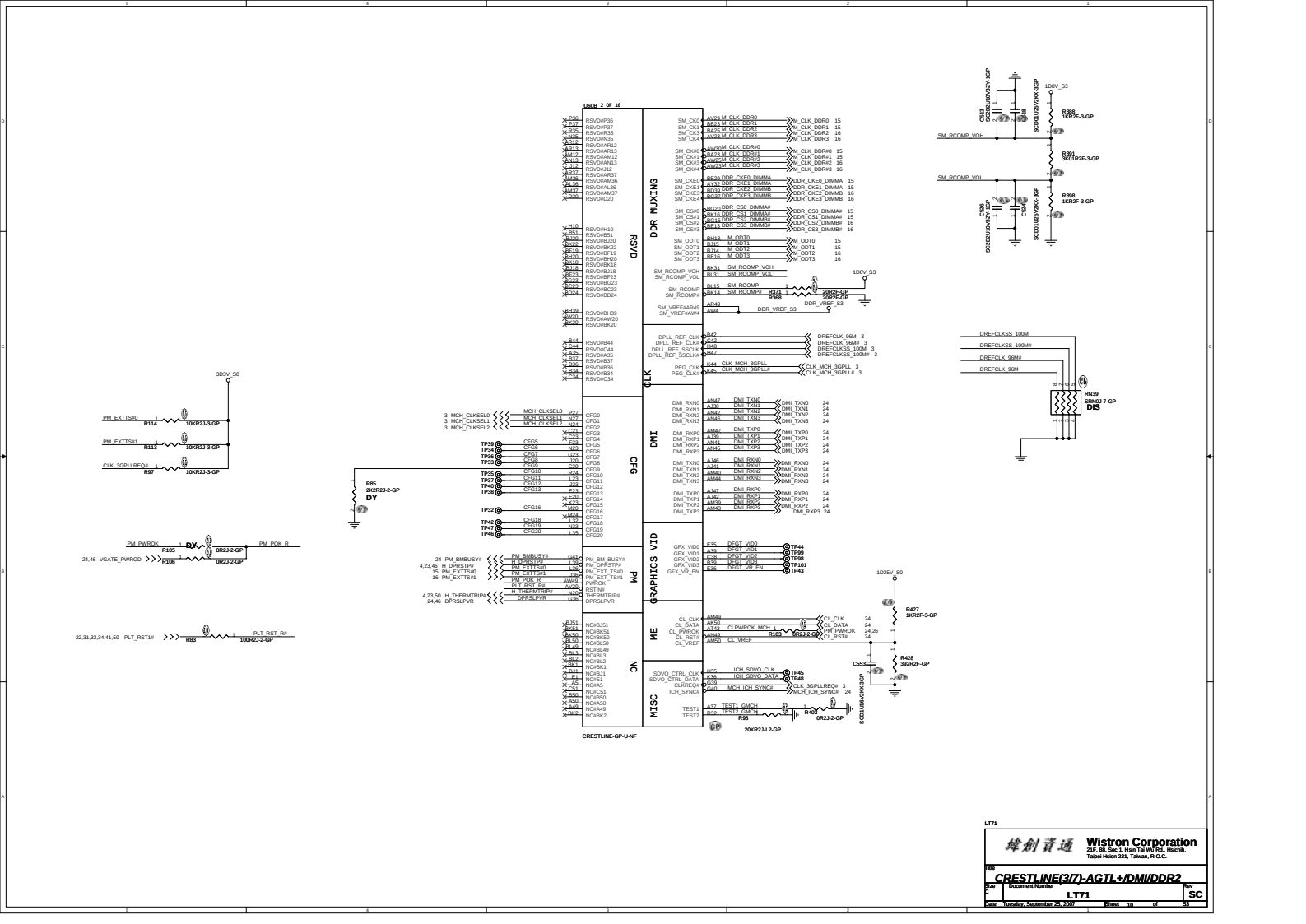
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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
CRESTLINE(3/7)-AGTL+/DMI/DDR2
Size
C
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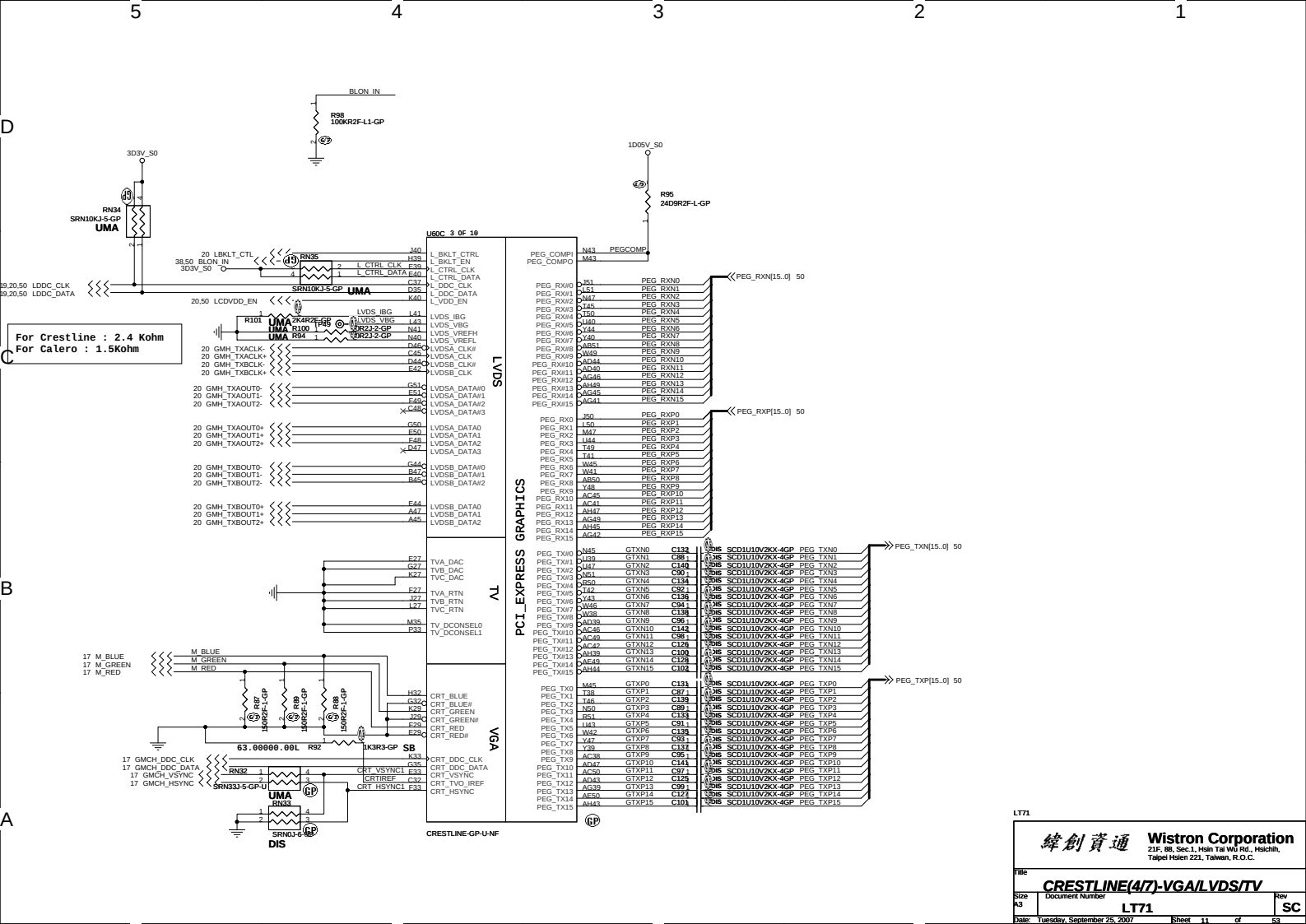
LOCATION 2 OF 10
RSVD
DDR MUXING
CLK
DMI
CFG
GRAPHICS VID
PM
ME
NC
MISC
CRESTLINE-GP-U-NF
3DV_S0
1DBV_S3
1DV_S3
1D25V_S0
DDR_VREF_S3
PM_EXTTSH0
PM_EXTTSH1
CLK_3GPLLREQ#
PM_PWROK
PM_POK_R
SM_RCOMP_VOH
SM_RCOMP_VOL
DREFCLKSS_100M
DREFCLKSS_100M#
DREFCLK_96M#
DREFCLK_96M
22,31,32,34,41,50 PLT_RST1#
PLT_RST_R#
24,46 VGATE_PWRGD
3 MCH_CLKSEL0
3 MCH_CLKSEL1
3 MCH_CLKSEL2
24 PM_BMBUSY#
4,23,46 H_DPRSTP#
15 PM_EXTTSH0
16 PM_EXTTSH1
4,23,50 H_THERMTRIP#
24,46 DPRSLPVR
R85 2K2R2J-2-GP DY
R105 0R2J-2-GP BY
R106 0R2J-2-GP
R114 10KR2J-3-GP
R113 10KR2J-3-GP
R97 10KR2J-3-GP
R83 100R2J-2-GP
R103 0R2J-2-GP
R371 20R2F-GP
R368 20R2F-GP
R93 20KR2J-L2-GP
R427 1KR2F-3-GP
R428 392R2F-GP
R388 1KR2F-3-GP
R391 3K01R2F-3-GP
R398 1KR2F-3-GP
RN39 SRN47-7-GP DIS
C523 SCD1U16V2KX-3GP
C524 SCD1U16V2KX-3GP
C525 SCD1U16V2KX-3GP
C526 SCD1U16V2KX-3GP
C553 SCD1U16V2KX-3GP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3
2
1
D
C
B
A
BLON_IN
R98
100KR2F-L1-GP
3D3V_S0
RN34
SRN10KJ-5-GP
UMA
1D05V_S0
R95
24D9R2F-L-GP
U60C 3 OF 10
20 LBKLT_CTL
38,50 BLON_IN
3D3V_S0
RN35
L_CTRL_CLK
L_CTRL_DATA
SRN10KJ-5-GP
UMA
19,20,50 LDDC_CLK
19,20,50 LDDC_DATA
20,50 LCDVDD_EN
L_BKLT_CTRL
L_BKLT_EN
L_CTRL_CLK
L_CTRL_DATA
L_DDC_CLK
L_DDC_DATA
L_VDD_EN
For Crestline : 2.4 Kohm
For Calero : 1.5Kohm
R101
2K4R2F-GP
R100
2R2J-2-GP
R94
2R2J-2-GP
LVDS_IBG
LVDS_VBG
LVDS_VREFH
LVDS_VREFL
LVDS
20 GMH_TXACLK-
20 GMH_TXACLK+
20 GMH_TXBCLK-
20 GMH_TXBCLK+
LVDSA_CLK#
LVDSA_CLK
LVDSB_CLK#
LVDSB_CLK
20 GMH_TXAOUT0-
20 GMH_TXAOUT1-
20 GMH_TXAOUT2-
20 GMH_TXAOUT0+
20 GMH_TXAOUT1+
20 GMH_TXAOUT2+
LVDSA_DATA#0
LVDSA_DATA#1
LVDSA_DATA#2
LVDSA_DATA#3
LVDSA_DATA0
LVDSA_DATA1
LVDSA_DATA2
LVDSA_DATA3
20 GMH_TXBOUT0-
20 GMH_TXBOUT1-
20 GMH_TXBOUT2-
20 GMH_TXBOUT0+
20 GMH_TXBOUT1+
20 GMH_TXBOUT2+
LVDSB_DATA#0
LVDSB_DATA#1
LVDSB_DATA#2
LVDSB_DATA0
LVDSB_DATA1
LVDSB_DATA2
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PEG_COMPO
PEGCOMP
PEG_RX#[0..15]
PEG_RX[0..15]
PEG_RXN[15..0] 50
PEG_RXP[15..0] 50
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PEG_RXN1
PEG_RXN2
PEG_RXN3
PEG_RXN4
PEG_RXN5
PEG_RXN6
PEG_RXN7
PEG_RXN8
PEG_RXN9
PEG_RXN10
PEG_RXN11
PEG_RXN12
PEG_RXN13
PEG_RXN14
PEG_RXN15
PEG_RXP0
PEG_RXP1
PEG_RXP2
PEG_RXP3
PEG_RXP4
PEG_RXP5
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PEG_RXP8
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PEG_RXP15
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PEG_TXP[15..0] 50
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GTXN1
GTXN2
GTXN3
GTXN4
GTXN5
GTXN6
GTXN7
GTXN8
GTXN9
GTXN10
GTXN11
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GTXN13
GTXN14
GTXN15
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GTXP1
GTXP2
GTXP3
GTXP4
GTXP5
GTXP6
GTXP7
GTXP8
GTXP9
GTXP10
GTXP11
GTXP12
GTXP13
GTXP14
GTXP15
SCD1U10V2KX-4GP
TLV
TVA_DAC
TVB_DAC
TVC_DAC
TVA_RTN
TVB_RTN
TVC_RTN
TV_DCONSEL0
TV_DCONSEL1
17 M_BLUE
17 M_GREEN
17 M_RED
R87
150R2F-1-GP
R89
150R2F-1-GP
R88
150R2F-1-GP
63.00000.00L
R92
1K3R3-GP
SB
VGA
CRT_BLUE
CRT_BLUE#
CRT_GREEN
CRT_GREEN#
CRT_RED
CRT_RED#
17 GMCH_DDC_CLK
17 GMCH_DDC_DATA
17 GMCH_VSYNC
17 GMCH_HSYNC
RN32
SRN33J-5-GP-U
UMA
RN33
SRN0J-5-GP
DIS
CRT_DDC_CLK
CRT_DDC_DATA
CRT_VSYNC
CRT_TVO_IREF
CRT_HSYNC
CRT_VSYNC1
CRTIREF
CRT_HSYNC1
CRESTLINE-GP-U-NF
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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
CRESTLINE(4/7)-VGA/LVDS/TV
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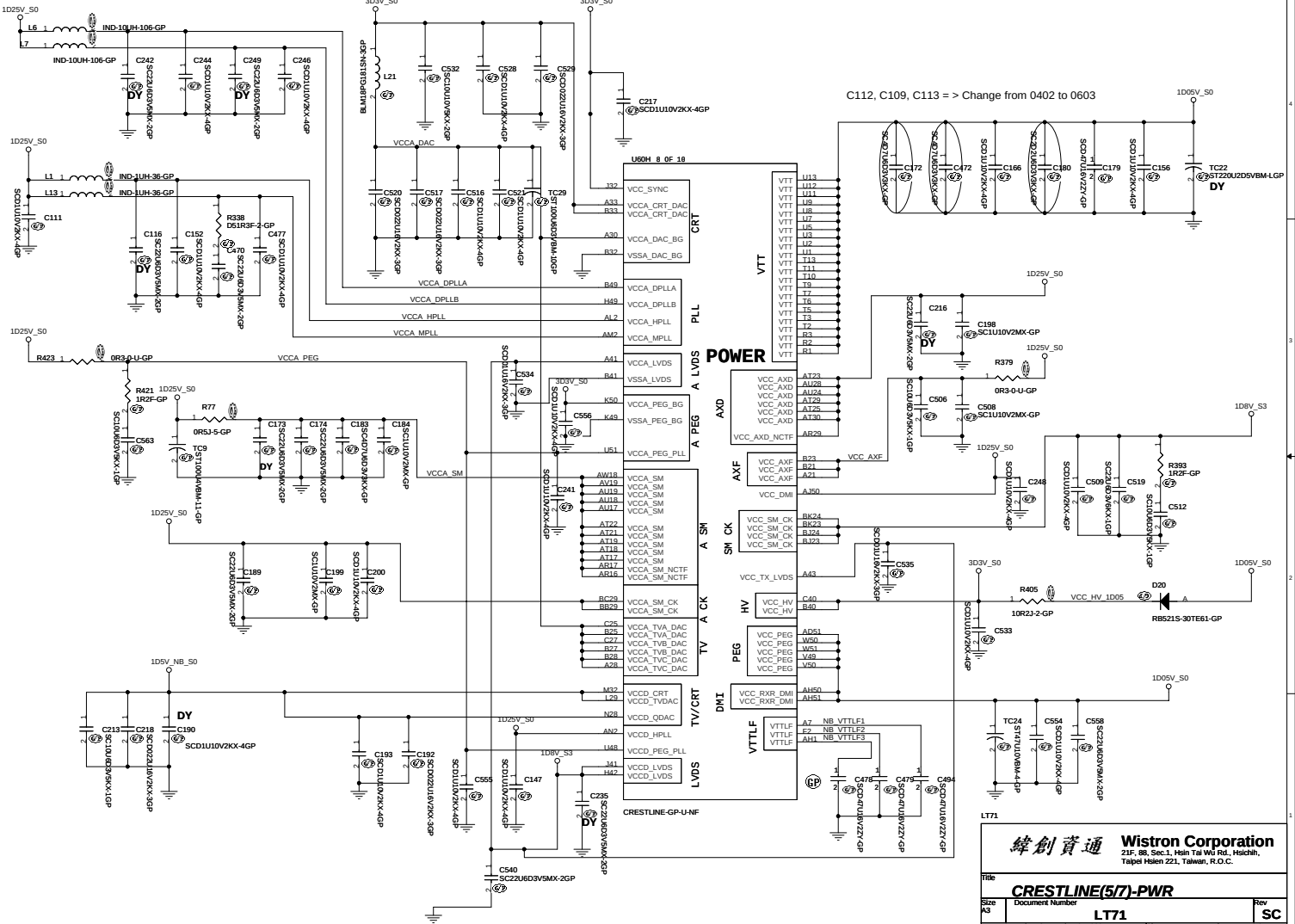
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U60H 8 OF 10
CRESTLINE-GP-U-NF
POWER
CRT
PLL
A LVDS
A PEG
AXD
AXF
SM CK
A SM
A CK
HV
PEG
TV
TV/CRT
DMI
VTTLF
LVDS
VTT
VCCA_DAC
VCCA_DPLLA
VCCA_DPLLB
VCCA_HPLL
VCCA_MPLL
VCCA_PEG
VCCA_SM
1D25V_S0
3D3V_S0
1D8V_S3
1D05V_S0
1D5V_NB_S0
LT71
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
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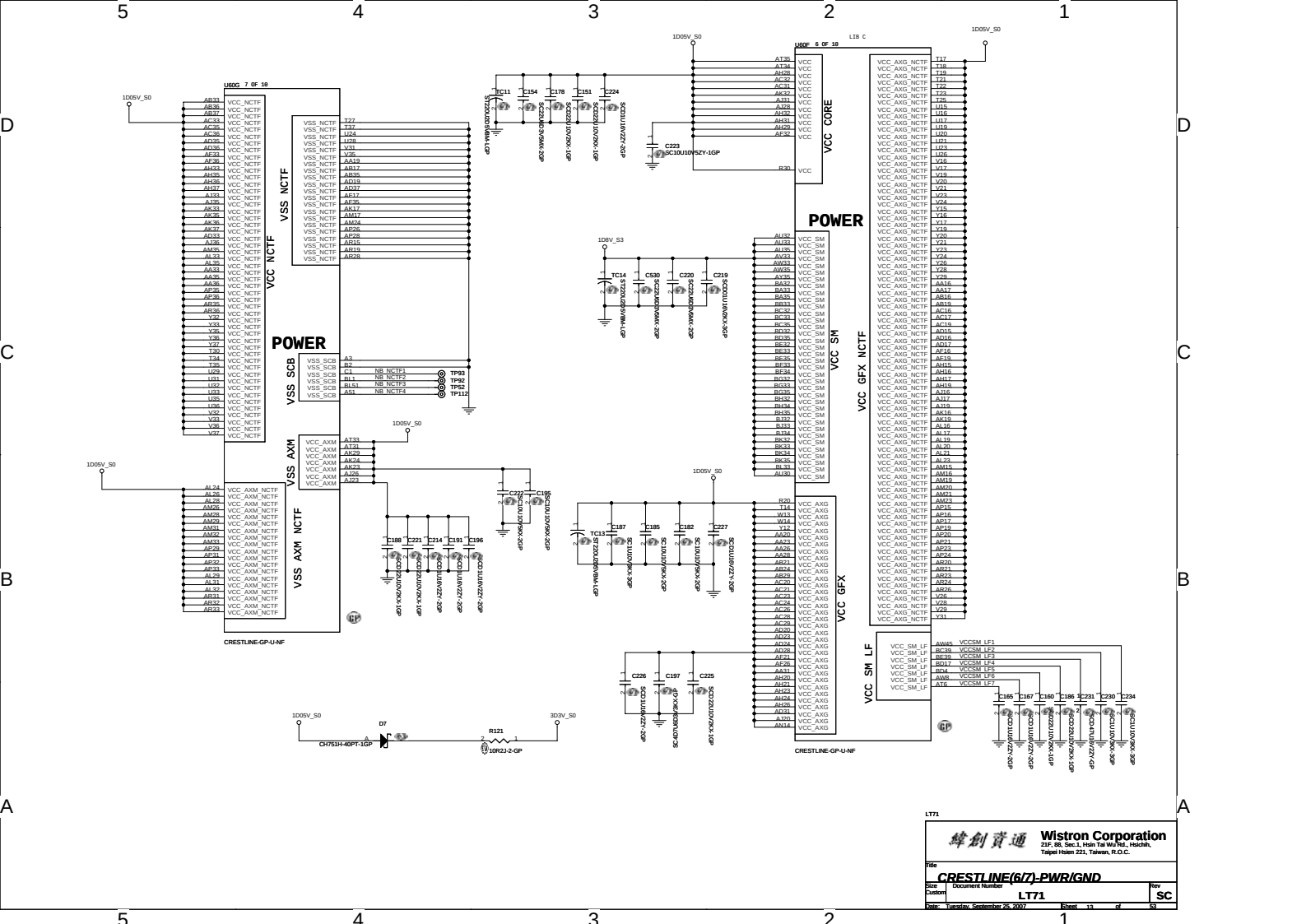
U60G 7 OF 10
U60F 6 OF 10
LIB C
POWER
VCC CORE
VCC SM
VCC GFX NCTF
VCC SM LF
VSS NCTF
VCC NCTF
VSS SCB
VSS AXM
VCC AXM NCTF
VSS AXM NCTF
VCC GFX
CRESTLINE-GP-U-NF
1D05V_S0
1D8V_S3
3D3V_S0
D7
CH751H-40PT-1GP
R121
10R2J-2-GP
LT71
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
CRESTLINE(6/7)-PWR/GND
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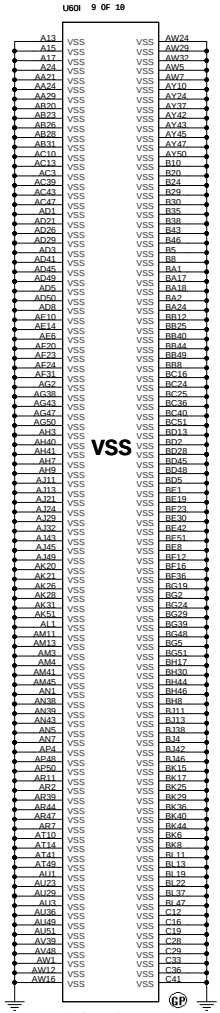
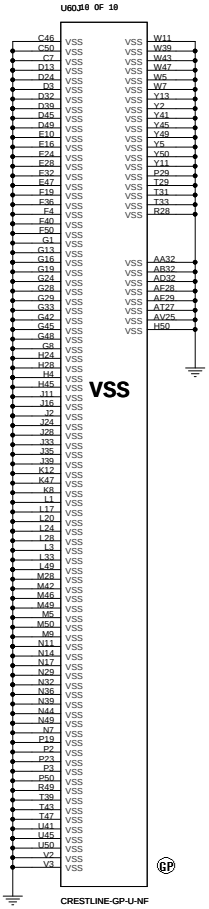
D C B A





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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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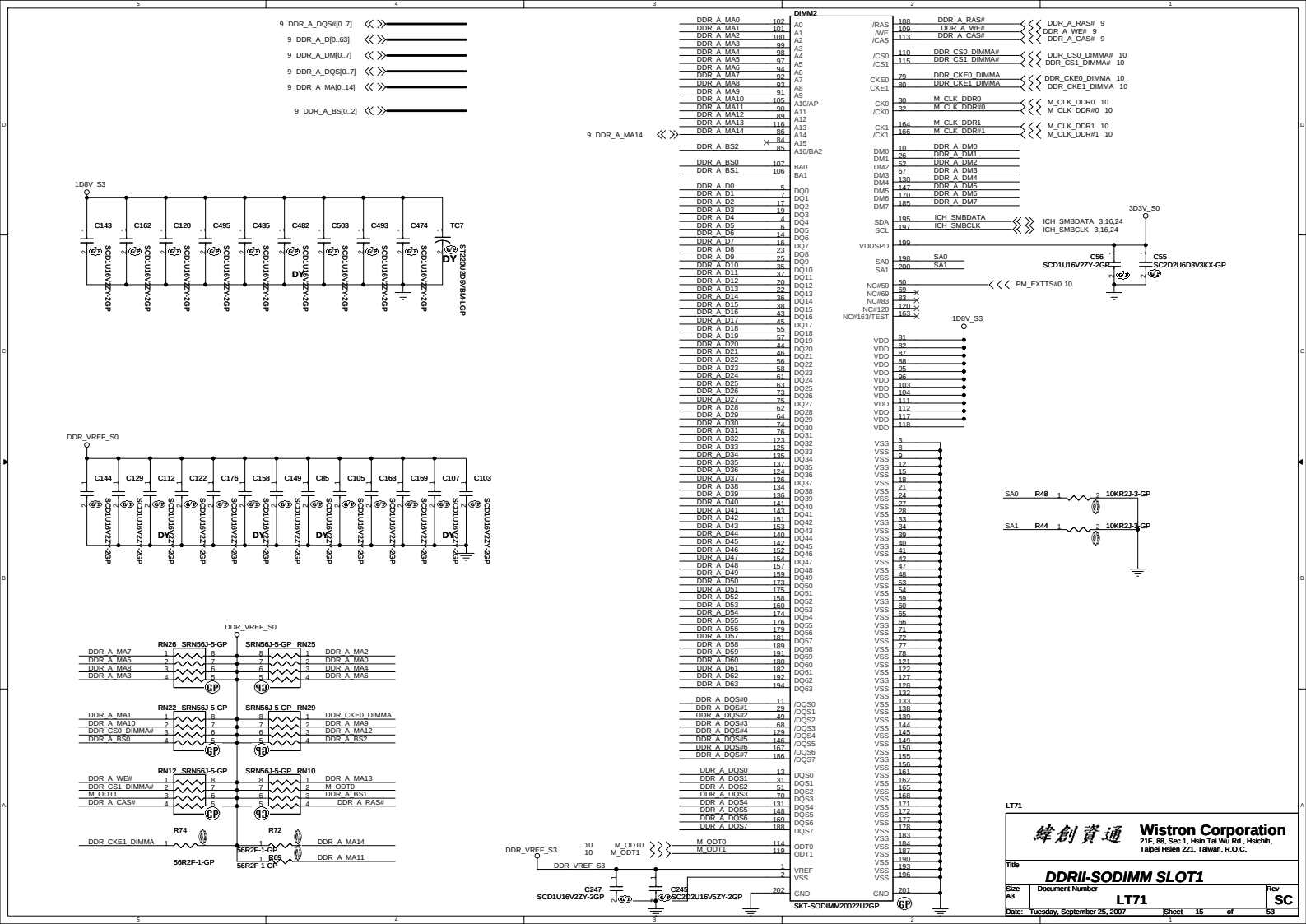

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緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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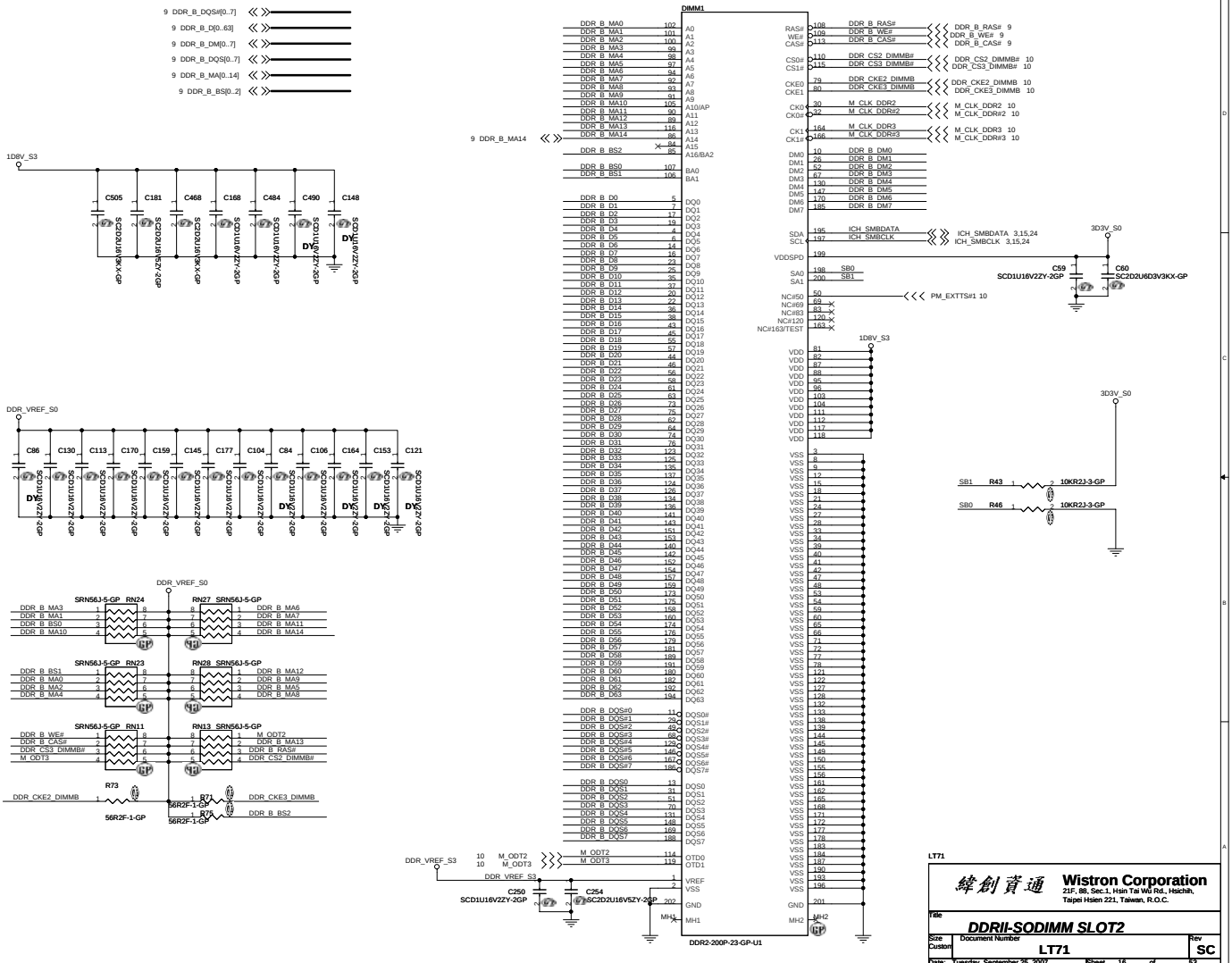
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9 DDR_B_D[0..63]
9 DDR_B_DM[0..7]
9 DDR_B_DQS[0..7]
9 DDR_B_MA[0..14]
9 DDR_B_BS[0..2]
DIMM1
DDR2-200P-23-GP-U1
1D8V_S3
C505
C181
C468
C168
C484
C490
C148
SCD1U16V2ZY-2GP
SC2D2U16V5ZY-2GP
SC2D2U16V3KX-GP
DDR_VREF_S0
C86
C130
C113
C170
C159
C145
C177
C104
C84
C106
C164
C153
C121
SRN56J-5-GP_RN24
RN27_SRN56J-5-GP
SRN56J-5-GP_RN23
RN28_SRN56J-5-GP
SRN56J-5-GP_RN11
RN13_SRN56J-5-GP
R73
R71
R75
56R2F-1-GP
DDR_VREF_S3
C250
C254
SC2D2U16V5ZY-2GP
3D3V_S0
C59
C60
SC2D2U6D3V3KX-GP
PM_EXTTS#1 10
SB1
R43
SB0
R46
10KR2J-3-GP
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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
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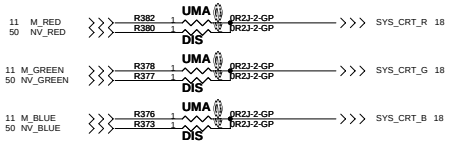
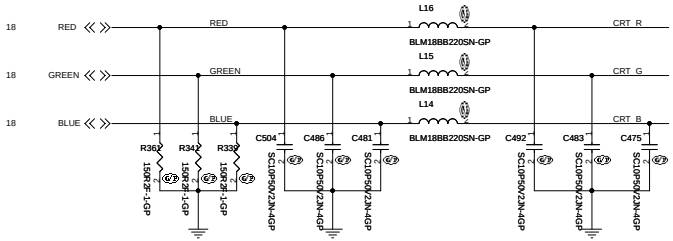
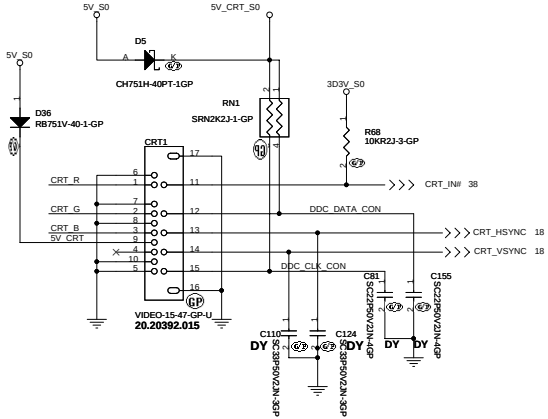
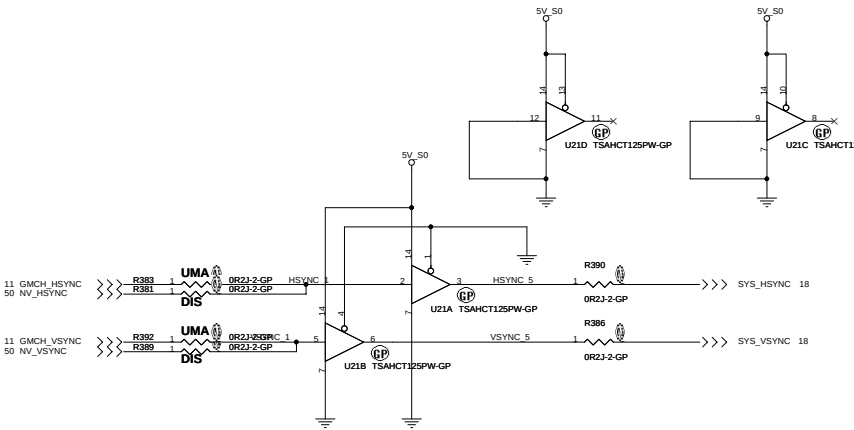
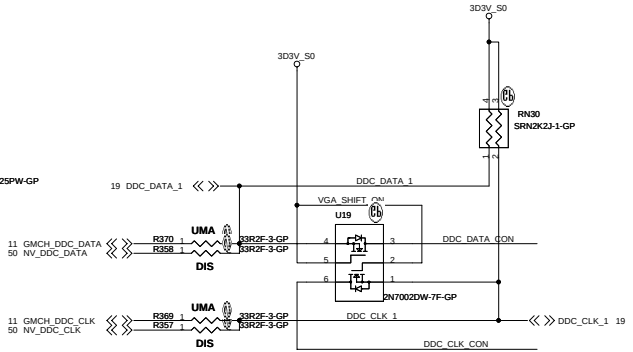
CRT I/F & CONNECTOR











LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	**CRT Connector**	
Size A3	Document Number **LT71**	Rev **SC**
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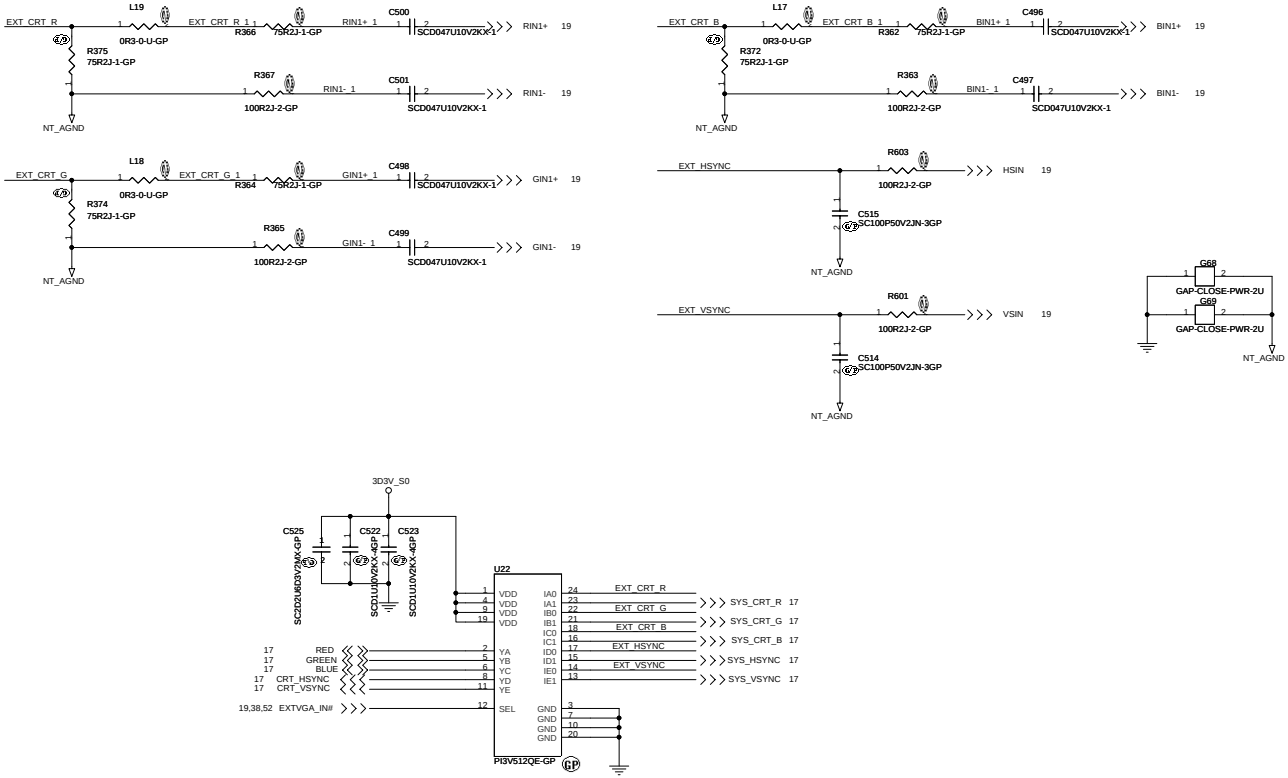
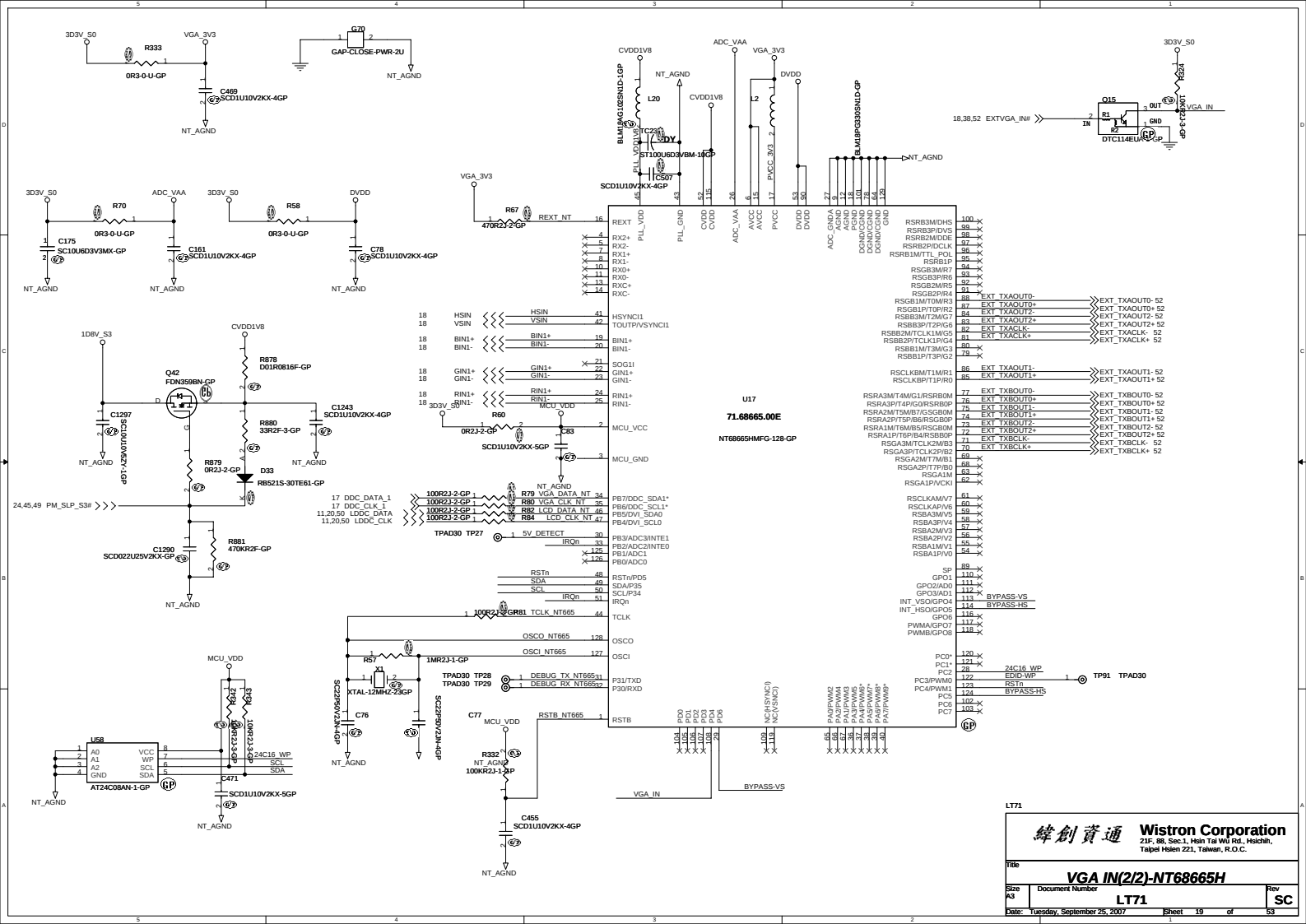


LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***VGA IN(1/2)-RGB IN***	
Size A3	Document Number **LT71**	Rev **SC**
Date: Tuesday, September 25, 2007	Sheet 18 of 53	


3D3V_S0
VGA_3V3
R333
0R3-0-U-GP
C469
SCD1U10V2KX-4GP
NT_AGND
G70
GAP-CLOSE-PWR-2U
CVDD1V8
ADC_VAA
DVDD
BLM18PG601SN1D-1GP
L20
L2
BLM18PG330SN1D-GP
ITC23
ST100U6D3VBM-16GP
C507
SCD1U10V2KX-4GP
R70
R58
C175
SC10U6D3V3MX-GP
C161
C78
R67
470R2J-2-GP
REXT_NT
1D8V_S3
Q42
FDN359BN-GP
R878
D01R0816F-GP
C1297
C1243
R880
33R2F-3-GP
R879
0R2J-2-GP
D33
RB521S-30TE61-GP
24,45,49 PM_SLP_S3#
C1290
SCD022U25V2KX-GP
R881
470KR2F-GP
R60
0R2J-2-GP
C83
SCD1U10V2KX-5GP
MCU_VDD
MCU_GND
MCU_VCC
U17
71.68665.00E
NT68665HMFG-128-GP
17 DDC_DATA_1
17 DDC_CLK_1
11,20,50 LDDC_DATA
11,20,50 LDDC_CLK
R79 VGA_DATA_NT
R80 VGA_CLK_NT
R82 LCD_DATA_NT
R84 LCD_CLK_NT
TPAD30 TP27
5V_DETECT
IRQn
HSIN
VSIN
BIN1+
BIN1-
GIN1+
GIN1-
RIN1+
RIN1-
RSTn
SDA
SCL
TCLK_NT665
OSCO_NT665
OSCI_NT665
1MR2J-1-GP
R57
X1
XTAL-12MHZ-23GP
SC22P50V2JN-4GP
TPAD30 TP28
TPAD30 TP29
DEBUG_TX_NT665
DEBUG_RX_NT665
C76
C77
RSTB_NT665
R332
100KR2J-1-GP
C455
SCD1U10V2KX-4GP
VGA_IN
BYPASS-VS
U58
AT24C08AN-1-GP
24C16_WP
SCL
SDA
C471
SCD1U10V2KX-5GP
R824
10KR2J-2-GP
R825
10KR2J-2-GP
18,38,52 EXTVGA_IN#
Q15
DTC114EU/A-GP
R1
R2
10KR2J-2-GP
EXT_TXAOUT0-
EXT_TXAOUT0+
EXT_TXAOUT2-
EXT_TXAOUT2+
EXT_TXACLK-
EXT_TXACLK+
EXT_TXAOUT1-
EXT_TXAOUT1+
EXT_TXBOUT0-
EXT_TXBOUT0+
EXT_TXBOUT1-
EXT_TXBOUT1+
EXT_TXBOUT2-
EXT_TXBOUT2+
EXT_TXBCLK-
EXT_TXBCLK+
BYPASS-VS
BYPASS-HS
24C16_WP
EDID-WP
RSTn
BYPASS-HS
TP91 TPAD30
LT71
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
VGA IN(2/2)-NT68665H
Size A3
Document Number LT71
Rev SC
Date: Tuesday, September 25, 2007
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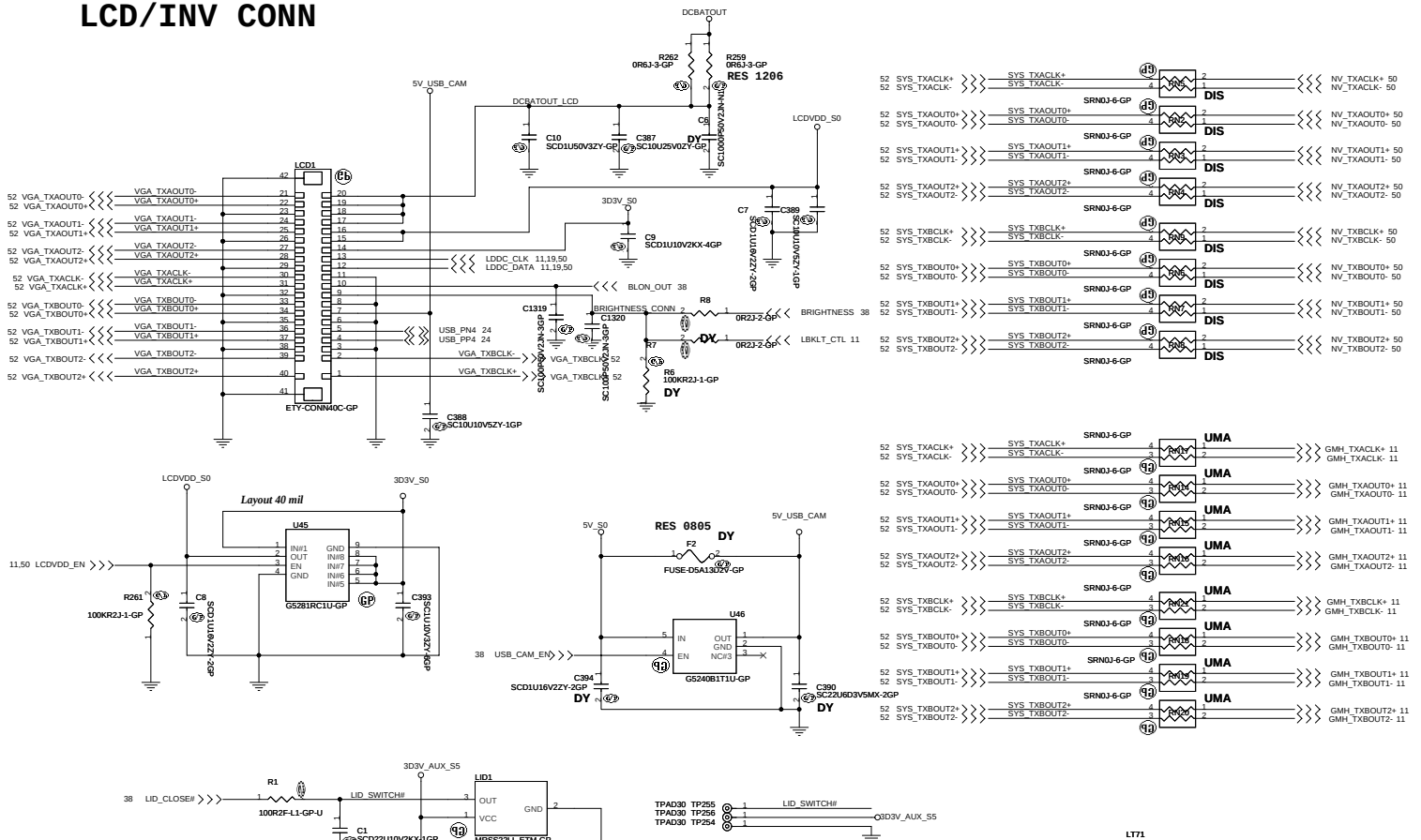
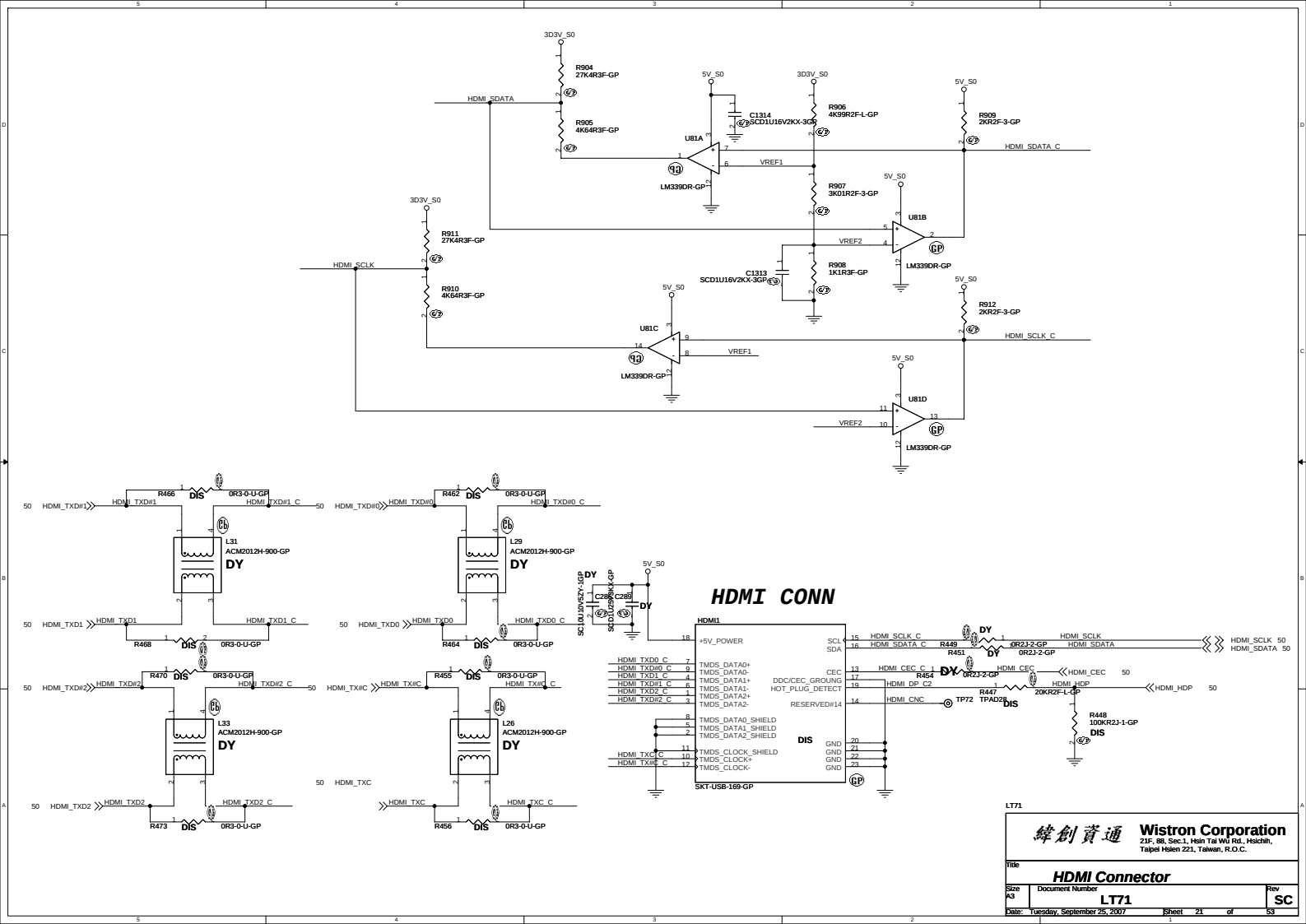
LED / INVERTER INTERFACE

LCD/INV CONN



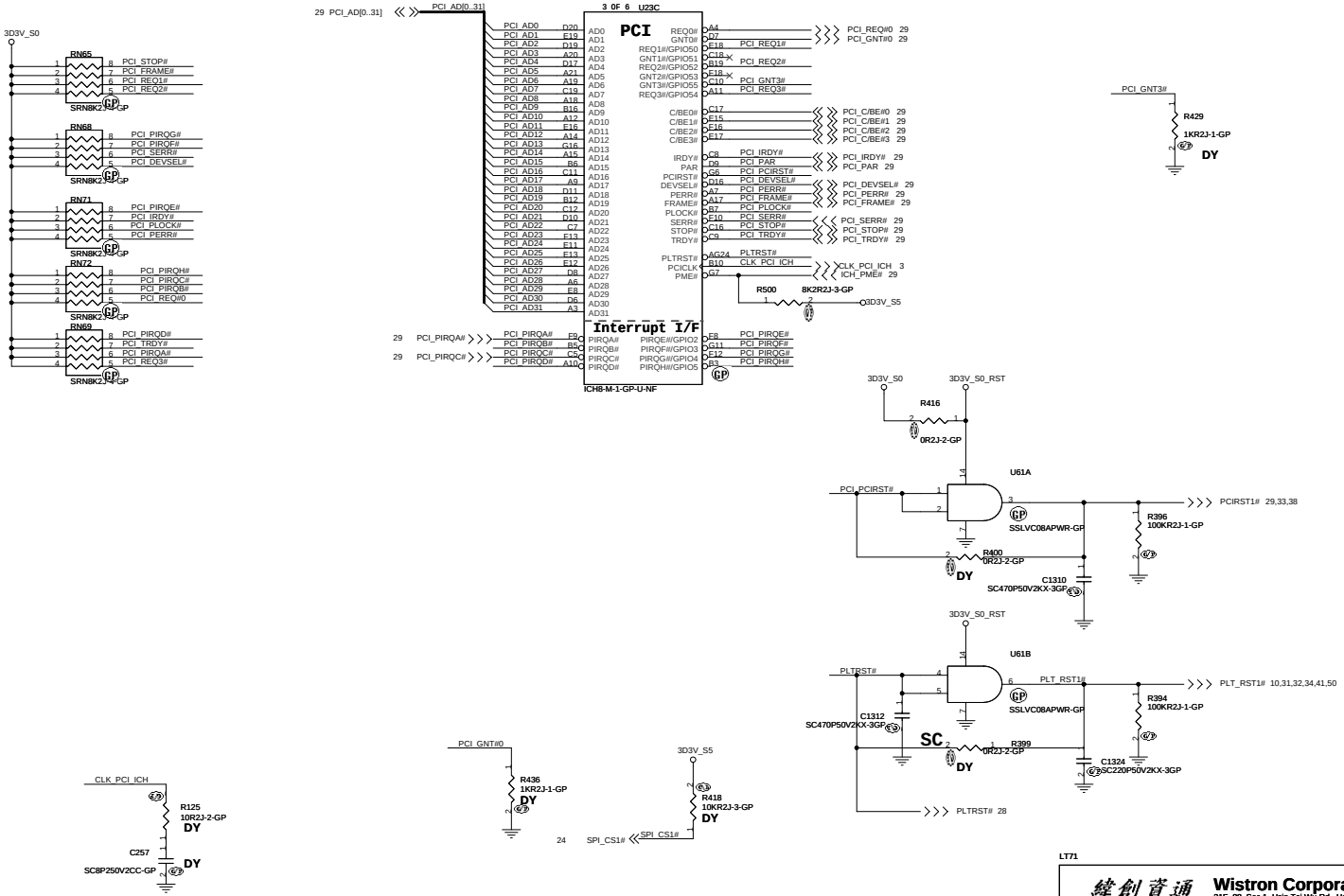
LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	**LCD/Inverter Connector**	
Size A3	Document Number **LT71**	Rev **SC**
Date: Tuesday, September 25, 2007	Sheet 20 of 53	



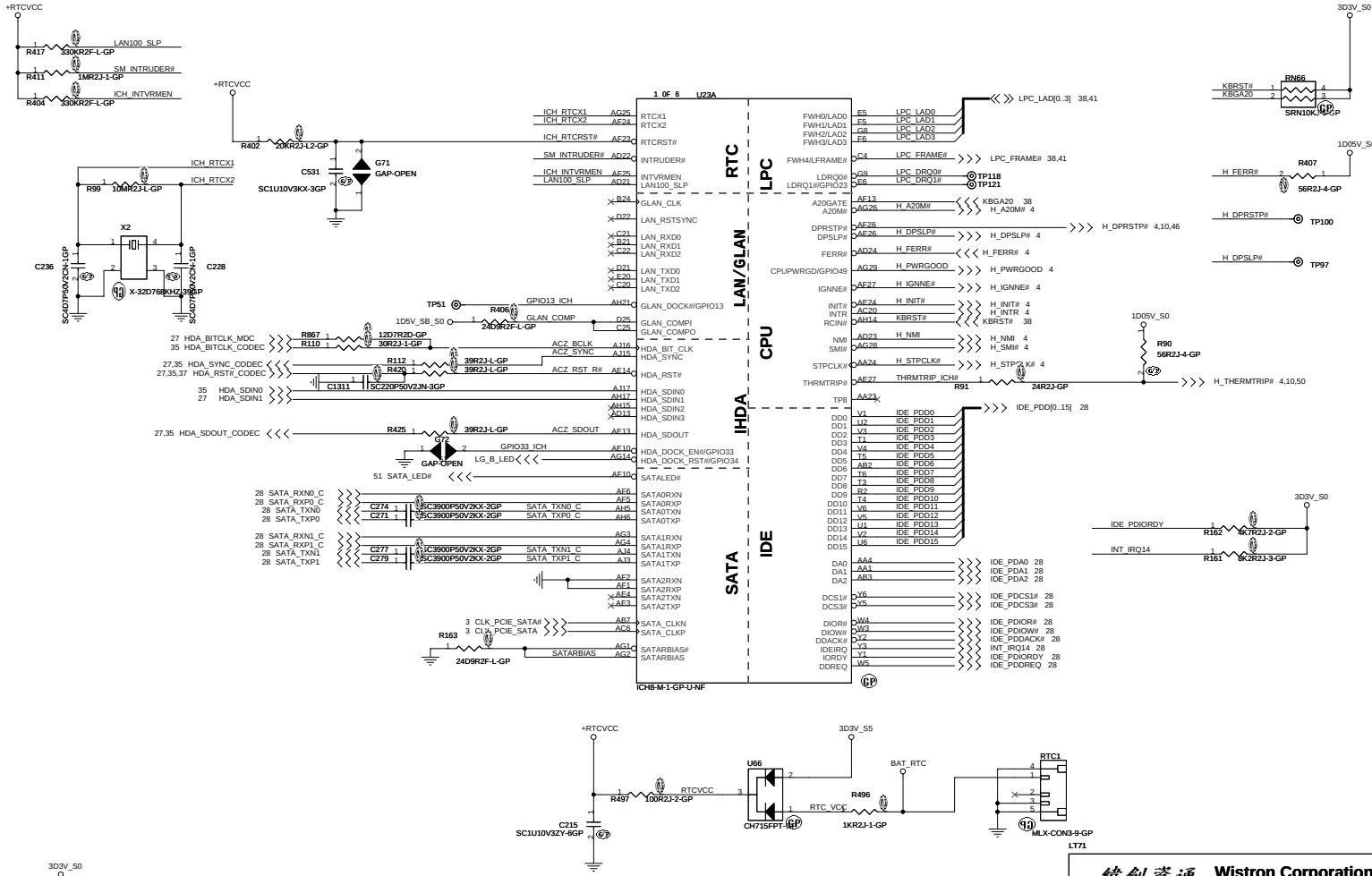
LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***HDMI Connector***	
Size A3	Document Number **LT71**	Rev **SC**
Date: Tuesday, September 25, 2007	Sheet 21 of 53	

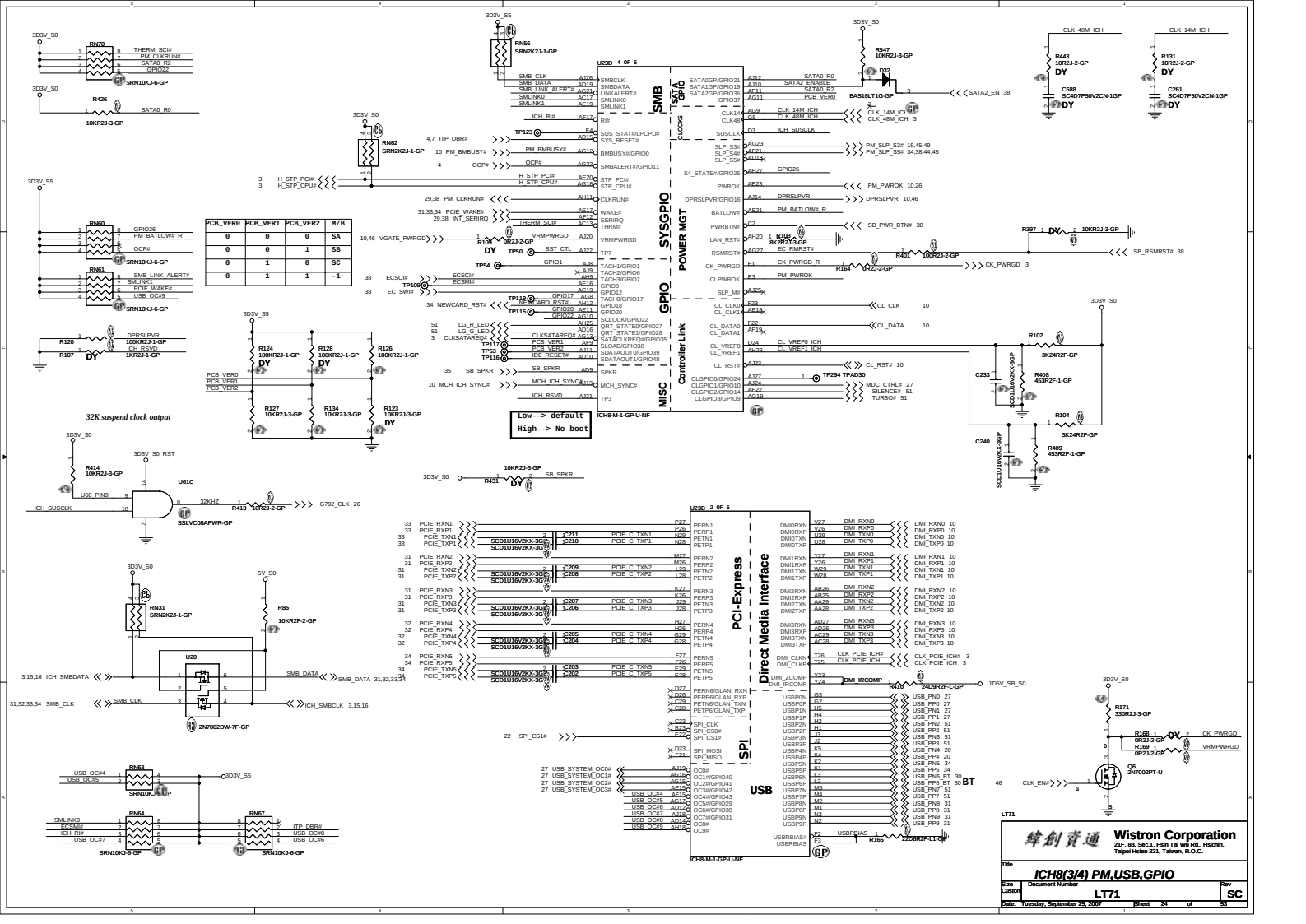


LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	*ICH8(1/4)-PCI/INT*	
Size: A3	Document Number: LT71	Rev: SC
Date: Tuesday, September 25, 2007	Sheet 22 of 53	



緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title	ICH8(2/4) LAN,HD,IDE,LPC		
Size A3	Document Number LT71		Rev SC
Date: Tuesday, September 25, 2007	Sheet 23 of 53		


SMB
SATA GPIO
CLOCKS
SYSGPIO
POWER MGT
GPIO
Controller Link
MISC
PCI-Express
Direct Media Interface
SPI
USB
BT
U23D 4 OF 6
U23B 2 OF 6
ICH8-M-1-GP-U-NF
SMB_CLK
SMB_DATA
SMB_LINK_ALERT#
SMLINK0
SMLINK1
ICH_RI#
SUS_STAT#/LPCPD#
SYS_RESET#
BMBUSY#/GPIO0
SMBALERT#/GPIO11
STP_PCI#
STP_CPU#
CLKRUN#
WAKE#
SERIRQ
THRM#
VRMPWRGD
SST_CTL
GPIO1
TACH1/GPIO1
TACH2/GPIO6
TACH3/GPIO7
GPIO8
GPIO12
TACH0/GPIO17
GPIO18
GPIO20
GPIO22
SCLOCK/GPIO22
QRT_STATE0/GPIO27
QRT_STATE1/GPIO28
SATACLKREQ#/GPIO35
SLOAD/GPIO38
SDATAOUT0/GPIO39
SDATAOUT1/GPIO48
SPKR
MCH_SYNC#
TP3
SATA0GP/GPIO21
SATA1GP/GPIO19
SATA2GP/GPIO36
GPIO37
CLK14
CLK48
SUSCLK
SLP_S3#
SLP_S4#
SLP_S5#
S4_STATE#/GPIO26
PWROK
DPRSLPVR/GPIO16
BATLOW#
PWRBTN#
LAN_RST#
RSMRST#
CK_PWRGD
CLPWROK
SLP_M#
CL_CLK0
CL_CLK1
CL_DATA0
CL_DATA1
CL_VREF1
CL_VREF0
CL_RST#
CLGPIO0/GPIO24
CLGPIO1/GPIO10
CLGPIO2/GPIO14
CLGPIO3/GPIO5
PCIE_RXN1
PCIE_RXP1
PCIE_TXN1
PCIE_TXP1
PCIE_RXN2
PCIE_RXP2
PCIE_TXN2
PCIE_TXP2
PCIE_RXN3
PCIE_RXP3
PCIE_TXN3
PCIE_TXP3
PCIE_RXN4
PCIE_RXP4
PCIE_TXN4
PCIE_TXP4
PCIE_RXN5
PCIE_RXP5
PCIE_TXN5
PCIE_TXP5
PERN1
PERP1
PETN1
PETP1
PERN2
PERP2
PETN2
PETP2
PERN3
PERP3
PETN3
PETP3
PERN4
PERP4
PETN4
PETP4
PERN5
PERP5
PETN5
PETP5
PERN6/GLAN_RXN
PERP6/GLAN_RXP
PETN6/GLAN_TXN
PETP6/GLAN_TXP
SPI_CLK
SPI_CS0#
SPI_CS1#
SPI_MOSI
SPI_MISO
OC0#
OC1#/GPIO40
OC2#/GPIO41
OC3#/GPIO42
OC4#/GPIO43
OC5#/GPIO29
OC6#/GPIO30
OC7#/GPIO31
OC8#
OC9#
USBRBIAS#
USBRBIAS
USBP0N
USBP0P
USBP1N
USBP1P
USBP2N
USBP2P
USBP3N
USBP3P
USBP4N
USBP4P
USBP5N
USBP5P
USBP6N
USBP6P
USBP7N
USBP7P
USBP8N
USBP8P
USBP9N
USBP9P
DMI0RXN
DMI0RXP
DMI0TXN
DMI0TXP
DMI1RXN
DMI1RXP
DMI1TXN
DMI1TXP
DMI2RXN
DMI2RXP
DMI2TXN
DMI2TXP
DMI3RXN
DMI3RXP
DMI3TXN
DMI3TXP
DMI_CLKN
DMI_CLKP
DMI_ZCOMP
DMI_IRCOMP
DMI_RXN0
DMI_RXP0
DMI_TXN0
DMI_TXP0
DMI_RXN1
DMI_RXP1
DMI_TXN1
DMI_TXP1
DMI_RXN2
DMI_RXP2
DMI_TXN2
DMI_TXP2
DMI_RXN3
DMI_RXP3
DMI_TXN3
DMI_TXP3
CLK_PCIE_ICH#
CLK_PCIE_ICH
USB_PN0
USB_PP0
USB_PN1
USB_PP1
USB_PN2
USB_PP2
USB_PN3
USB_PP3
USB_PN4
USB_PP4
USB_PN5
USB_PP5
USB_PN6_BT
USB_PP6_BT
USB_PN7
USB_PP7
USB_PN8
USB_PP8
USB_PN9
USB_PP9
Low--> default
High--> No boot
32K suspend clock output
SSLVC08APWR-GP
SRN2K2J-1-GP
SRN10K-8-GP
2N7002DW-7F-GP
BAS16LT1G-GP
Q6
2N7002PT-U
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
ICH8(3/4) PM,USB,GPIO
LT71
SC
Tuesday, September 25, 2007
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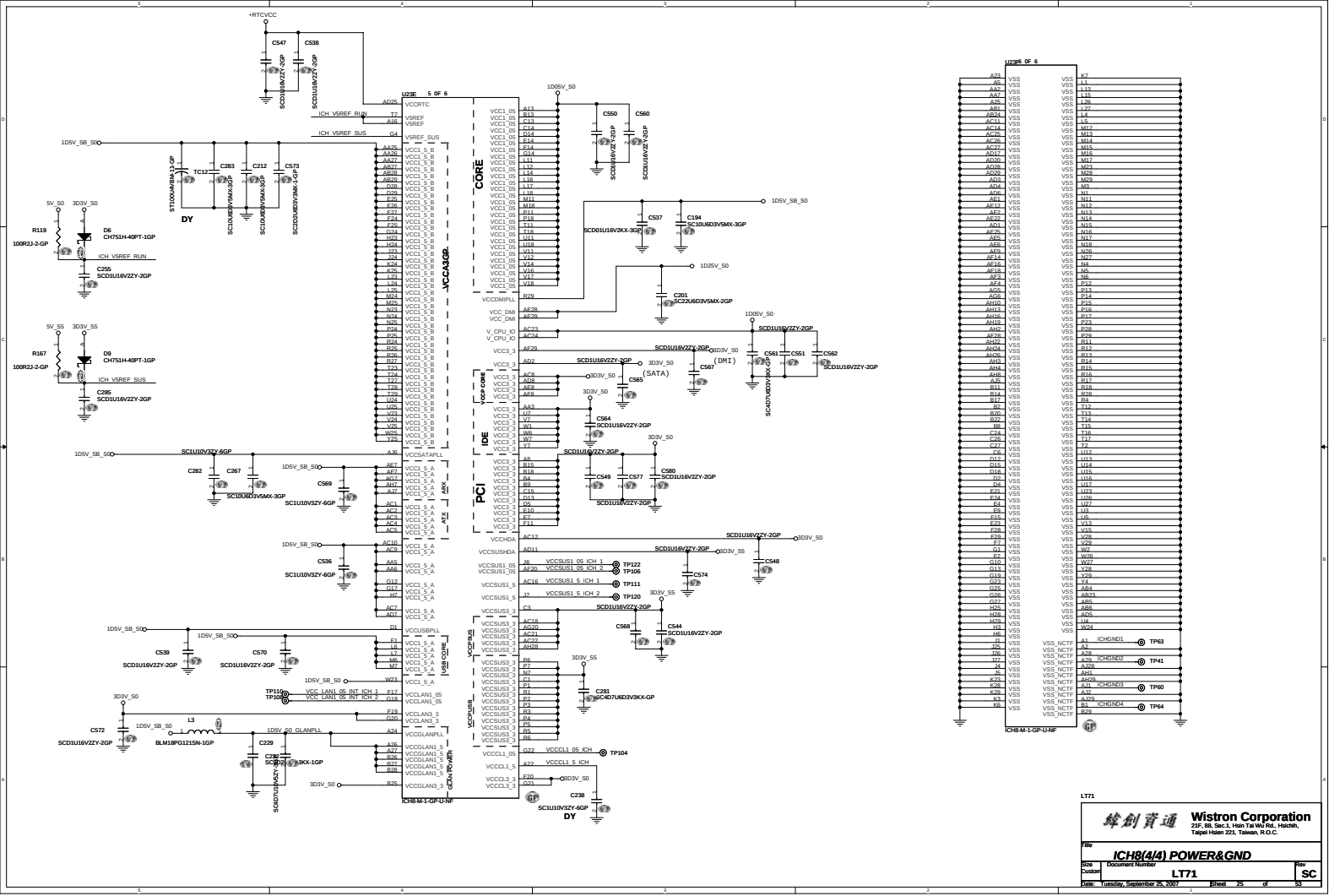
53



鋒創資通	**Wistron Corporation**	
	21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	*ICH8(4/4) POWER&GND*	
Size: Custom	Document Number: LT71	Rev: SC
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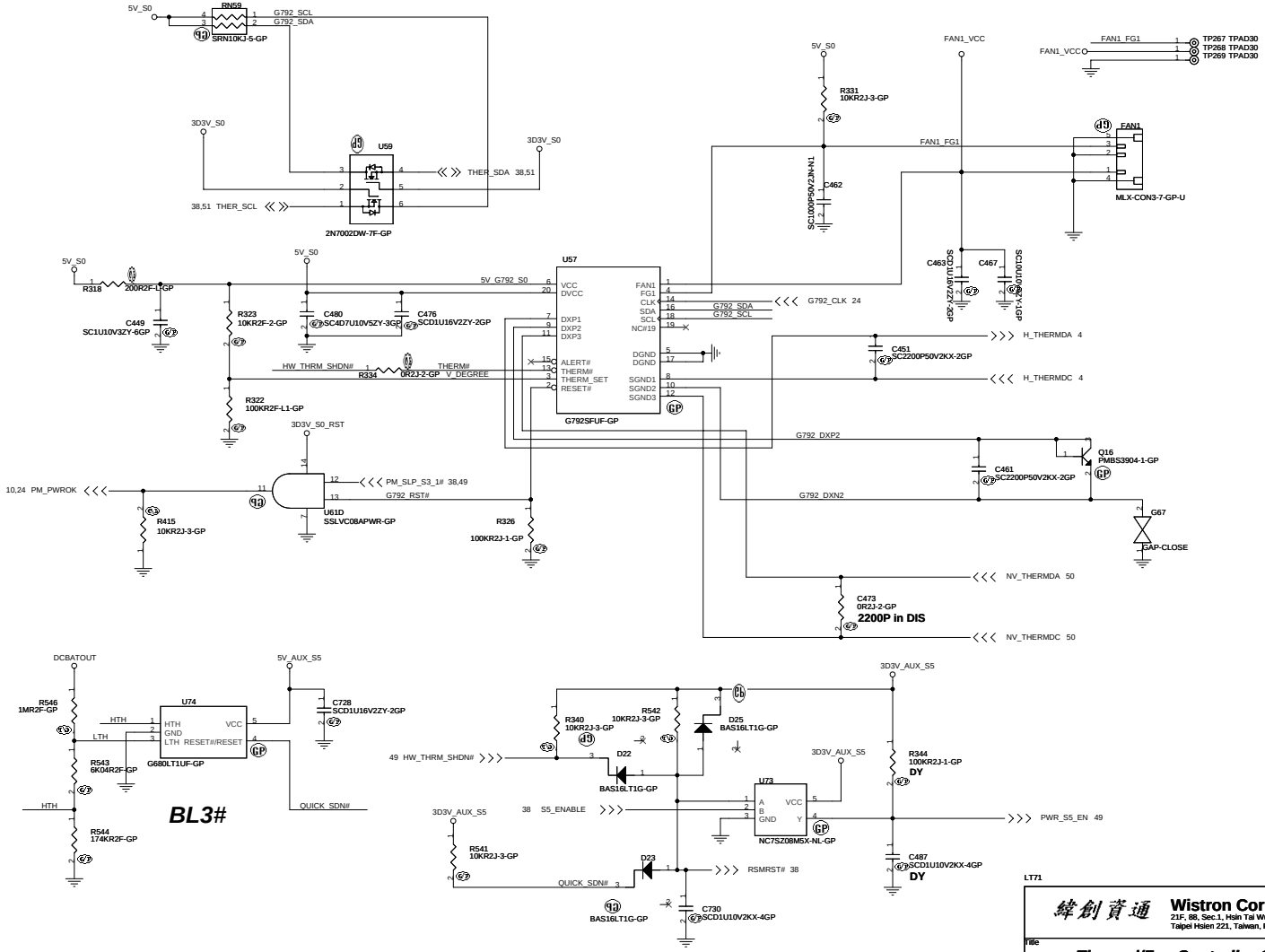

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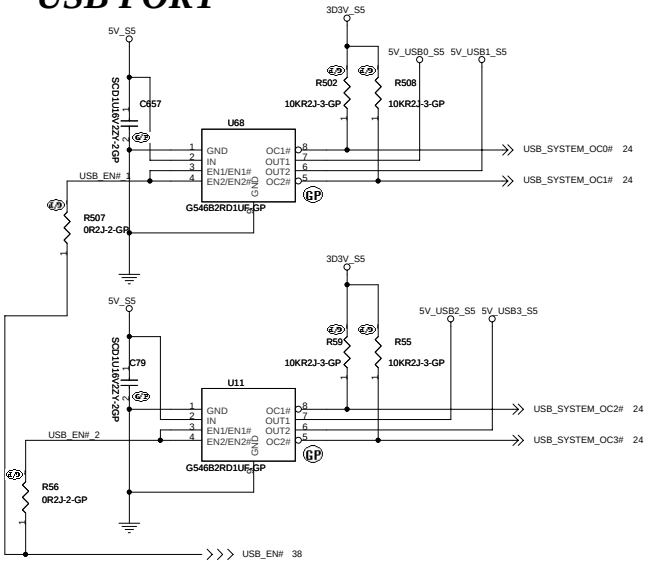
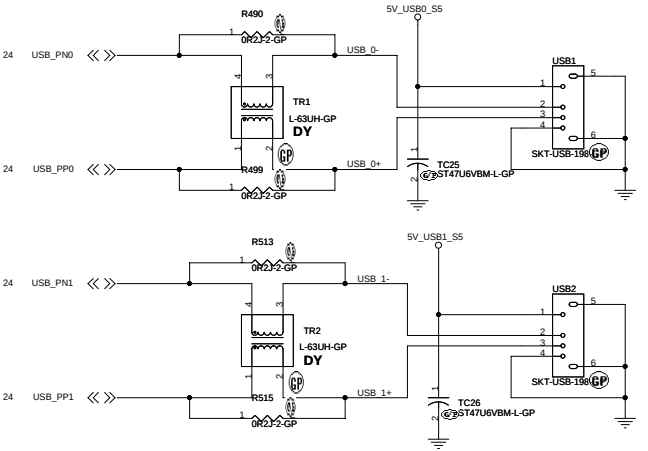
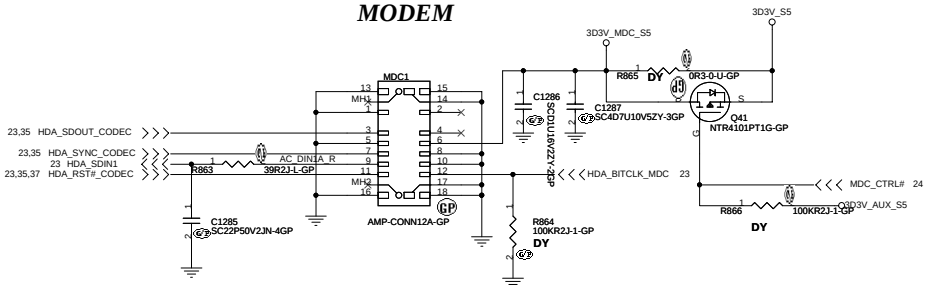
緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***Thermal/Fan Controllor G792***	
Size: Custom	Document Number: LT71	Rev: SC
Date: Tuesday, September 25, 2007	Sheet 26 of 53	

USB PORT





MODEM



LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	**USB I/O**	
Size B	Document Number **LT71**	Rev **SC**
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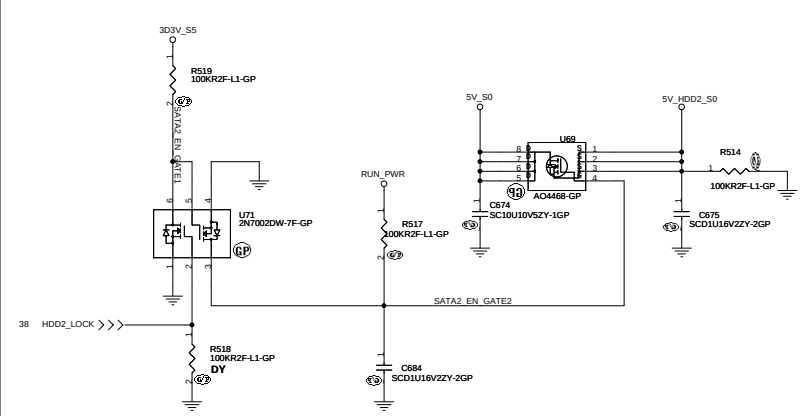
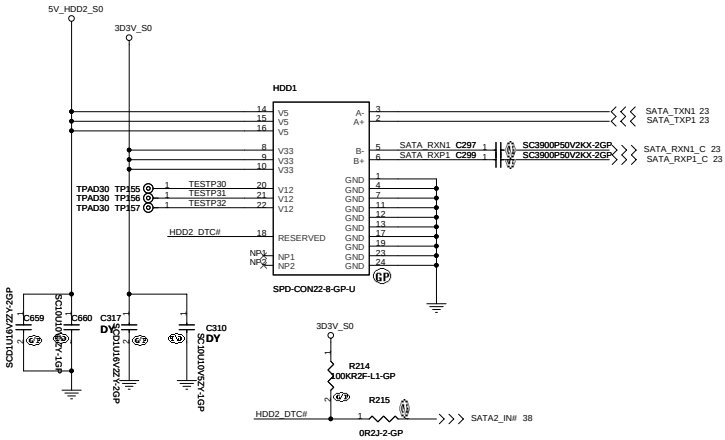
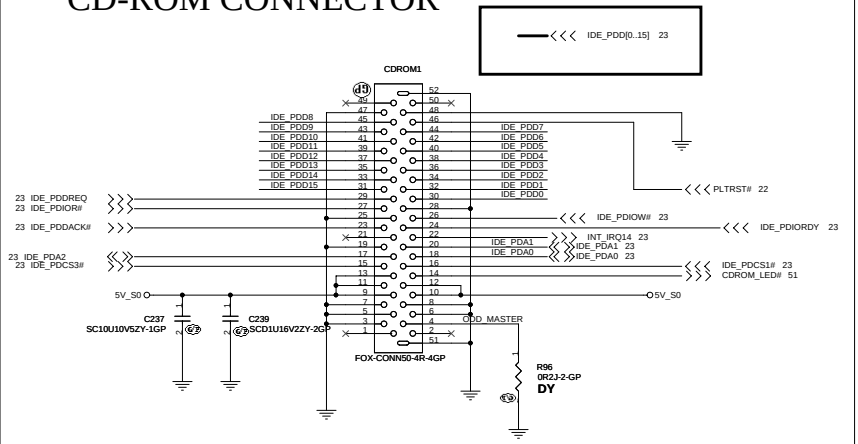
SATA HDD2 Connector

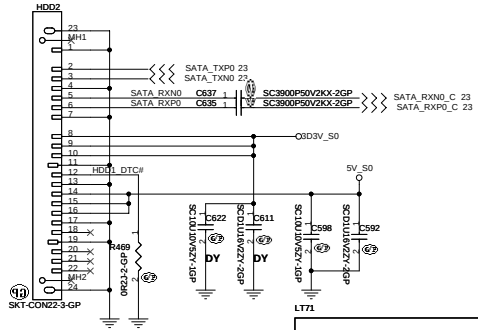




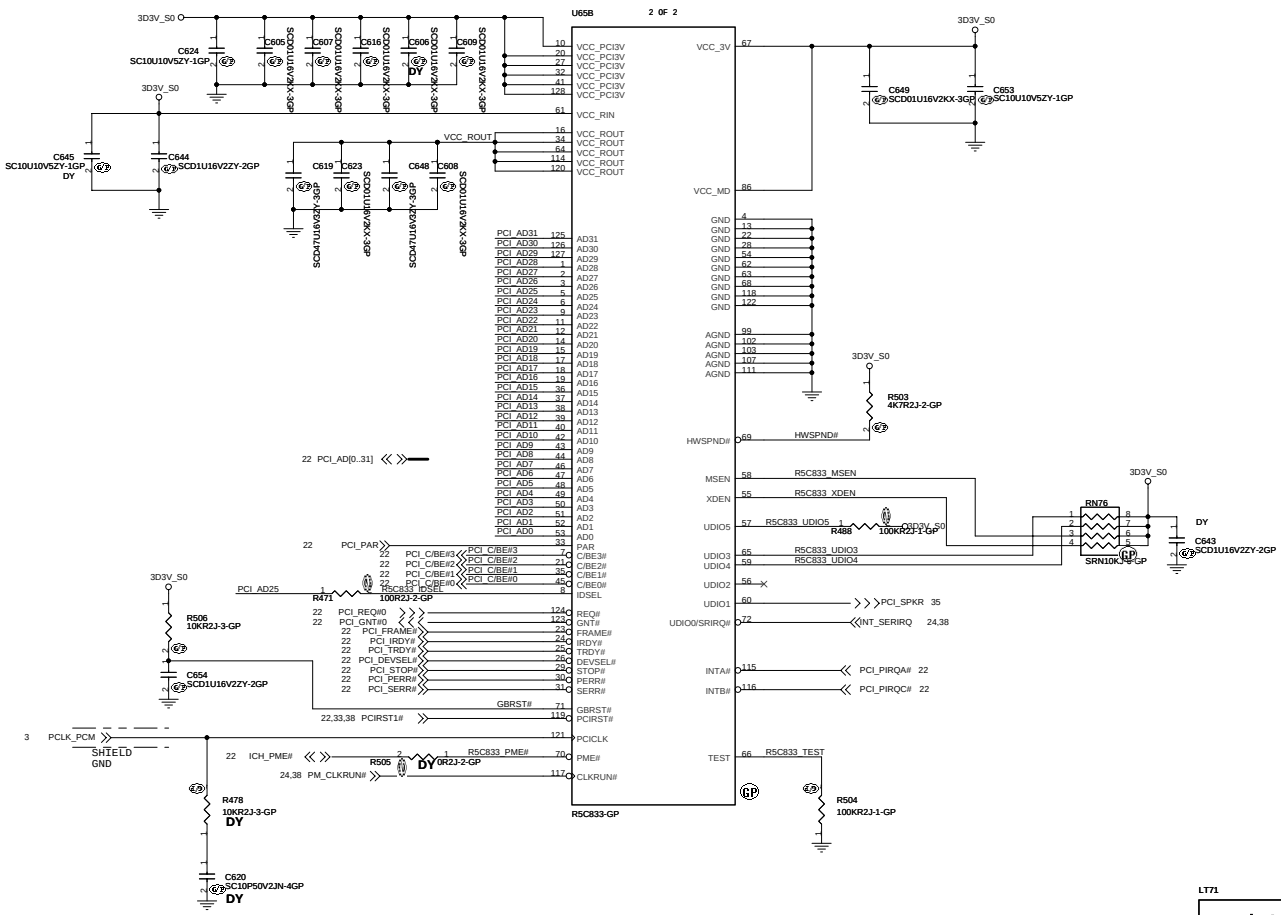
CD-ROM CONNECTOR



SATA HDD1 Connector



緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***HDD/CDROM***	
Size A3	Document Number **LT71**	Rev **SC**
Date: Tuesday, September 25, 2007	Sheet 28 of 53	



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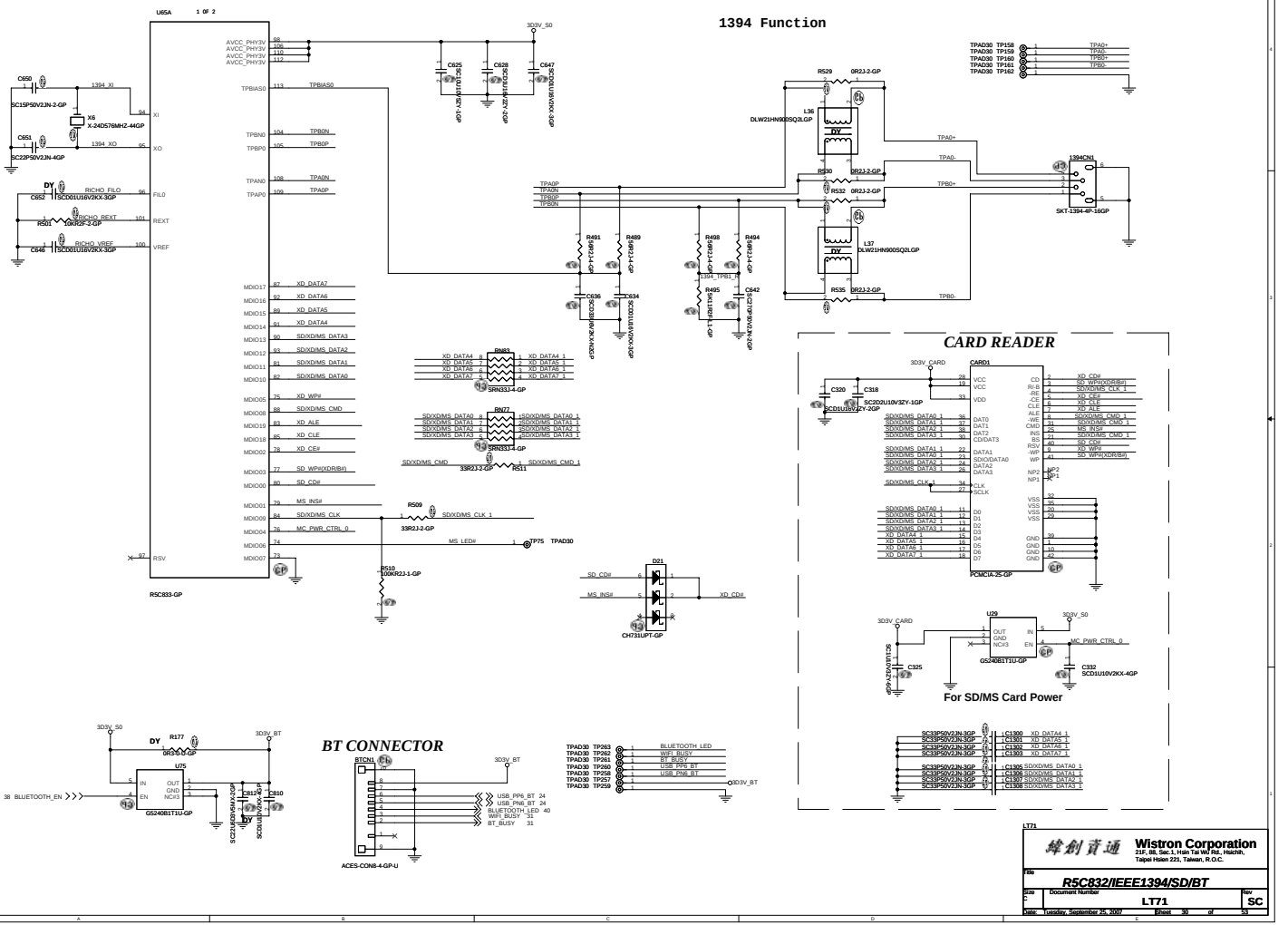
緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***R5C832/PCI***	
Size A3	Document Number **LT71**	Rev **SC**
Date: Tuesday, September 25, 2007	Sheet 29 of 53	


1394 Function
CARD READER
BT CONNECTOR
U65A 1 OF 2
R5C833-GP
DLW21HN900SQ2L-GP
DLW21HN900SQ2L-GP
1394CN1
SKT-1394-4P-16GP
CARD1
PCMCIA-25-GP
U29
GS240BT1U-GP
U75
GS240BT1U-GP
BTCN1
ACES-CON8-4-GP-U
CH731UPT-GP
SRN33J-4-GP
SRN33J-4-GP
33R2J-2-GP
510K/R2J-1-GP
TPAD30 TP158
TPAD30 TP159
TPBD30 TP160
TPBD30 TP161
TPBD30 TP162
TPAD+
TPAD-
TPBD+
TPBD-
TPAD30 TP263
TPAD30 TP262
TPAD30 TP261
TPAD30 TP260
TPAD30 TP258
TPAD30 TP257
TPAD30 TP259
BLUETOOTH_LED
WIFI_BUSY
BT_BUSY
USB_PPB_BT
USB_PNB_BT
BLUETOOTH_LED
WIFI_BUSY
BT_BUSY
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
R5C832/IEEE1394/SD/BT
LT71
Size C
Document Number
LT71
Rev SC
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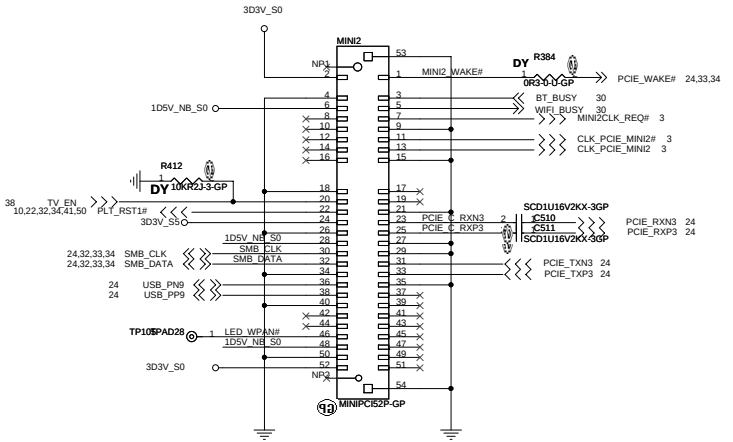
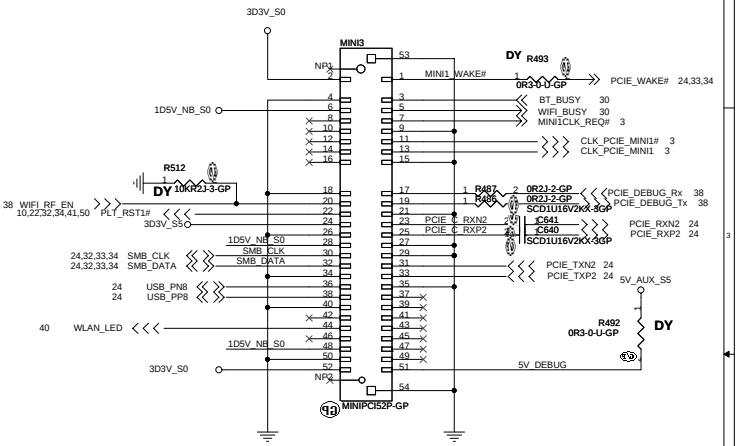
Mini PCI-E Connector

WWAN/TV-TUNER



Wireless Card



LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***MINI CARD CONN(1/2) .***	
Size B	Document Number **LT71**	Rev **SC**
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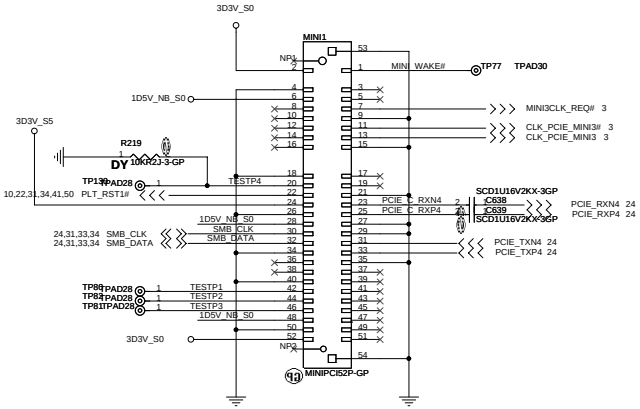
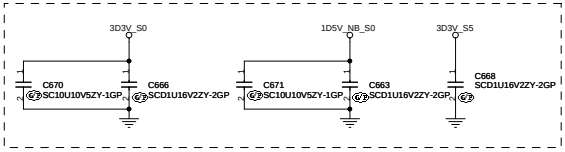
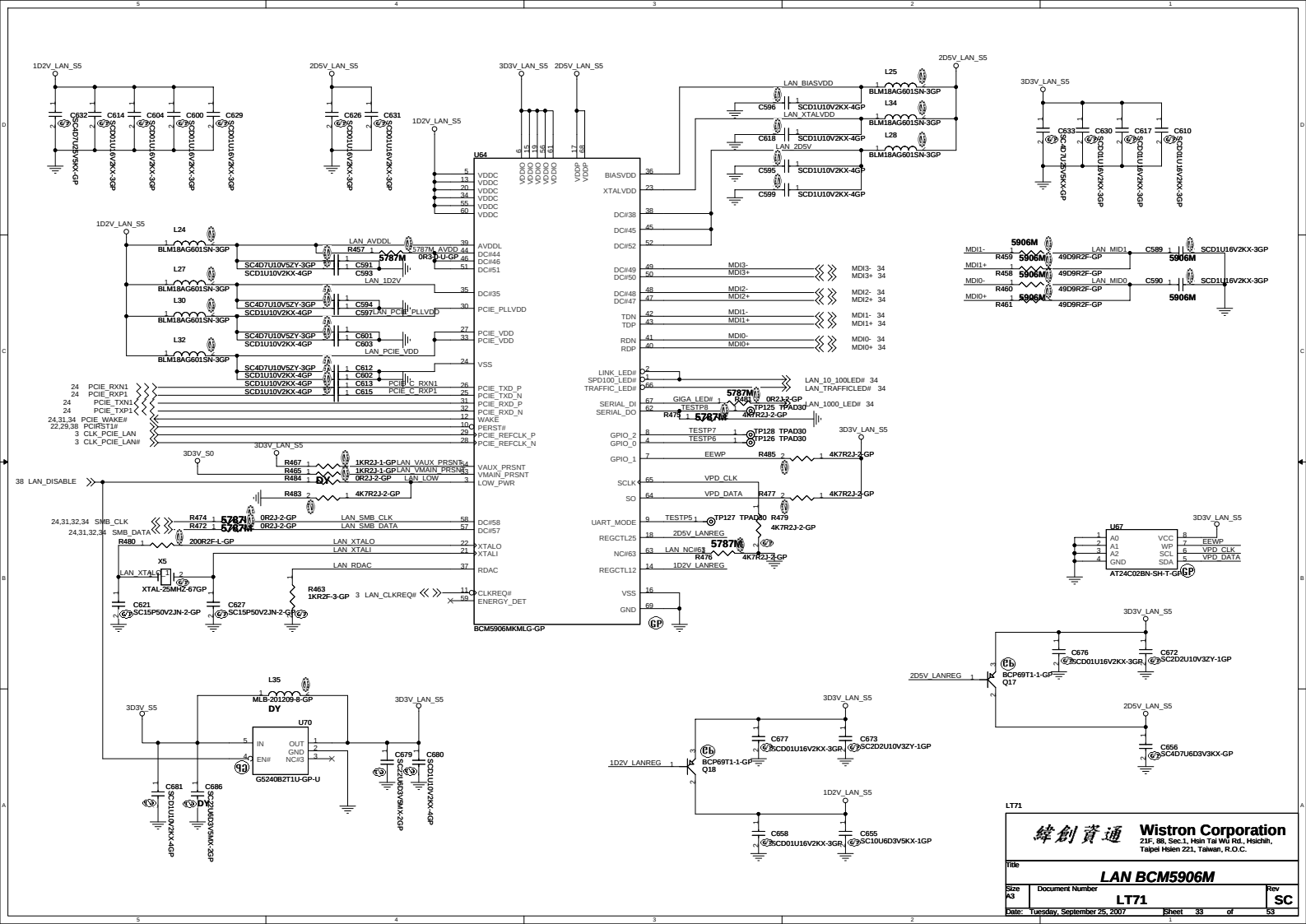
Mini PCI-E Connector



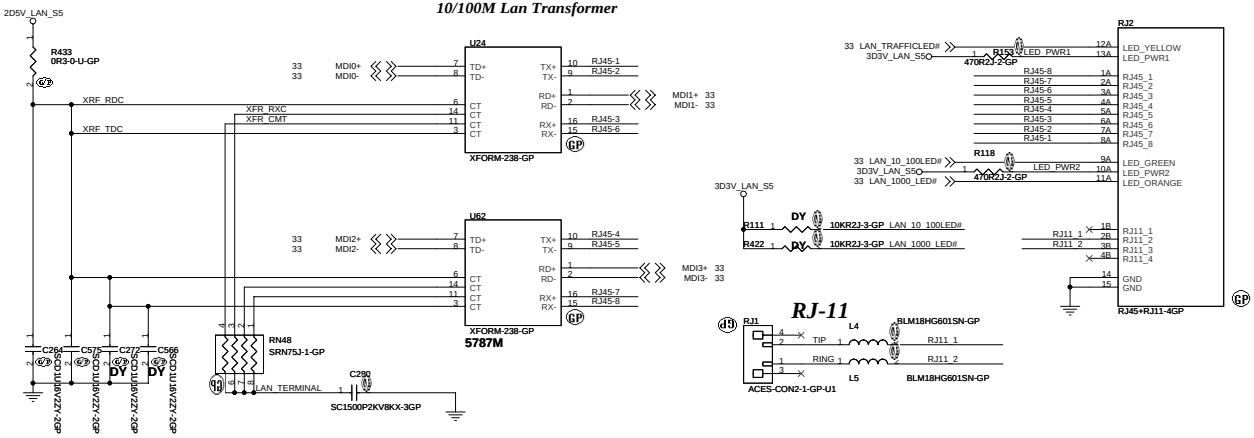


LT71

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: *MINIPCIE Robson*		
Size: A3	Document Number: LT71	Rev: SC
Date: Tuesday, September 25, 2007	Sheet 32 of 53	

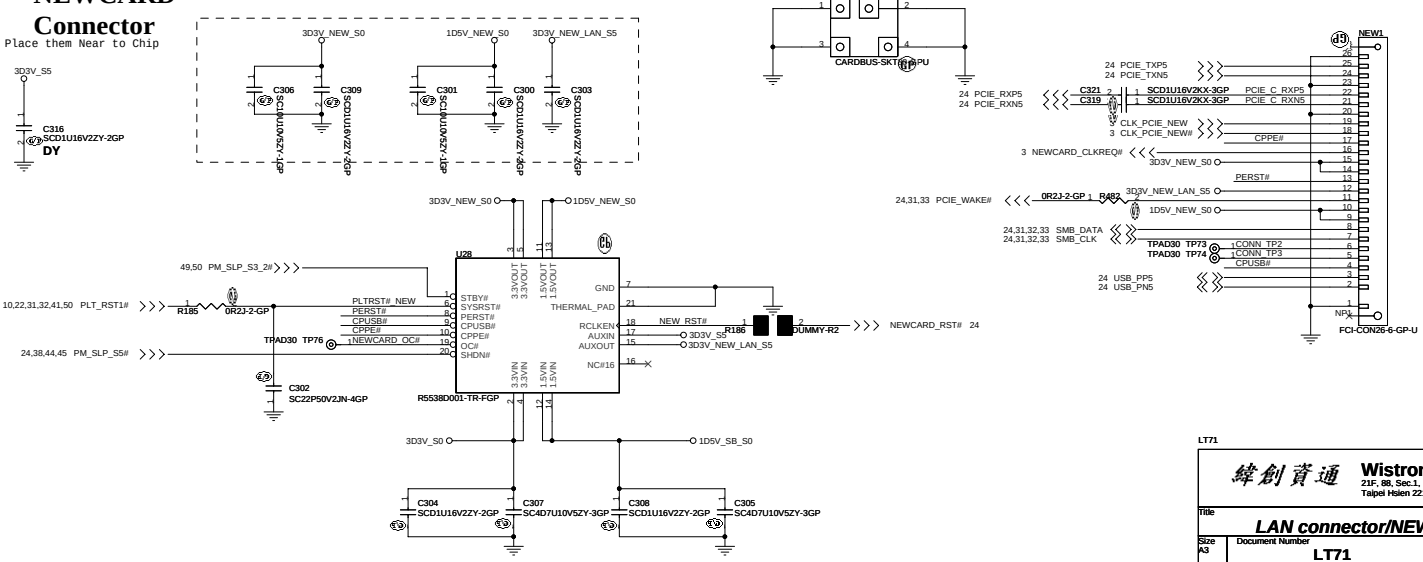

1D2V_LAN_S5
2D5V_LAN_S5
3D3V_LAN_S5
C632 SC4D7U6V5KX-GP
C614 SCD01U16V2KX-3GP
C604 SCD01U16V2KX-3GP
C600 SCD01U16V2KX-3GP
C629 SCD01U16V2KX-3GP
C626 SCD01U16V2KX-3GP
C631 SCD01U16V2KX-3GP
C633 SC4D7U6V5KX-GP
C630 SCD01U16V2KX-3GP
C617 SCD01U16V2KX-3GP
C610 SCD01U16V2KX-3GP
U64
BCM5906MKMLG-GP
VDDC
VDIO
VDDP
BIASVDD
XTALVDD
DC#38
DC#45
DC#52
DC#49
DC#50
DC#48
DC#47
TDN
TDP
RDN
RDP
L25 BLM18AG601SN-3GP
L34 BLM18AG601SN-3GP
L28 BLM18AG601SN-3GP
LAN_BIASVDD
LAN_XTALVDD
LAN_2D5V
C596 SCD1U10V2KX-4GP
C618 SCD1U10V2KX-4GP
C595 SCD1U10V2KX-4GP
C599 SCD1U10V2KX-4GP
L24 BLM18AG601SN-3GP
L27 BLM18AG601SN-3GP
L30 BLM18AG601SN-3GP
L32 BLM18AG601SN-3GP
LAN_AVDDL
LAN_1D2V
LAN_PCIE_PLLVDD
LAN_PCIE_VDD
R457 5787M_AVDD 0R3J-U-GP
SC4D7U10V5ZY-3GP SCD1U10V2KX-4GP
C591 C593 C594 C597 C601 C603
AVDDL
DC#44
DC#46
DC#51
DC#35
PCIE_PLLVDD
PCIE_VDD
VSS
C612 C602 C613 C615
PCIE_C_RXN1
PCIE_C_RXP1
PCIE_TXD_P
PCIE_TXD_N
PCIE_RXD_P
PCIE_RXD_N
WAKE
PERST#
PCIE_REFCLK_P
PCIE_REFCLK_N
24 PCIE_RXN1
24 PCIE_RXP1
24 PCIE_TXN1
24 PCIE_TXP1
24,31,34 PCIE_WAKE#
22,29,38 PCIRST1#
3 CLK_PCIE_LAN
3 CLK_PCIE_LAN#
MDI3- MDI3+ MDI2- MDI2+ MDI1- MDI1+ MDI0- MDI0+
MDI3- 34 MDI3+ 34 MDI2- 34 MDI2+ 34 MDI1- 34 MDI1+ 34 MDI0- 34 MDI0+ 34
LINK_LED#
SPD100_LED#
TRAFFIC_LED#
SERIAL_DI
SERIAL_DO
GPIO_2
GPIO_0
GPIO_1
SCLK
SO
UART_MODE
REGCTL25
NC#63
REGCTL12
LAN_10_100LED# 34
LAN_TRAFFICLED# 34
LAN_1000_LED# 34
TESTP8
TESTP7
TESTP6
EEWP
VPD_CLK
VPD_DATA
TESTP5
2D5V_LANREG
1D2V_LANREG
LAN_NC#63
R0R2J-2-GP
4K7R2J-2-GP
TP125 TPAD30
TP128 TPAD30
TP126 TPAD30
TP127 TPAD30
R485 R479 R477 R476
5787M
3D3V_S0
3D3V_LAN_S5
R467 1KR2J-1-GP LAN_VAUX_PRSNT
R465 1KR2J-1-GP LAN_VMAIN_PRSNT
R484 0R2J-2-GP LAN_LOW
VAUX_PRSNT
VMAIN_PRSNT
LOW_PWR
38 LAN_DISABLE
R483 4K7R2J-2-GP
24,31,32,34 SMB_CLK
24,31,32,34 SMB_DATA
R474 R472 0R2J-2-GP
LAN_SMB_CLK
LAN_SMB_DATA
DC#58
DC#57
R480 200R2F-L-GP
LAN_XTALO
LAN_XTALI
XTALO
XTALI
X5
XTAL-25MHZ-67GP
C621 SC15P50V2JN-2-GP
C627 SC15P50V2JN-2-GP
LAN_RDAC
RDAC
R463 1KR2F-3-GP
3 LAN_CLKREQ#
CLKREQ#
ENERGY_DET
L35 MLB-201209-8-GP
DY
3D3V_S5
U70
G5240B2T1U-GP-U
IN
OUT
GND
EN#
NC#3
C679 SC22U6D3V5MX-2GP
C680 SCD1U10V2KX-4GP
C681 SCD1U10V2KX-4GP
C686 SC22U6D3V5MX-3GP
5906M
MDI1- MDI1+ MDI0- MDI0+
R459 R458 R460 R461 49D9R2F-GP
LAN_MID1
LAN_MID0
C589 C590 SCD1U16V2KX-3GP
U67
AT24C02BN-SH-T-GP
A0 A1 A2 GND VCC WP SCL SDA
EEWP VPD_CLK VPD_DATA
C676 SCD01U16V2KX-3GR
C672 SC2D2U10V3ZY-1GP
BCP69T1-1-GP Q17
C677 SCD01U16V2KX-3GR
C673 SC2D2U10V3ZY-1GP
BCP69T1-1-GP Q18
C658 SCD01U16V2KX-3GR
C655 SC10U6D3V5KX-1GP
C656 SC4D7U6D3V3KX-GP
LT71
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
LAN BCM5906M
Size A3
Document Number LT71
Rev SC
Date: Tuesday, September 25, 2007
Sheet 33 of 53

10/100M Lan Transformer



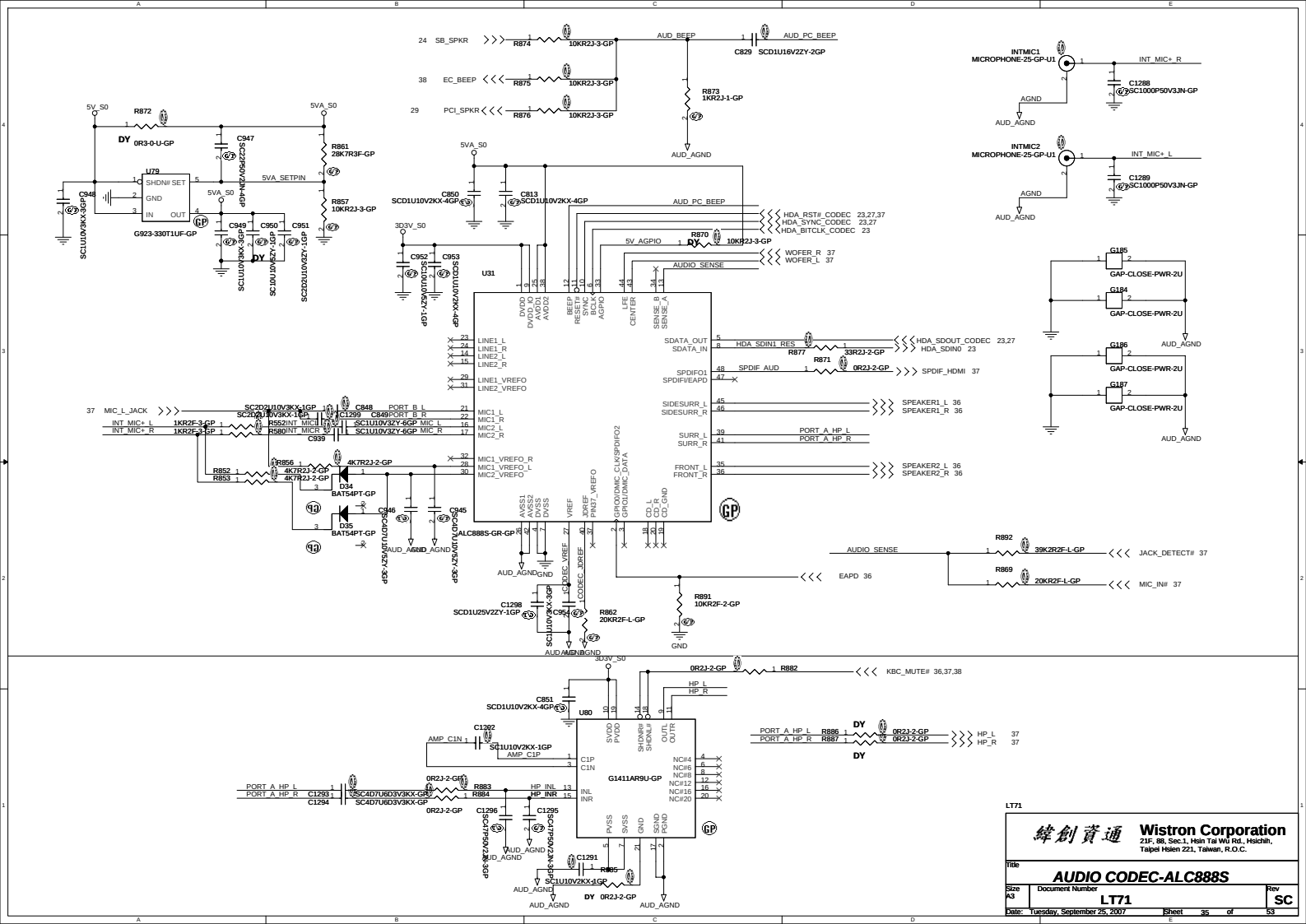
NEWCARD Connector

Place them Near to Chip



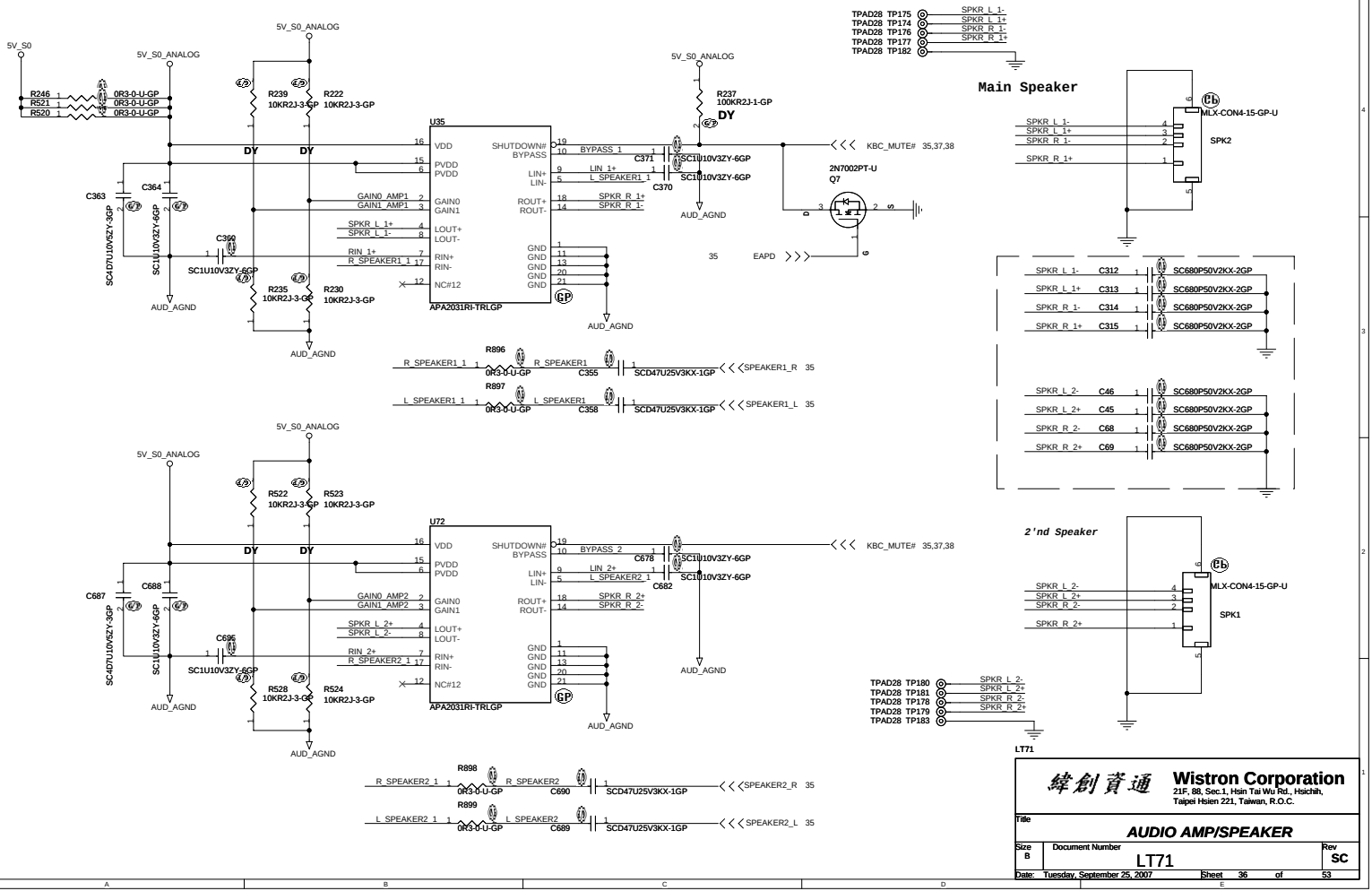
LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	**LAN connector/NEW CARD/SIM**	
Size A3	Document Number **LT71**	Rev **SC**
Date: Tuesday, September 25, 2007	Sheet 34 of 53	

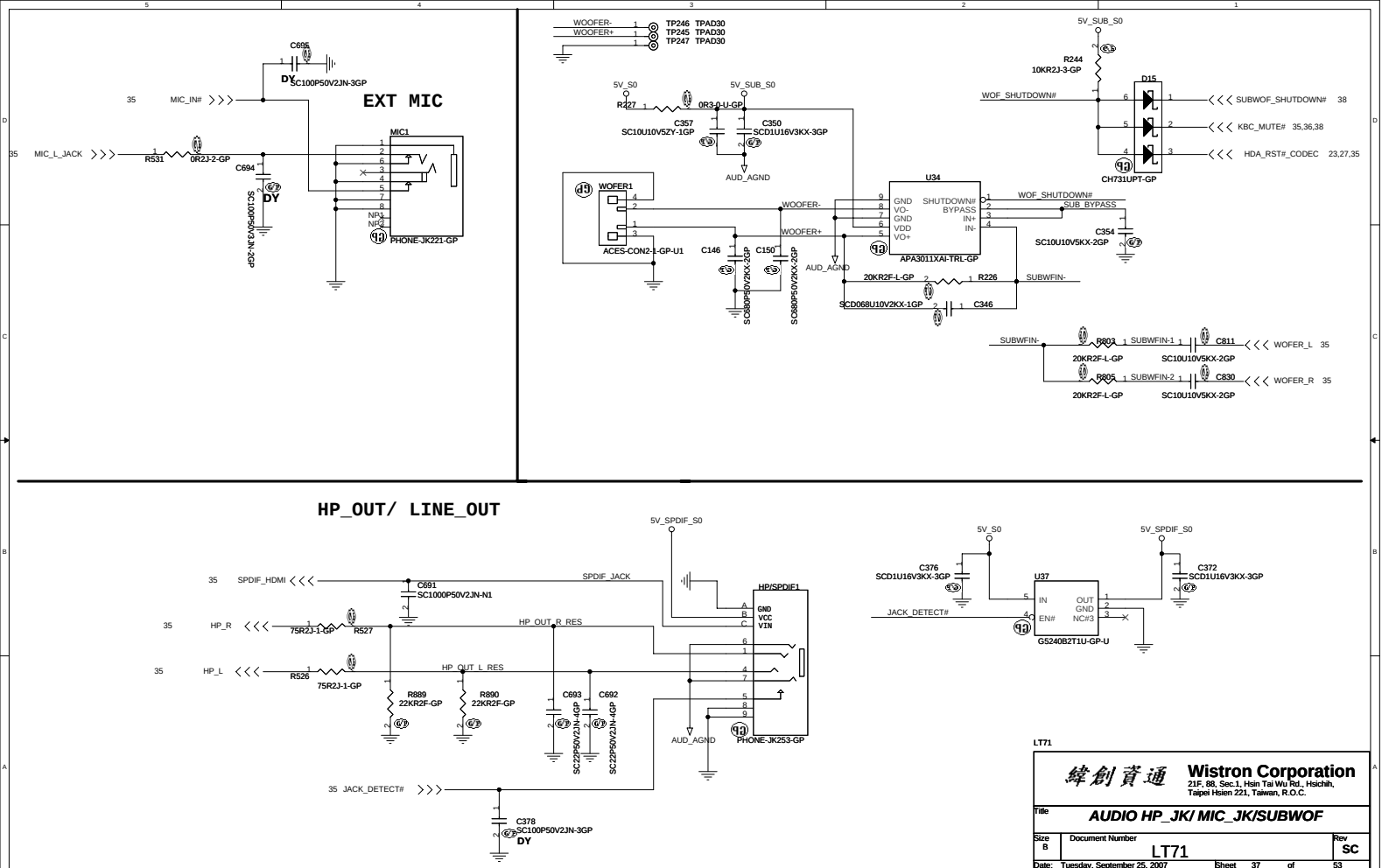


LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	**AUDIO CODEC-ALC888S**	
Size: A3	Document Number: LT71	Rev: SC
Date: Tuesday, September 25, 2007	Sheet 35 of 53	



緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***AUDIO AMP/SPEAKER***	
Size B	Document Number LT71	Rev SC
Date: Tuesday, September 25, 2007	Sheet 36 of 53	



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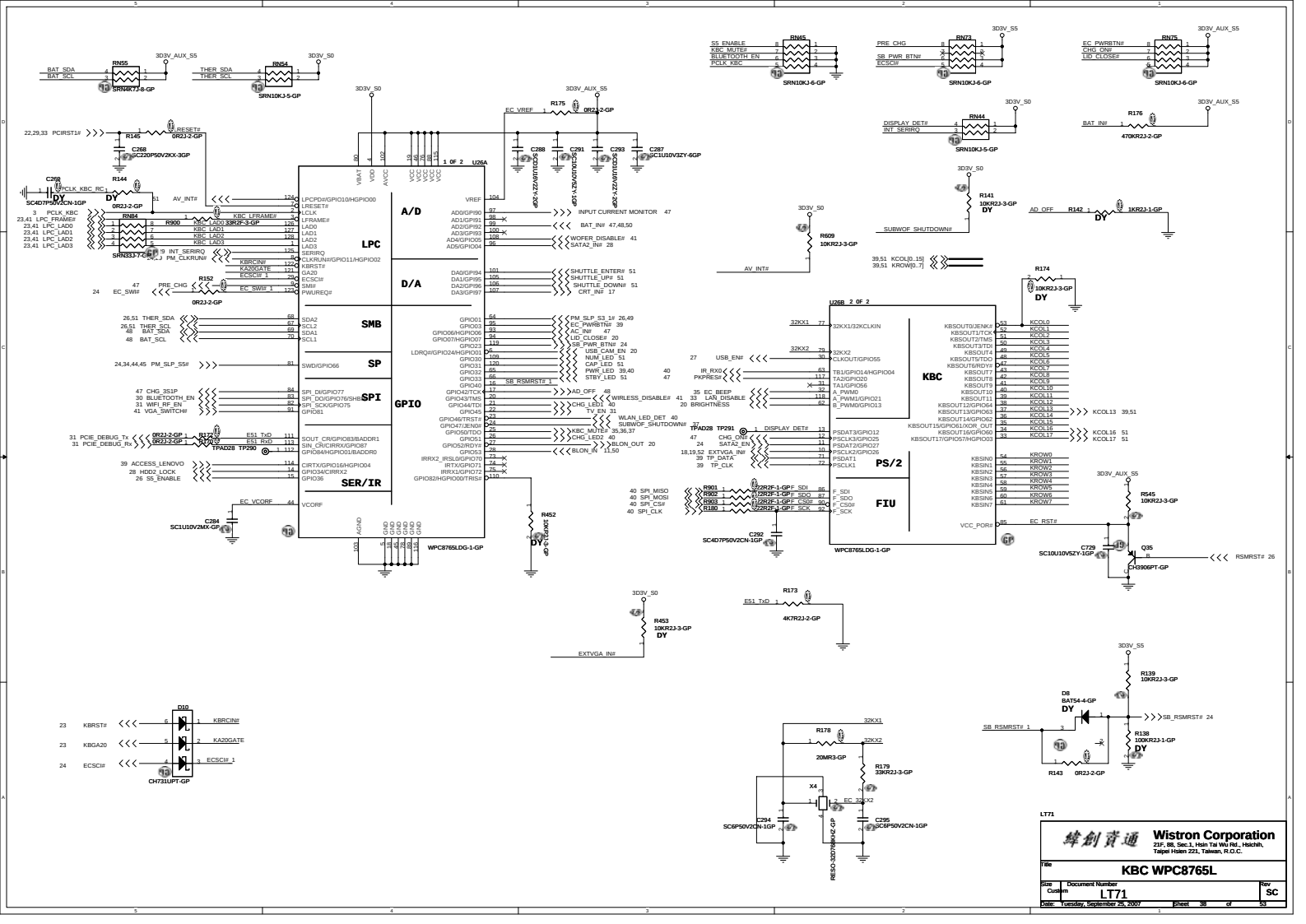
緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***AUDIO HP_JK/ MIC_JK/SUBWOF***	
Size B	Document Number LT71	Rev SC
Date: Tuesday, September 25, 2007	Sheet 37 of 53	


LPC
A/D
D/A
SMB
SP
SPI
GPIO
SER/IR
PS/2
FIU
KBC
WPC8765LDG-1-GP
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
KBC WPC8765L
LT71
Size: Custom
Document Number: LT71
Rev: SC
Date: Tuesday, September 25, 2007
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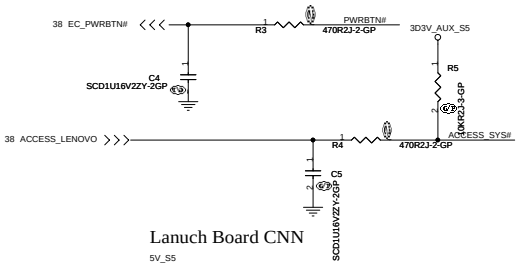
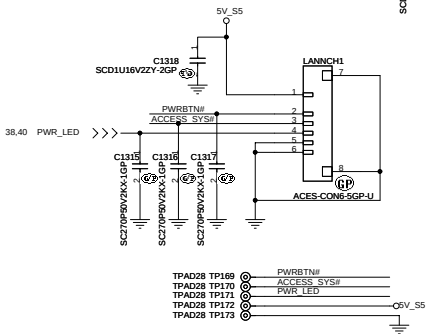
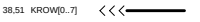
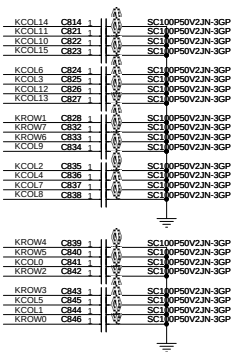


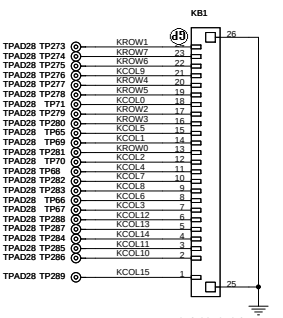
Lanuch Board CNN



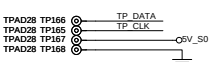
Internal KeyBoard Connector

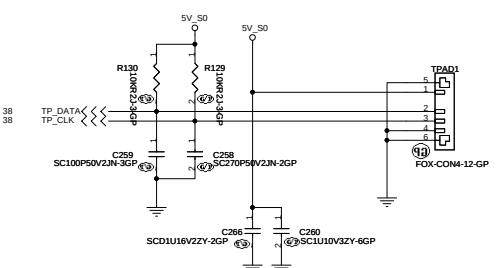






TouchPad Connector





LT71

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***Keyboard /Touch Pad***	
Size: A3	Document Number: **LT71**	Rev: **SC**
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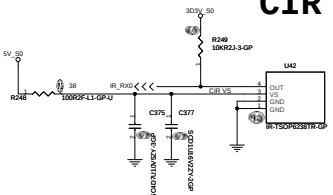
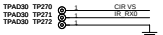
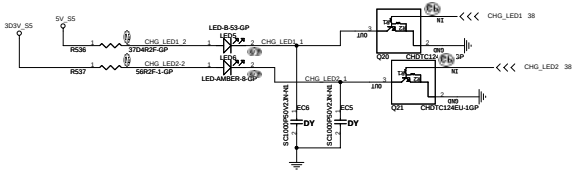
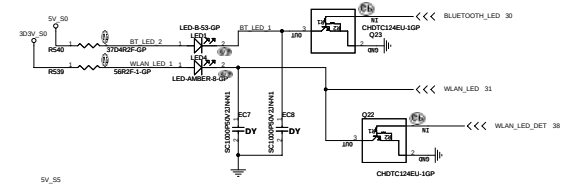
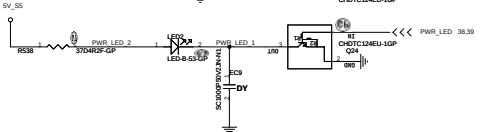
CIR



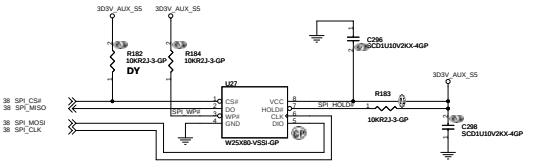








SPI ROM for System & KBC



LT71

緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	G-sensor / SPI / LEDs	
Size: C	Document Number: LT71	Rev: SC
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GOLDEN FINGER FOR DEBUG BOARD

23,38 LPC_LAD[0..3]

TPAD28 TP57 — EXT_FWH#
TPAD28 TP62 — LPC_LAD0
TPAD28 TP61 — LPC_LAD1
TPAD28 TP59 — LPC_LAD2
TPAD28 TP56 — LPC_LAD3

TPAD28 TP58 — PCLK_FWH >>> PCLK_FWH 3

TPAD28 TP55 — LPC_FRAME# >>> LPC_FRAME# 23,38
TPAD28 TP129 — PLT_RST1# >>> PLT_RST1# 10,22,31,32,34,50

3D3V_S5
R149 1 2 EXT_FWH#
8K2R2J-3-GP

WIRELESS SWITCH

3D3V_S0
R534 100KR2J-1-GP
SW2
SW-SLIDE64-GP
NP1 NP2
WIRLESS_DISABLE# >>> WIRLESS_DISABLE# 38
C697 SCD1U16V2ZY-2GP
DY

TPAD30 TP249 — WIRLESS_DISABLE#
TPAD30 TP248

WOFER SWITCH

3D3V_S0
R533 100KR2J-1-GP
SW3
SW-SLIDE64-GP
NP1 NP2
WOFER_DISABLE# >>> WOFER_DISABLE# 38
C696 SCD1U16V2ZY-2GP
DY

TPAD30 TP250 — WOFER_DISABLE#

VGA IN SWITCH

3D3V_S5
R395 10KR2J-3-GP
SW1
SW-SLIDE59-GP
VGA_SWITCH# >>> VGA_SWITCH# 38
C527 SCD22U10V2KX-1GP

TPAD30 TP253 — VGA_SWITCH#
TPAD30 TP252

LT71

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***FWH and Debug***

Size	Document Number	Rev
B	LT71	SC

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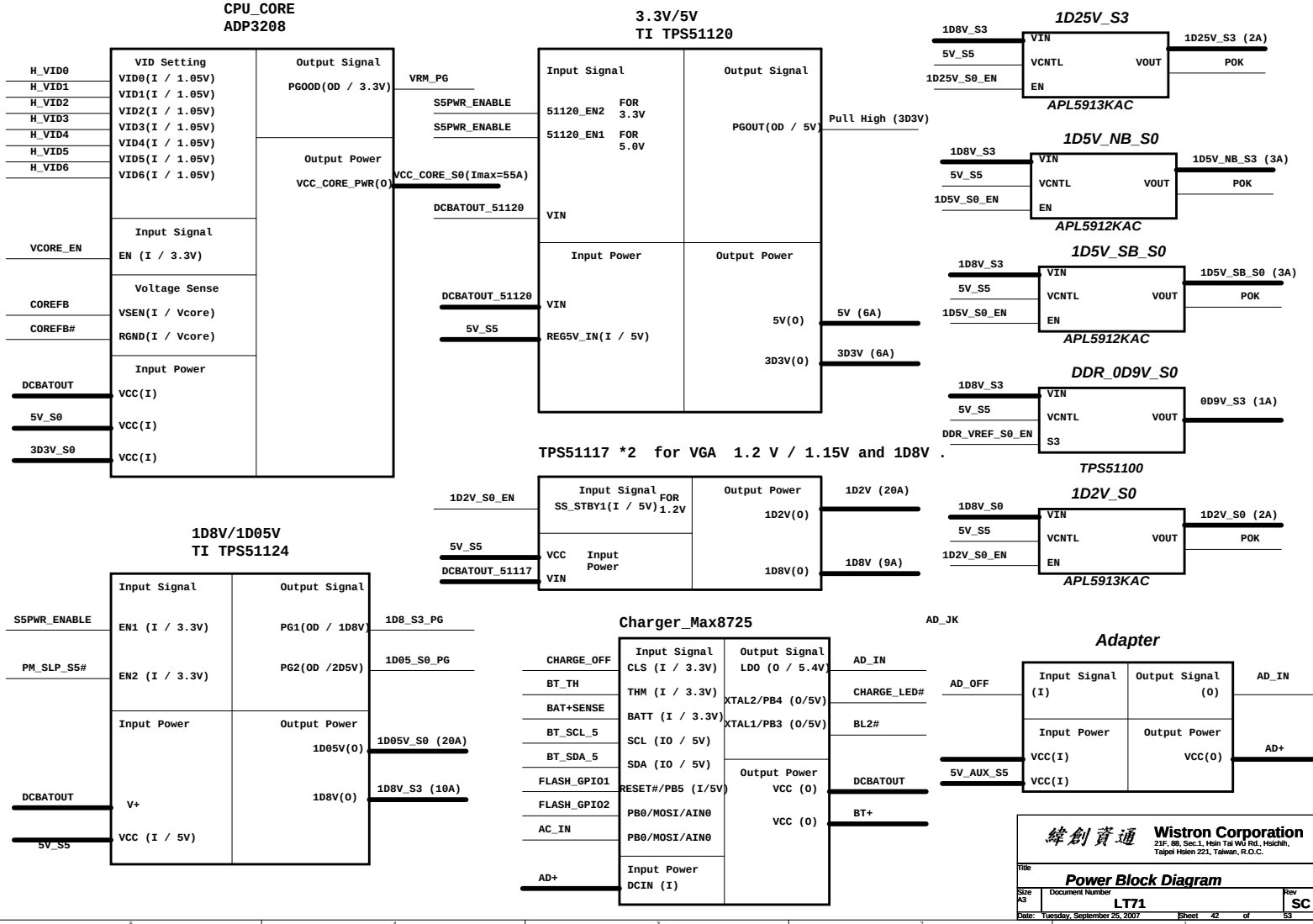
CPU_CORE
ADP3208
VID Setting
VID0(I / 1.05V)
VID1(I / 1.05V)
VID2(I / 1.05V)
VID3(I / 1.05V)
VID4(I / 1.05V)
VID5(I / 1.05V)
VID6(I / 1.05V)
Output Signal
PGOOD(OD / 3.3V)
Output Power
VCC_CORE_PWR(O)
Input Signal
EN (I / 3.3V)
Voltage Sense
VSEN(I / Vcore)
RGND(I / Vcore)
Input Power
VCC(I)
VCC(I)
VCC(I)
H_VID0
H_VID1
H_VID2
H_VID3
H_VID4
H_VID5
H_VID6
VCORE_EN
COREFB
COREFB#
DCBATOUT
5V_S0
3D3V_S0
VRM_PG
S5PWR_ENABLE
S5PWR_ENABLE
VCC_CORE_S0(Imax=55A)
DCBATOUT_51120
3.3V/5V
TI TPS51120
Input Signal
51120_EN2 FOR 3.3V
51120_EN1 FOR 5.0V
VIN
Output Signal
PGOUT(OD / 5V)
Input Power
VIN
REG5V_IN(I / 5V)
Output Power
5V(O)
3D3V(O)
Pull High (3D3V)
DCBATOUT_51120
5V_S5
5V (6A)
3D3V (6A)
1D25V_S3
1D8V_S3
5V_S5
1D25V_S0_EN
VIN
VCNTL
VOUT
EN
1D25V_S3 (2A)
POK
APL5913KAC
1D5V_NB_S0
1D8V_S3
5V_S5
1D5V_S0_EN
VIN
VCNTL
VOUT
EN
1D5V_NB_S3 (3A)
POK
APL5912KAC
1D5V_SB_S0
1D8V_S3
5V_S5
1D5V_S0_EN
VIN
VCNTL
VOUT
EN
1D5V_SB_S0 (3A)
POK
APL5912KAC
DDR_0D9V_S0
1D8V_S3
5V_S5
DDR_VREF_S0_EN
VIN
VCNTL
VOUT
S3
0D9V_S3 (1A)
TPS51100
TPS51117 *2 for VGA 1.2 V / 1.15V and 1D8V .
1D2V_S0_EN
5V_S5
DCBATOUT_51117
Input Signal
SS_STBY1(I / 5V)
FOR 1.2V
VCC Input Power
VIN
Output Power
1D2V(O)
1D8V(O)
1D2V (20A)
1D8V (9A)
1D2V_S0
1D8V_S0
5V_S5
1D2V_S0_EN
VIN
VCNTL
VOUT
EN
1D2V_S0 (2A)
POK
APL5913KAC
1D8V/1D05V
TI TPS51124
Input Signal
EN1 (I / 3.3V)
EN2 (I / 3.3V)
Output Signal
PG1(OD / 1D8V)
PG2(OD /2D5V)
Input Power
V+
VCC (I / 5V)
Output Power
1D05V(O)
1D8V(O)
S5PWR_ENABLE
PM_SLP_S5#
DCBATOUT
5V_S5
1D8_S3_PG
1D05_S0_PG
1D05V_S0 (20A)
1D8V_S3 (10A)
Charger_Max8725
CHARGE_OFF
BT_TH
BAT+SENSE
BT_SCL_5
BT_SDA_5
FLASH_GPIO1
FLASH_GPIO2
AC_IN
AD+
Input Signal
CLS (I / 3.3V)
THM (I / 3.3V)
BATT (I / 3.3V)
SCL (IO / 5V)
SDA (IO / 5V)
RESET#/PB5 (I/5V)
PB0/MOSI/AIN0
PB0/MOSI/AIN0
Input Power
DCIN (I)
Output Signal
LDO (O / 5.4V)
XTAL2/PB4 (O/5V)
XTAL1/PB3 (O/5V)
Output Power
VCC (O)
VCC (O)
AD_IN
CHARGE_LED#
BL2#
DCBATOUT
BT+
AD_JK
Adapter
AD_OFF
5V_AUX_S5
Input Signal (I)
Output Signal (O)
Input Power
VCC(I)
VCC(I)
Output Power
VCC(O)
AD_IN
AD+
緯創資通
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title
Power Block Diagram
Size A3
Document Number
LT71
Rev SC
Date: Tuesday, September 25, 2007
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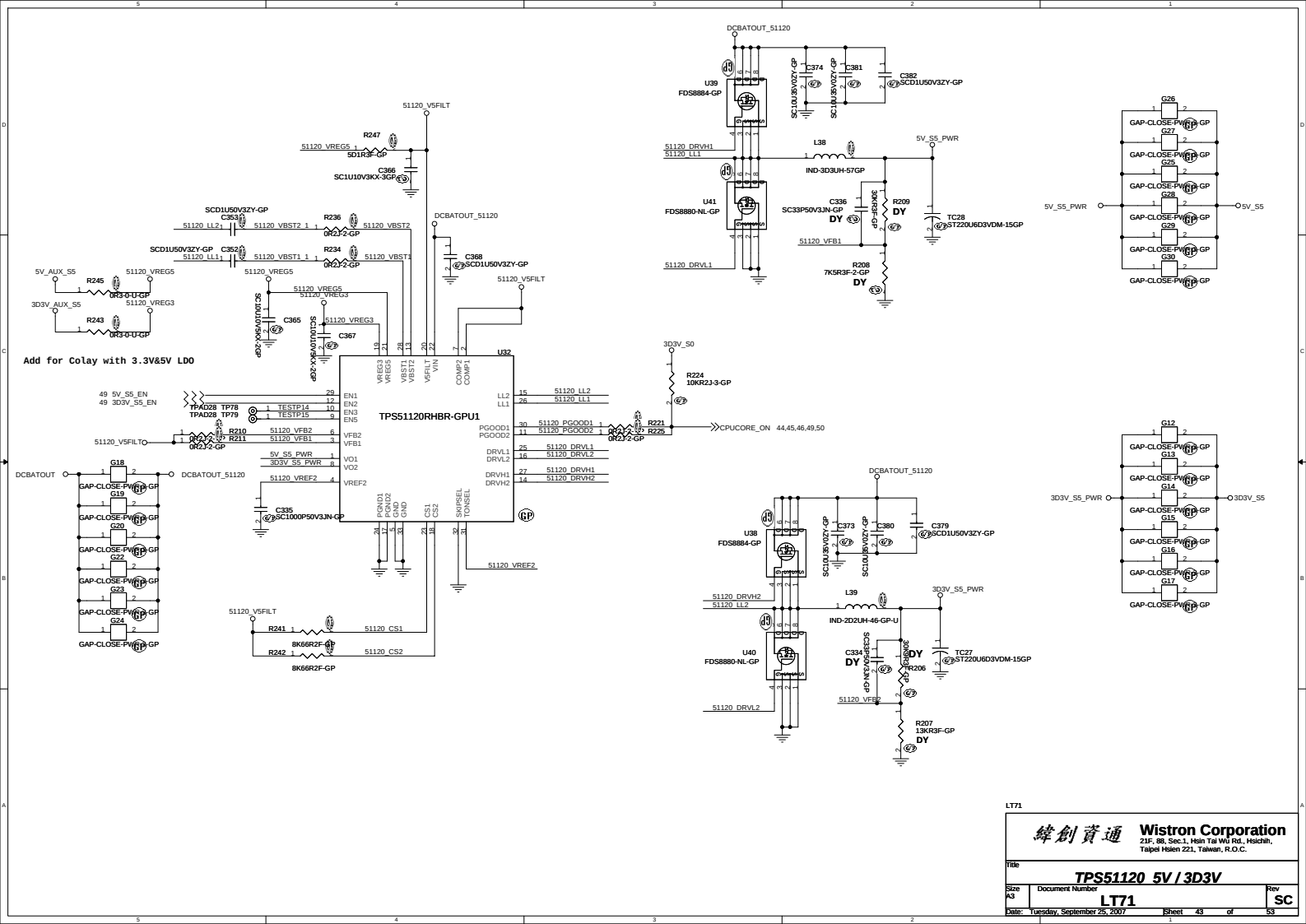

LT71

偉創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***TPS51120 5V / 3D3V***	
Size A3	Document Number **LT71**	Rev **SC**
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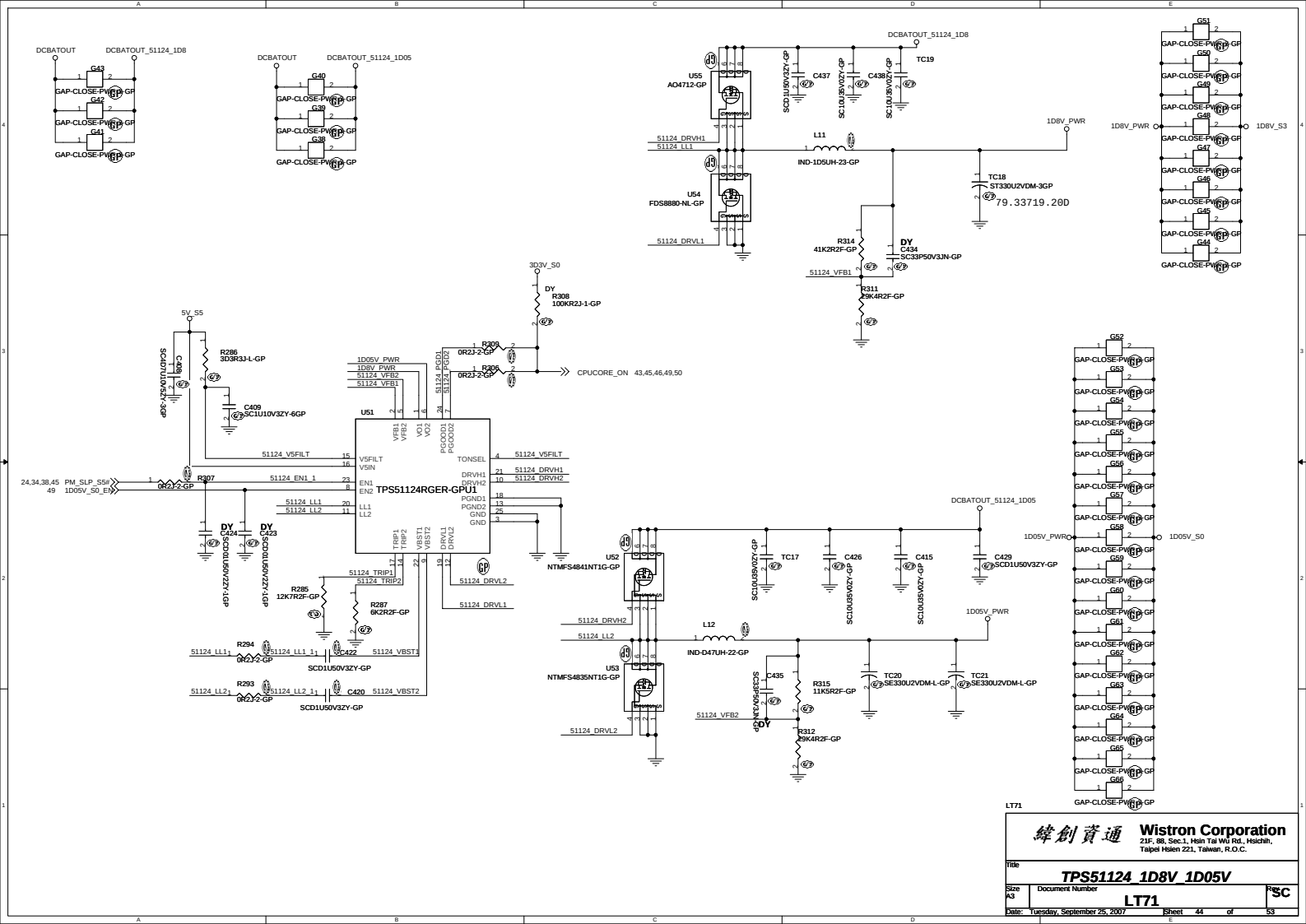
DCBATOUT
DCBATOUT_51124_1D8
G43
GAP-CLOSE-PWR-GP
G42
GAP-CLOSE-PWR-GP
G41
GAP-CLOSE-PWR-GP
DCBATOUT
DCBATOUT_51124_1D05
G40
GAP-CLOSE-PWR-GP
G39
GAP-CLOSE-PWR-GP
G38
GAP-CLOSE-PWR-GP
DCBATOUT_51124_1D8
U55
AO4712-GP
SCD1U50V3ZY-GP
C437
SC10U35V6ZY-GP
C438
SC22U25V6ZY-GP
TC19
1D8V_PWR
51124_DRVH1
51124_LL1
L11
IND-1D5UH-23-GP
U54
FDS8880-NL-GP
51124_DRVL1
TC18
ST330U2VDM-3GP
79.33719.20D
R314
41K2R2F-GP
DY
C434
SC33P50V3JN-GP
51124_VFB1
R311
29K4R2F-GP
1D8V_PWR
1D8V_S3
G51
G50
G49
G48
G47
G46
G45
G44
GAP-CLOSE-PWR-GP
3D3V_S0
DY
R308
100KR2J-1-GP
R309
0R2J-2-GP
R305
0R2J-2-GP
CPUCORE_ON
43,45,46,49,50
5V_S5
SCD47U10V3ZY-3GP
C408
R286
3D3R3J-L-GP
C409
SC1U10V3ZY-6GP
1D05V_PWR
1D8V_PWR
51124_VFB2
51124_VFB1
51124_PGOD1
51124_PGOD2
U51
TPS51124RGER-GPU1
VFB1
VFB2
VO1
VO2
PGOOD1
PGOOD2
51124_V5FILT
V5FILT
V5IN
TONSEL
51124_V5FILT
DRVH1
DRVH2
51124_DRVH1
51124_DRVH2
24,34,38,45,49
PM_SLP_S5#
1D05V_S0_EN
R307
0R2J-2-GP
51124_EN1_1
EN1
EN2
PGND1
PGND2
GND
GND
51124_LL1
51124_LL2
LL1
LL2
DY
C424
SCD01U50V2ZY-1GP
DY
C423
SCD01U50V2ZY-1GP
TRIP1
TRIP2
VBST1
VBST2
DRVL1
DRVL2
51124_TRIP1
51124_TRIP2
R285
12K7R2F-GP
R287
6K2R2F-GP
51124_DRVL2
51124_DRVL1
R294
0R2J-2-GP
51124_LL1_1
C422
51124_VBST1
SCD1U50V3ZY-GP
R293
0R2J-2-GP
51124_LL2_1
C420
51124_VBST2
SCD1U50V3ZY-GP
DCBATOUT_51124_1D05
U52
NTMFS4841NT1G-GP
TC17
SC10U35V6ZY-GP
C426
SC10U35V6ZY-GP
C415
SC10U35V6ZY-GP
C429
SCD1U50V3ZY-GP
1D05V_PWR
L12
IND-D47UH-22-GP
51124_DRVH2
51124_LL2
U53
NTMFS4835NT1G-GP
C435
SC33P50V3JN-GP
R315
11K5R2F-GP
TC20
SE330U2VDM-L-GP
TC21
SE330U2VDM-L-GP
51124_VFB2
DY
R312
29K4R2F-GP
51124_DRVL2
1D05V_PWR
1D05V_S0
G52
G53
G54
G55
G56
G57
G58
G59
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G61
G62
G63
G64
G65
G66
GAP-CLOSE-PWR-GP
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緯創資通
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
TPS51124_1D8V_1D05V
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A3
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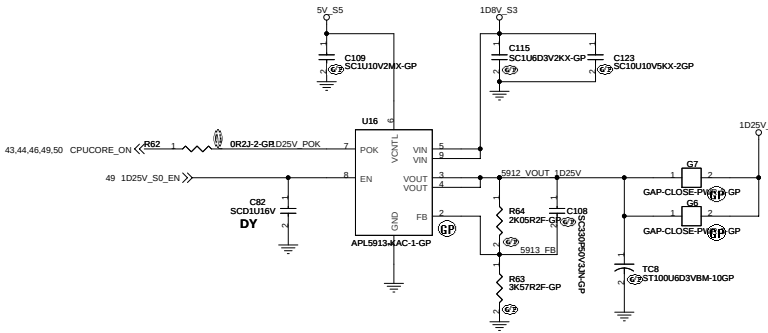

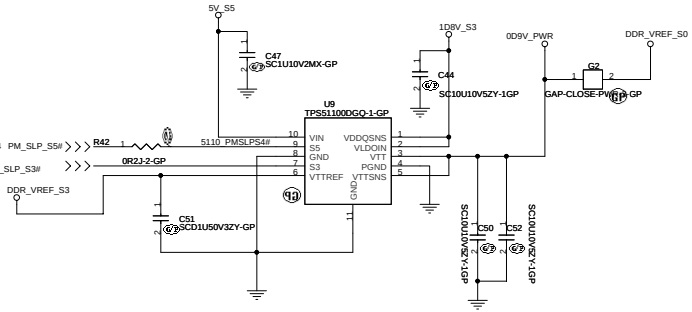

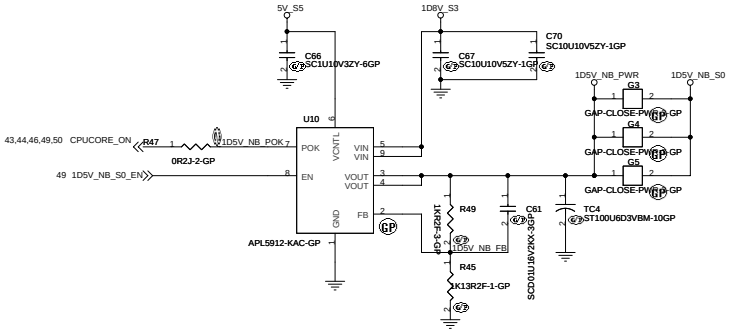

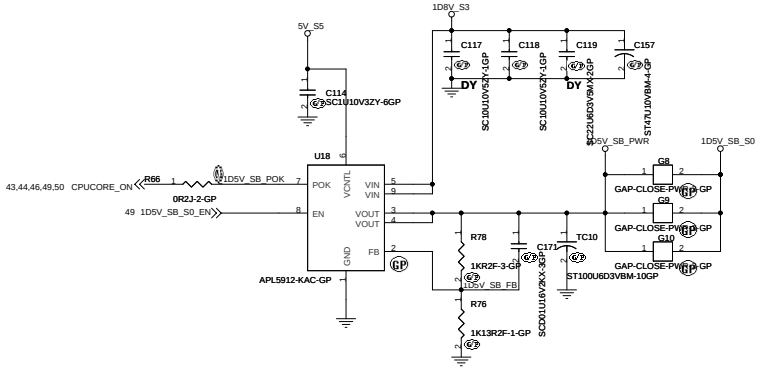

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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: *LDO 0D9V / 1D25V_S0 / 1D5V_S0*		
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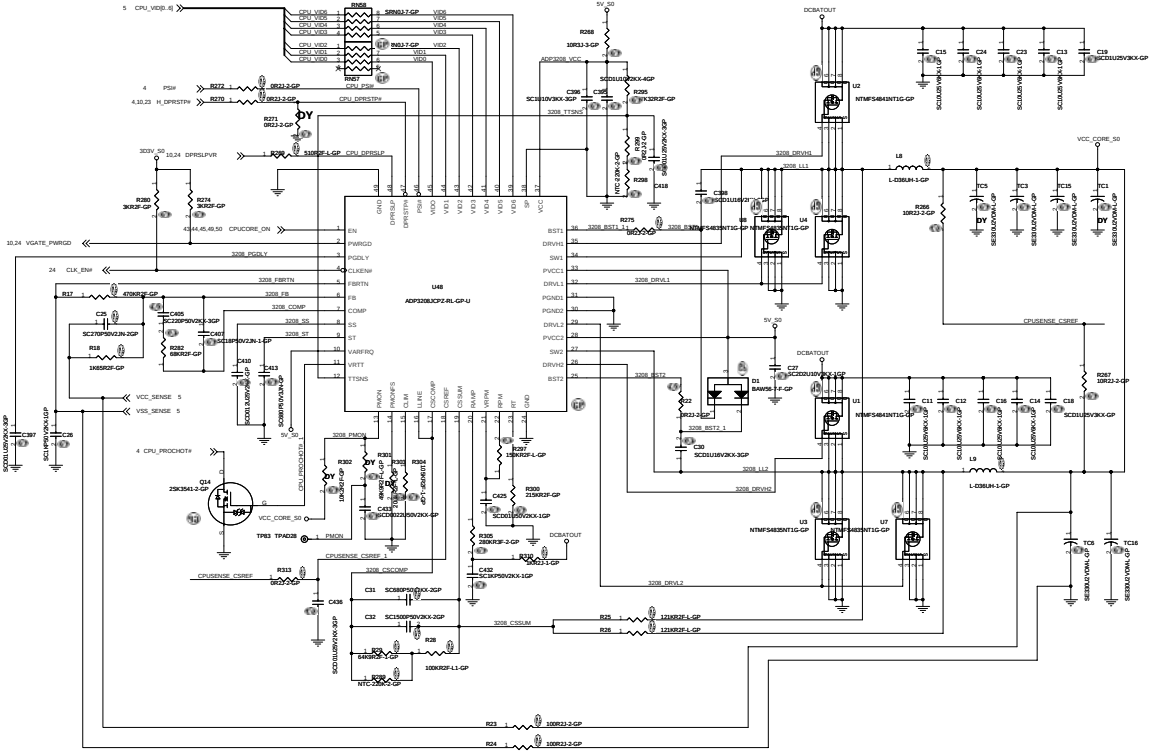

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偉創資通	Wistron Corporation		
	21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title	ADP3208_VCCCPUCORE		
Size A2	Document Number LT71		Rev SC
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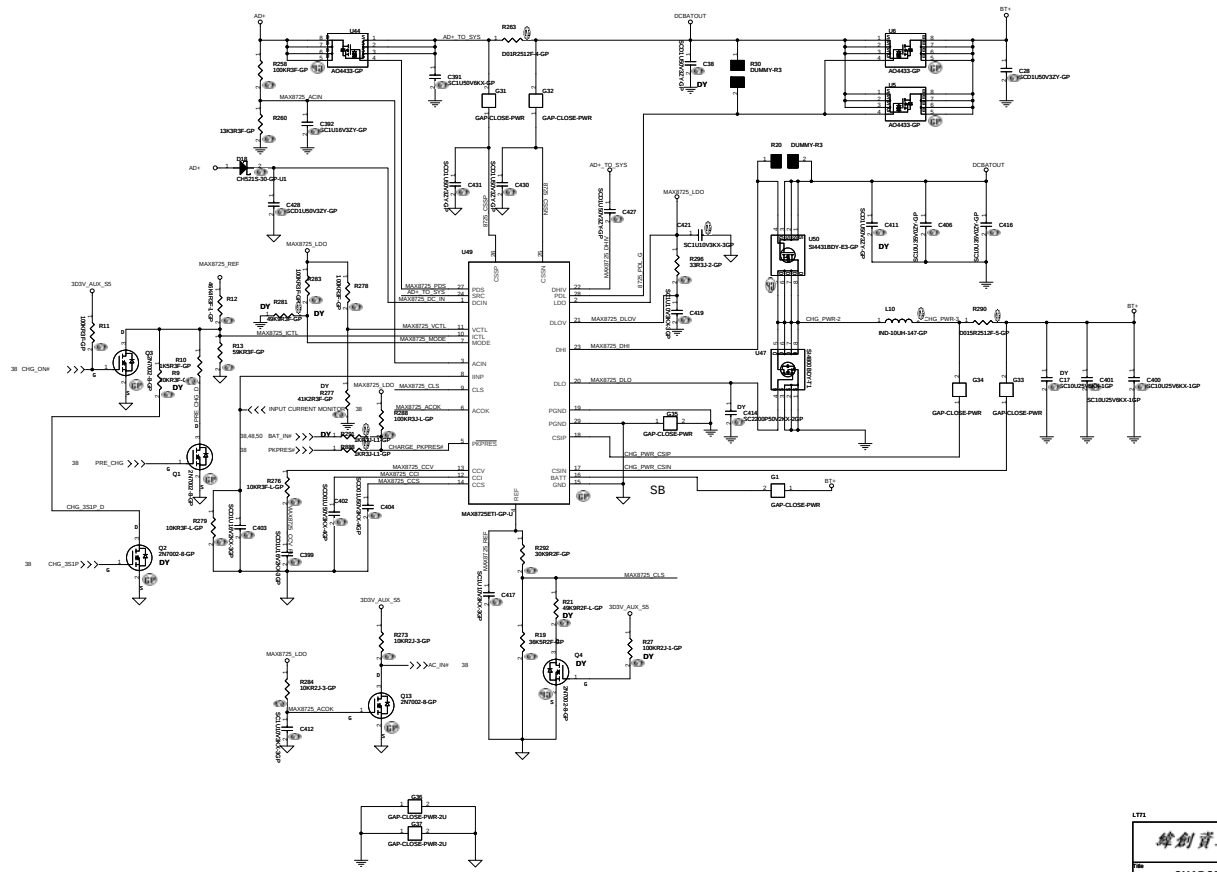

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緯創資通	**Wistron Corporation** 22F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title	***CHARGER MAX8725***
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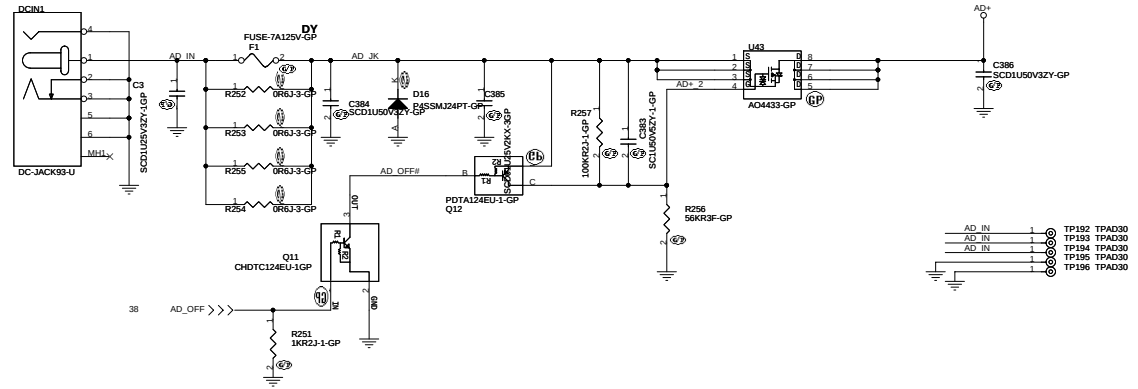
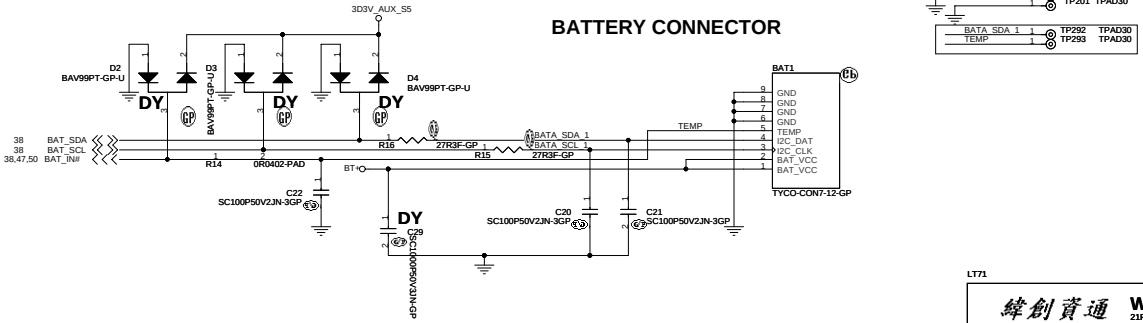
Adaptor in to generate DCBATOUT



BATTERY CONNECTOR



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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: ADT&BATT CONN		
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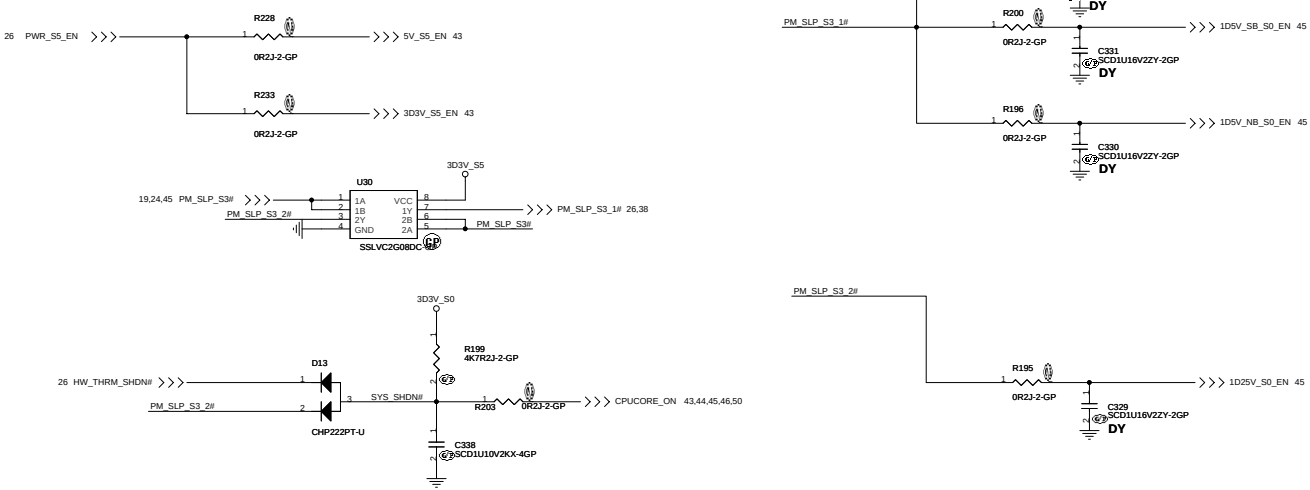
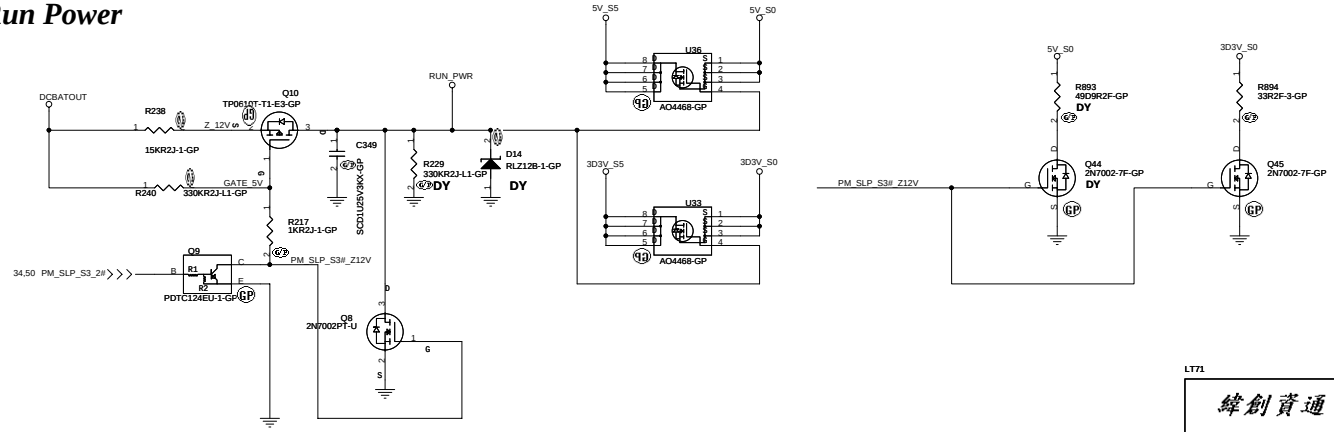


Run Power



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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***PWRPLANE&RESET LOGIC***	
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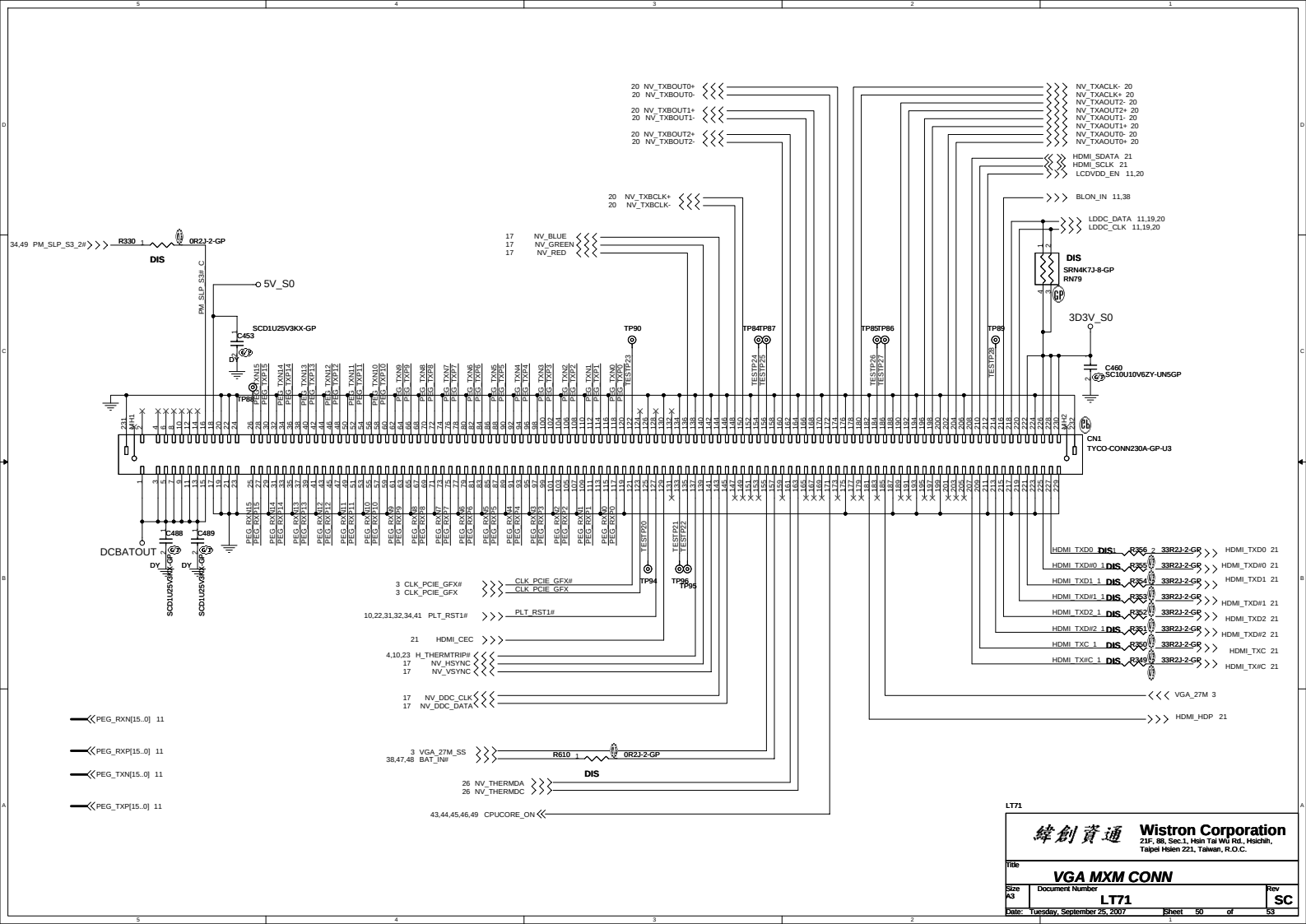

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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	**VGA MXM CONN**	
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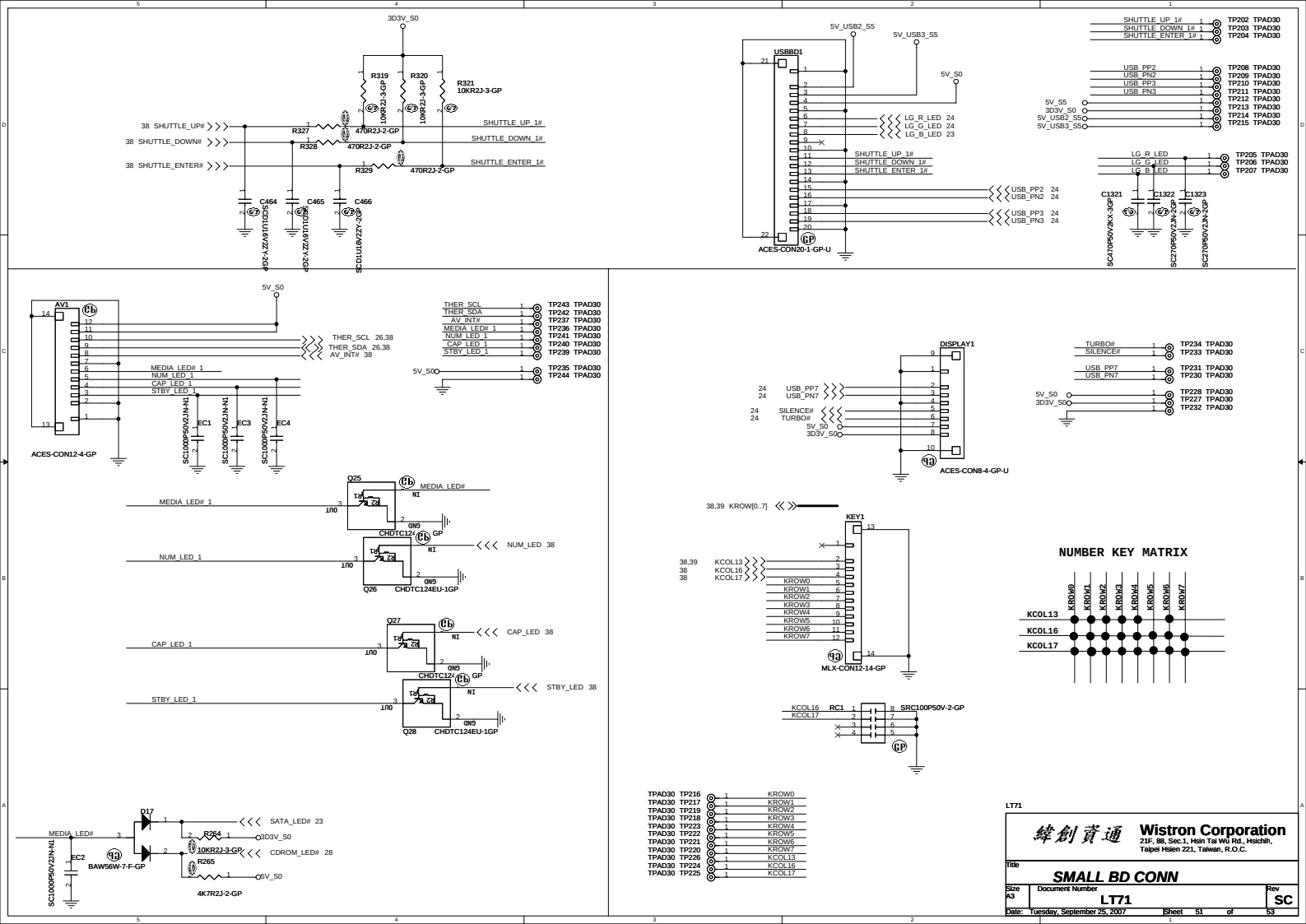

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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***SMALL BD CONN***	
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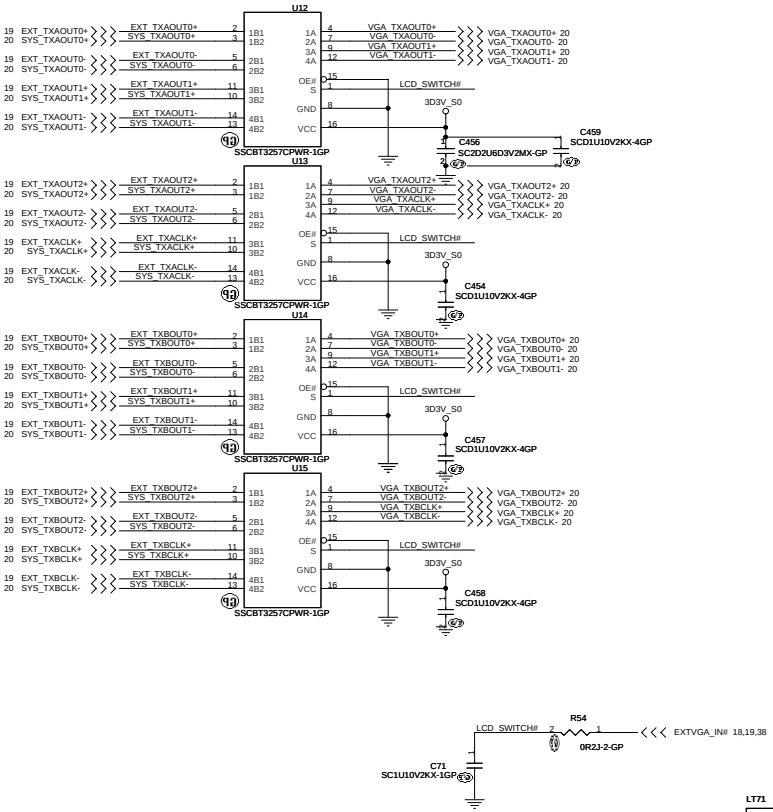

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緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***VGA SWITCH***	
Size A3	Document Number **LT71**	Rev **SC**
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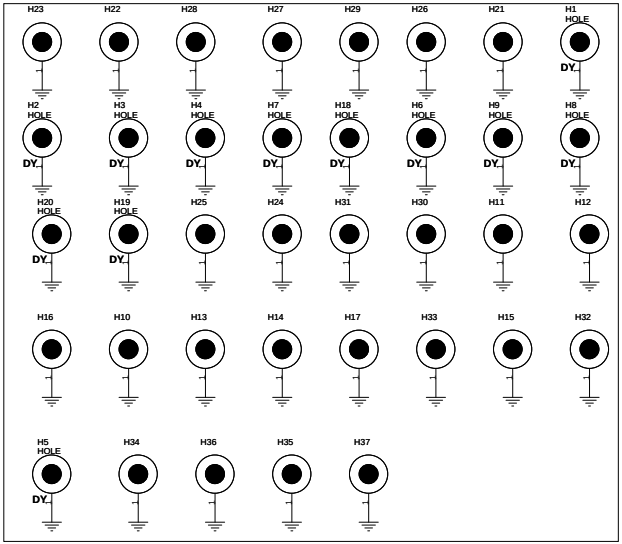

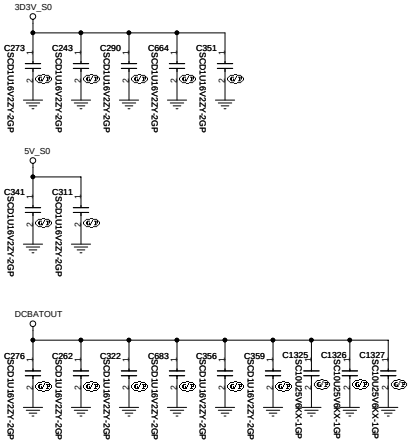

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緯創資通 **Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: ***Holes***		
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