

S-SERIES

Zygo(D) & Zodiac(D) & Ikon

PV_Build (A01)

2008.04.02

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC TITLE S-SERIES - Discrete
DESIGN					
CHECK					
RESPONSIBLE					
SIZE =	3	VER :			SIZE CODE
FILE NAME :	XXXX-XXXXXX-XX				A3 CS
P/N	XXXXXXXXXXXX				DOC. NUMBER
					1310A21614
					REV
					A01
					SHEET
					1 OF 54

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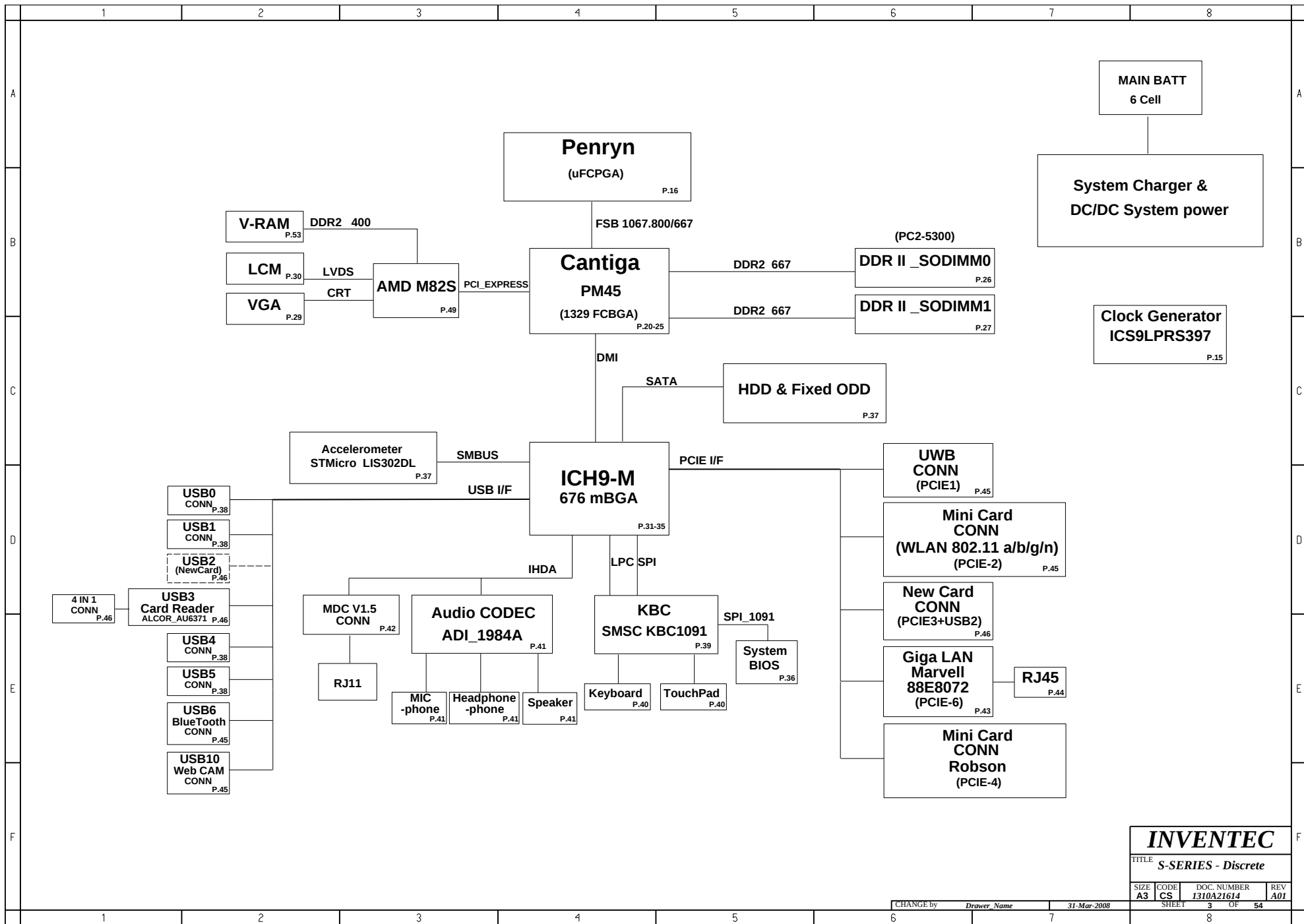
Page	Page	Page
5- DC & Battery Charger.	15- Clock Generator.	36- 15" ODD Extend/B.
6- Select & Battery CONN.	16- CPU Penryn-1.	37- HDD & ODD CONN&Accelerometer.
7- System Power (+V3A/+V5A).	17- CPU Penryn-2.	38- USB CONN.
8- System Power (+V1.8/+VCCP) .	18- CPU Penryn-3.	39- KBC & BIOS ROM.
9- Graphic Power (+VDD_CORE & +VPCIE).	19- XDP & Thermal & Fan.	40- Internal Keyboard & TouchPad CONN.
10- System Power (+V1.5S) .	20- N/B Cantiga-1.	41- Audio CODEC & Earphone & Speaker.
11- CPU Power (+VCC_CORE).	21- N/B Cantiga-2.	42- Audio MIC & MDC CONN.
12- DDR Termination Voltage(+V0.9S).	22- N/B Cantiga-3.	43- Giga LAN controller.
13- Power (Sleep: +V5S/+V3S/+V1.8S).	23- N/B Cantiga-4.	44- Magnetic & RJ45 CONN.
14- Power (Sequence).	24- N/B Cantiga-5.	45- Mini Card(WLAN/Robson)/BT/UWB/Web-CAM.
	25- N/B Cantiga-6.	46- New Card & 4 in 1 Card (SD/MMC/MS/XD).
	26- DDR2-DIMM0.	47- LEDs & Buttons & SW/B.
	27- DDR2-DIMM1.	48- Screws / Crack testing circuit.
	28- DDR2-DAMPING.	49- AMD M82SE-1.
	29- VGA CONN.	50- AMD M82SE-2.
	30- LCM CONN.	51- AMD M82SE-3.
	31- S/B ICH9-1.	52- AMD M82SE-4.
	32- S/B ICH9-2.	53- Video RAM-1 (GDDR2).
	33- S/B ICH9-3.	54- Video RAM-2 (GDDR2).
	34- S/B ICH9-4.	
	35- S/B ICH9-5.	

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TITLE **S-SERIES - Discrete**

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CHANGE by *Drawn Name* 31-Mar-2008



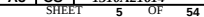
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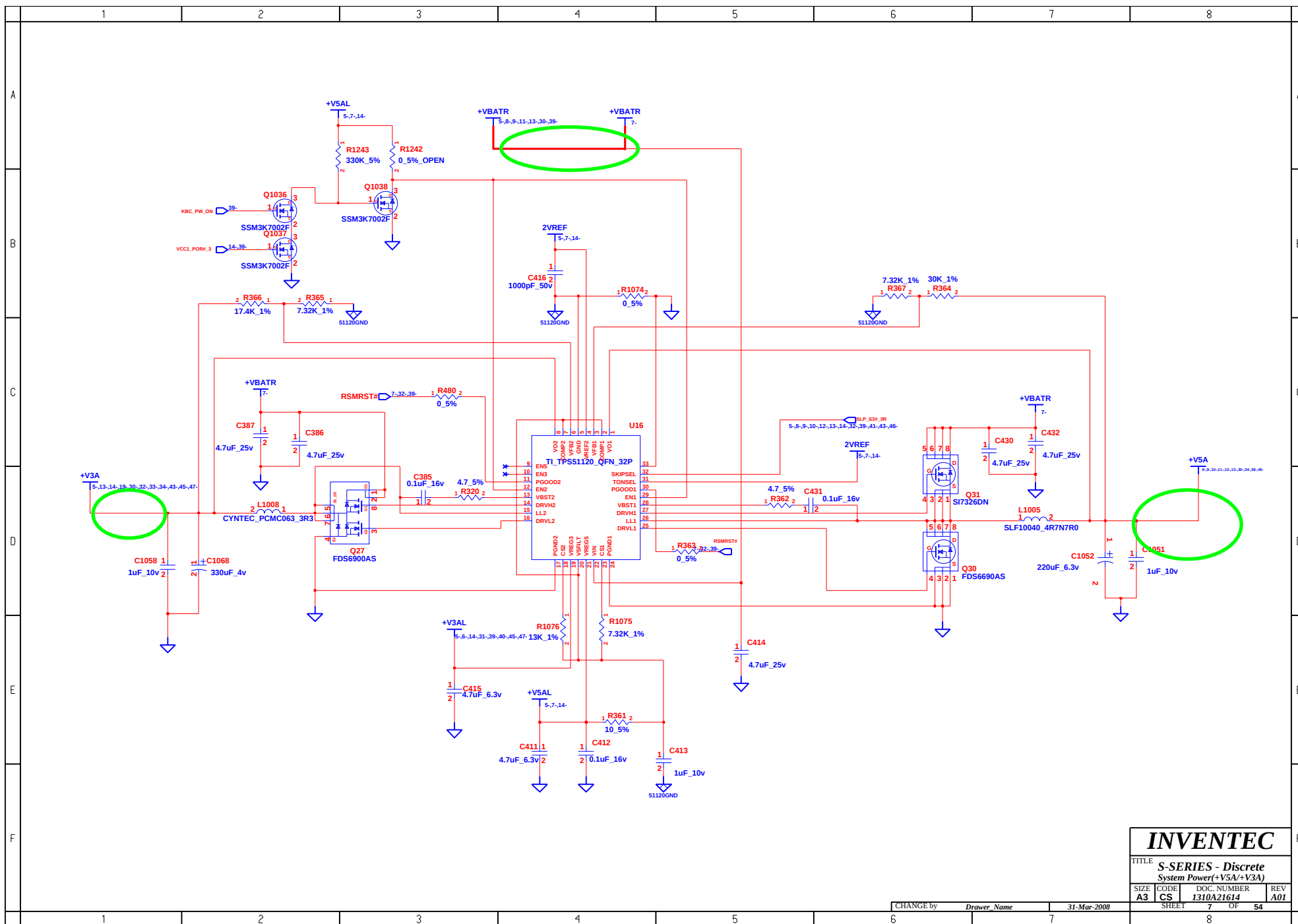
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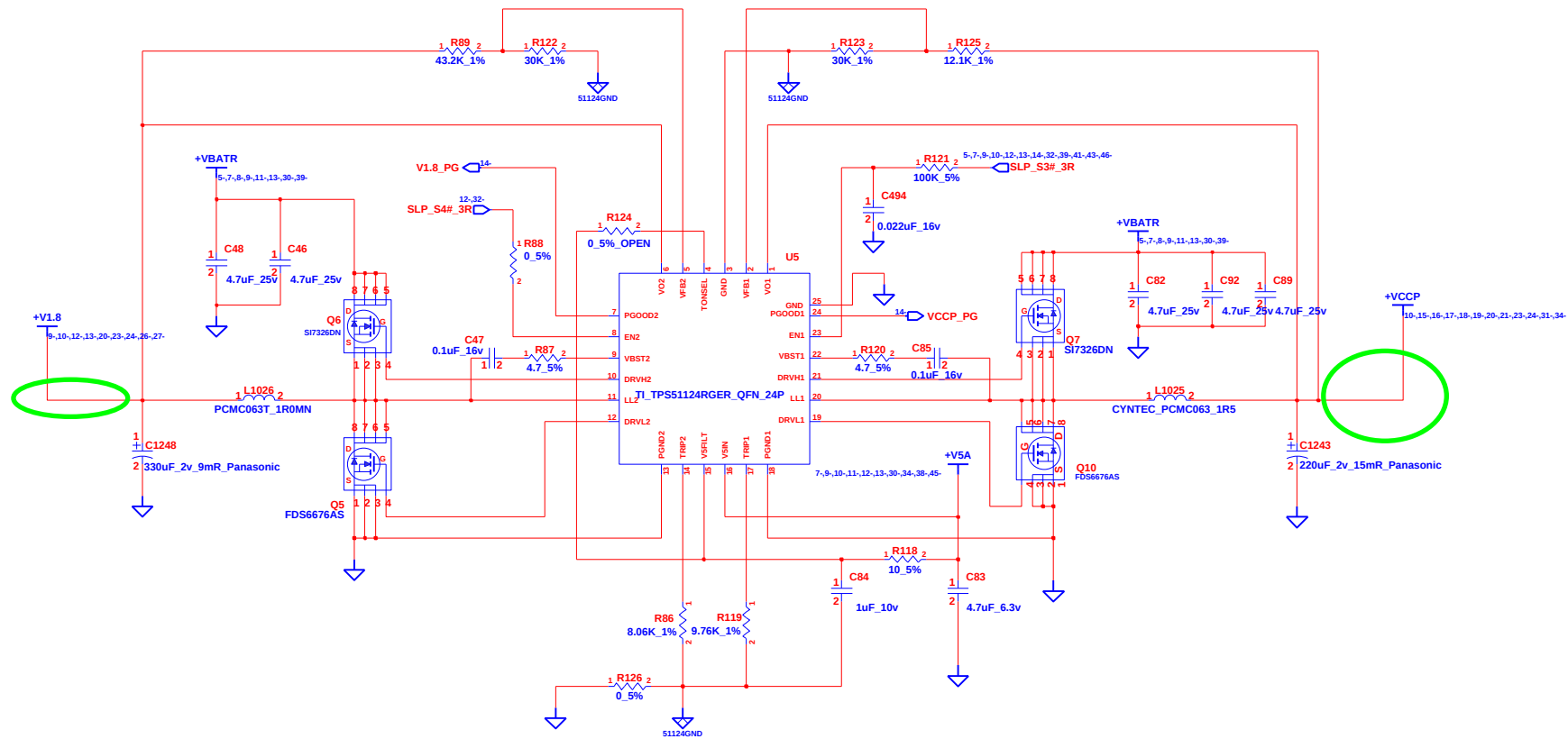
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CHANGE by **Drawer Name** 31-Mar-2008

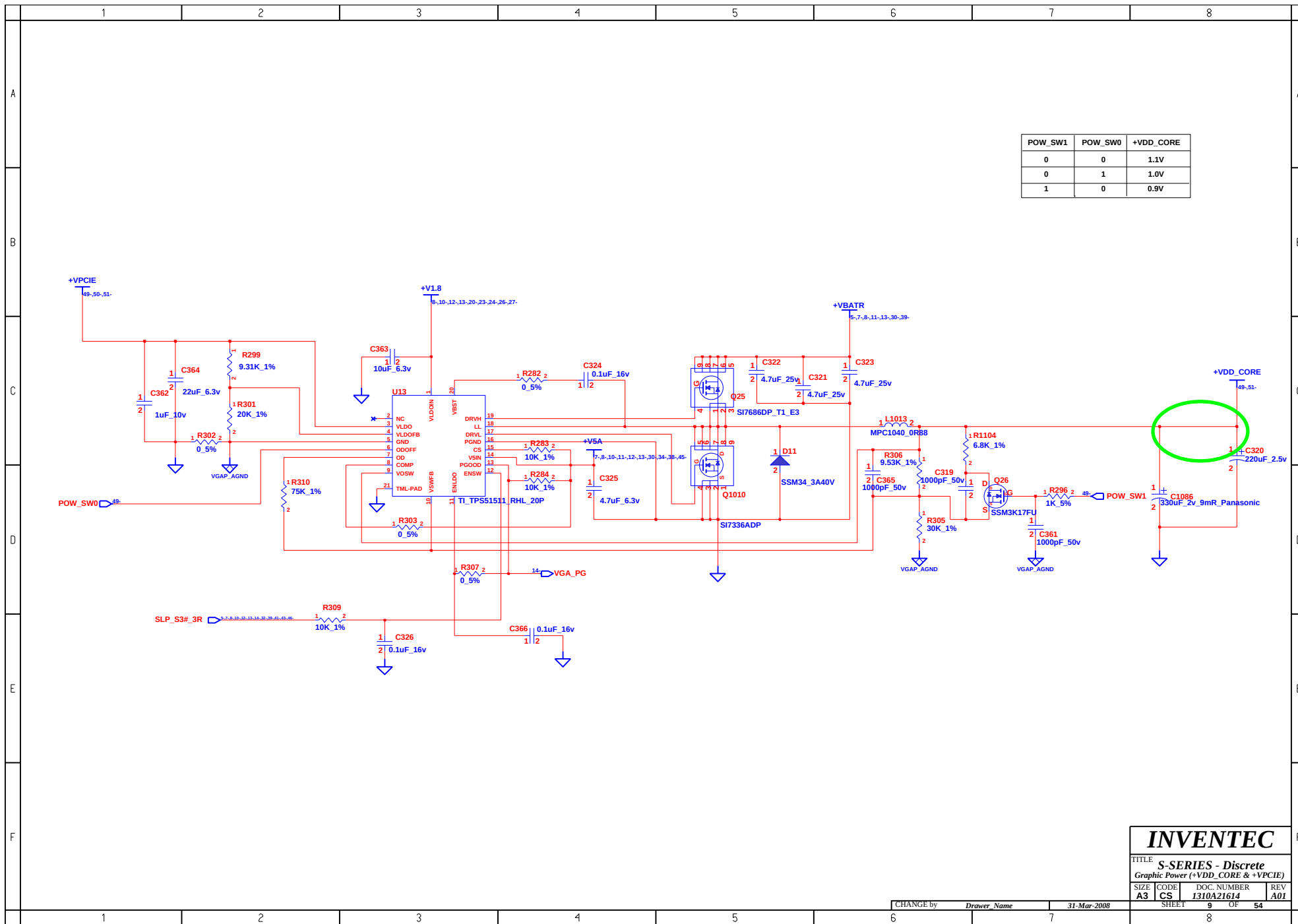
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INVENTEC			
TITLE S-SERIES - Discrete System Power(+V1.8/+VCCP)			
SIZE A3	CODE CS	DOC. NUMBER I310A21614	REV A01
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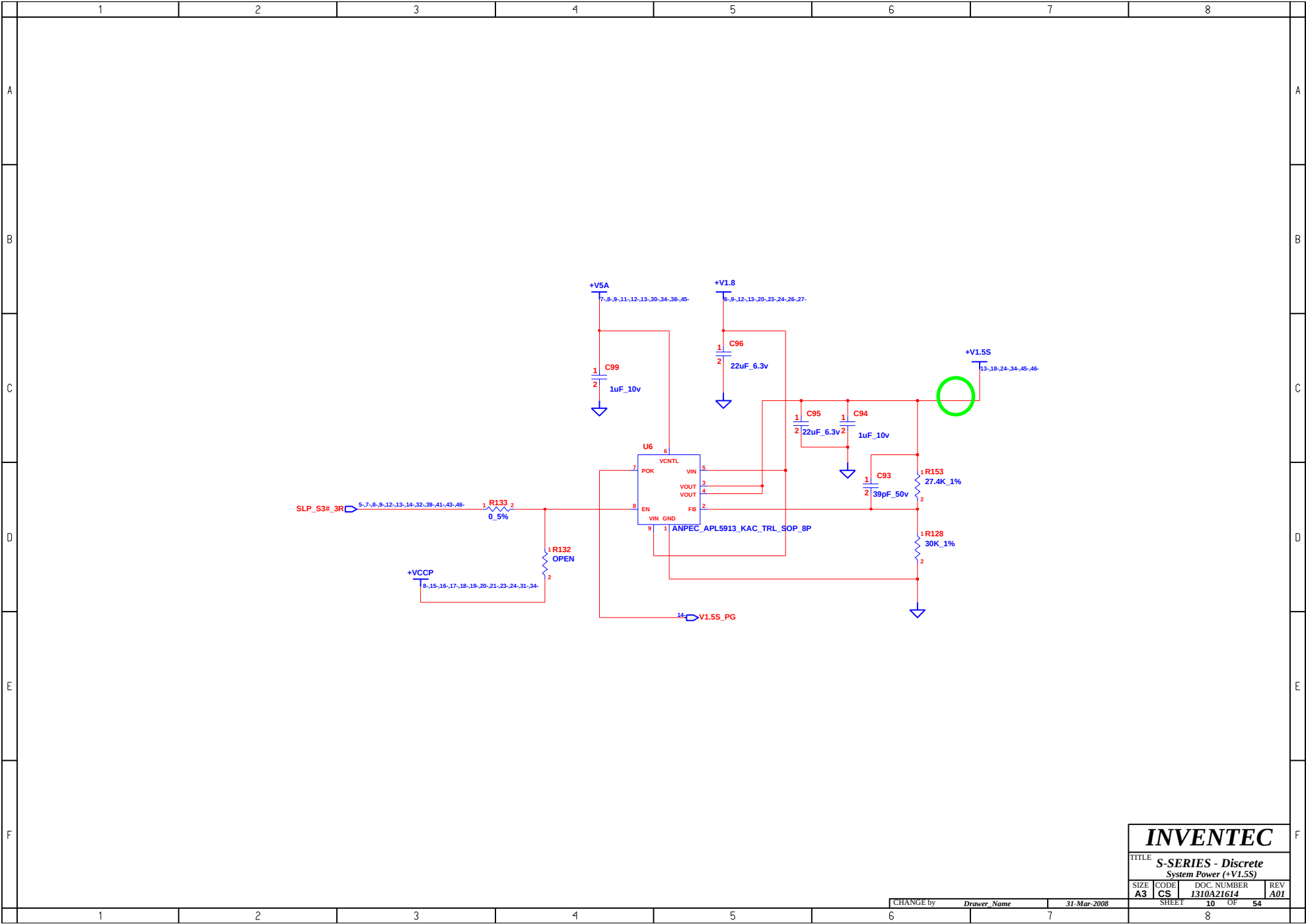
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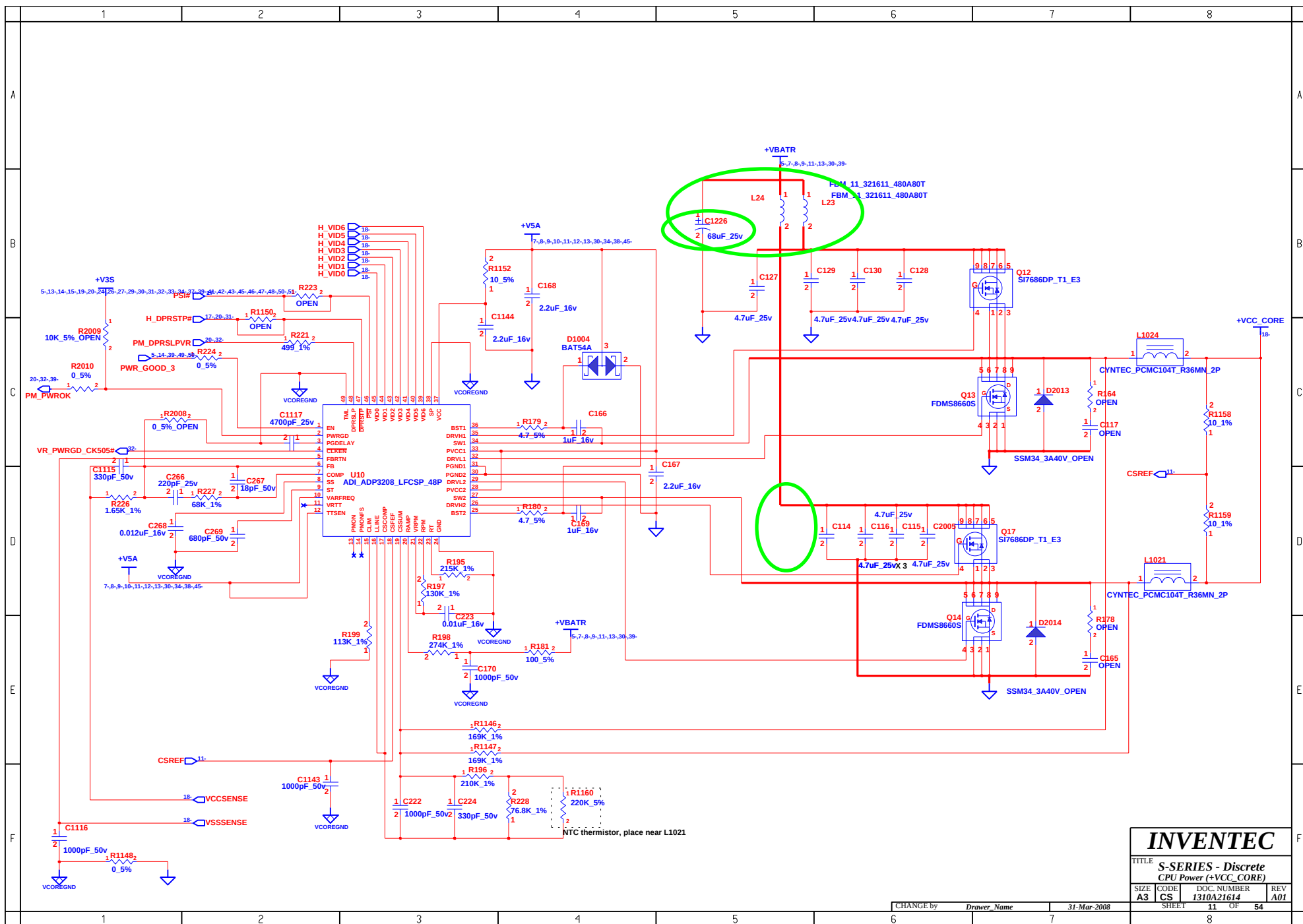
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Graphic Power (+VDD_CORE & +VPCIE)

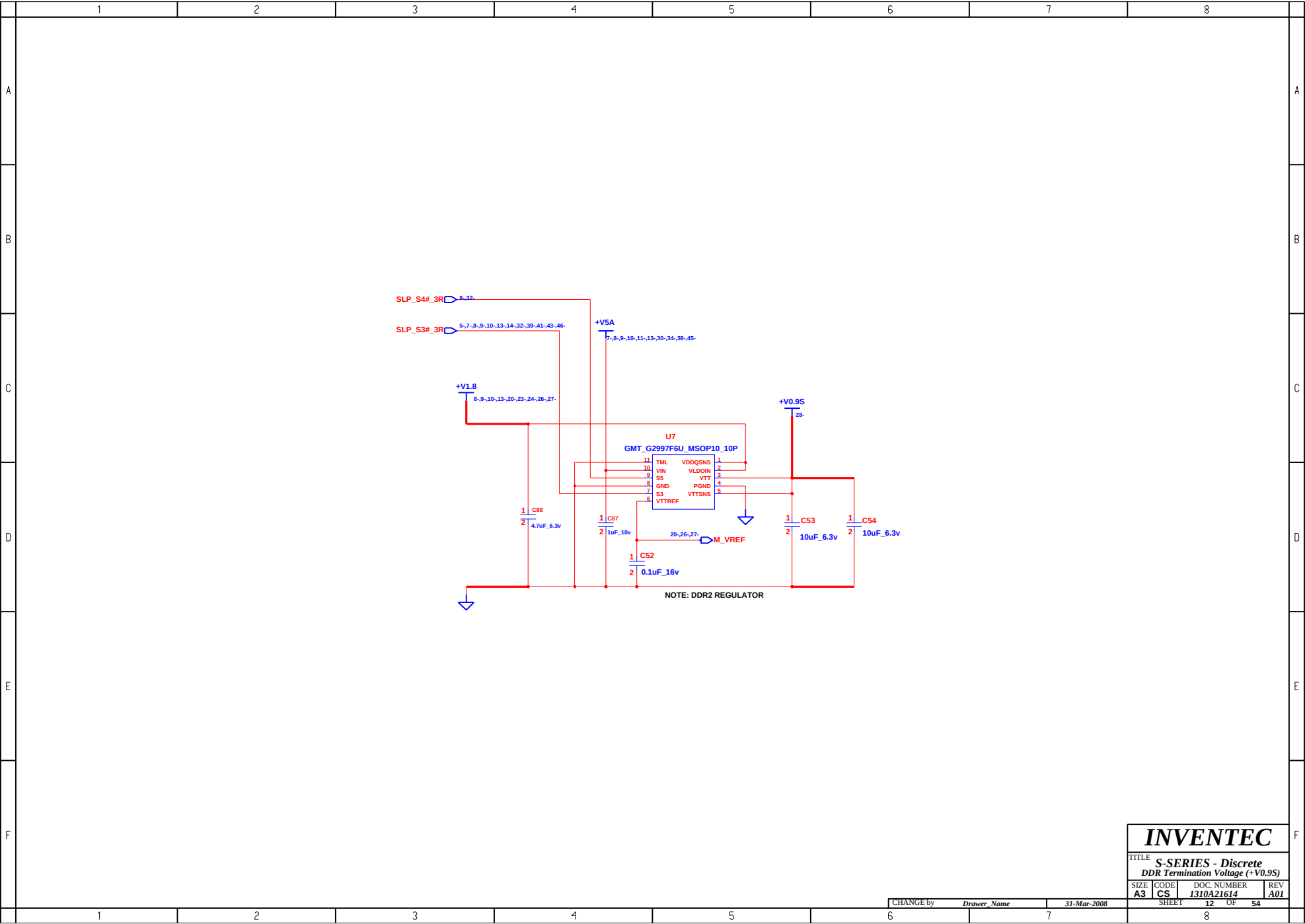
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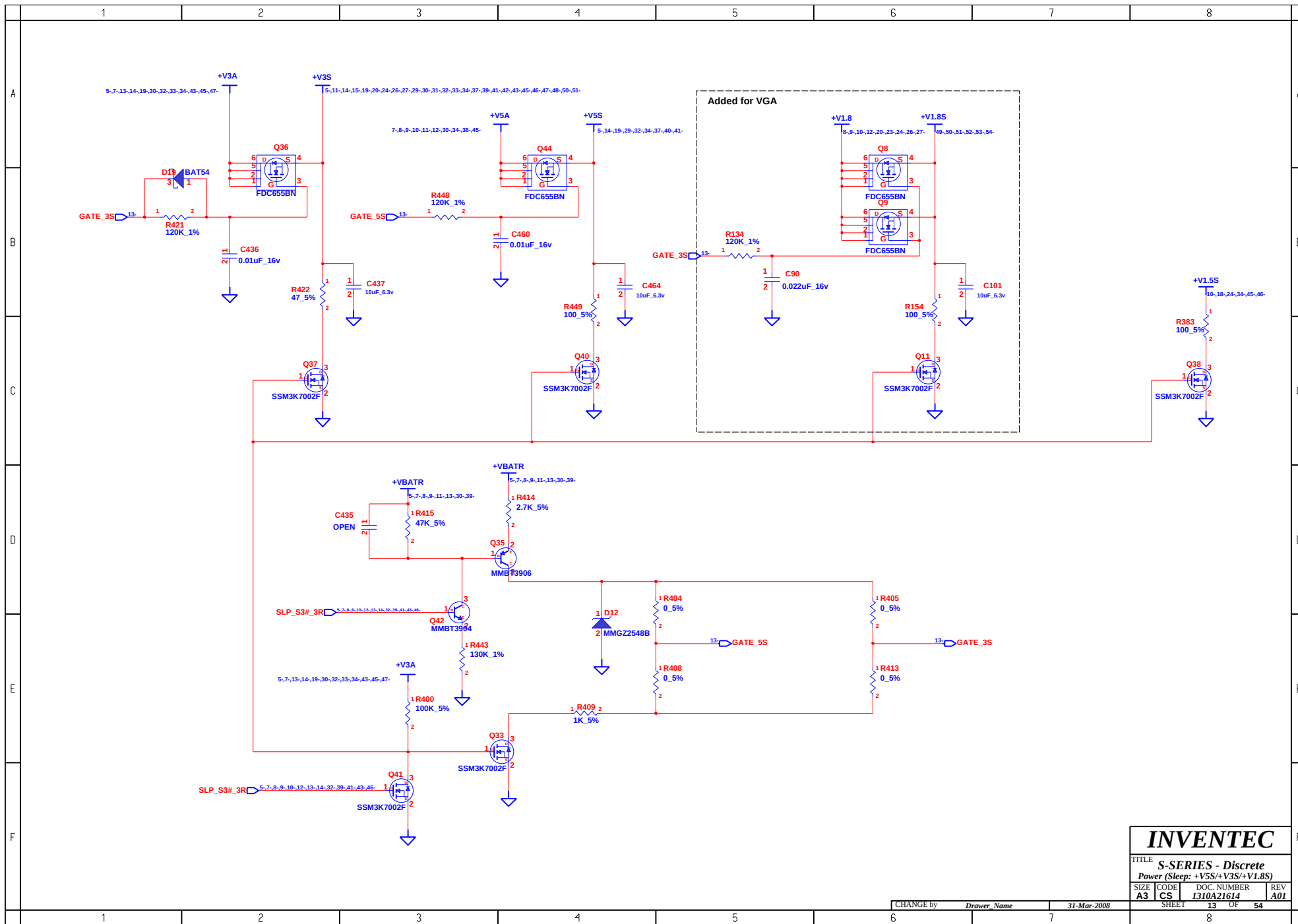


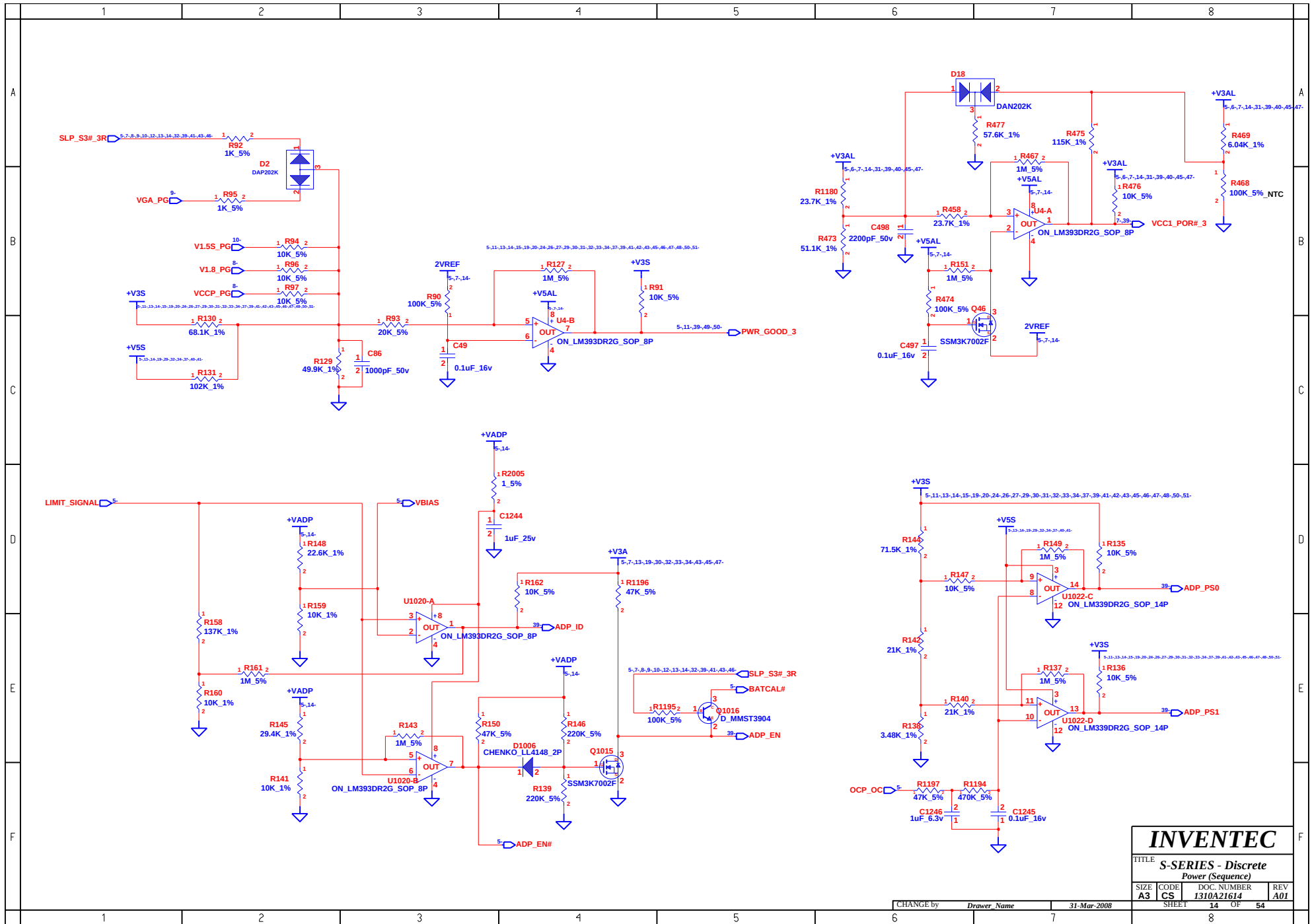


INVENTEC

TITLE
S-SERIES - Discrete
DDR Termination Voltage (+V0.9S)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310AZ1614	A01





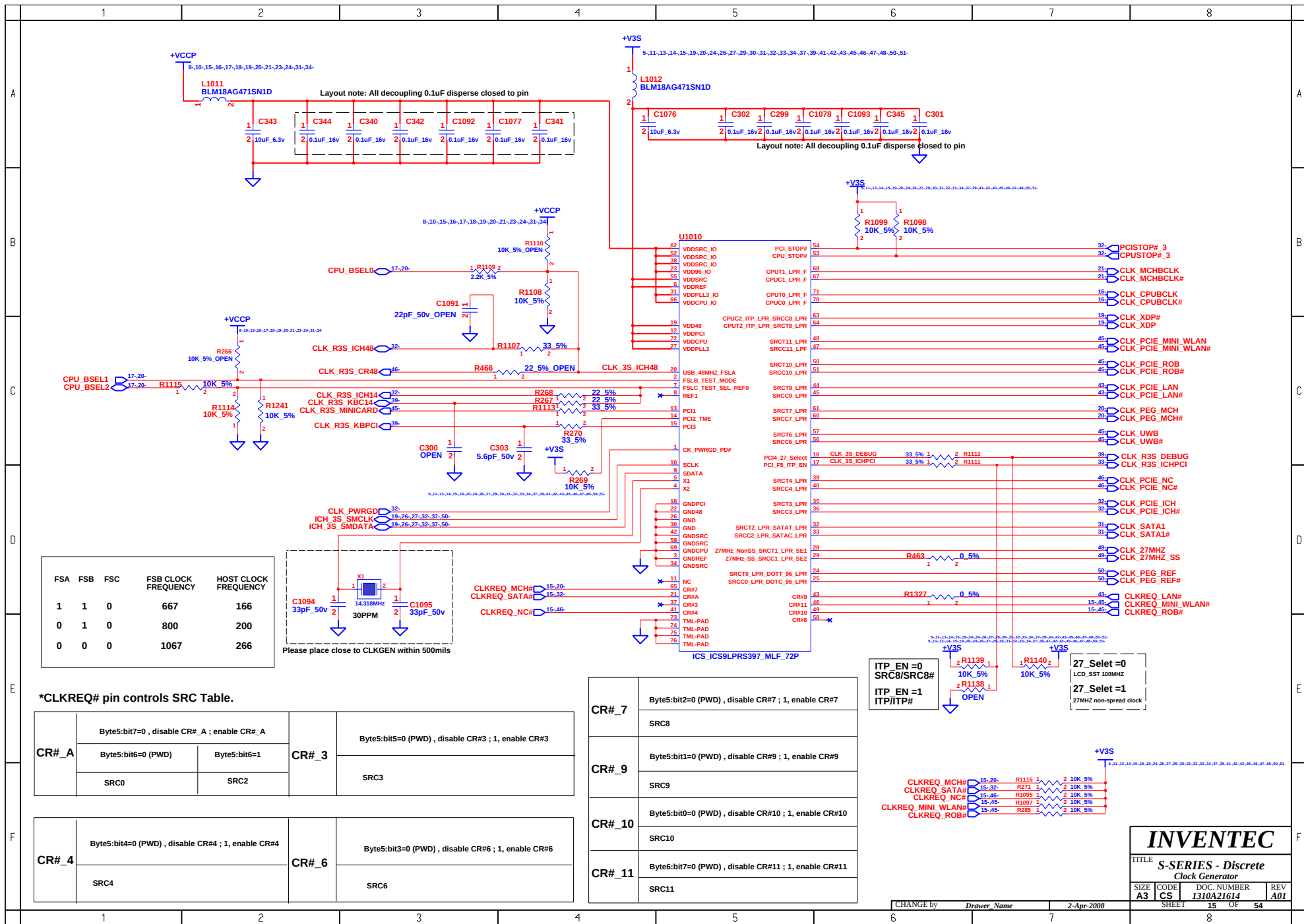
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TITLE
S-SERIES - Discrete
Power (Sequence)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A21614	A01

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FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200
0	0	0	1067	266

*CLKREQ# pin controls SRC Table.

CR#_A	Byte5:bit7=0 , disable CR#_A ; enable CR#_A		CR#_3	Byte5:bit5=0 (PWD) , disable CR#3 ; 1, enable CR#3	
	Byte5:bit6=0 (PWD)	Byte5:bit6=1		SRC3	
	SRC0	SRC2			

CR#_4	Byte5:bit4=0 (PWD) , disable CR#4 ; 1, enable CR#4	Byte5:bit3=0 (PWD) , disable CR#6 ; 1, enable CR#6	
	SRC4	SRC6	

CR#_7	Byte5:bit2=0 (PWD) , disable CR#7 ; 1, enable CR#7
SRC8	
CR#_9	Byte5:bit1=0 (PWD) , disable CR#9 ; 1, enable CR#9
SRC9	
CR#_10	Byte5:bit0=0 (PWD) , disable CR#10 ; 1, enable CR#10
SRC10	
CR#_11	Byte6:bit7=0 (PWD) , disable CR#11 ; 1, enable CR#11
SRC11	

ITP_EN=0
SRC8/SRC8#
ITP_EN=1
ITP/ITP#

27_Select=0
LCD_SST 100MHz
27_Select=1
27MHz non-spread clock

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TITLE

S-SERIES - Discrete

Clock Generator

SIZE CODE CS

1310AZ1614

DOC. NUMBER

15

REV

A01

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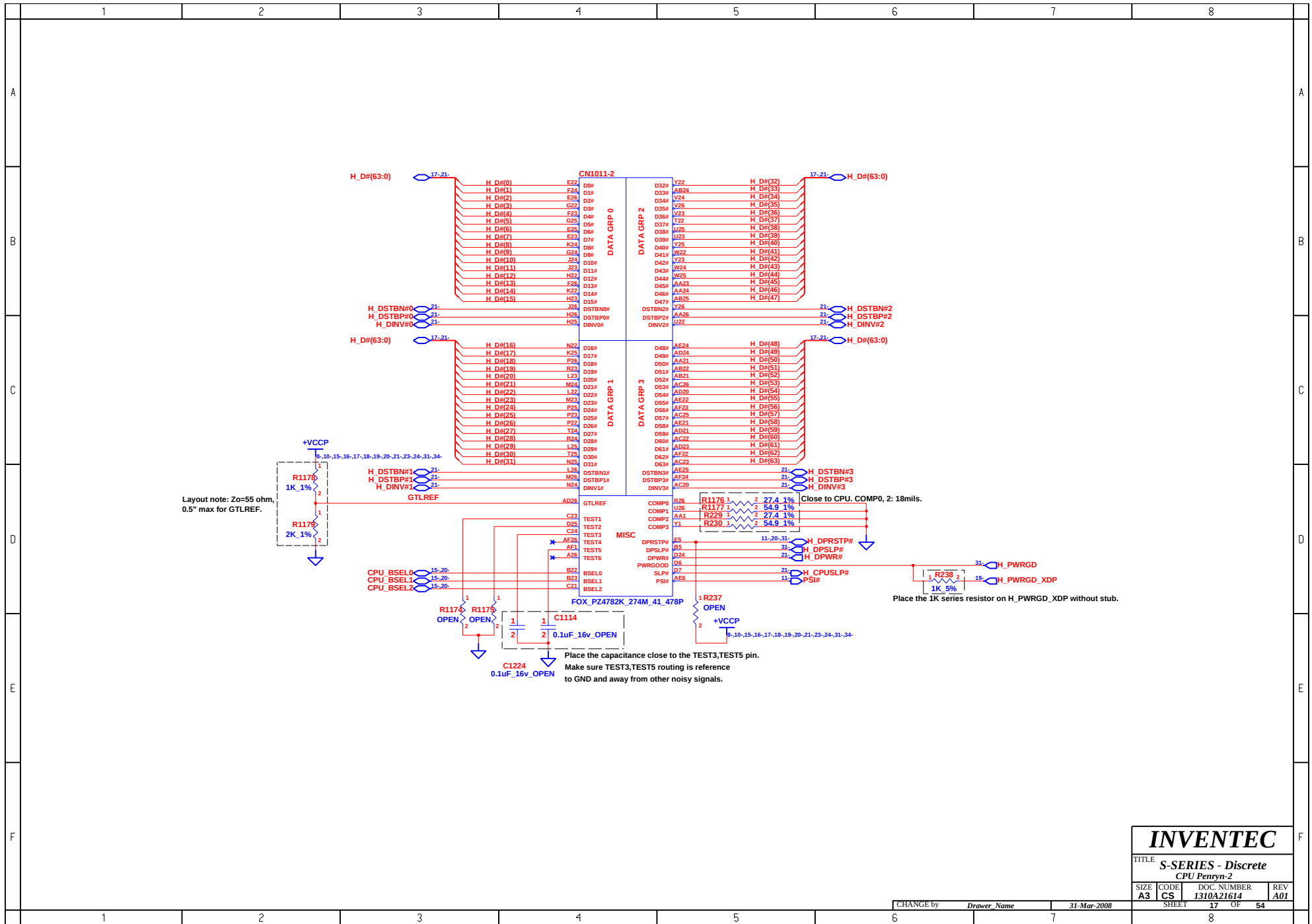
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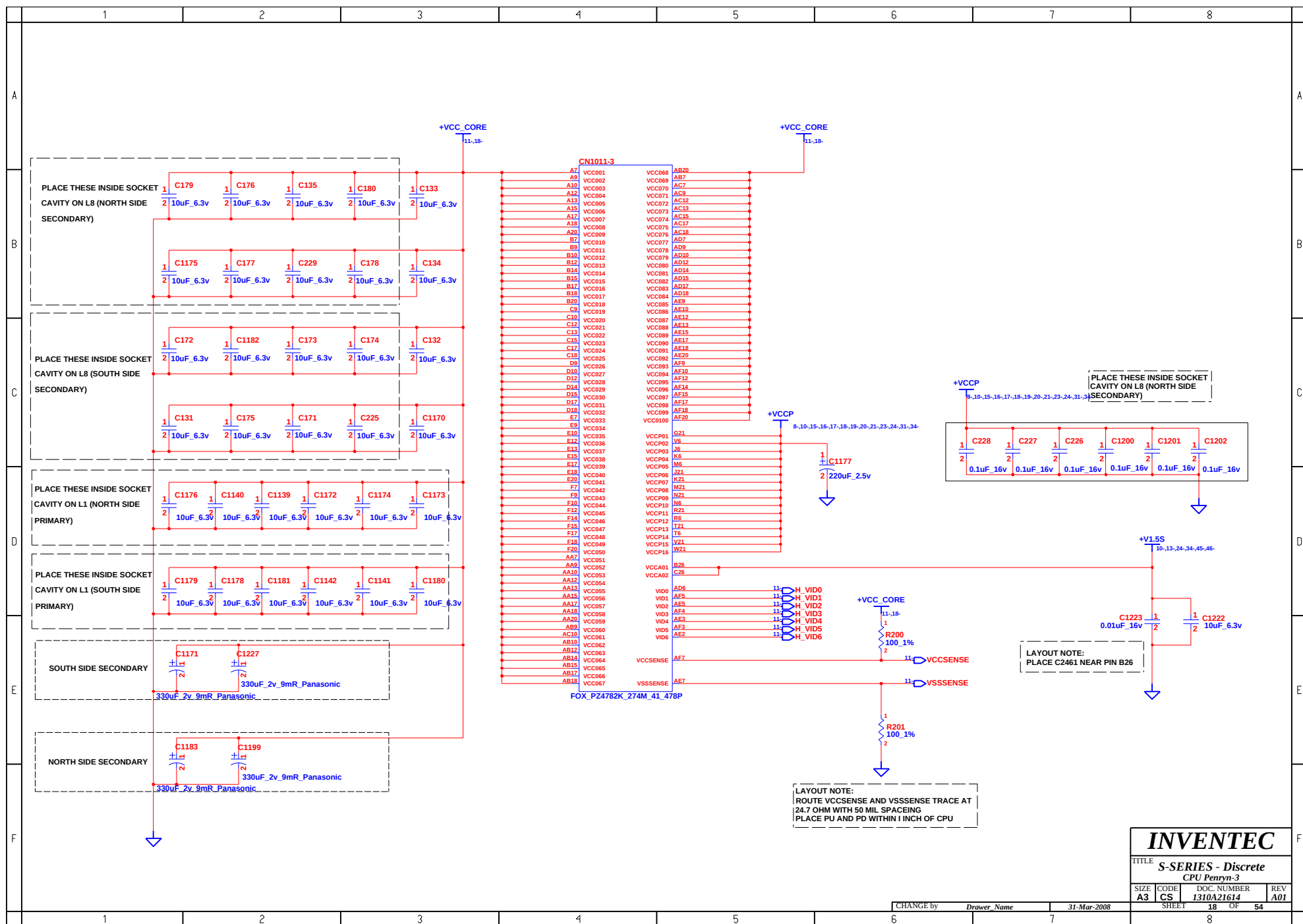
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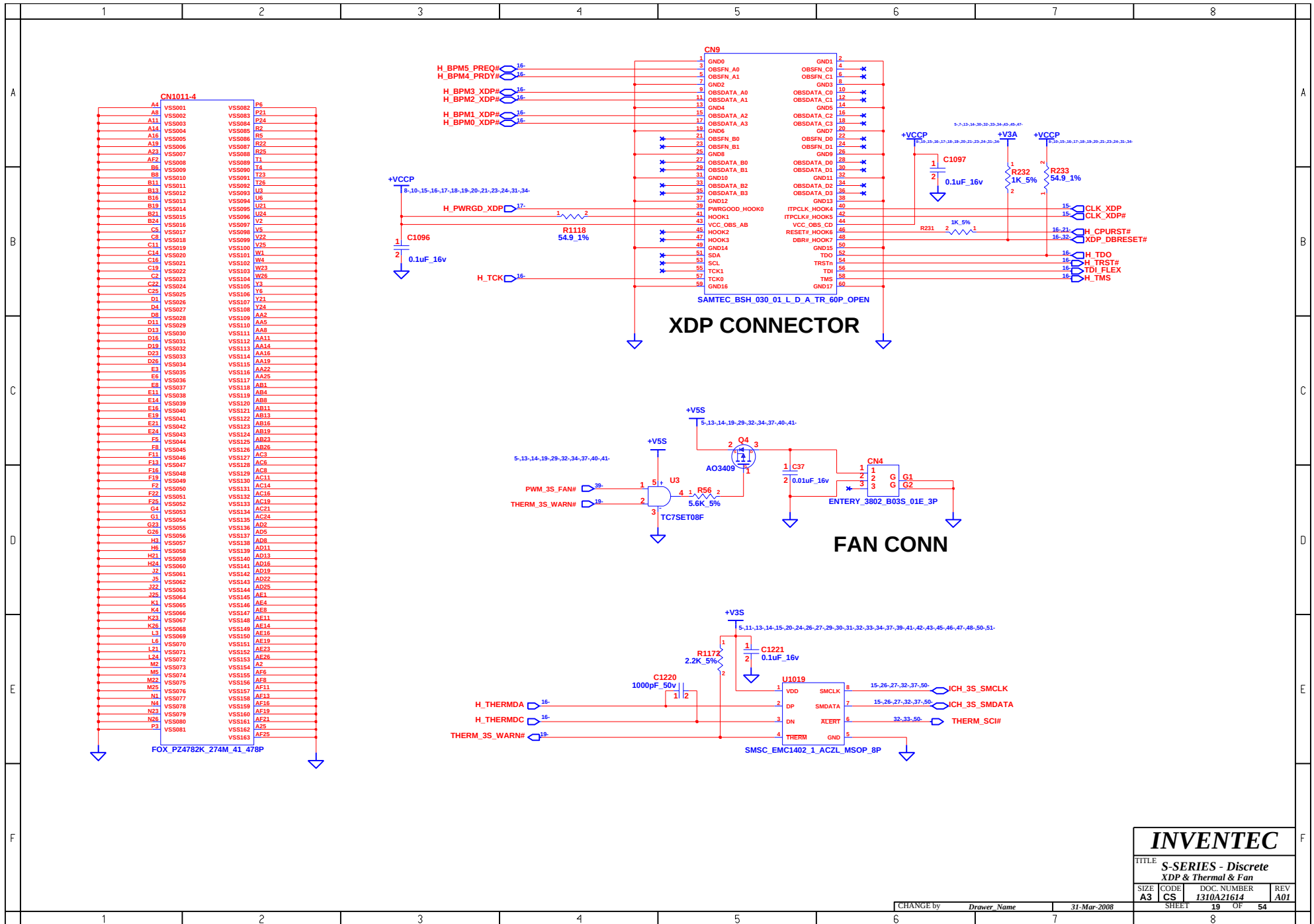
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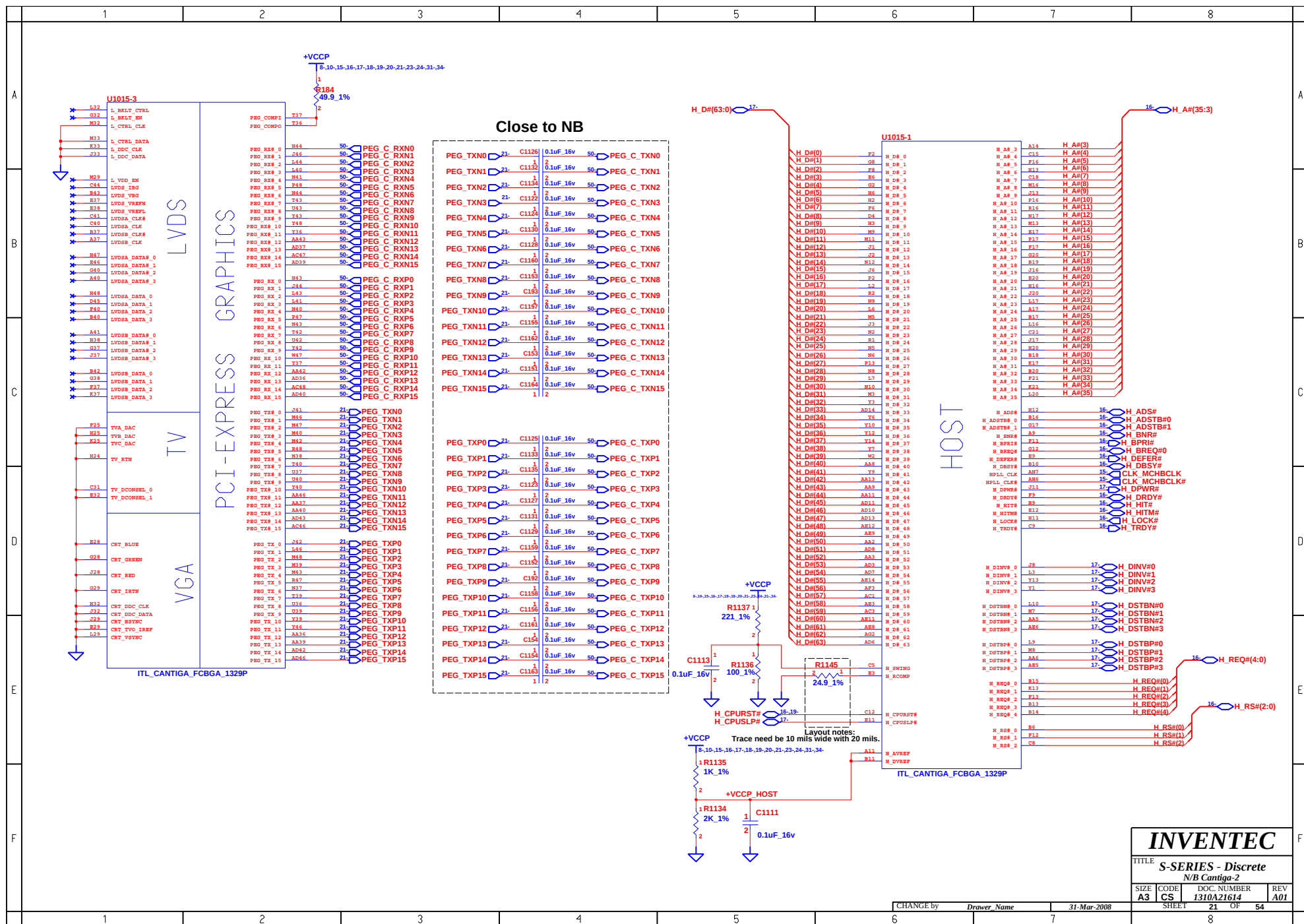
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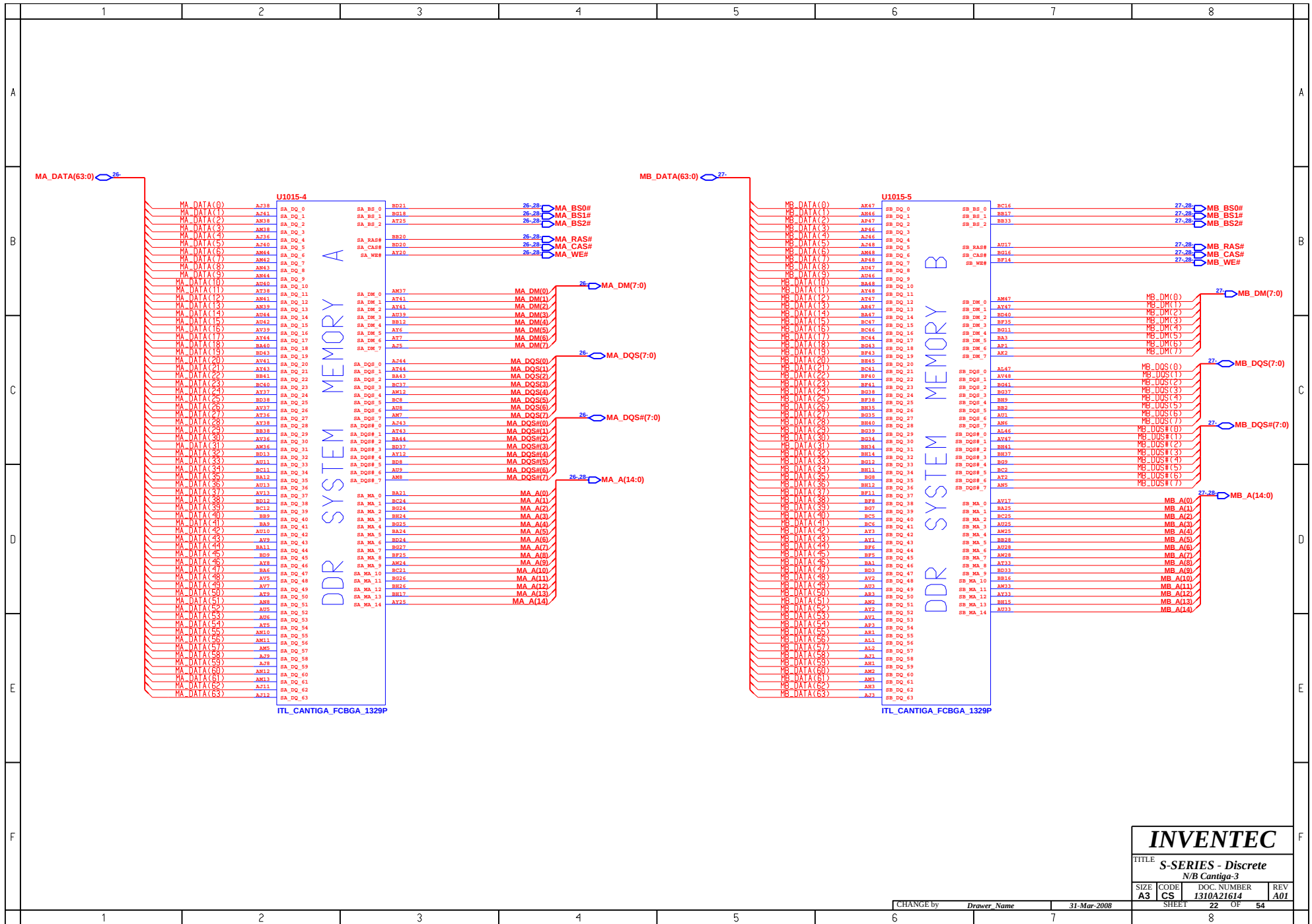
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S-SERIES - Discrete
XDP & Thermal & Fan

SIZE	CODE	DOC. NUMBER	REV
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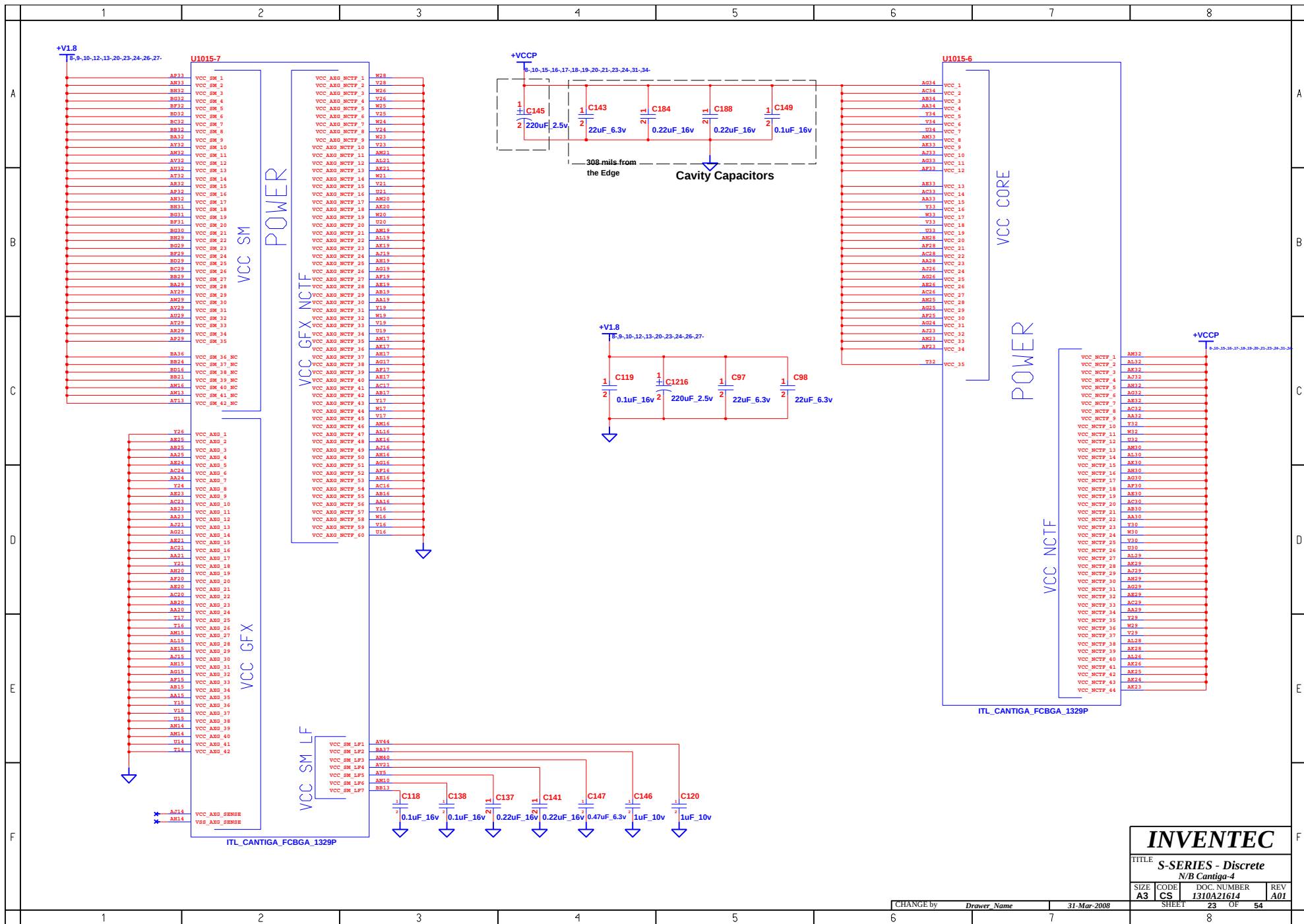
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TITLE
S-SERIES - Discrete
N/B Cantiga-3

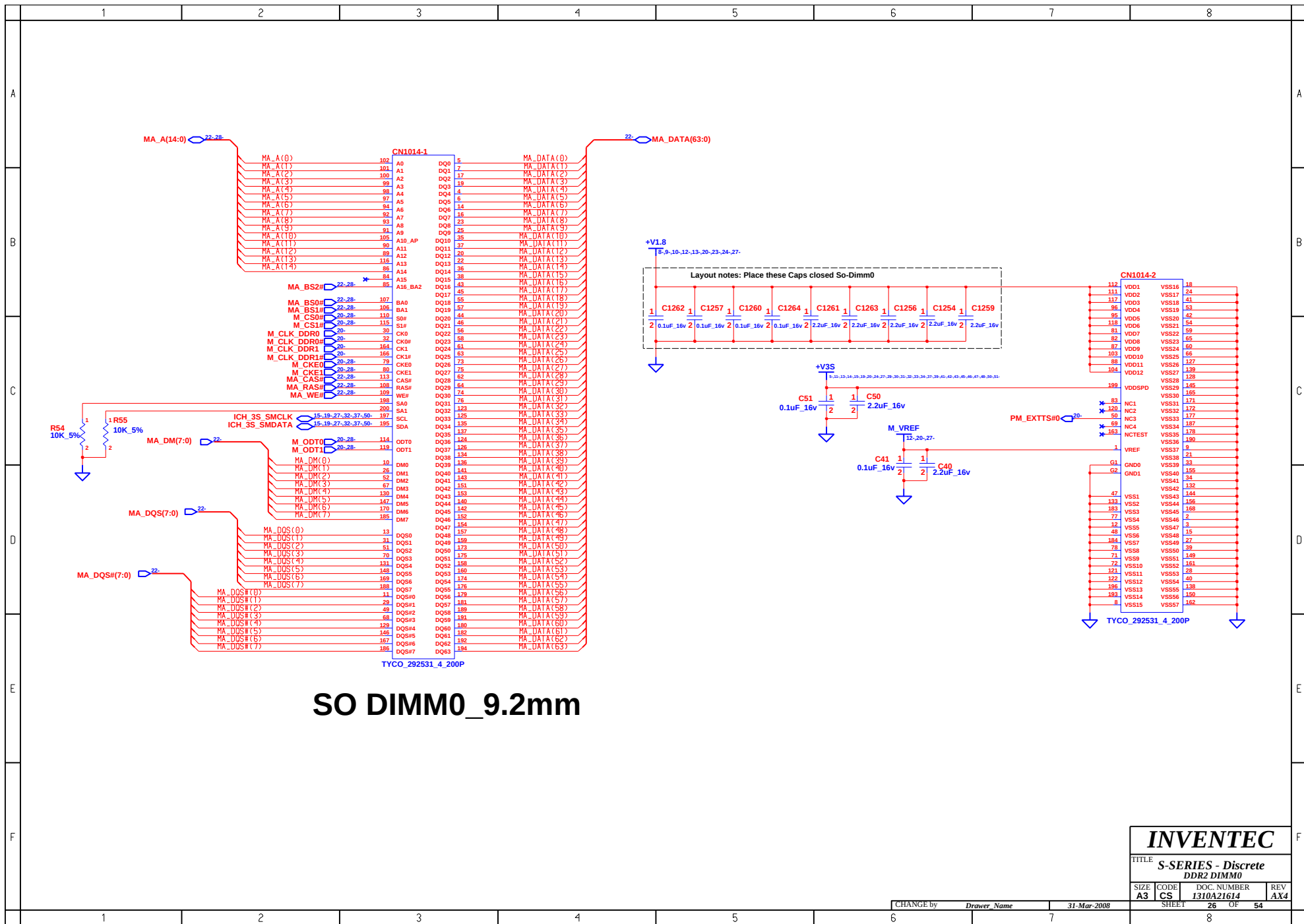
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A3	CS	1310A21614	A01

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[illegible]



SO DIMM0_9.2mm

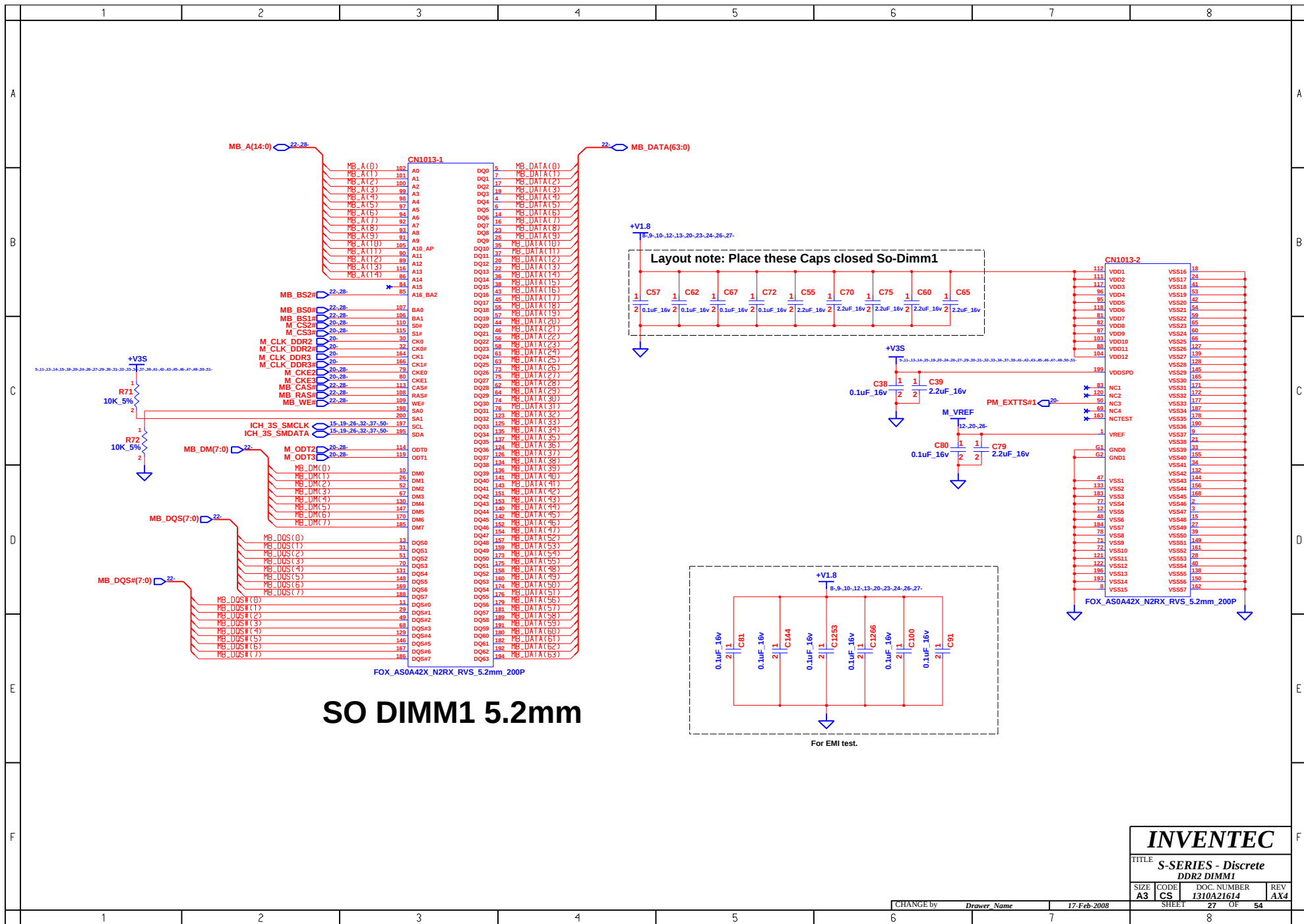
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S-SERIES - Discrete
DDR2 DIMM0

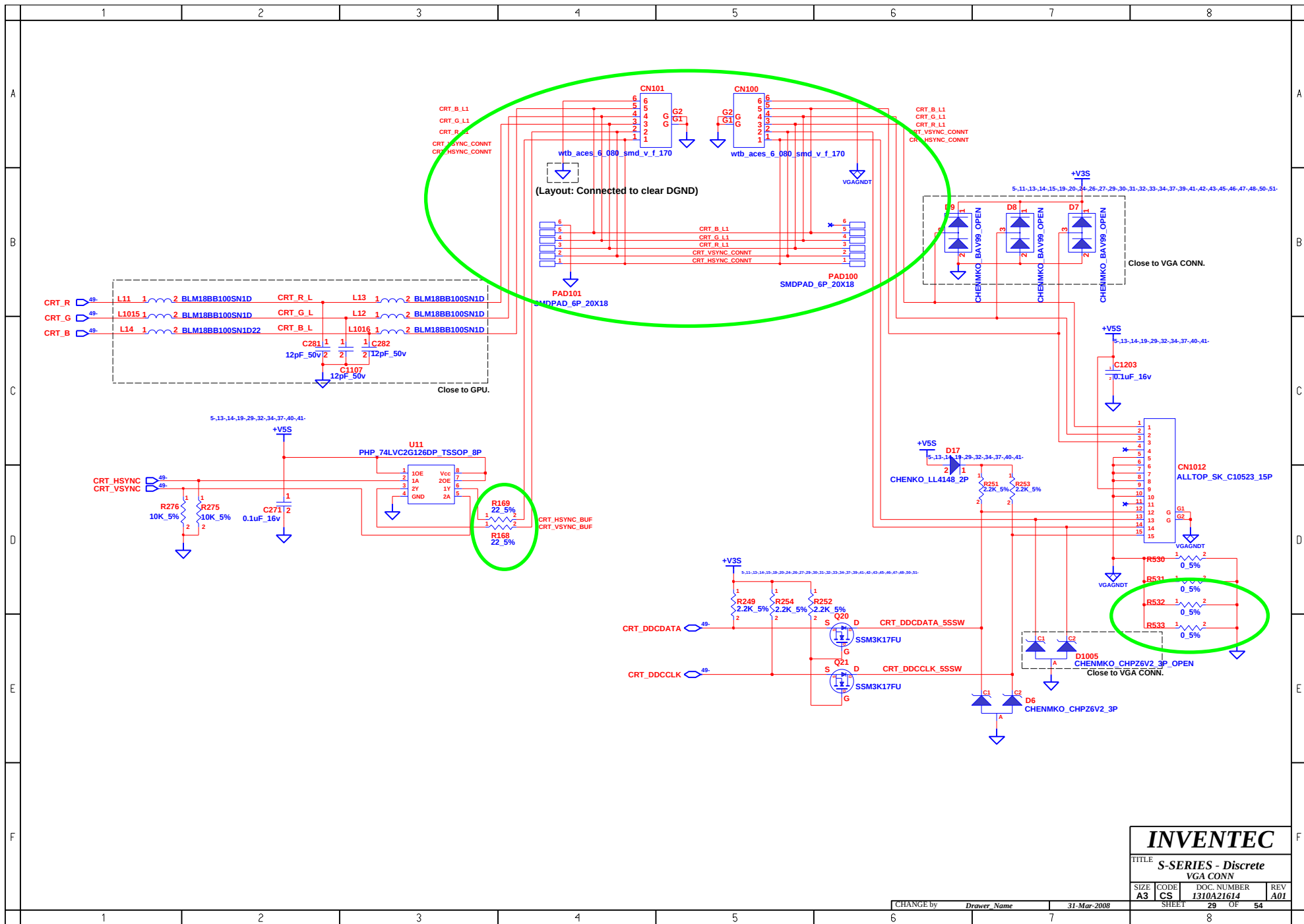
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A3	CS	1310A21614	AX4

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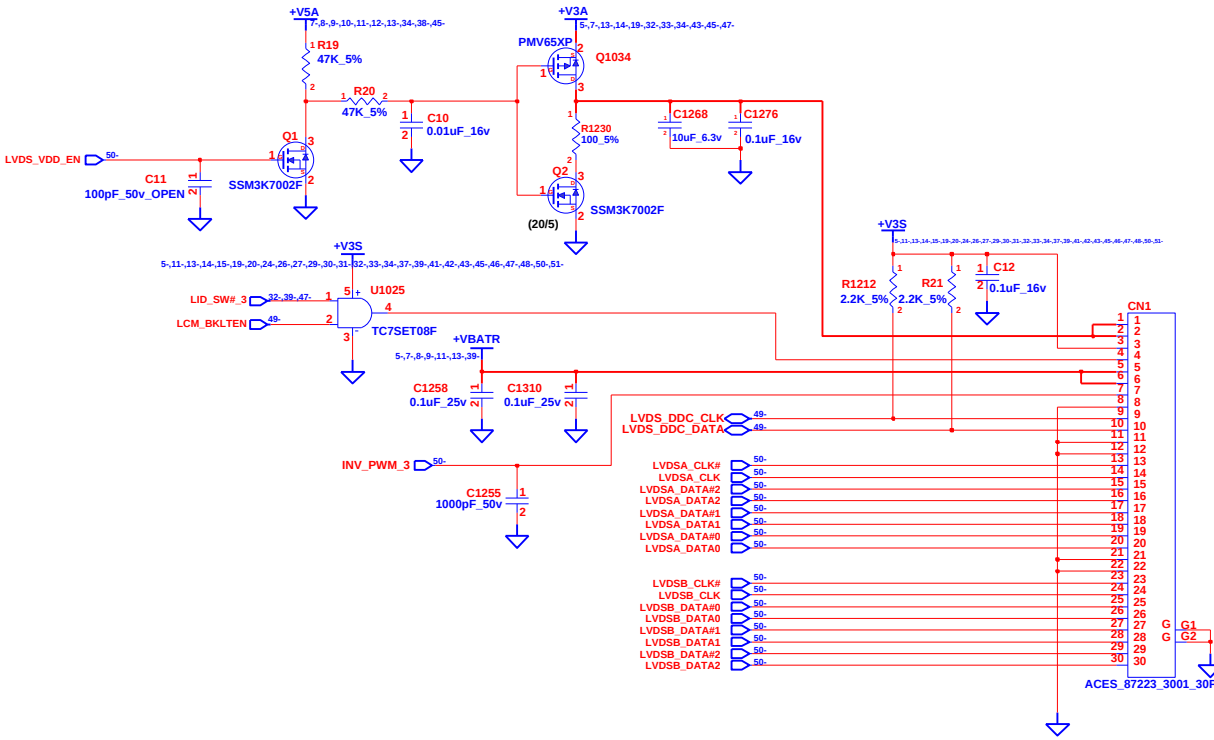
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Place closed to connector



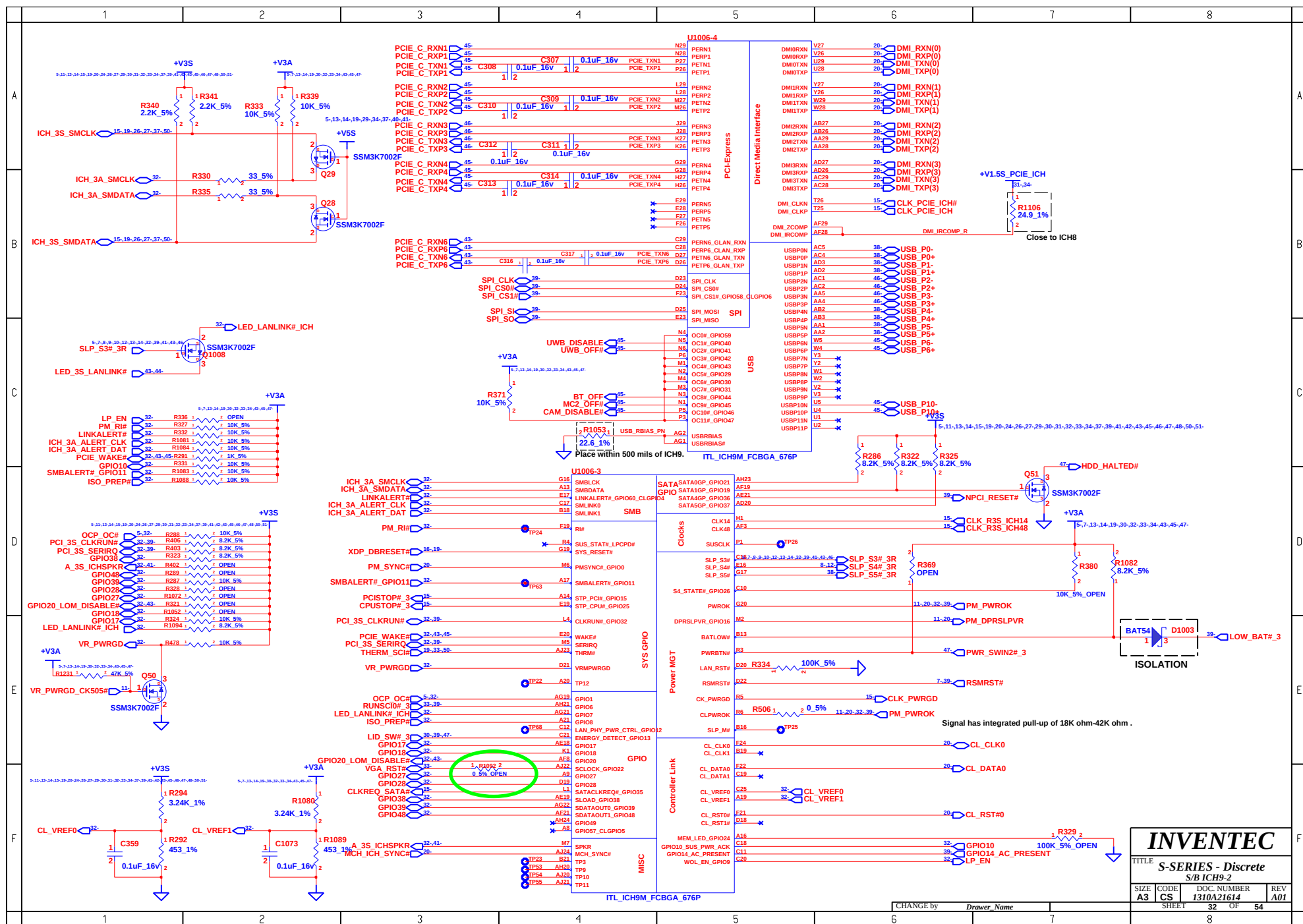
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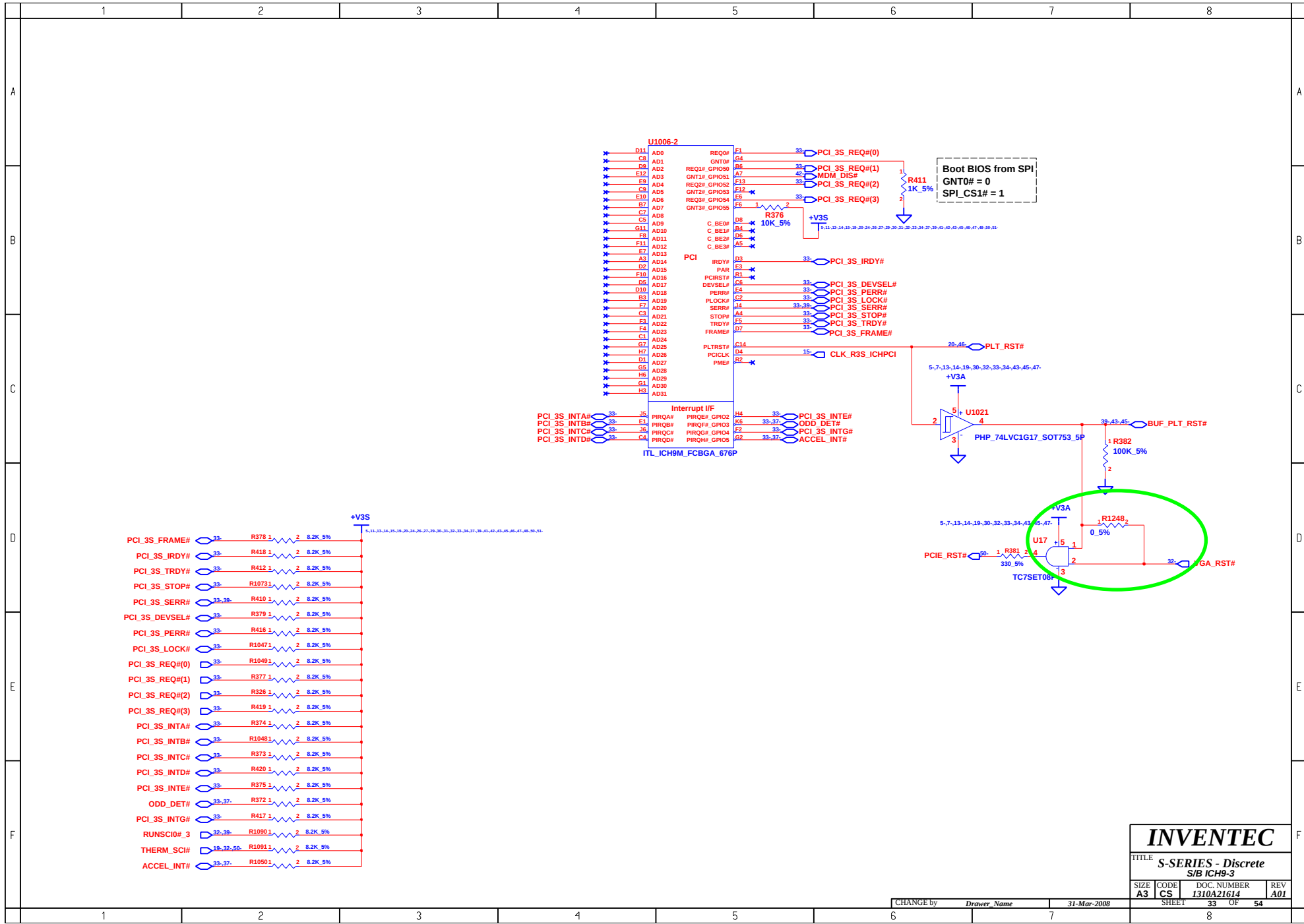
TITLE
S-SERIES - Discrete
LCM CONN

SIZE A3	CODE CS	DOC. NUMBER I310AZ1614	REV A01
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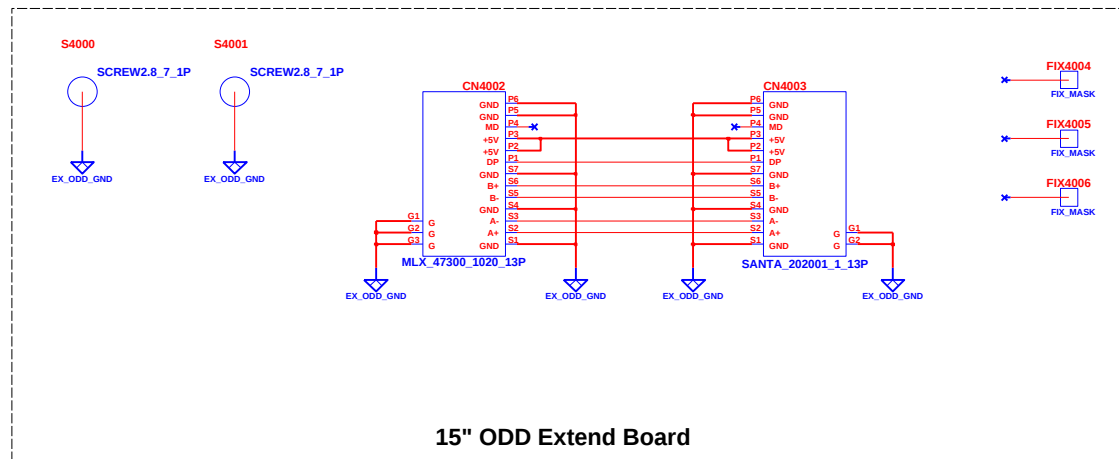


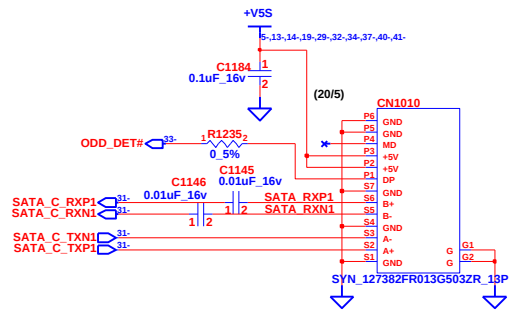
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TITLE S-SERIES - Discrete S/B ICH9-3			
SIZE A3	CODE CS	DOC. NUMBER 1310AZ1614	REV A01
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U1006-5

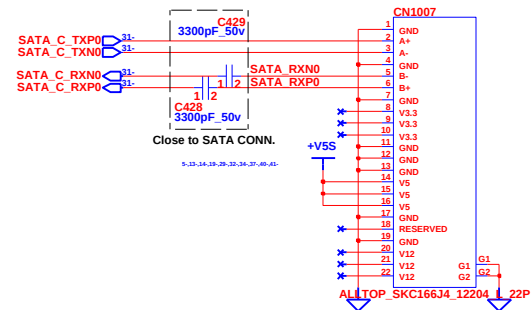
The diagram shows the pinout for the U1006-5 component, which is an ITL_ICH9M_FCBGA_676P. The component is represented by a central vertical line with pins extending outwards. The pins are labeled with their respective functions and values. The diagram is organized into columns labeled 1 through 8, corresponding to the pin numbers. The pins are connected to various components, including resistors (R), capacitors (C), and other integrated circuits (U). The connections are shown as red lines with dots at the connection points. The diagram is a detailed representation of the component's internal structure and its connections to the rest of the system.

Pin	Label	Value
1	AA26	VSS001
2	AA27	VSS002
3	AA3	VSS003
4	AA6	VSS004
5	AB1	VSS005
6	AB2	VSS006
7	AB3	VSS007
8	AB29	VSS008
9	AB4	VSS009
10	AB5	VSS010
11	AC17	VSS011
12	AC26	VSS012
13	AC27	VSS013
14	AC3	VSS014
15	AD1	VSS015
16	AD10	VSS016
17	AD12	VSS017
18	AD13	VSS018
19	AD14	VSS019
20	AD17	VSS020
21	AD18	VSS021
22	AD21	VSS022
23	AD28	VSS023
24	AD29	VSS024
25	AD4	VSS025
26	AD5	VSS026
27	AD6	VSS027
28	AD7	VSS028
29	AD9	VSS029
30	AE12	VSS030
31	AE13	VSS031
32	AE14	VSS032
33	AE16	VSS033
34	AE17	VSS034
35	AE2	VSS035
36	AE20	VSS036
37	AE24	VSS037
38	AE3	VSS038
39	AE4	VSS039
40	AE6	VSS040
41	AE9	VSS041
42	AF13	VSS042
43	AF16	VSS043
44	AF18	VSS044
45	AF22	VSS045
46	AH26	VSS046
47	AF26	VSS047
48	AF27	VSS048
49	AF5	VSS049
50	AF7	VSS050
51	AF9	VSS051
52	AG11	VSS052
53	AG16	VSS053
54	AG18	VSS054
55	AG20	VSS055
56	AG23	VSS056
57	AG3	VSS057
58	AG6	VSS058
59	AG9	VSS059
60	AH12	VSS060
61	AH14	VSS061
62	AH17	VSS062
63	AH18	VSS063
64	AH2	VSS064
65	AH22	VSS065
66	AH29	VSS066
67	AH28	VSS067
68	AH5	VSS068
69	AH8	VSS069
70	AJ12	VSS070
71	AJ14	VSS071
72	AJ17	VSS072
73	AJ8	VSS073
74	B11	VSS074
75	B14	VSS075
76	B17	VSS076
77	B2	VSS077
78	B20	VSS078
79	B23	VSS079
80	B5	VSS080
81	B8	VSS081
82	C26	VSS082
83	C27	VSS083
84	E11	VSS084
85	E14	VSS085
86	E18	VSS086
87	E2	VSS087
88	E21	VSS088
89	E24	VSS089
90	E3	VSS090
91	E8	VSS091
92	F16	VSS092
93	F28	VSS093
94	F29	VSS094
95	G12	VSS095
96	G14	VSS096
97	G18	VSS097
98	G21	VSS098
99	G24	VSS099
100	G26	VSS100
101	G27	VSS101
102	G8	VSS102
103	H2	VSS103
104	H23	VSS104
105	H28	VSS105
106	H29	VSS106
107	H5	VSS107
108	J23	VSS108
109	J26	VSS109
110	J27	VSS110
111	AC22	VSS111
112	K28	VSS112
113	K29	VSS113
114	L13	VSS114
115	L15	VSS115
116	L2	VSS116
117	L26	VSS117
118	L27	VSS118
119	L5	VSS119
120	L7	VSS120
121	M12	VSS121
122	M13	VSS122
123	M14	VSS123
124	M15	VSS124
125	M16	VSS125
126	M17	VSS126
127	M23	VSS127
128	M28	VSS128
129	M29	VSS129
130	N11	VSS130
131	N12	VSS131
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134	N15	VSS134
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139	P27	VSS139
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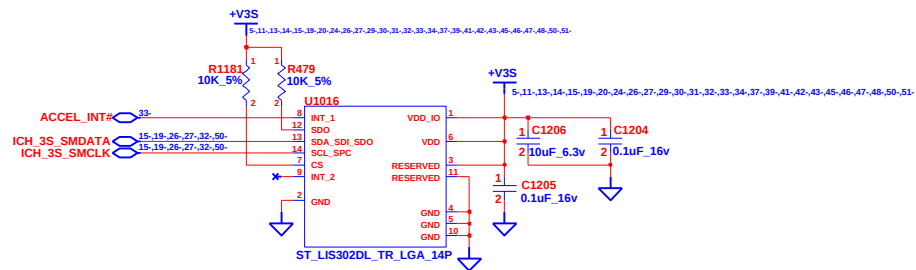




SATA ODD



SATA HDD

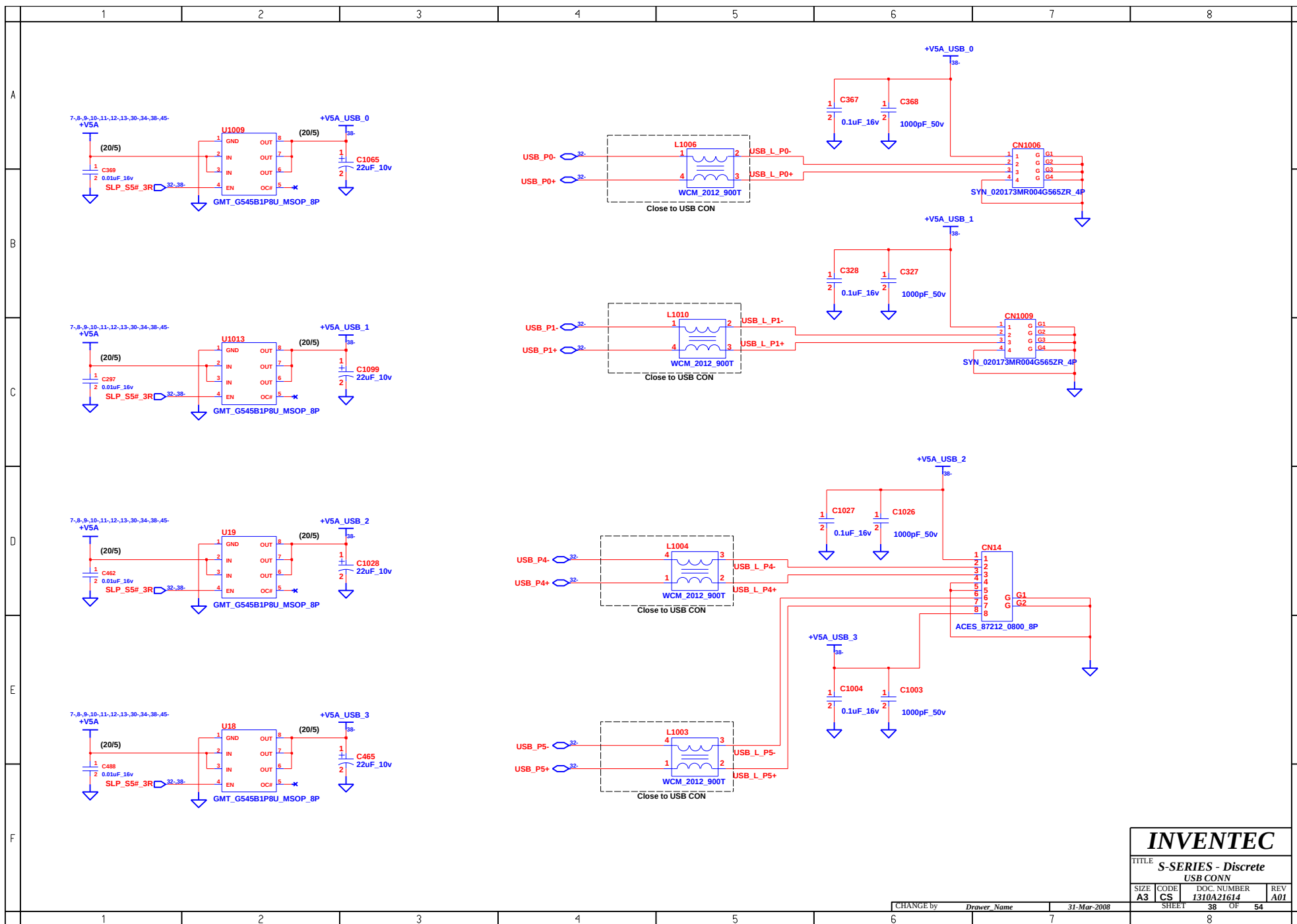


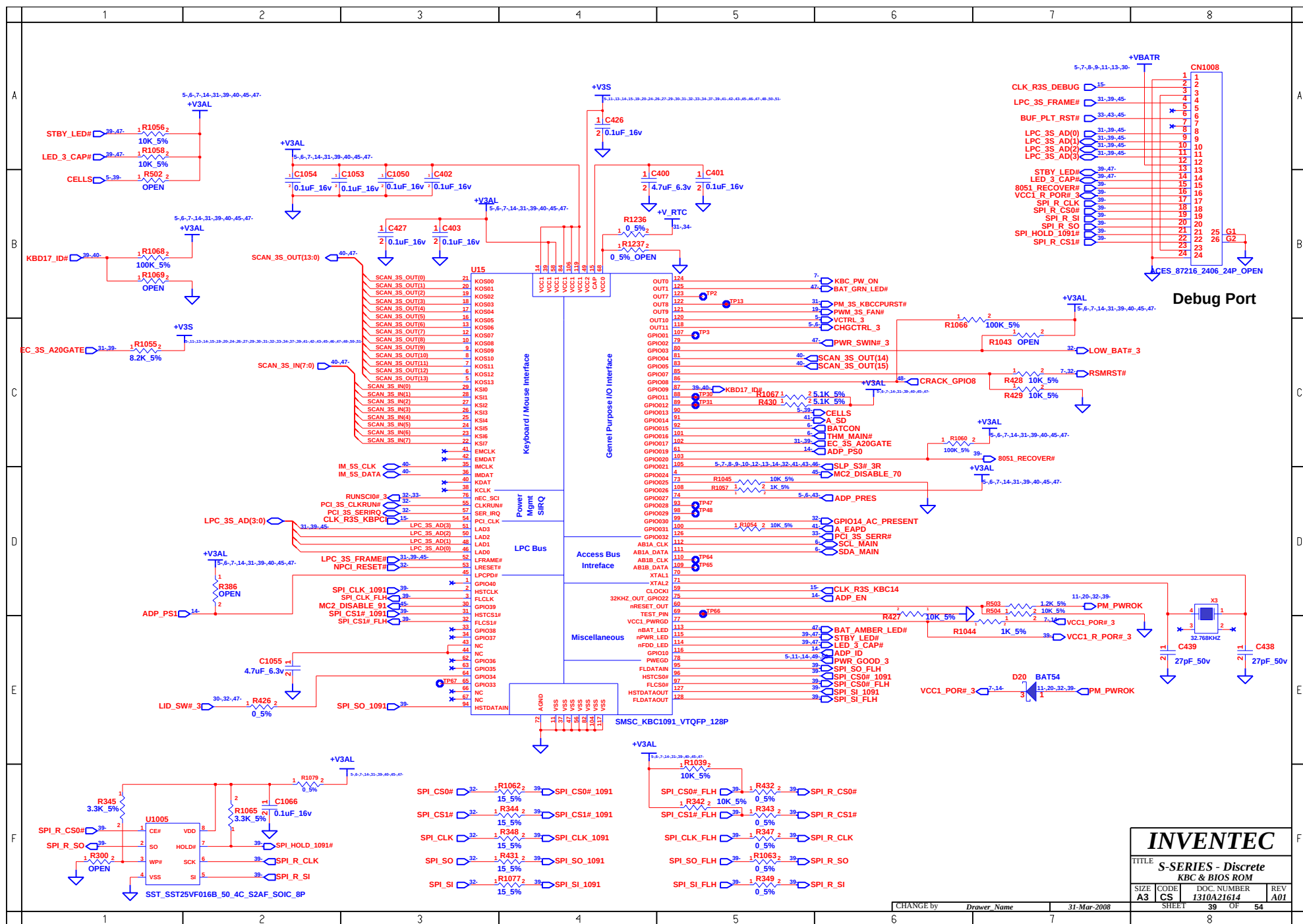
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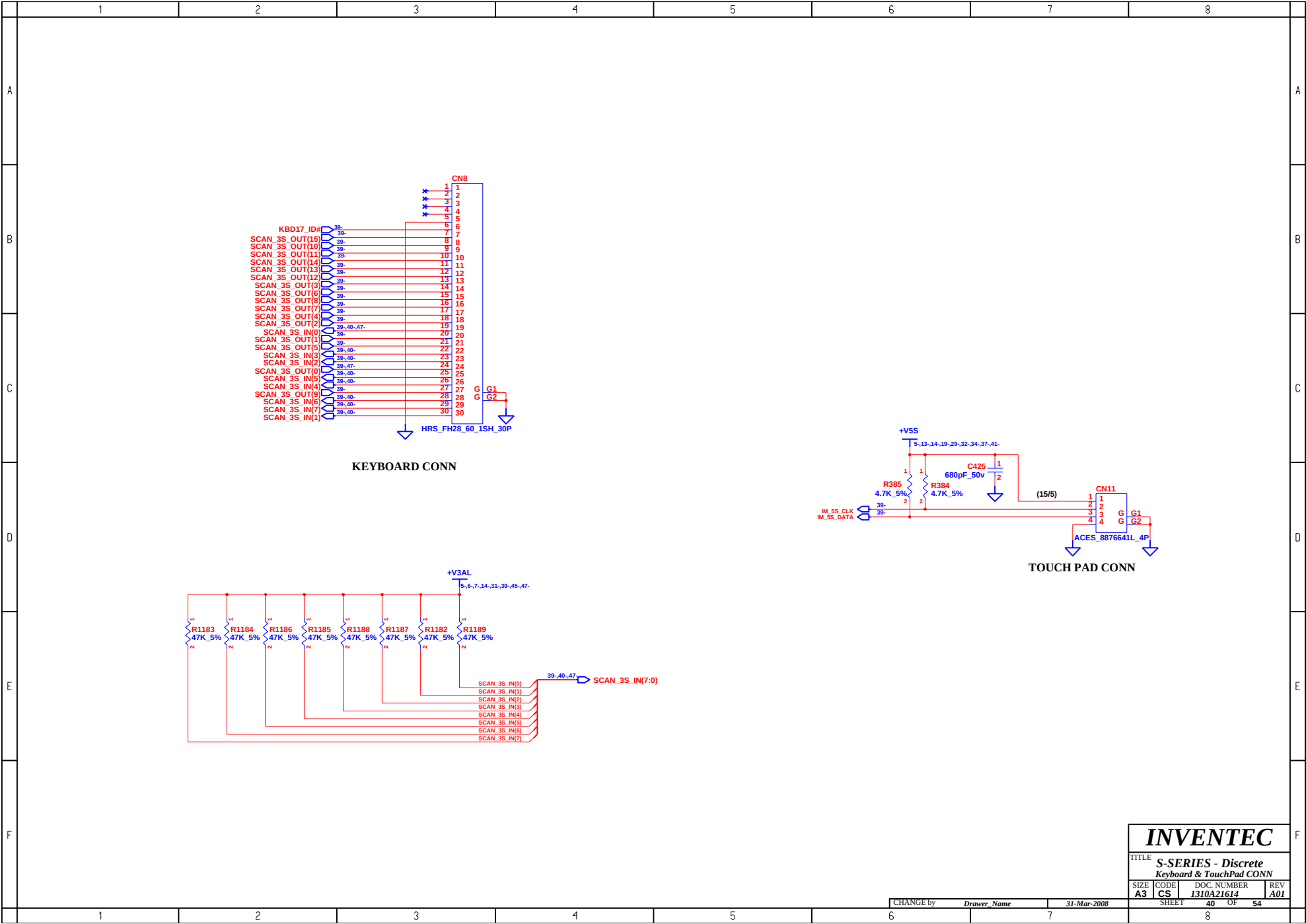
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TITLE			
S-SERIES - Discrete			
HDD & ODD CONN & Accelerometer			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A21614	A01

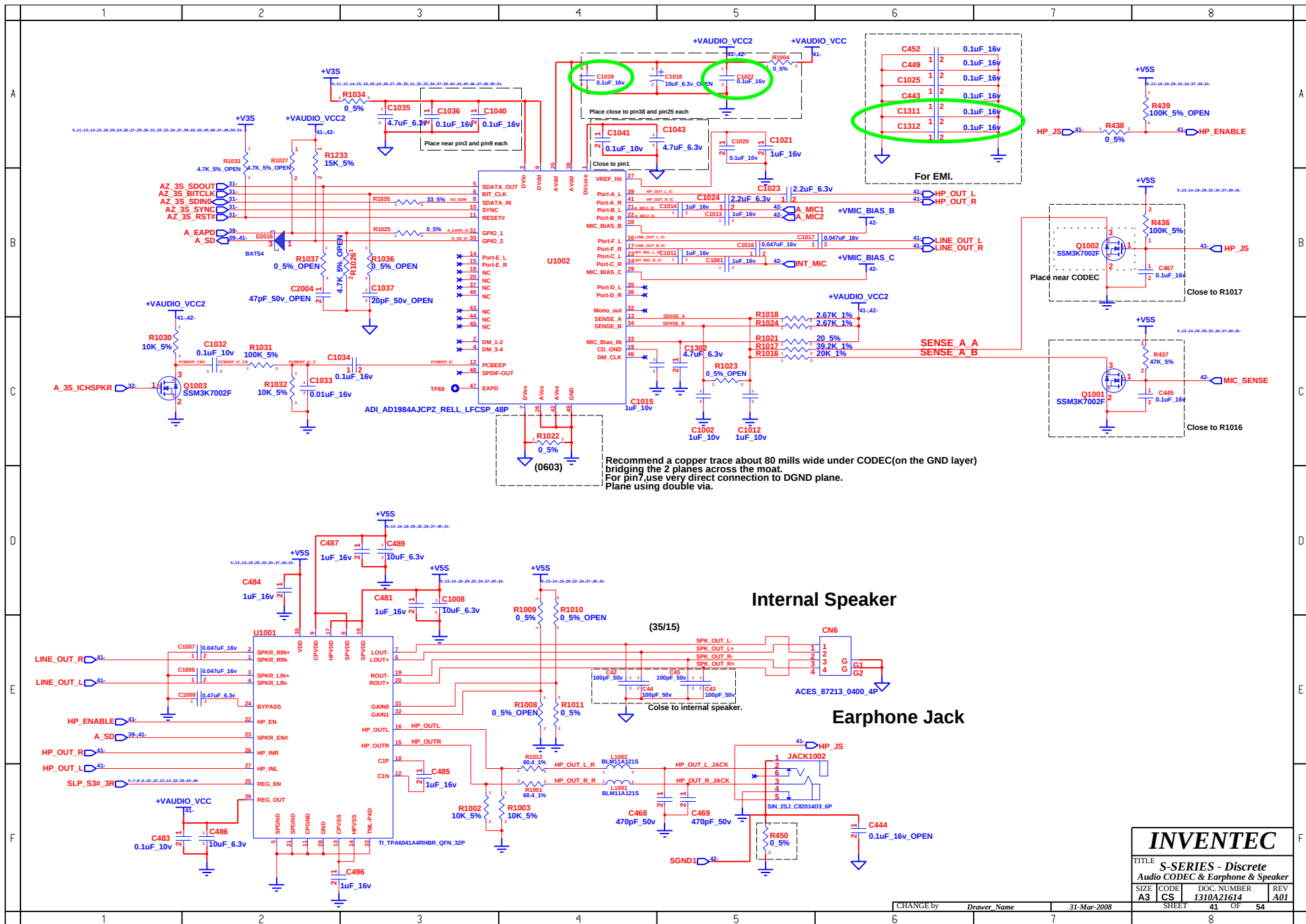
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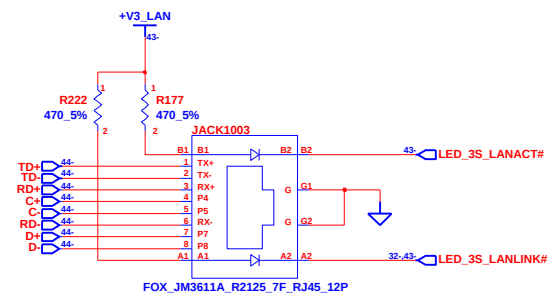
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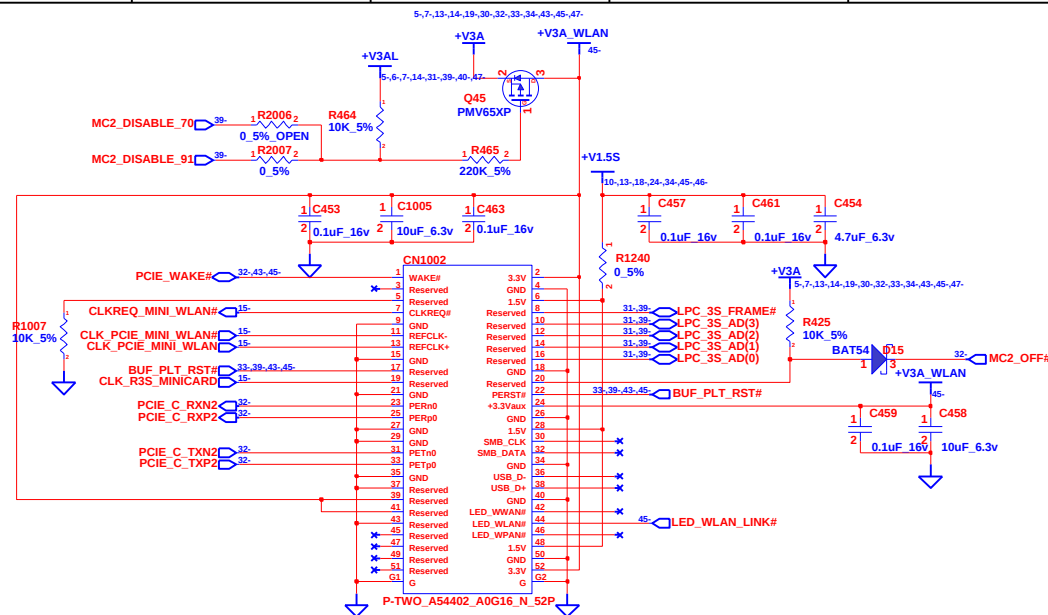


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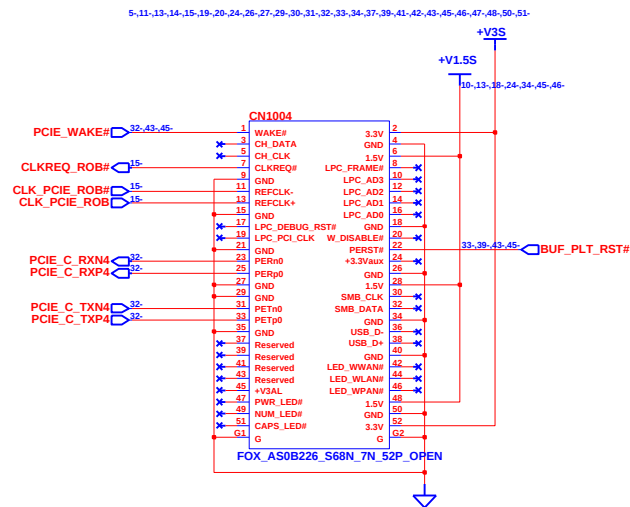
TITLE	<i>S-SERIES - Discrete</i> Magnetic & RJ45 CONN
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SIZE A3	CODE CS	DOC. NUMBER 1310A21614	REV A01
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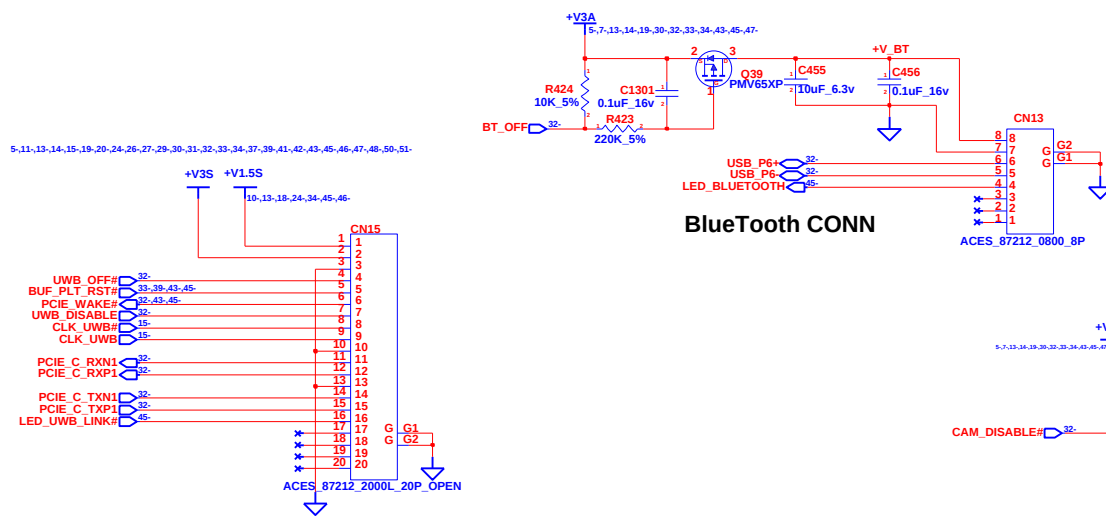
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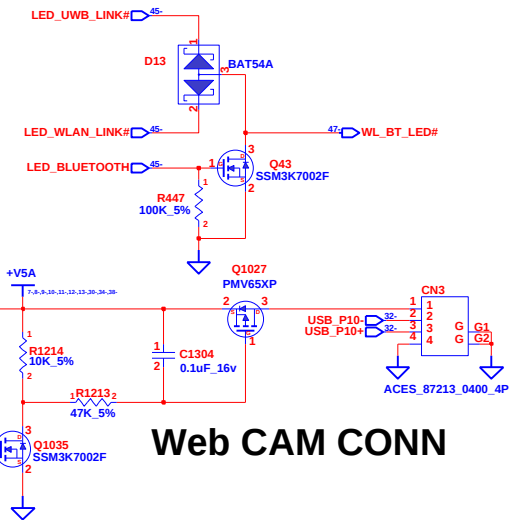
Mini Card-WLAN



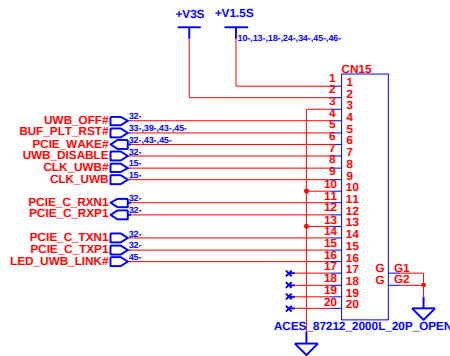
MINI Card-Robson



BlueTooth CONN



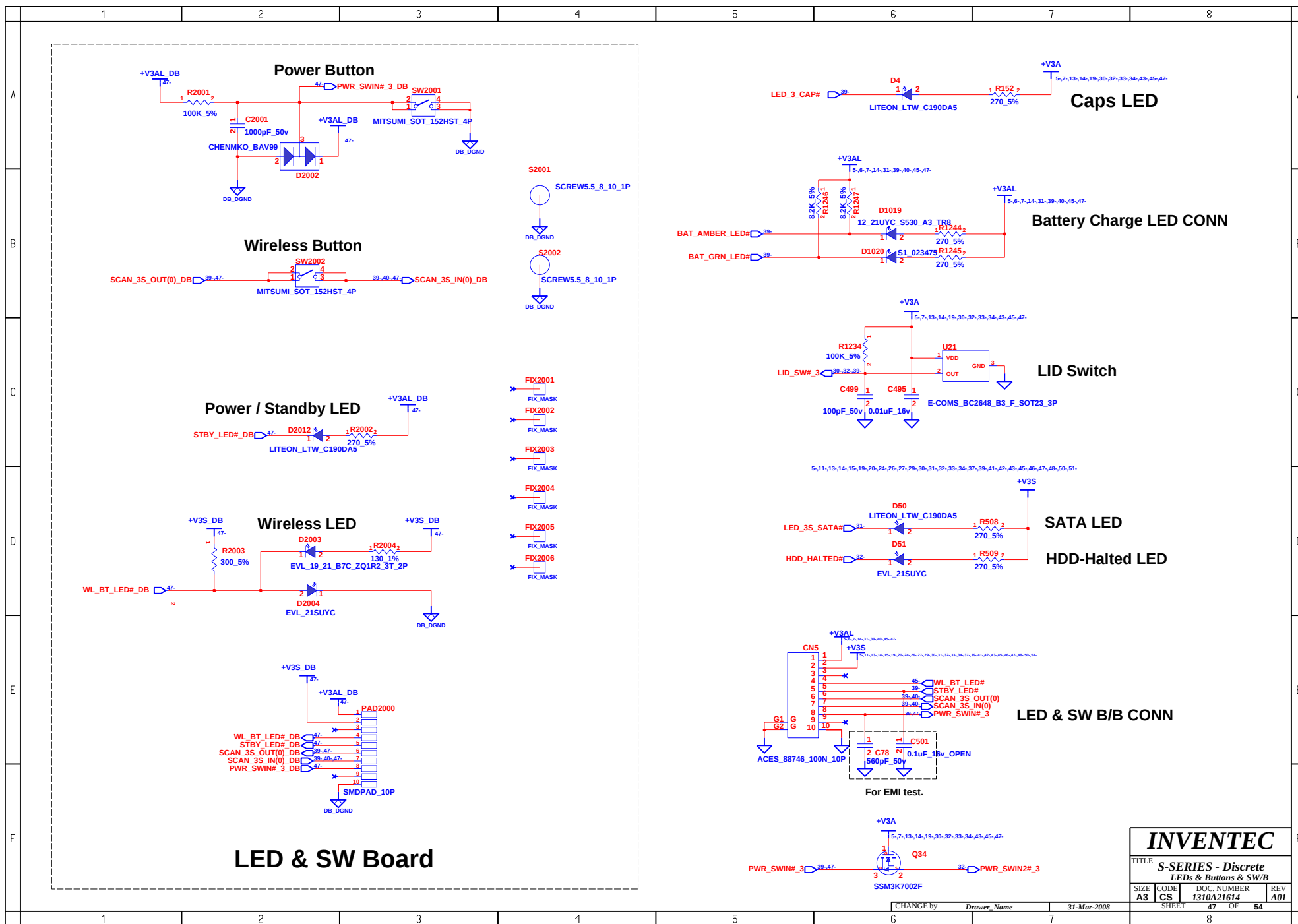
Web CAM CONN



UWB

INVENTEC			
TITLE S-SERIES - Discrete			
Mini Card(WLAN/Robson)/BT/UWB/Web-CAM			
SIZE A3	CODE CS	DOC. NUMBER 1310A21614	REV A01
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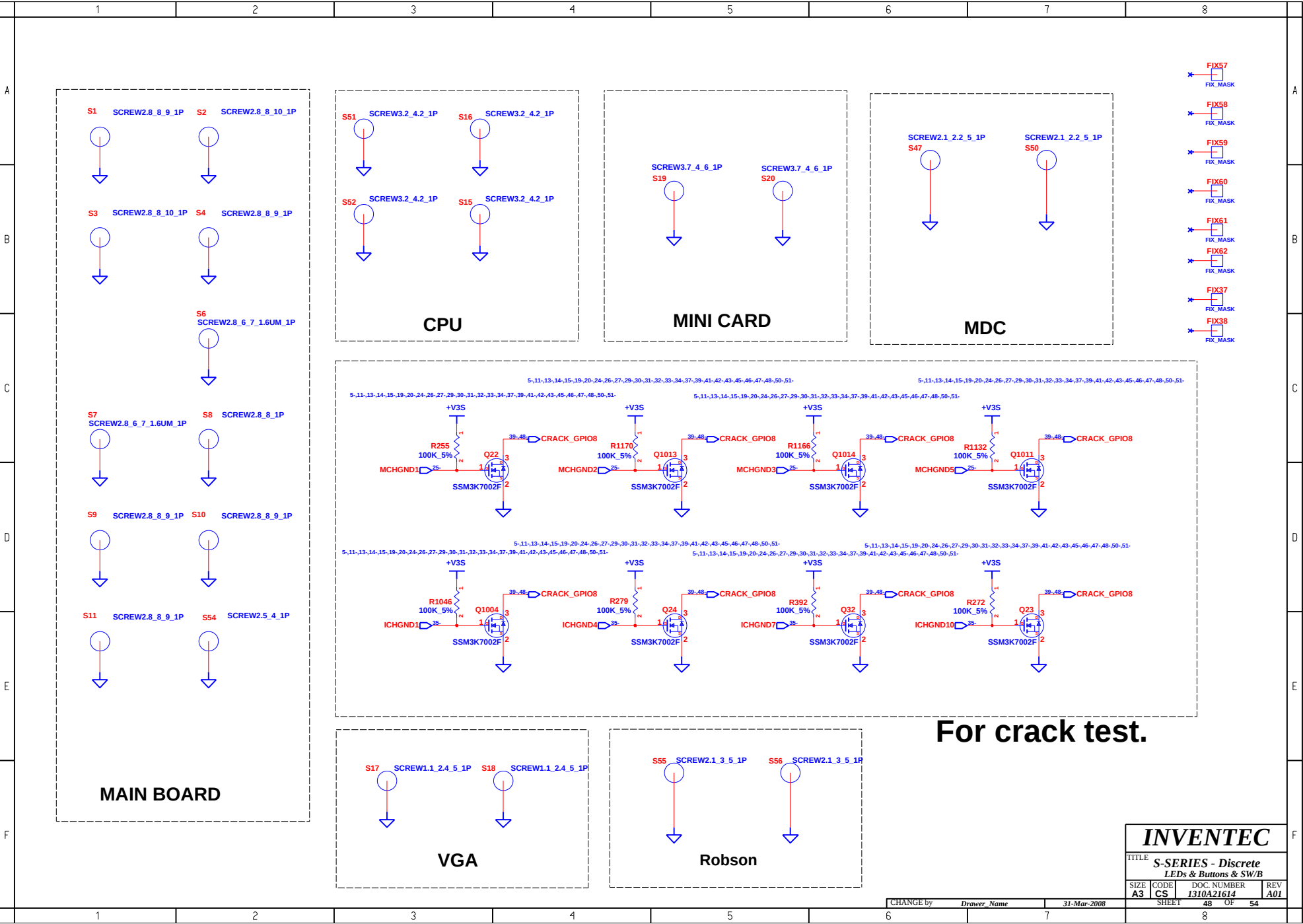
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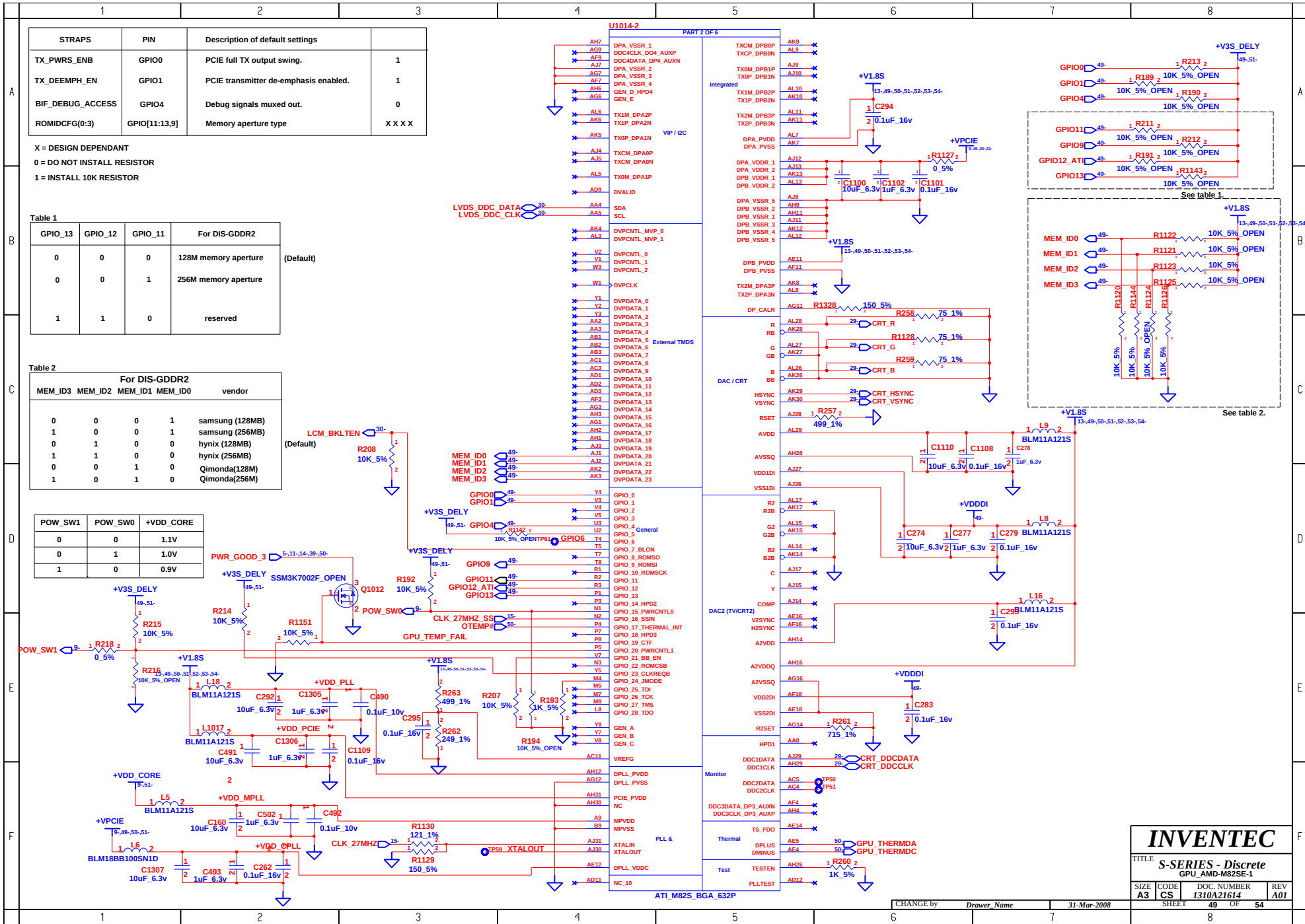
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S-SERIES - Discrete
LEDs & Buttons & SW/B

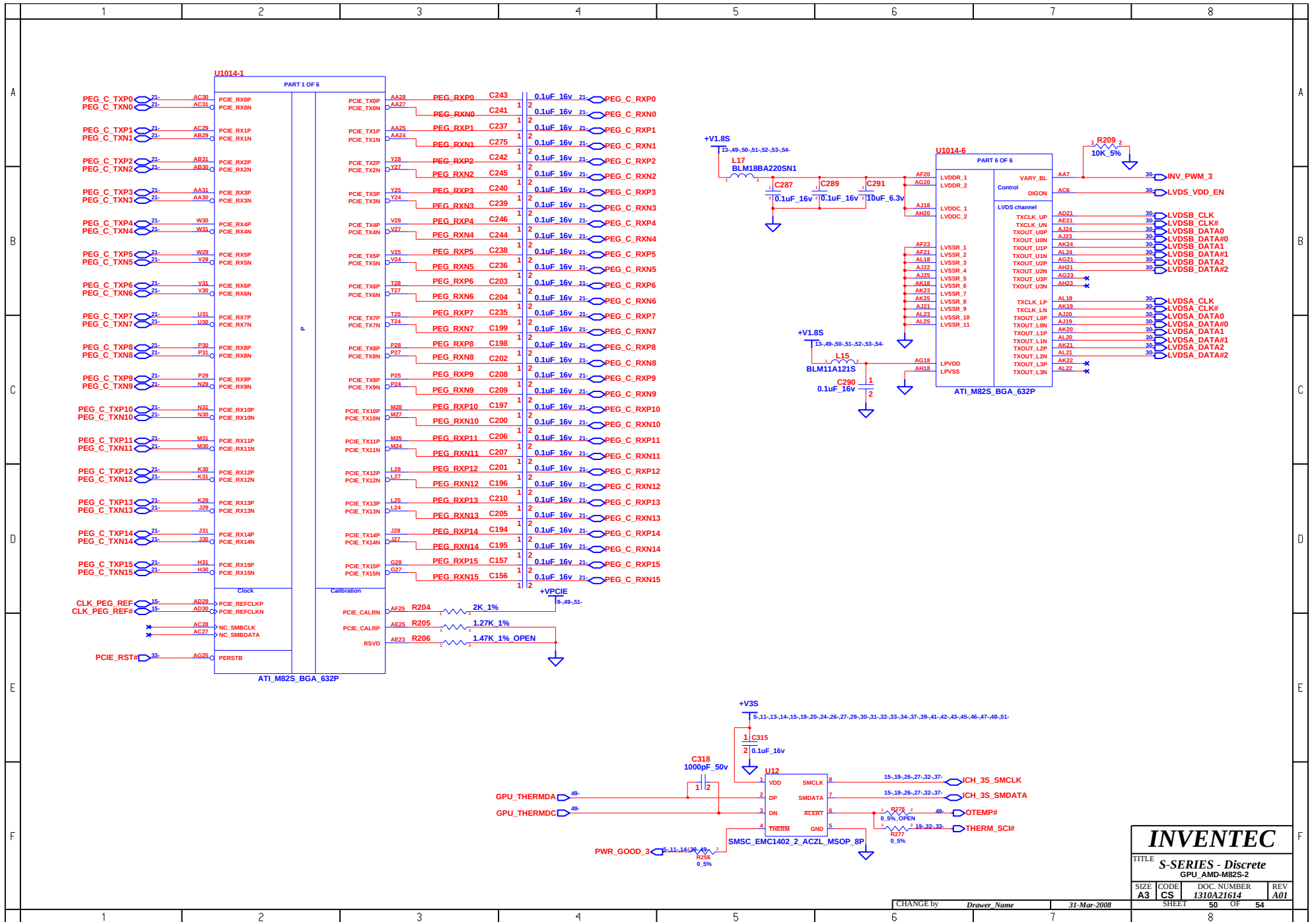
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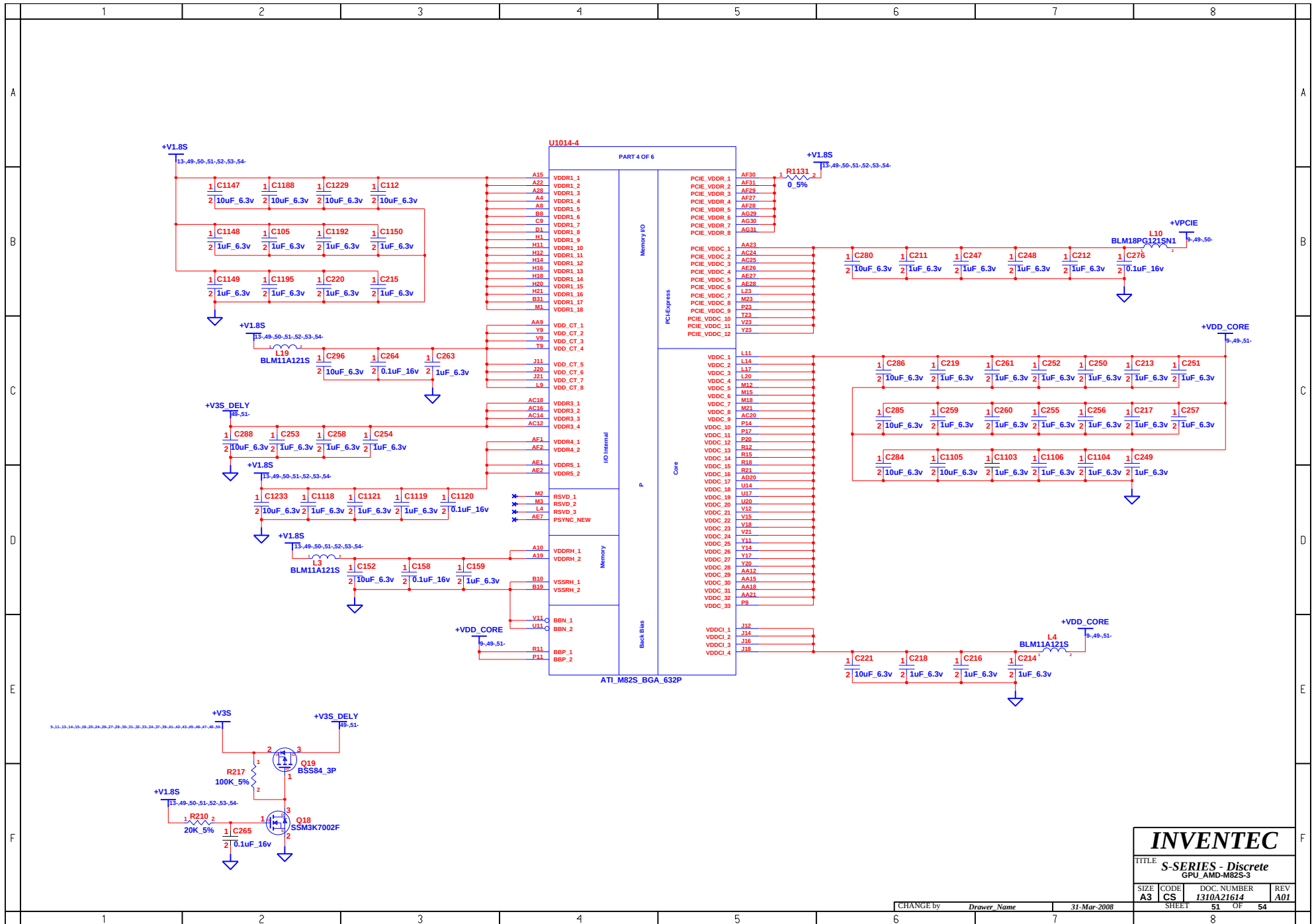
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TITLE
S-SERIES - Discrete
GPU_AMD-M82S-2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A21614	A01

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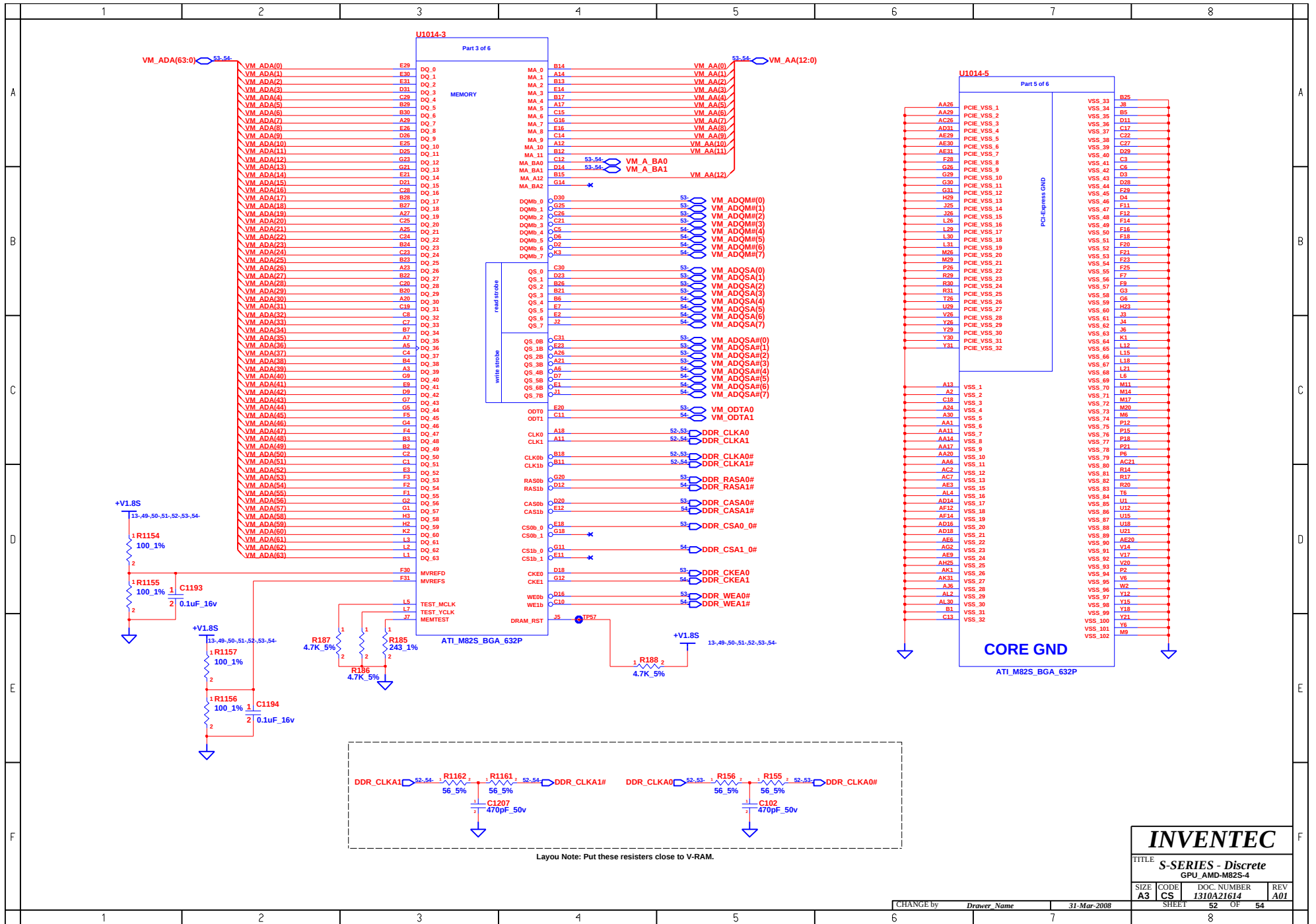
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TITLE
S-SERIES - Discrete
GPU_AMD-M82S-3

SIZE CODE CS DOC. NUMBER I310A21614 REV A01

CHANGE by *Drawn Name* 31-Mar-2008

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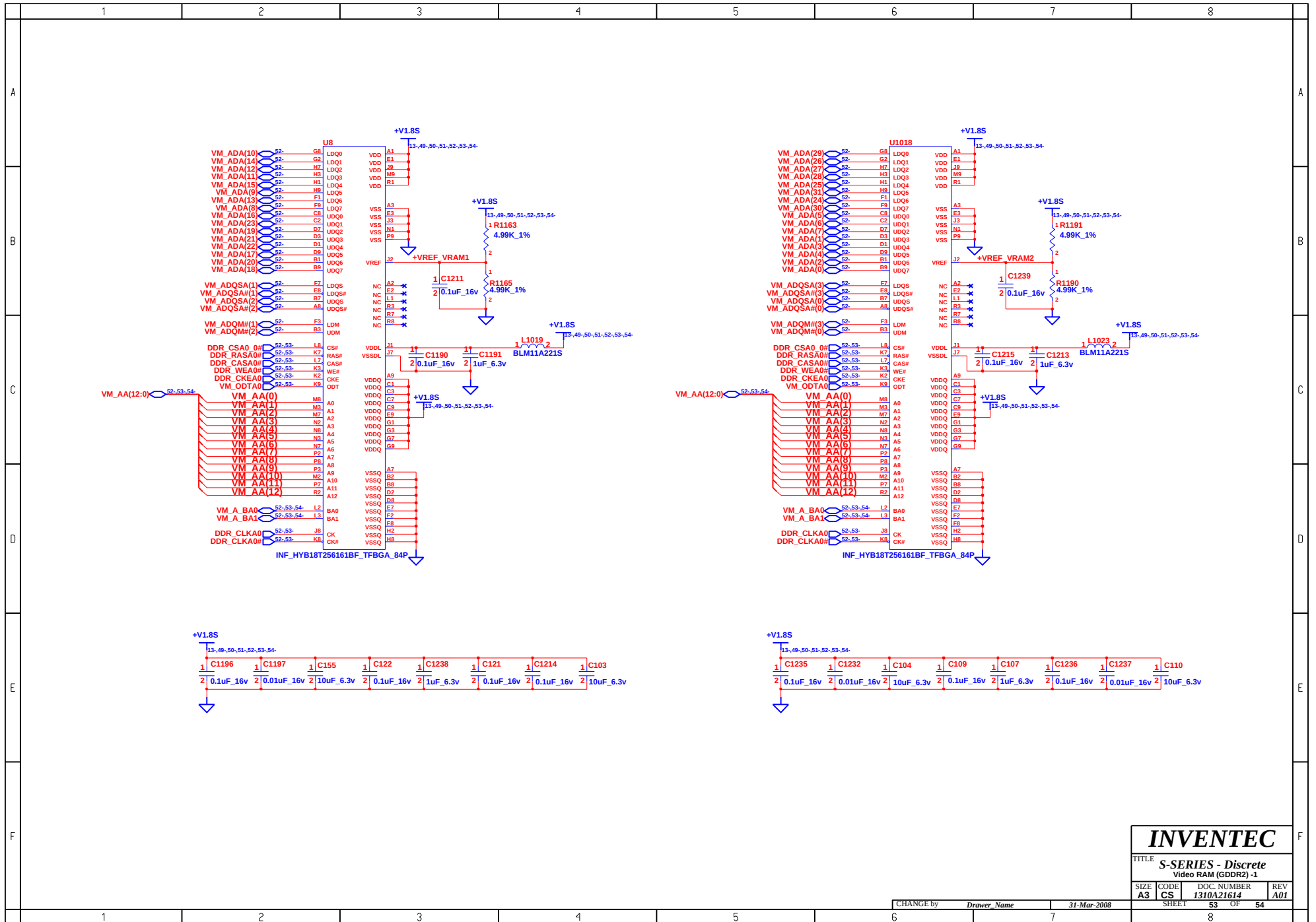
INVENTEC

TITLE
S-SERIES - Discrete
GPU_AMD-M82S-4

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A21614	A01

CHANGE by Draver Name 31-Mar-2008

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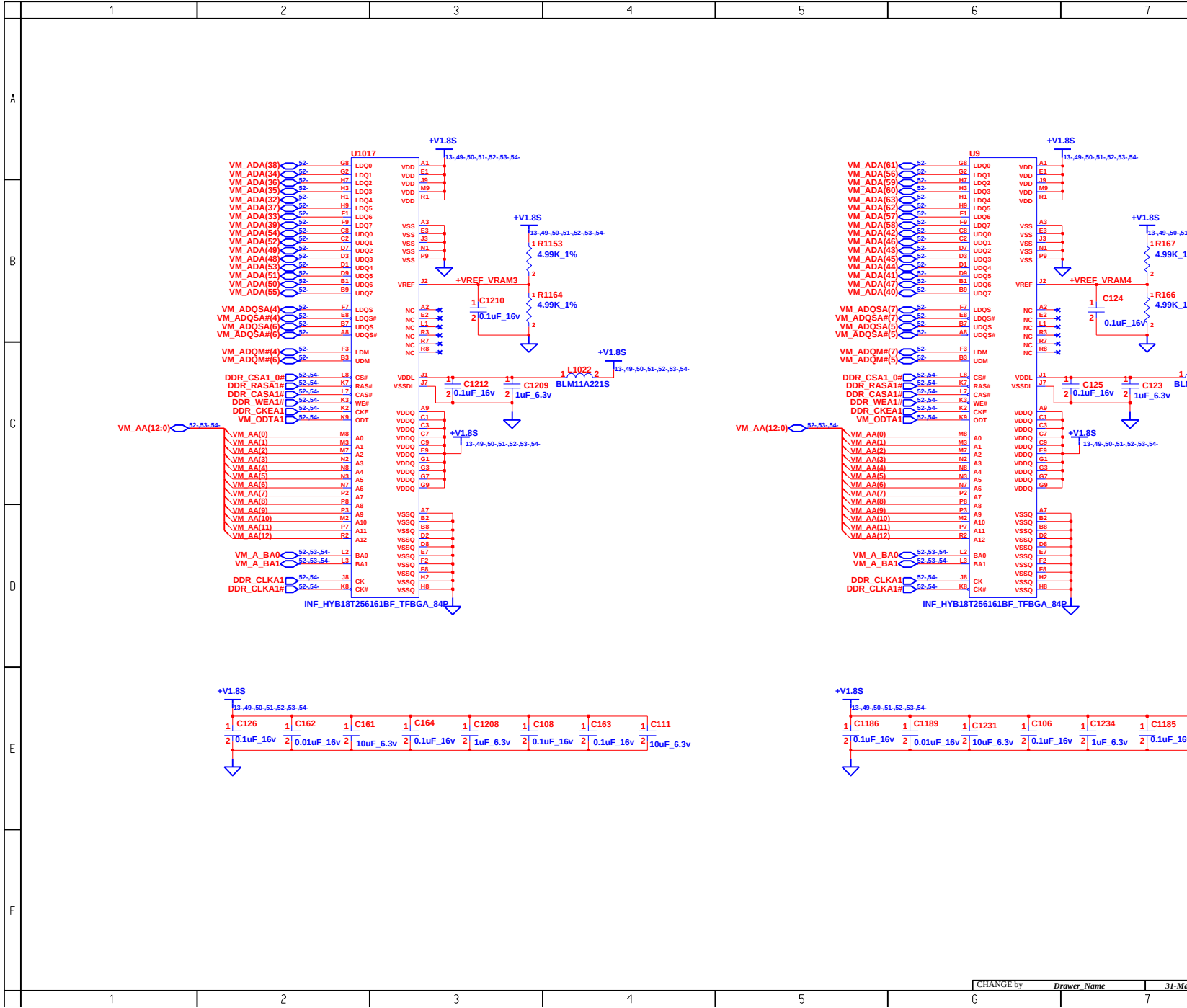
INVENTEC

TITLE
S-SERIES - Discrete
 Video RAM (GDDR2) -1

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A21614	A01

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