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HSF Property:ROHS or Halogen-Free(5L3?)

LEPUS

SODIMM_EDP

2011.10.13

LEPUS MB P/N:6050A2501101

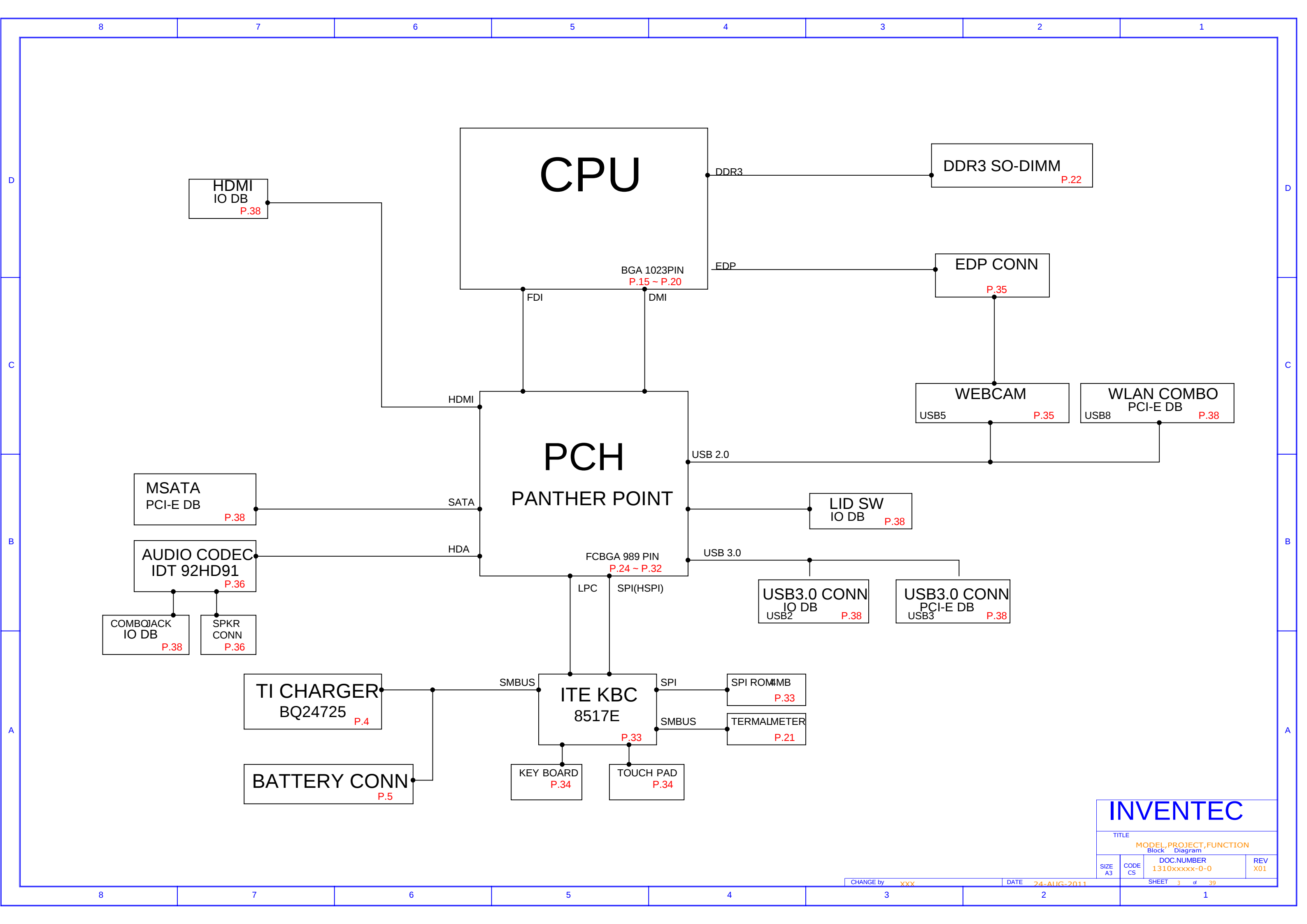
STAND OFF:CPU S4501 ,S4502,S4503

STAND OFF:6052B0205101

24-AUG-2011		
DATE	CHANGE NO.	REV

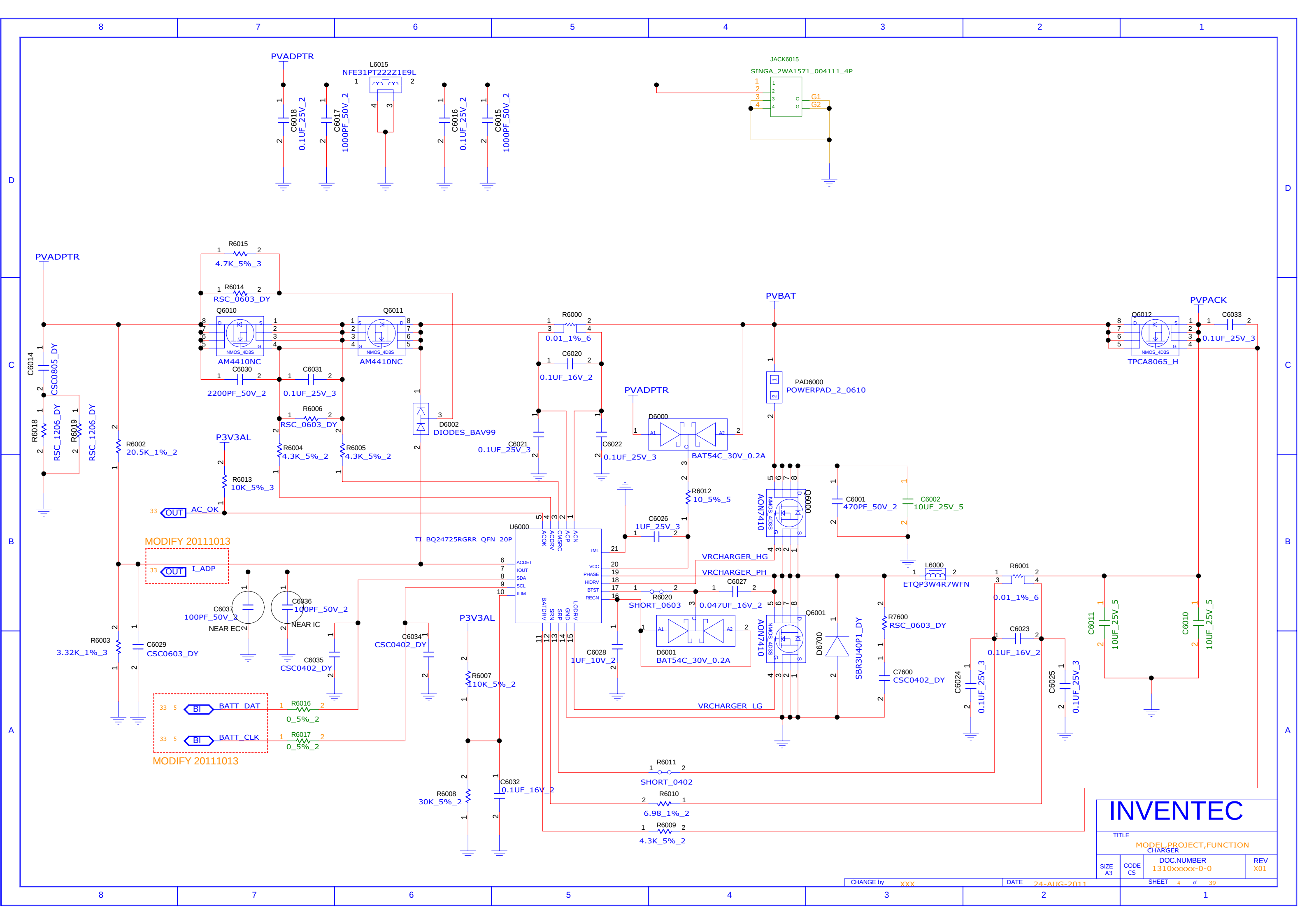
	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					TITLE MODEL,PROJECT,FUNCTION LEPUS			
DESIGN								
CHECK								
RESPONSIBLE								
SIZE=				VER:	SIZE C	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
FILE NAME:								
PIN	XXX				SHEET 1 of 39			

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23		XDP													
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25		PCH-2													



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

CHARGER

SIZE

A3

CODE

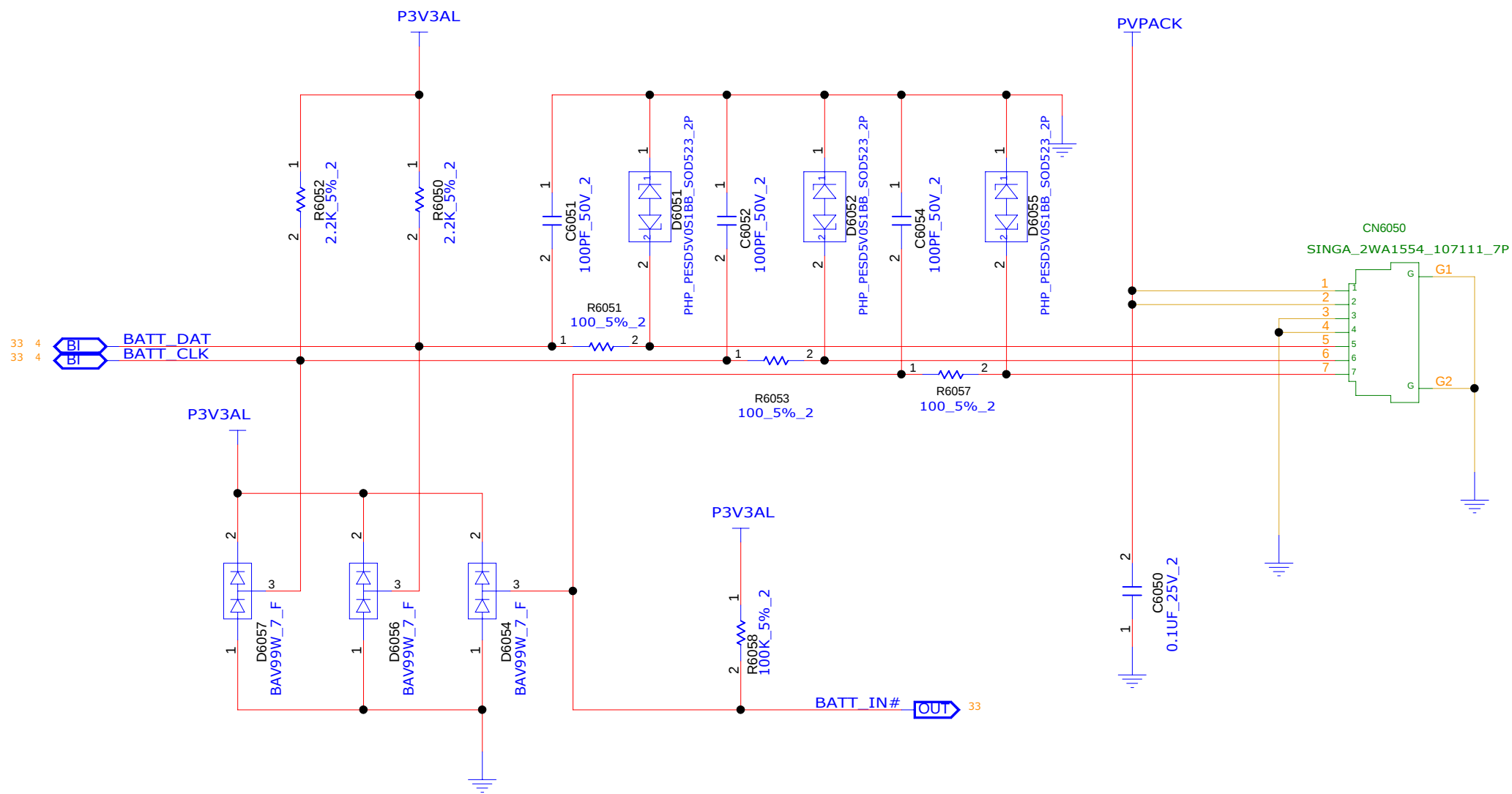
CS

DOC.NUMBER

1310xxxx-0-0

REV

X01



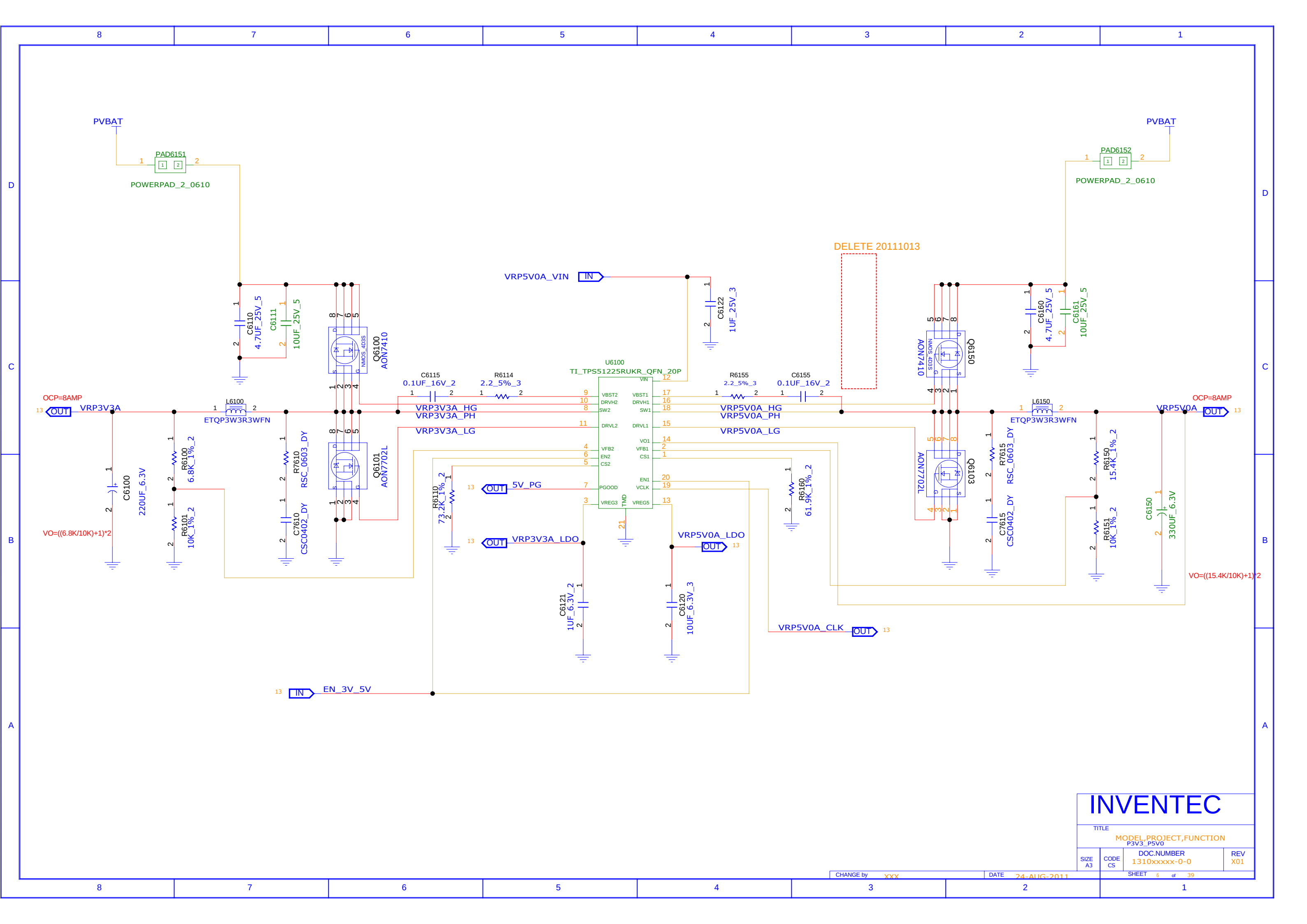
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
SELECTOR

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

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INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
P3V3_P5V0

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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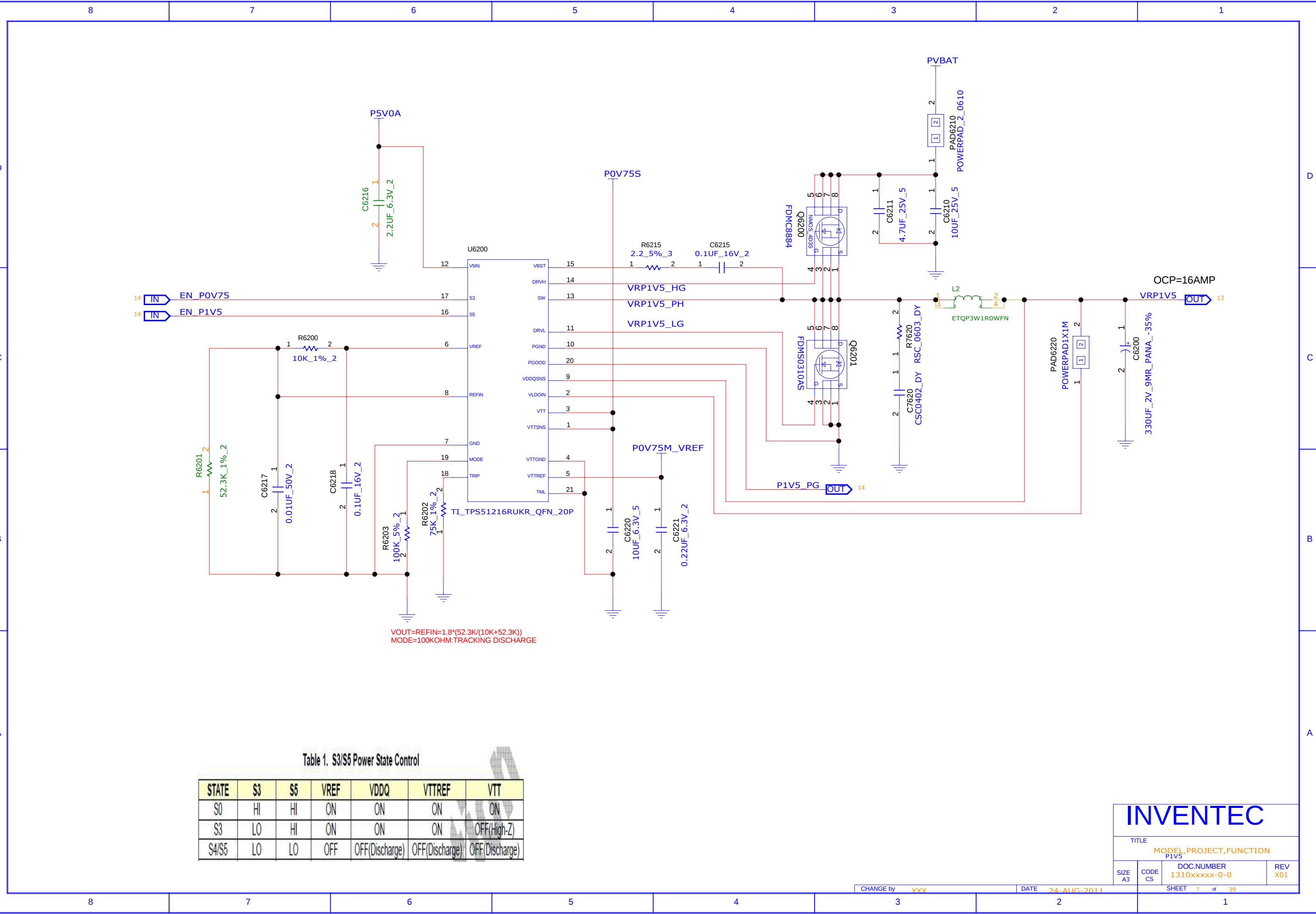


Table 1. S3/S5 Power State Control

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

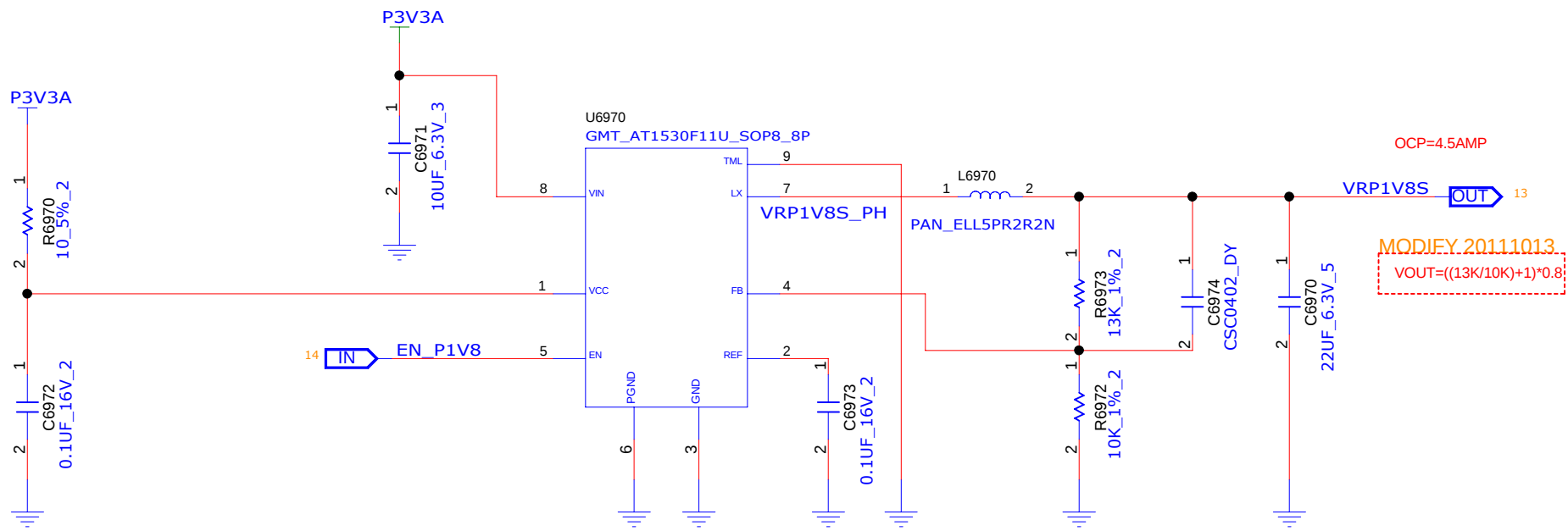
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
P1V5

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

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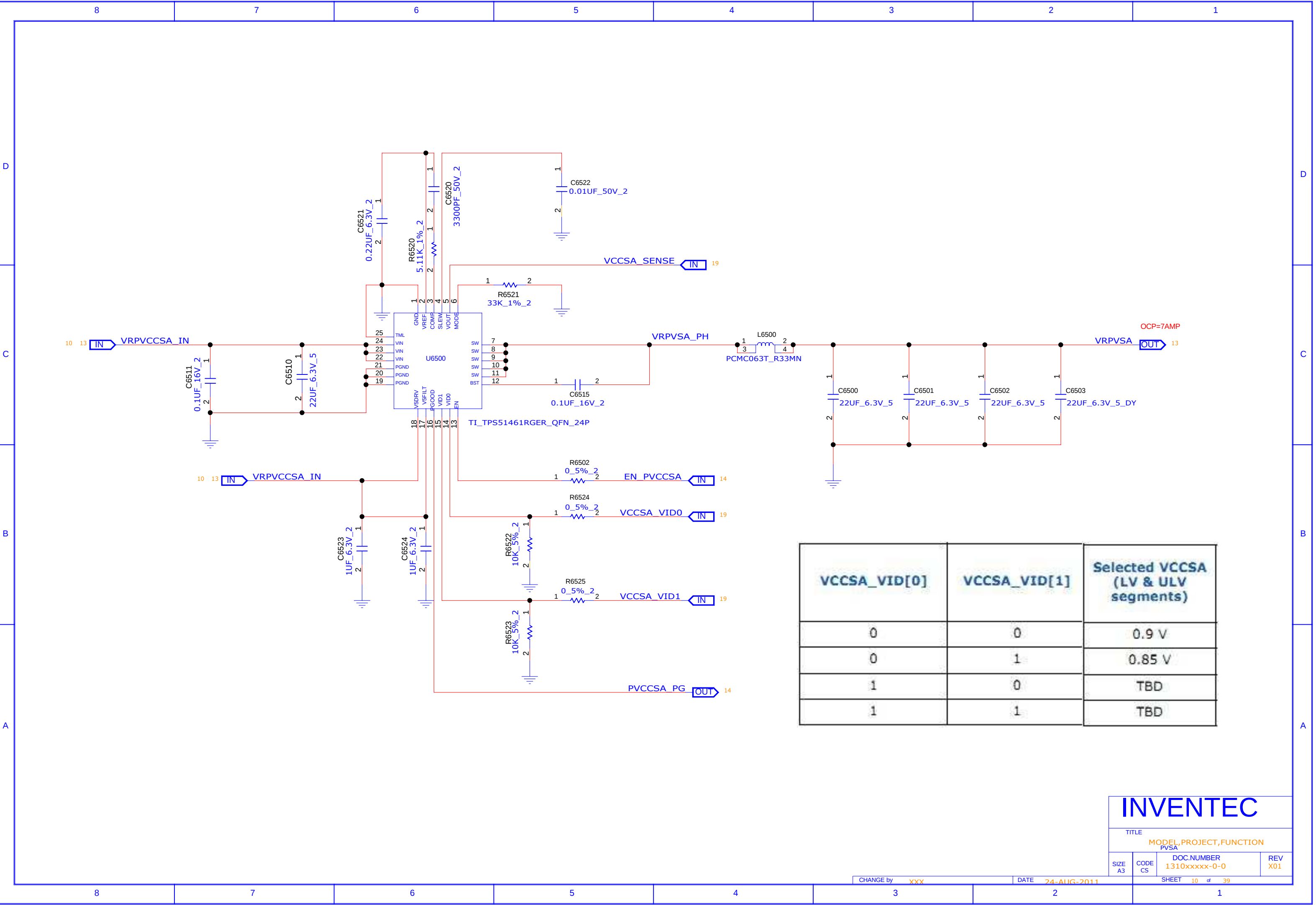


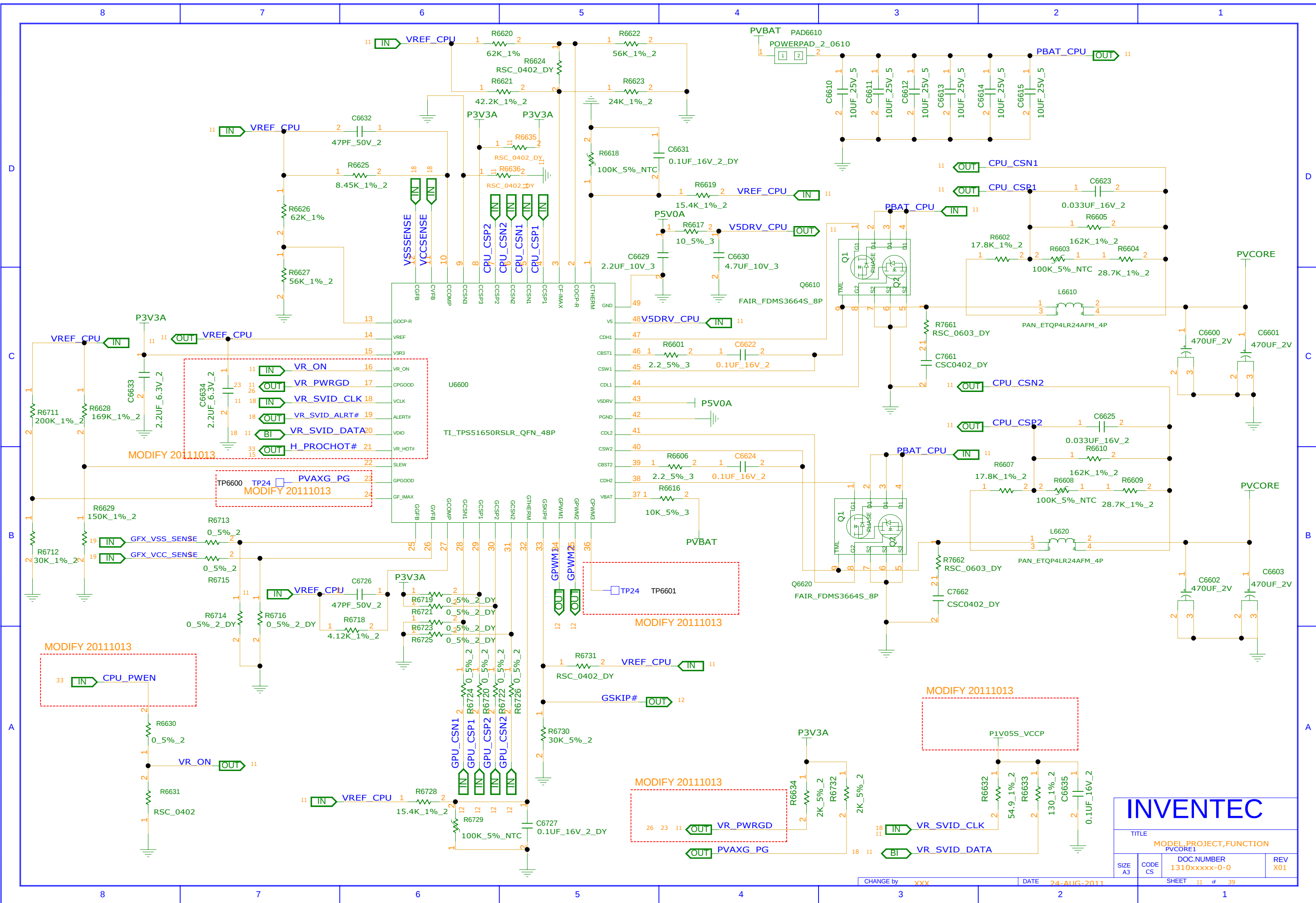
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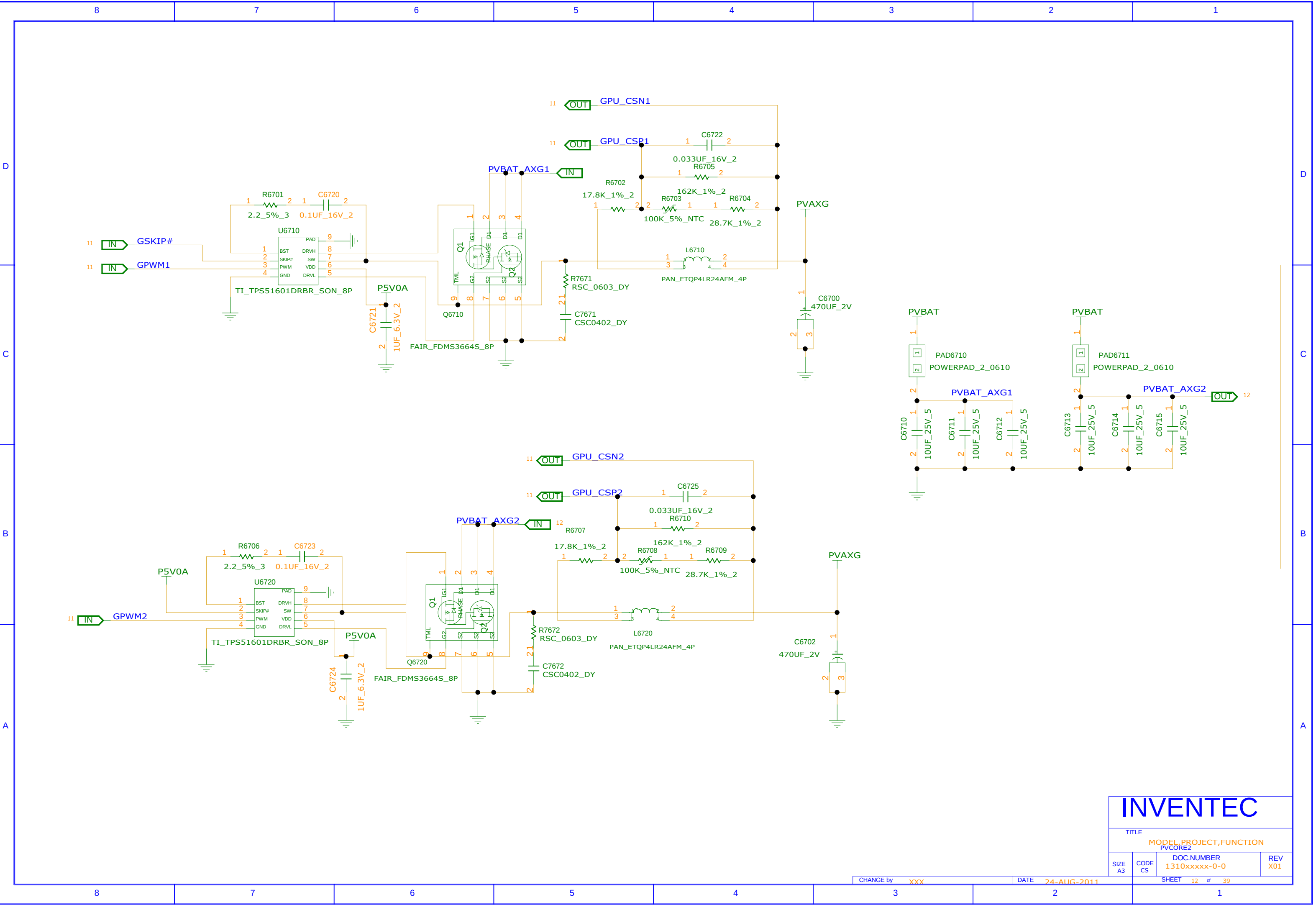
TITLE
MODEL_PROJECT,FUNCTION
P1V8S

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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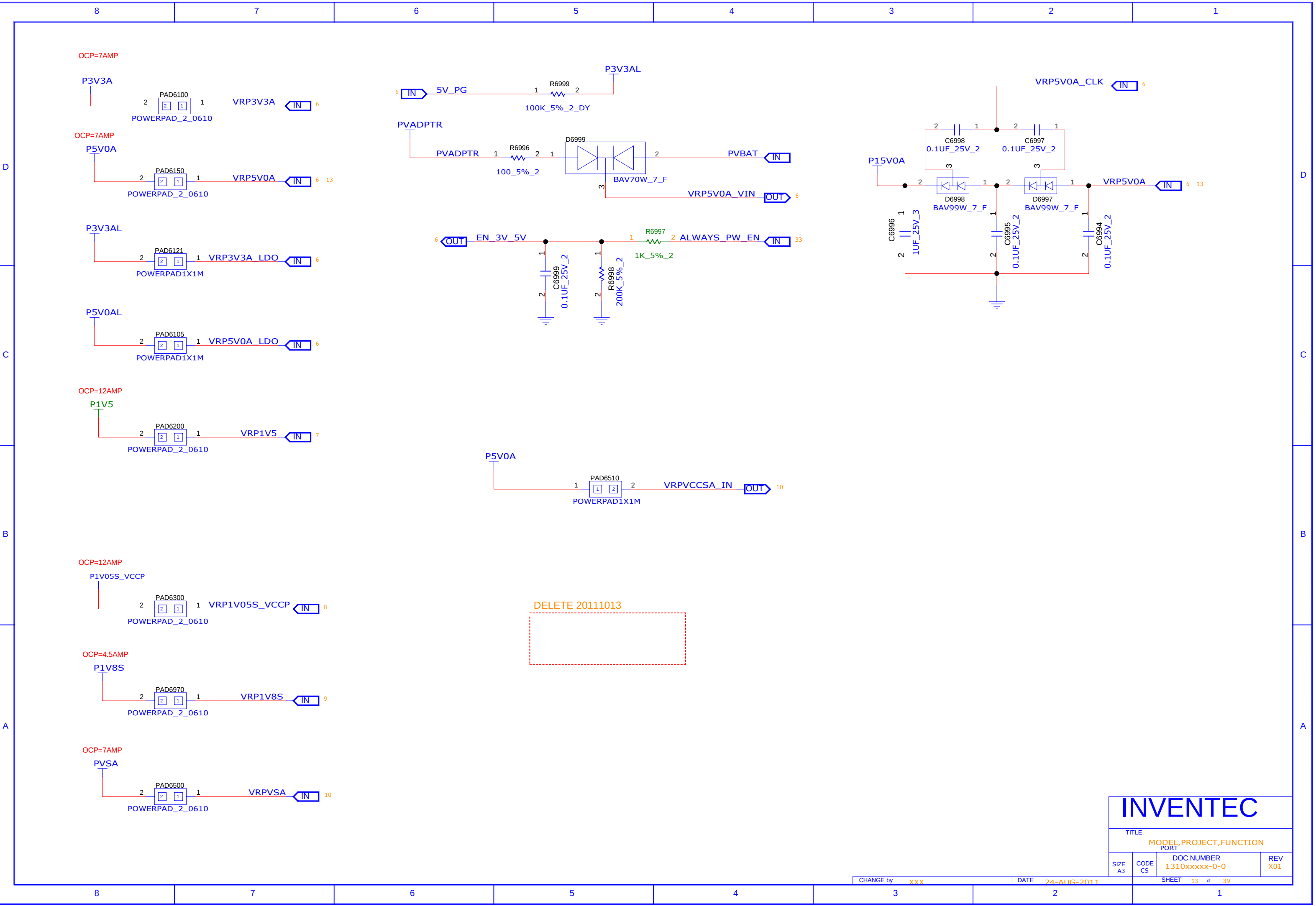


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PVCORE2

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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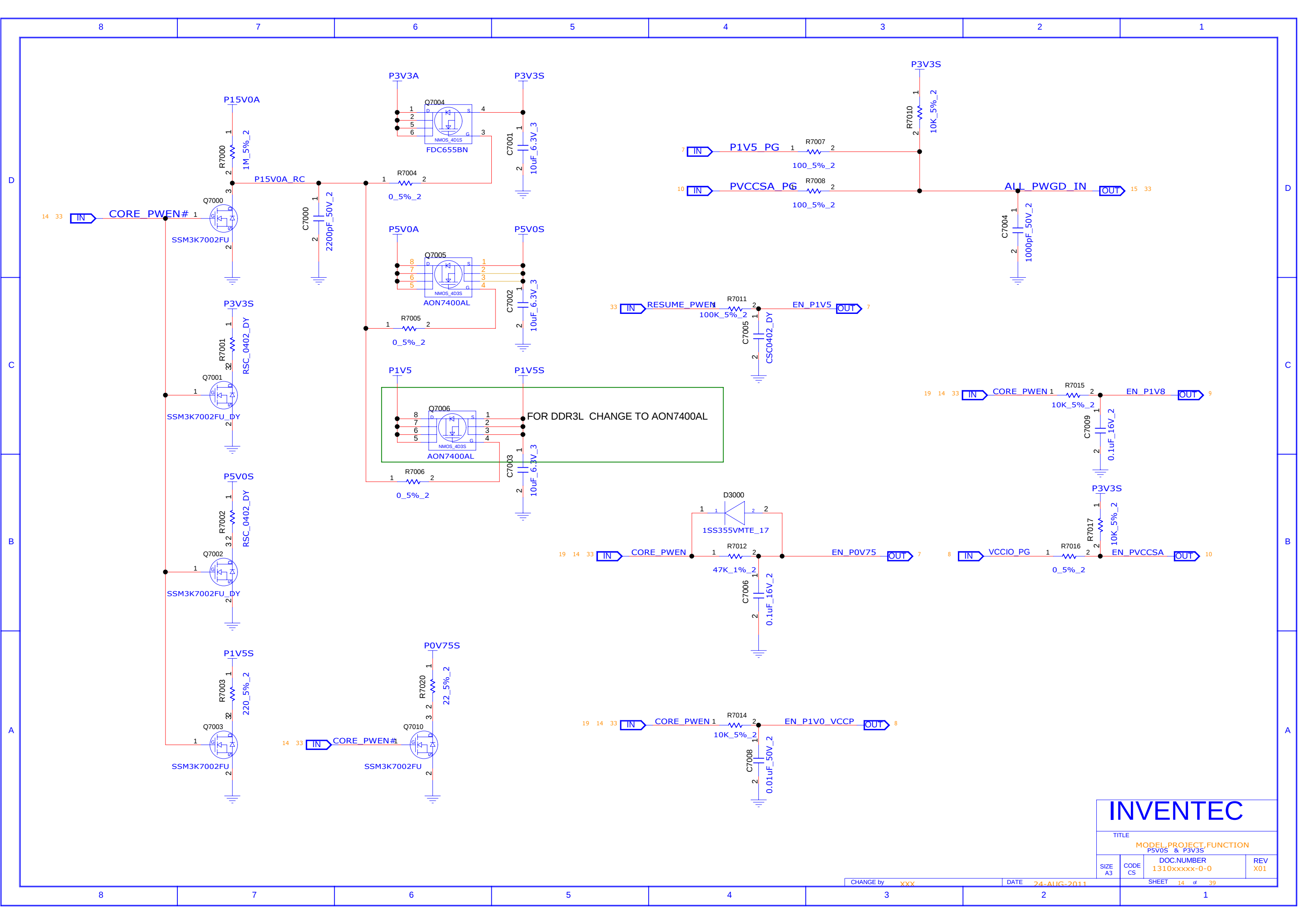
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PORT

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

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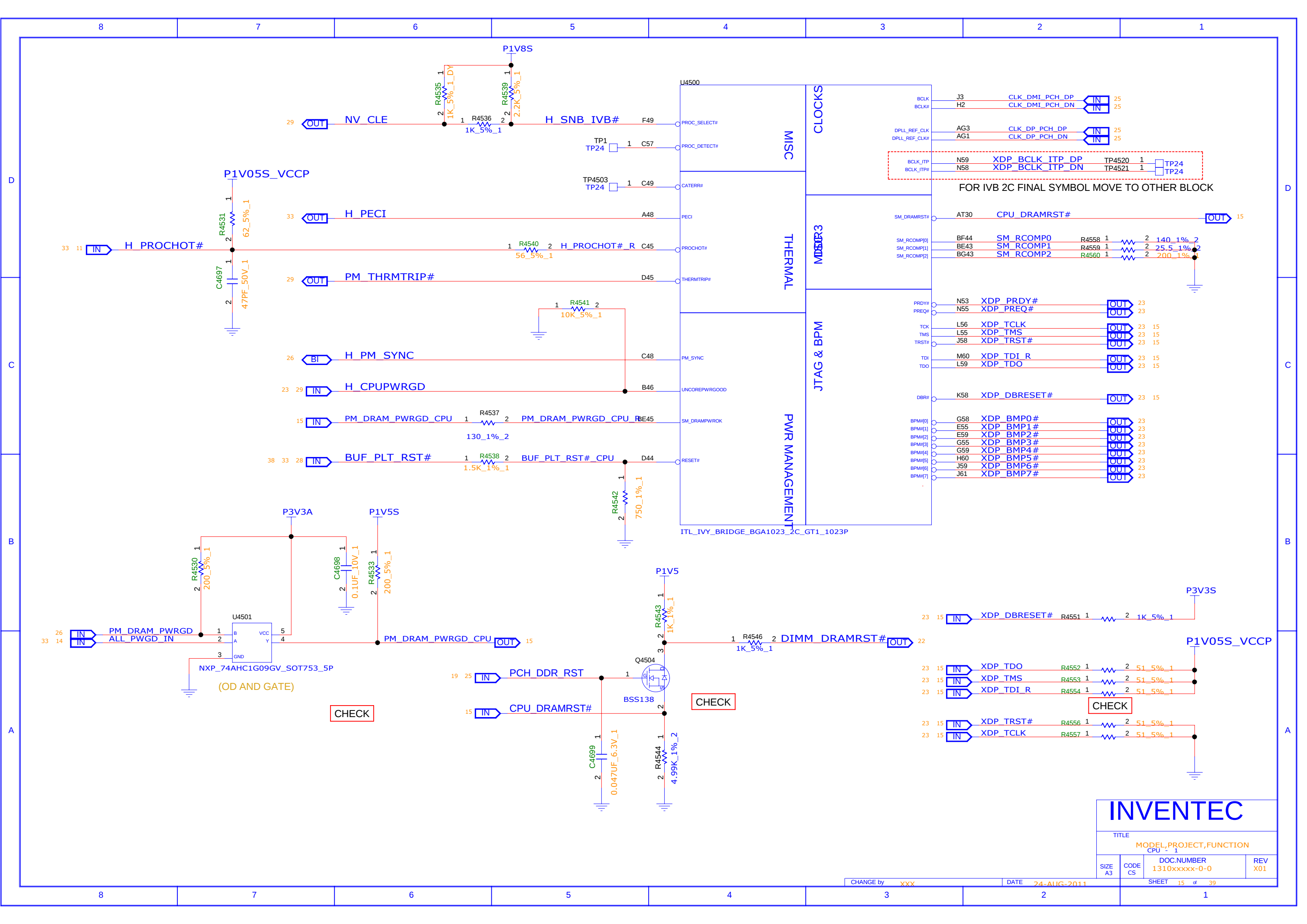


INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
P5V0S & P3V3S			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

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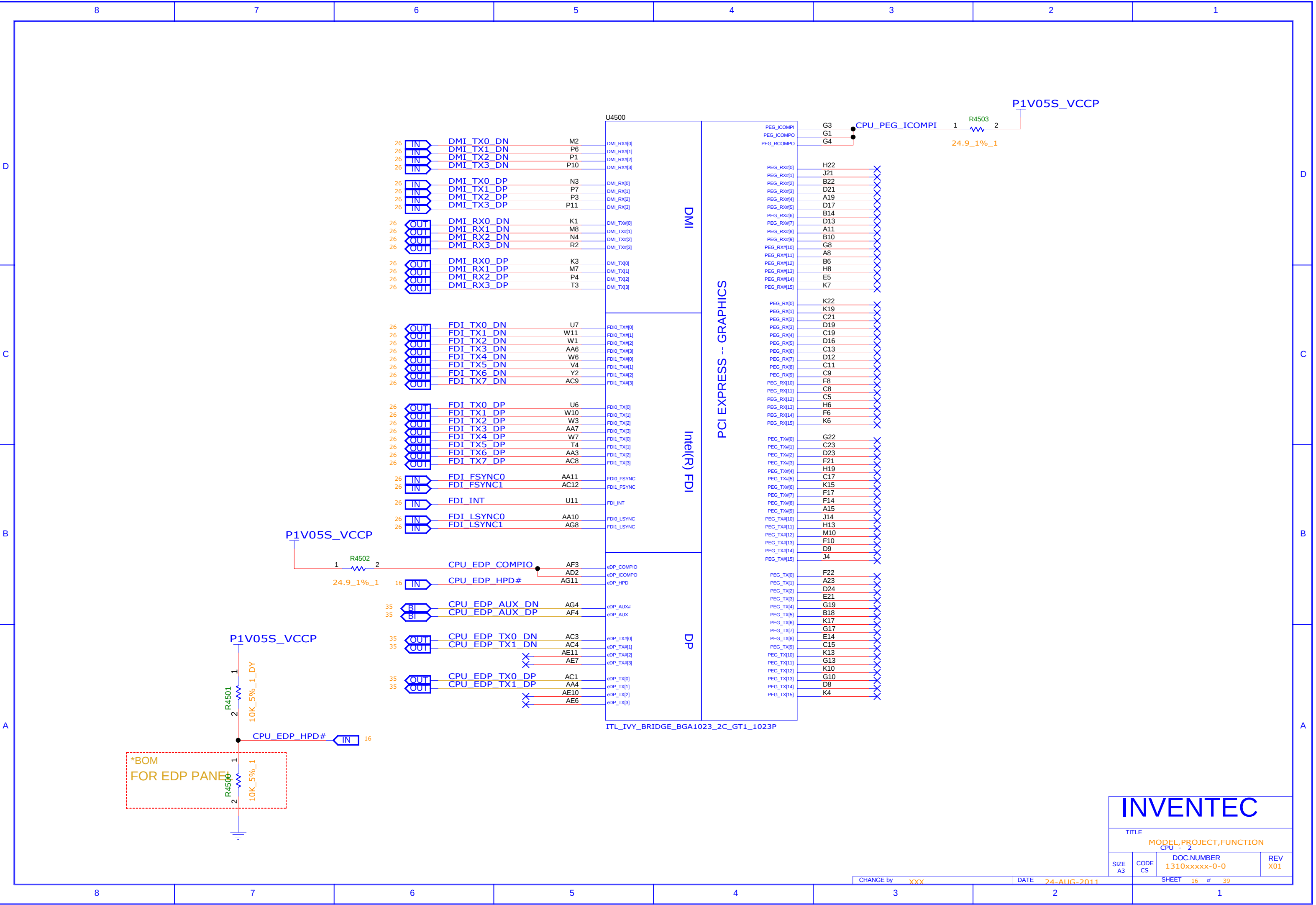


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
CPU - 1

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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INVENTEC

TITLE MODEL,PROJECT,FUNCTION

CPU - 2

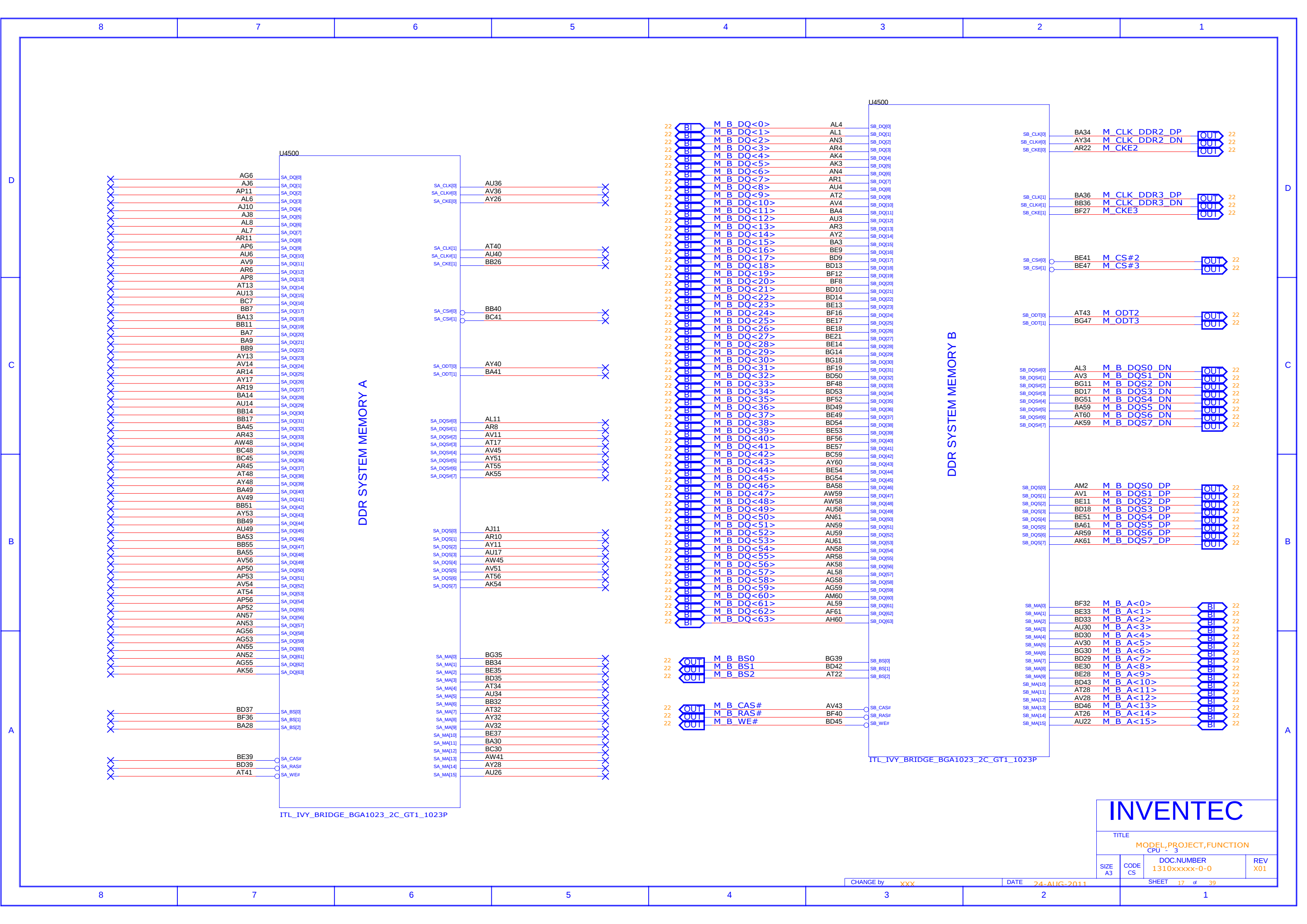
SIZE A3 CODE CS

DOC.NUMBER 1310xxxxx-0-0

REV X01

CHANGE by XXX DATE 24-AUG-2011

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INVENTEC

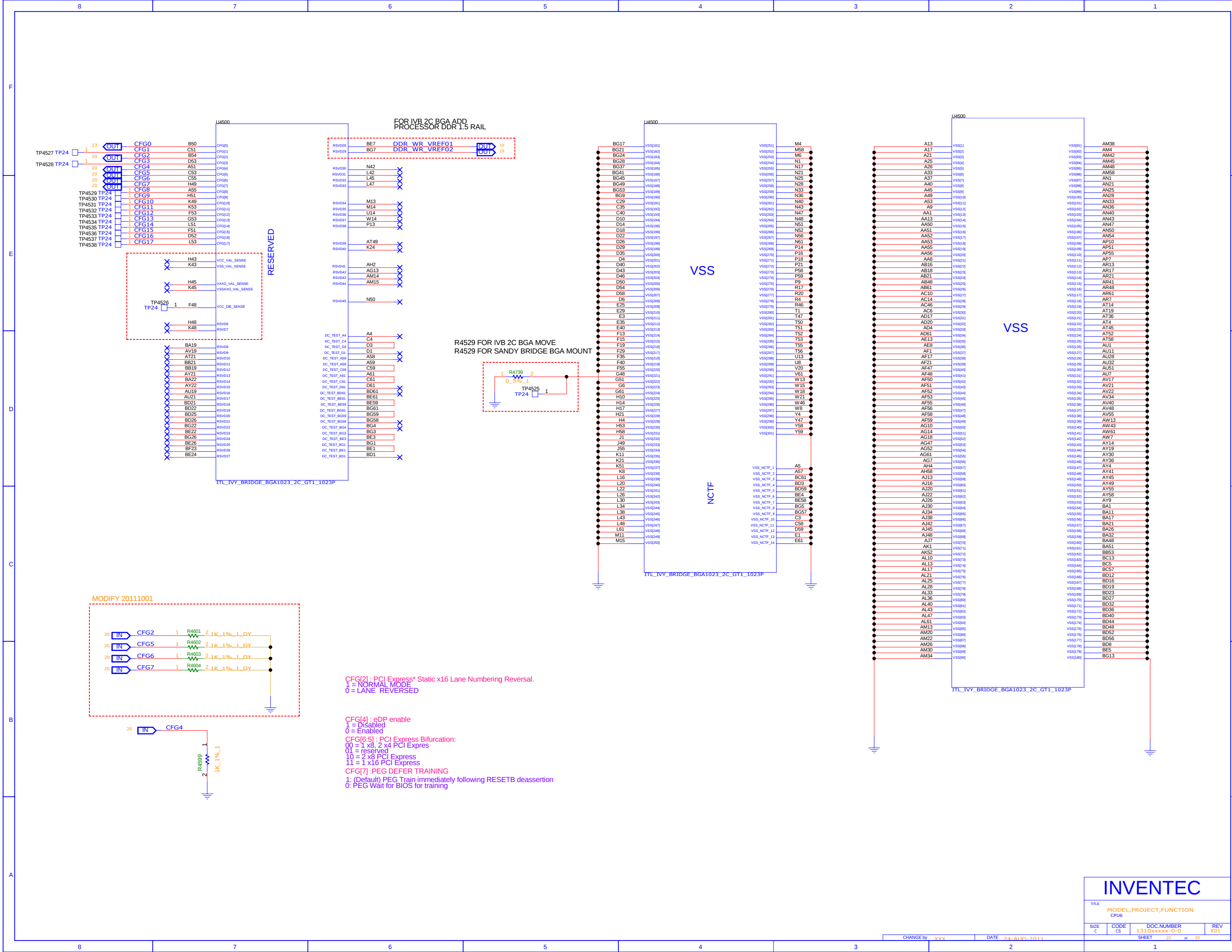
TITLE

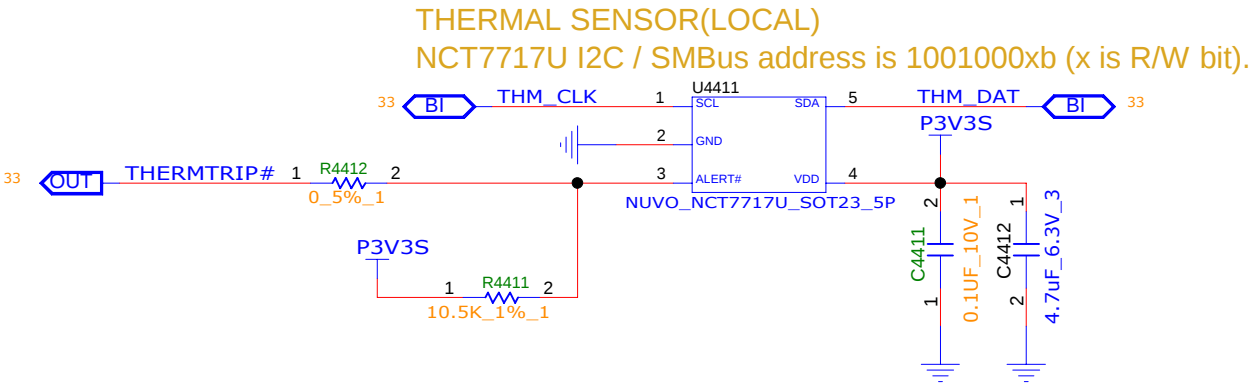
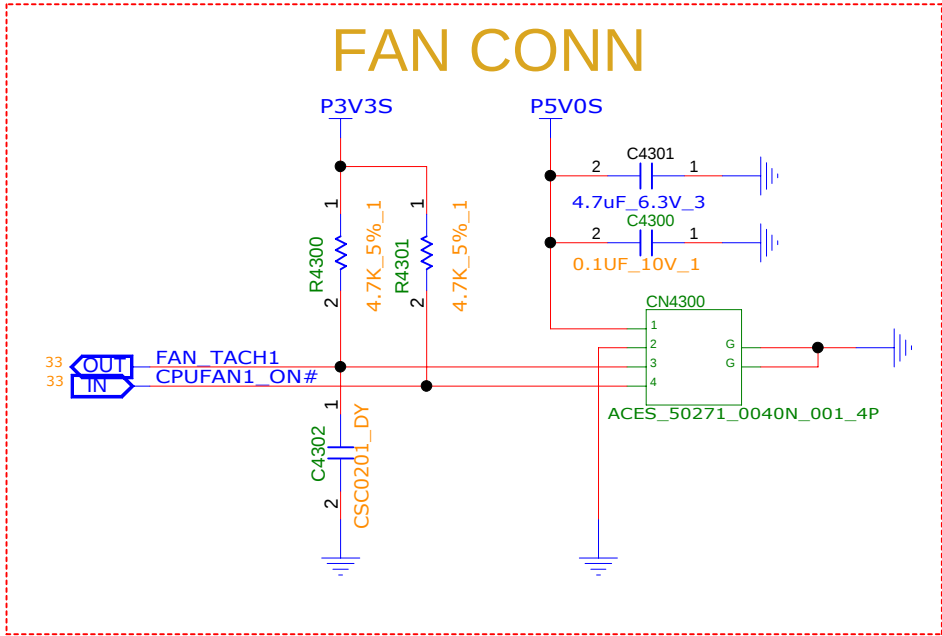
MODEL,PROJECT,FUNCTION
CPU - 3

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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6.6 ALERT# point hardware power-on setting
The default value could be set after power up 100ms by different pull-up resistor of ALERT# pin:

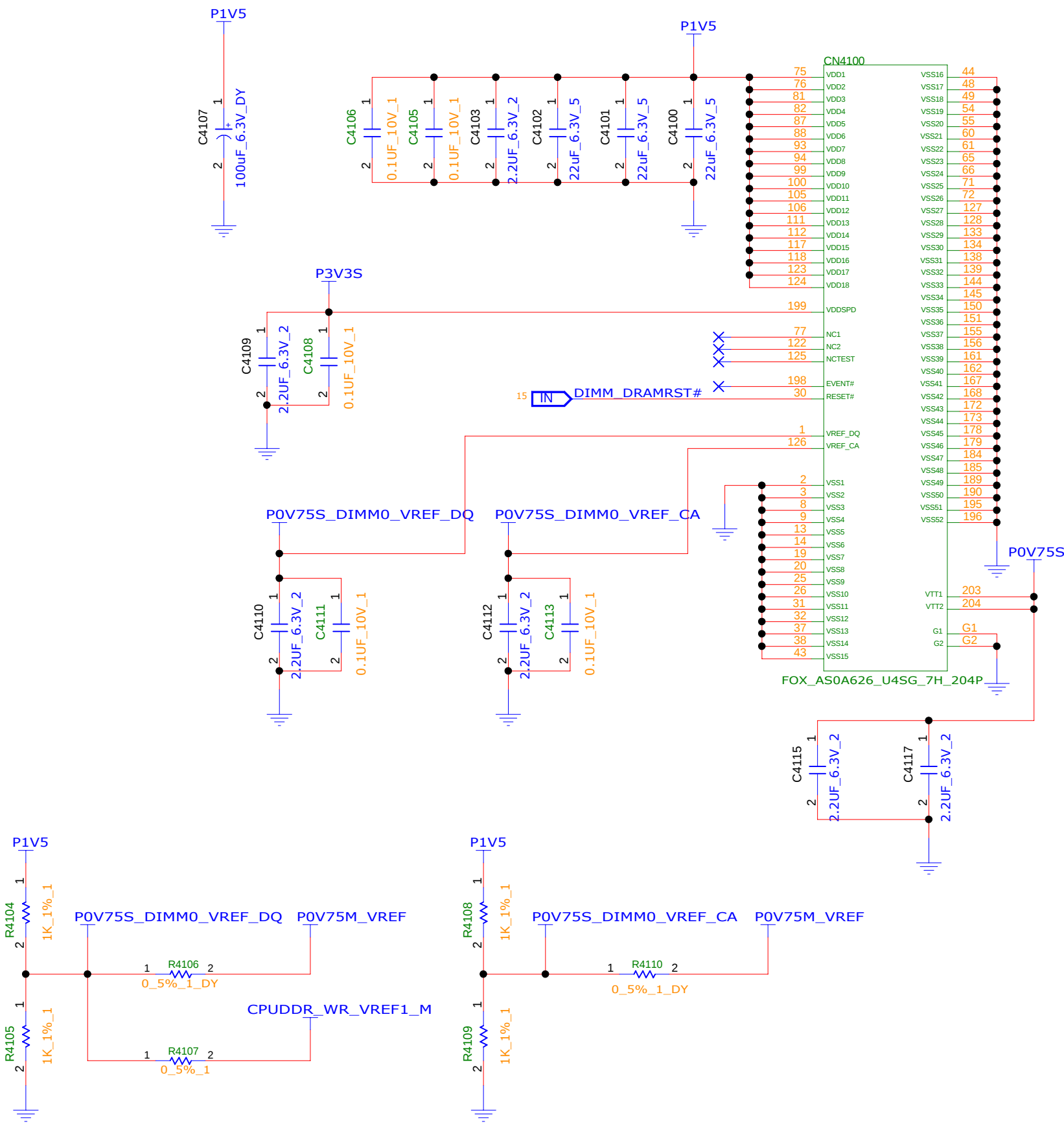
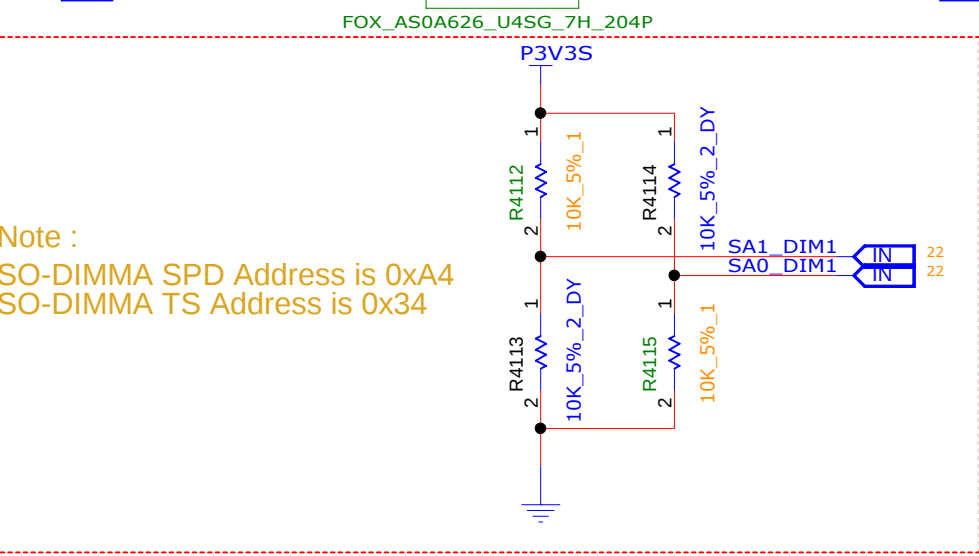
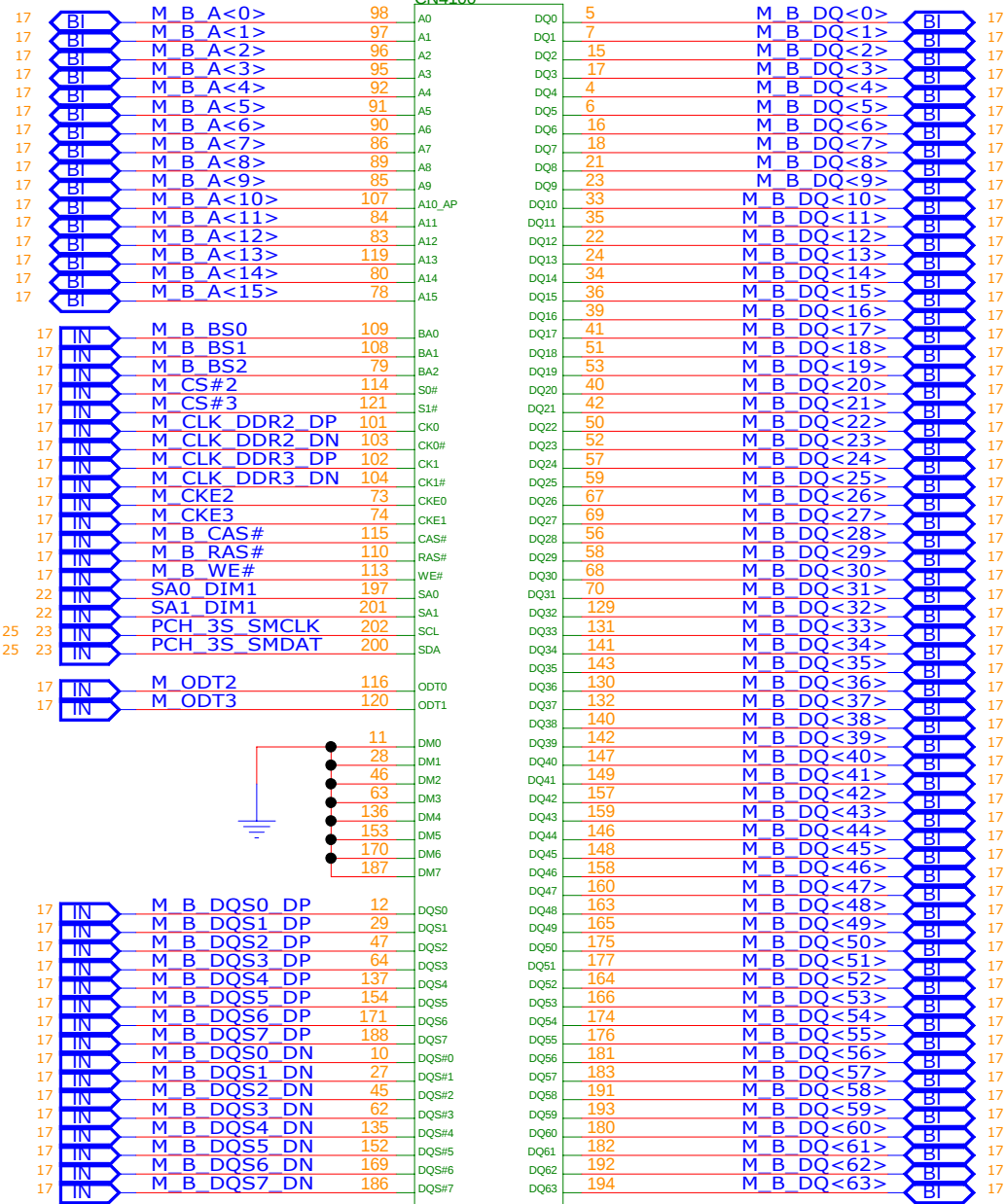
PULL-UP RESISTOR		TEMPERATURE (°C)
ALERT	2KΩ	75
	7.5KΩ	90
	10.5KΩ	100
	14KΩ	105
	18.7KΩ	110

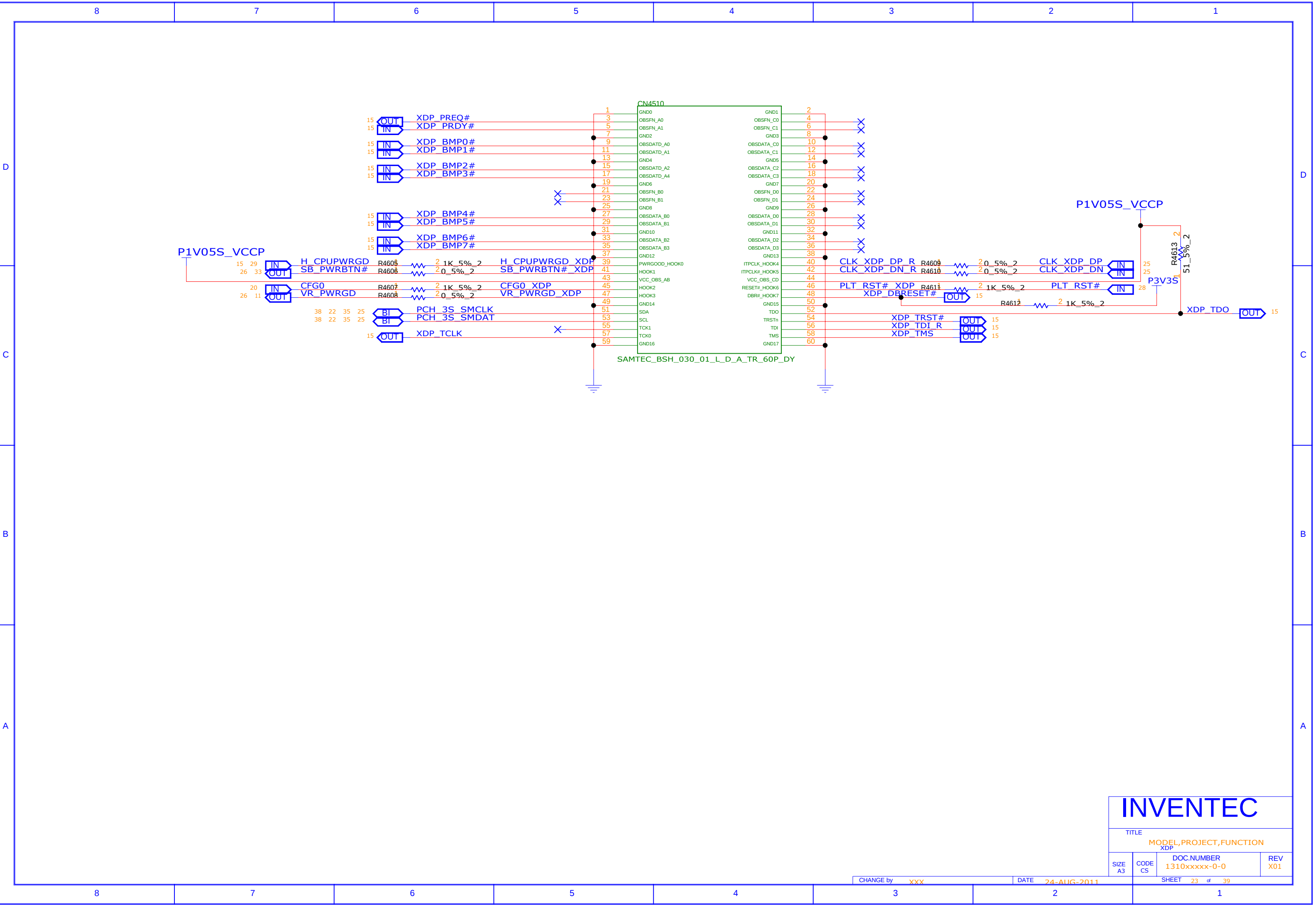
7.2.5 Register : Local Diode Temperature Alert Temperature
Location : Read = Address 05h Write = Address 0Bh
Type : Read/Write
Power on default value : Hardware strapping.

BIT	7	6	5	4	3	2	1	0
NAME	LDT Alert Temperature							
VALUE	SIGN	64	32	16	8	4	2	1
DEFAULT	Hardware strapping							

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION THERMAL & FAN			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01





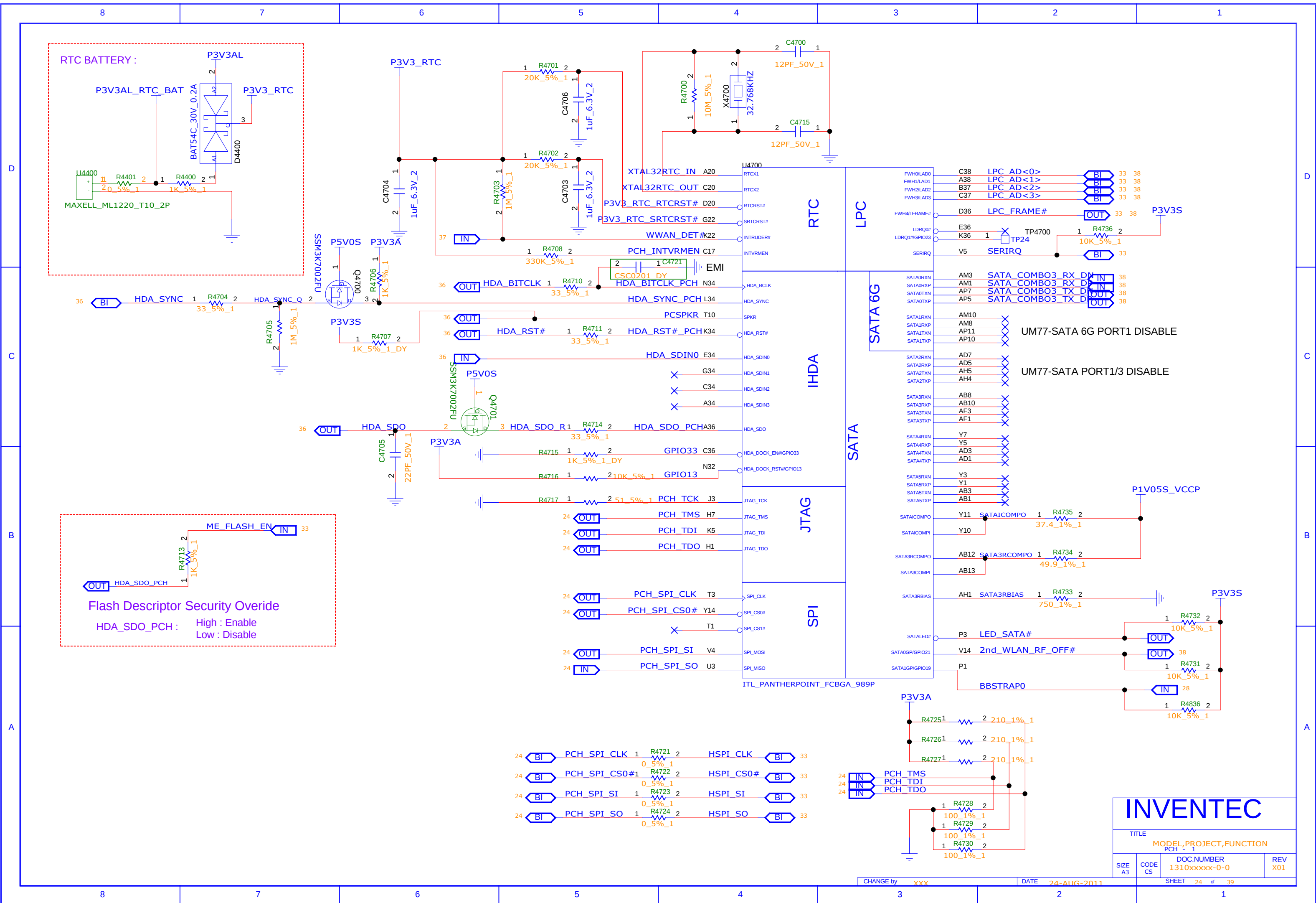
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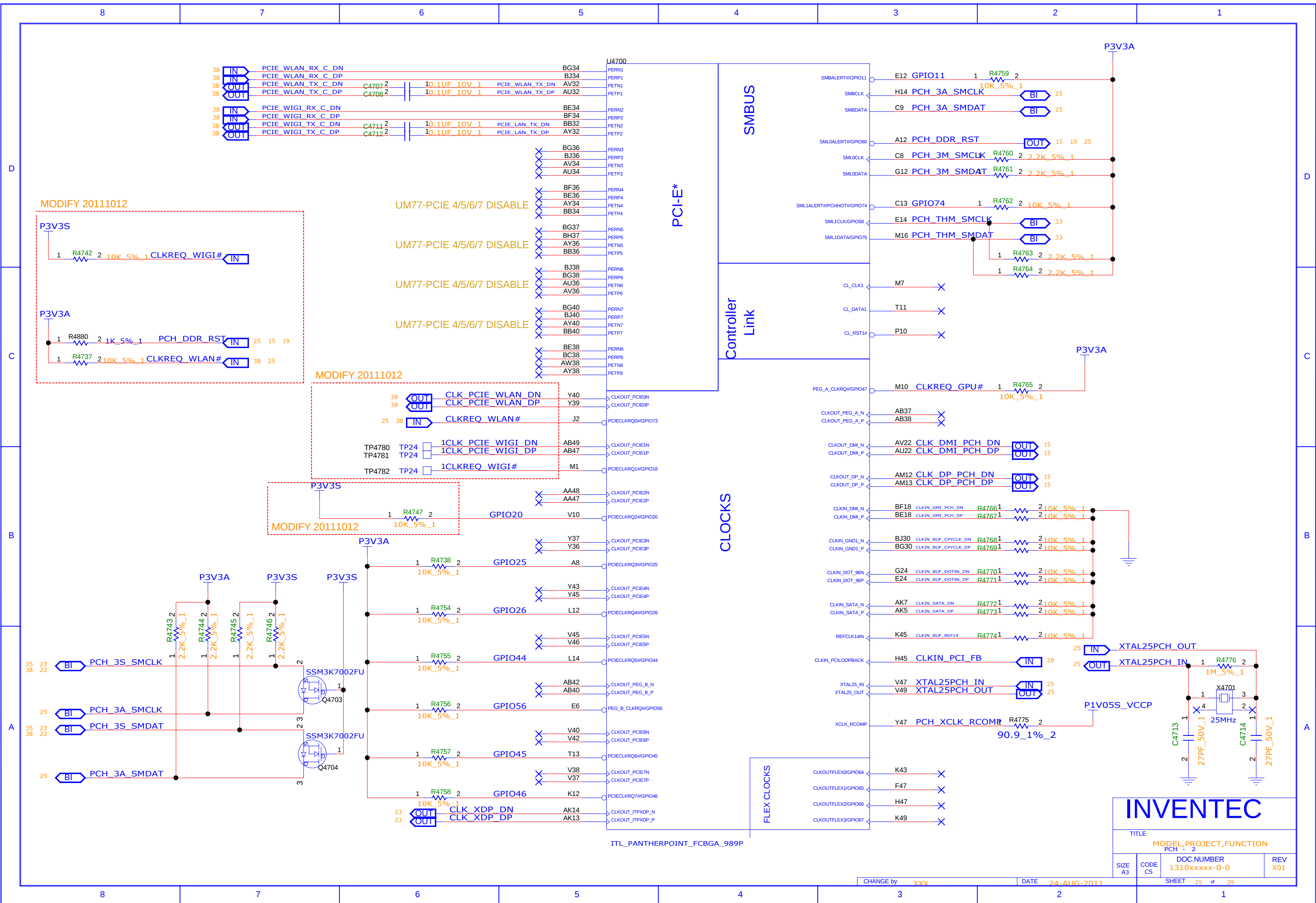
TITLE
MODEL,PROJECT,FUNCTION
XDP

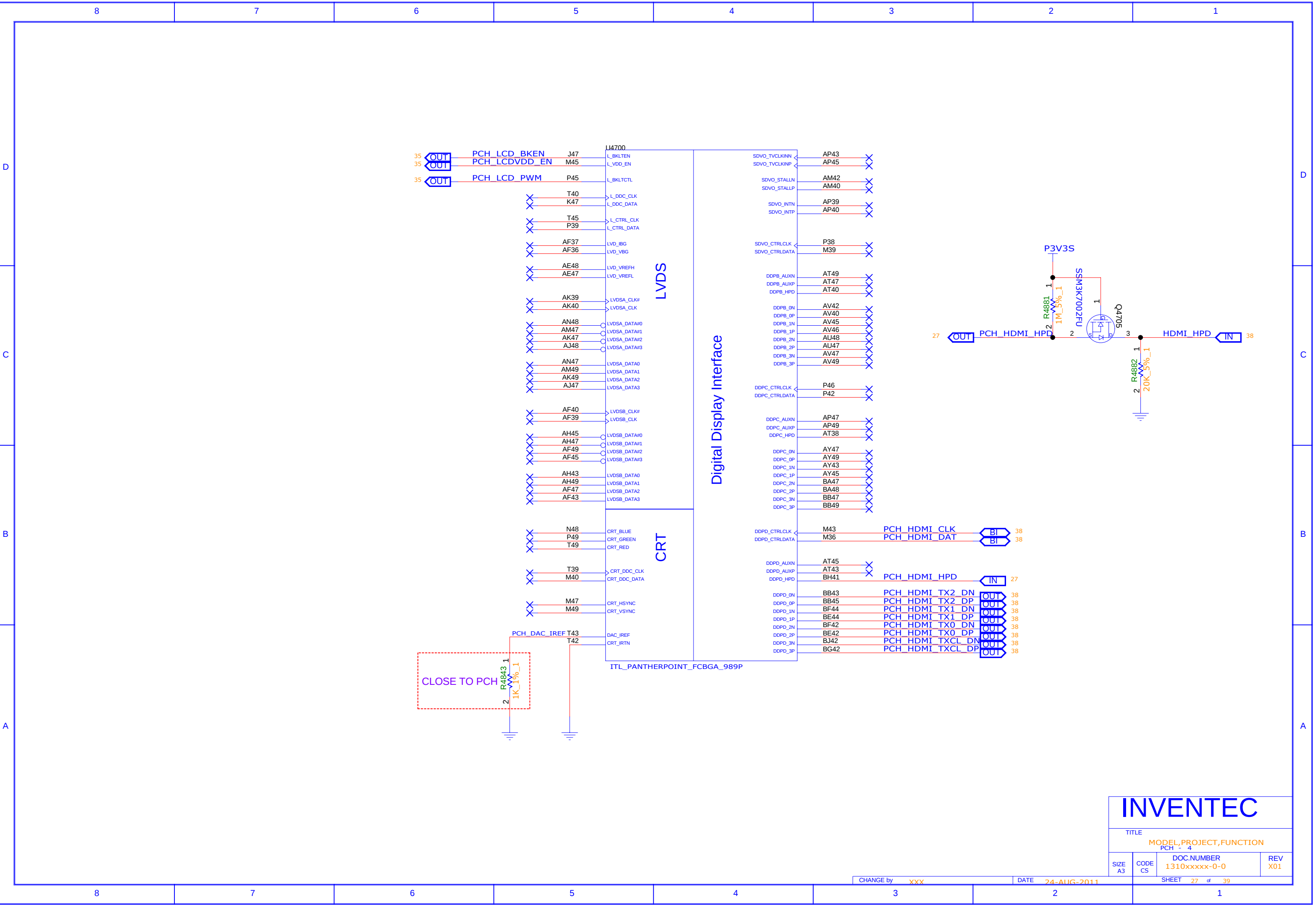
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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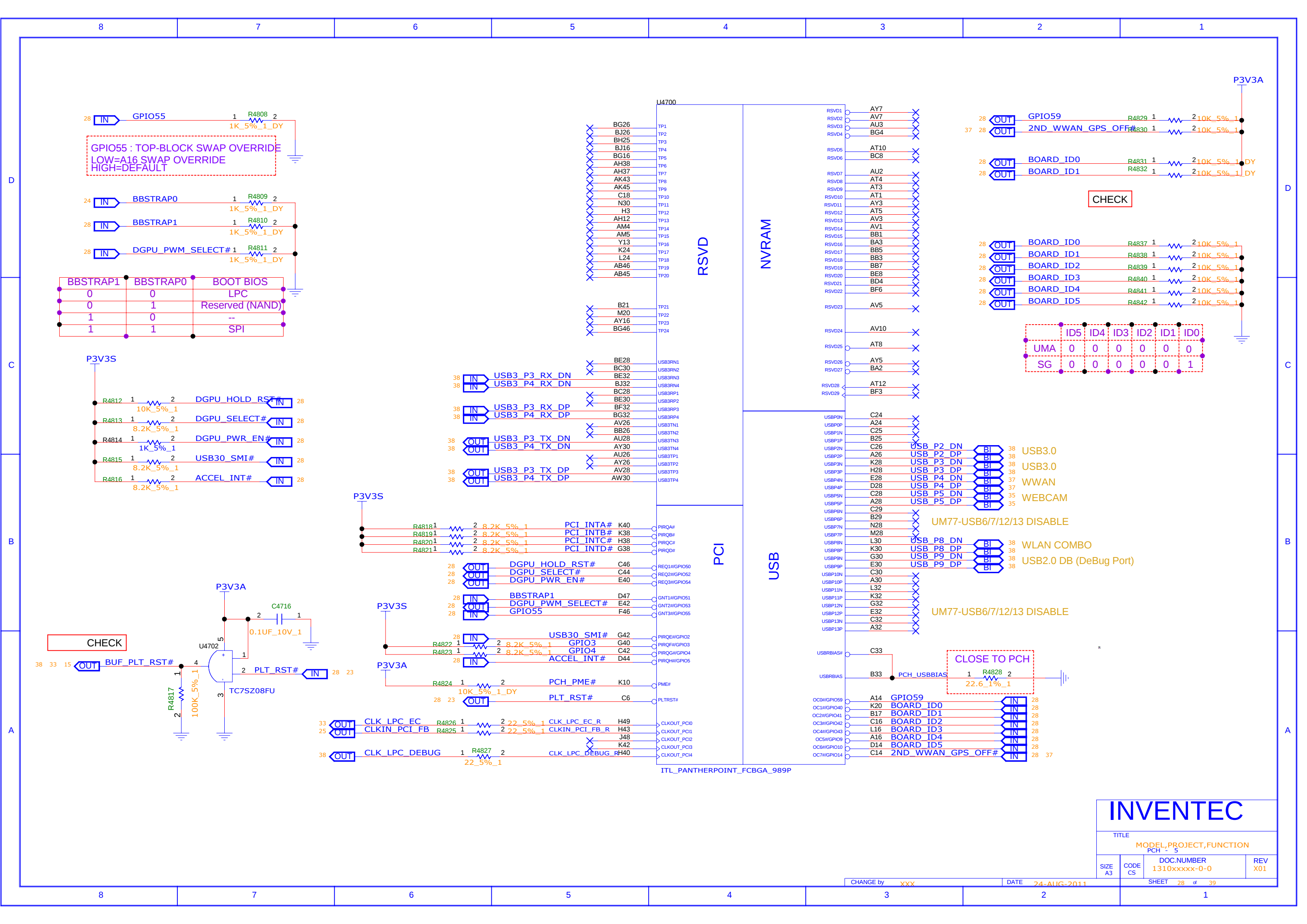
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 4

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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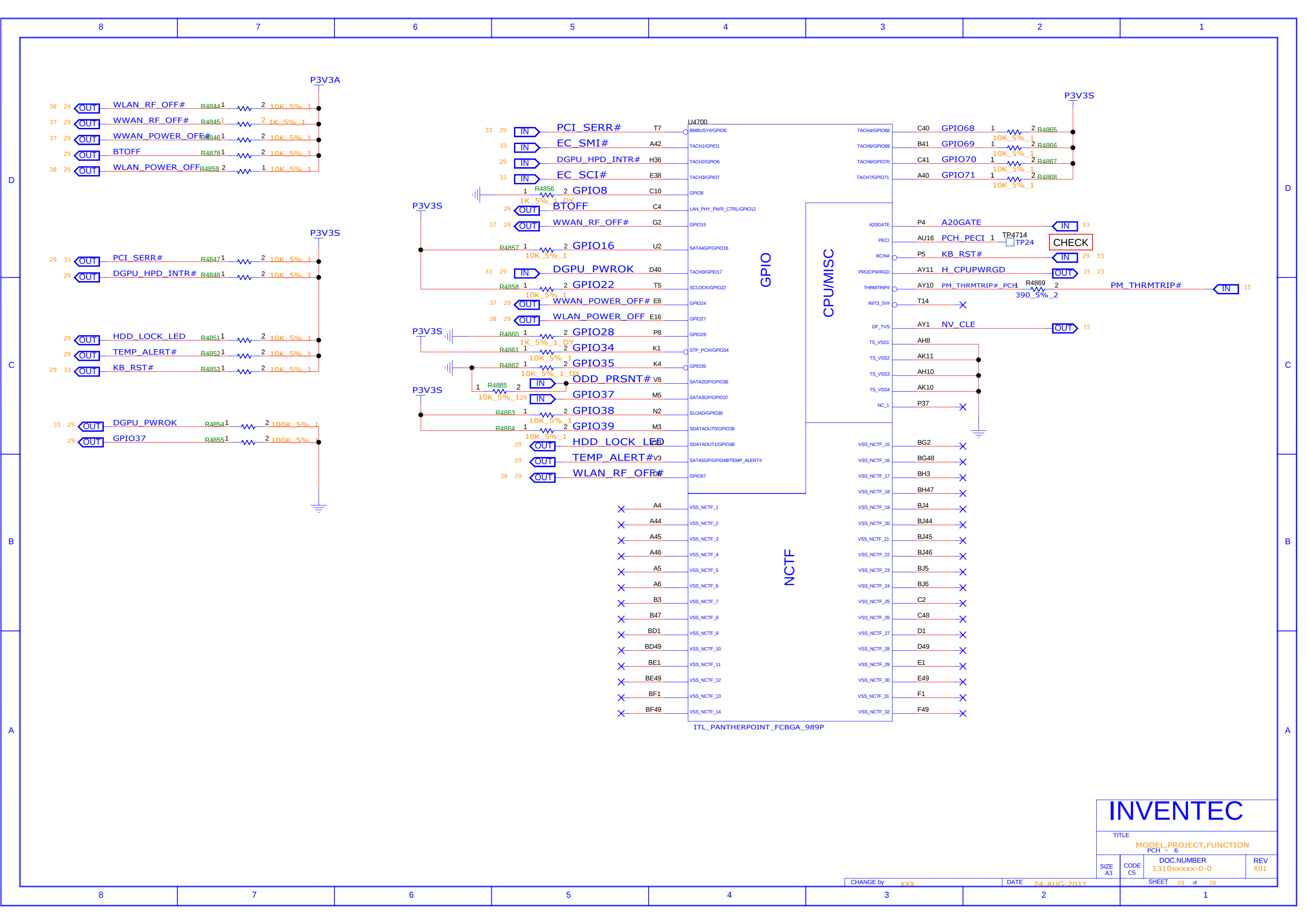


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 5

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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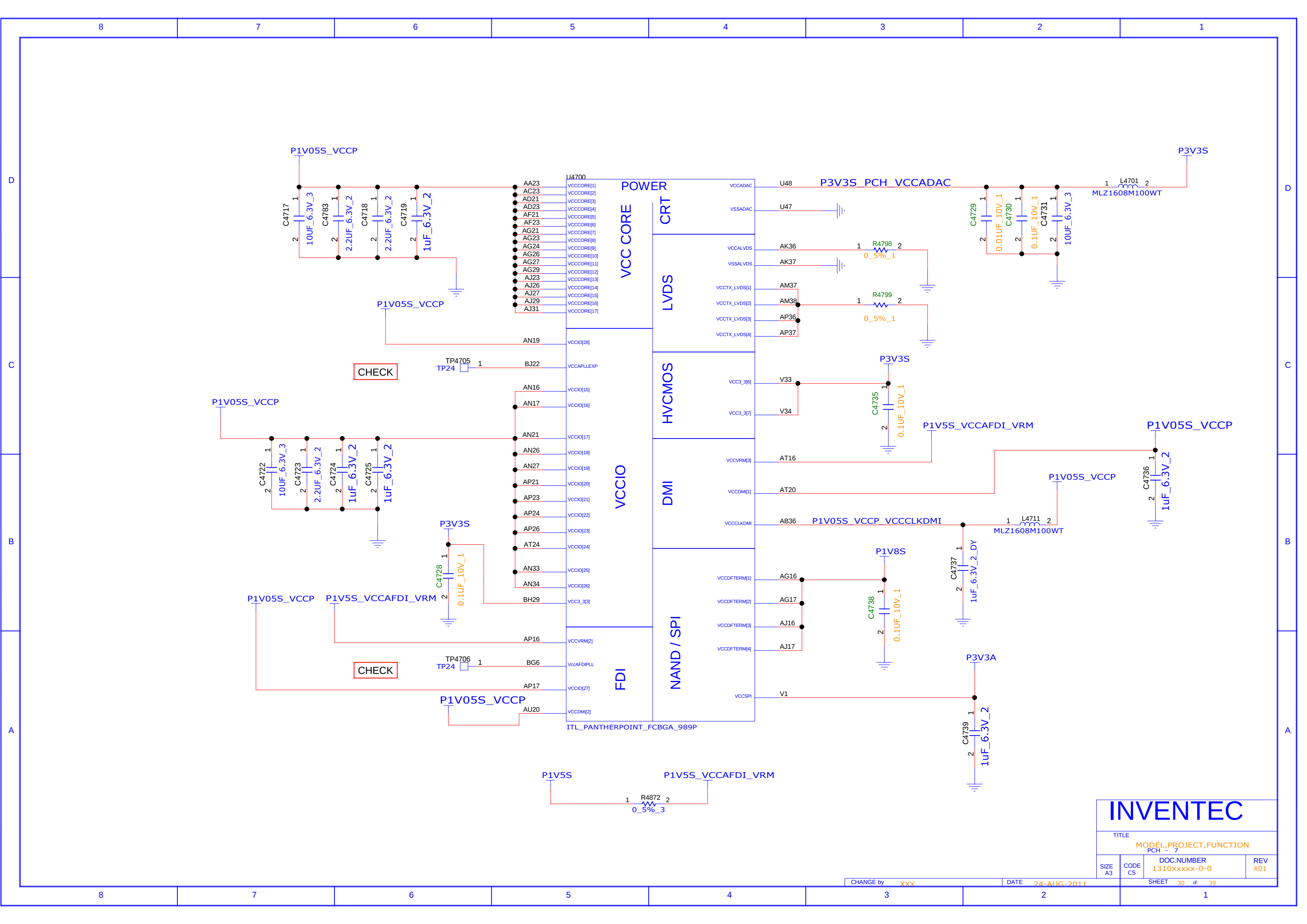


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 6

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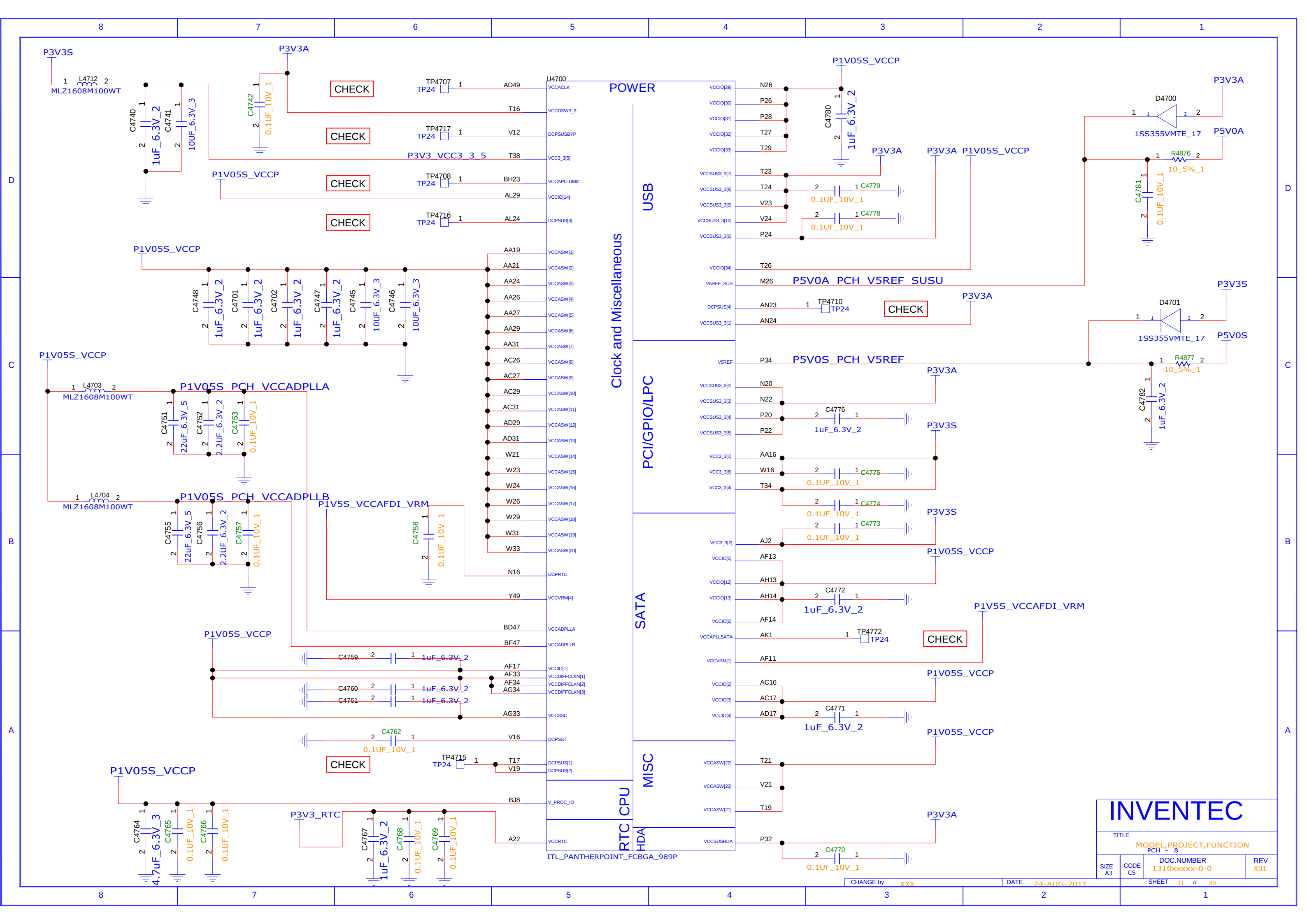
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 7

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

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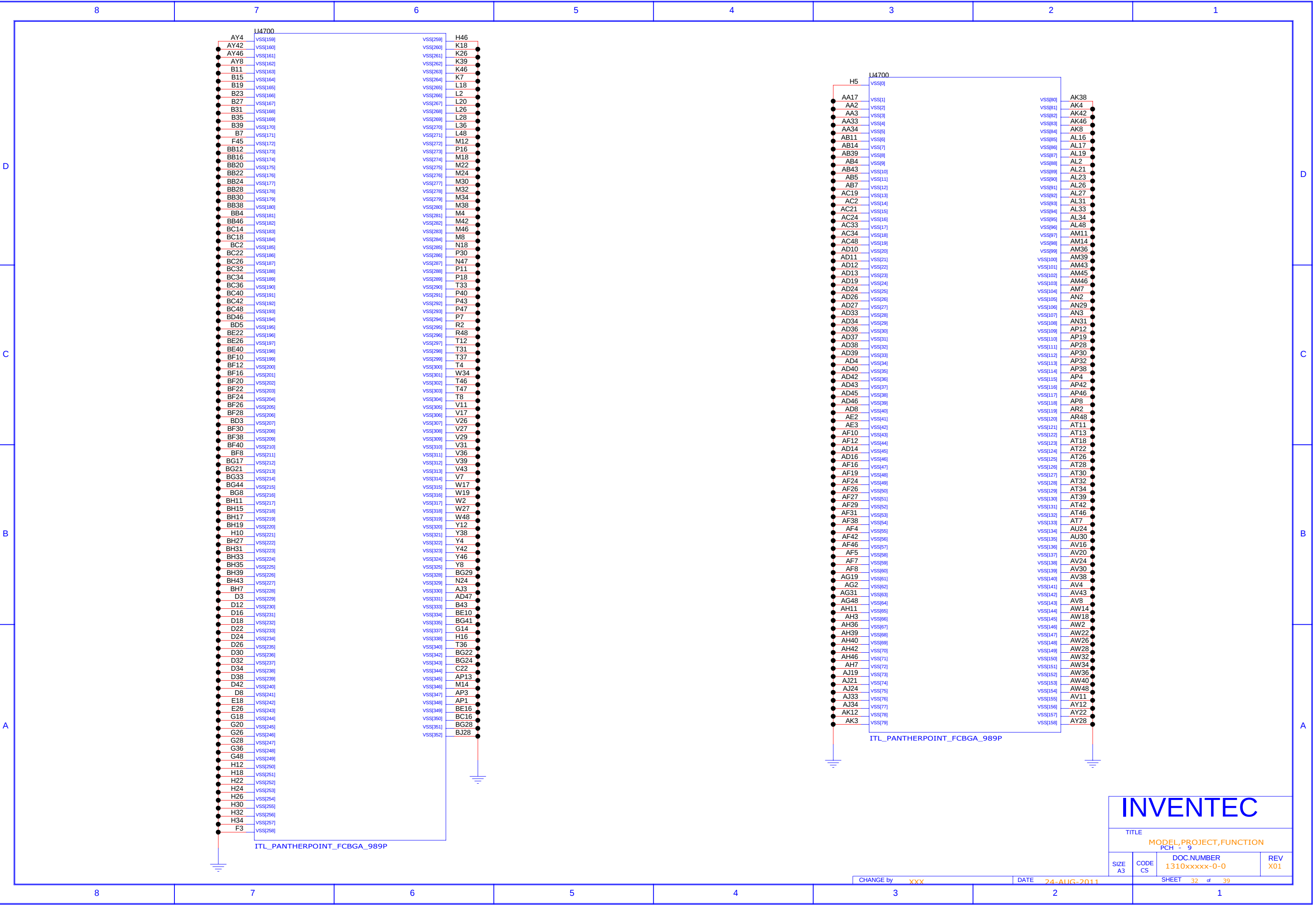


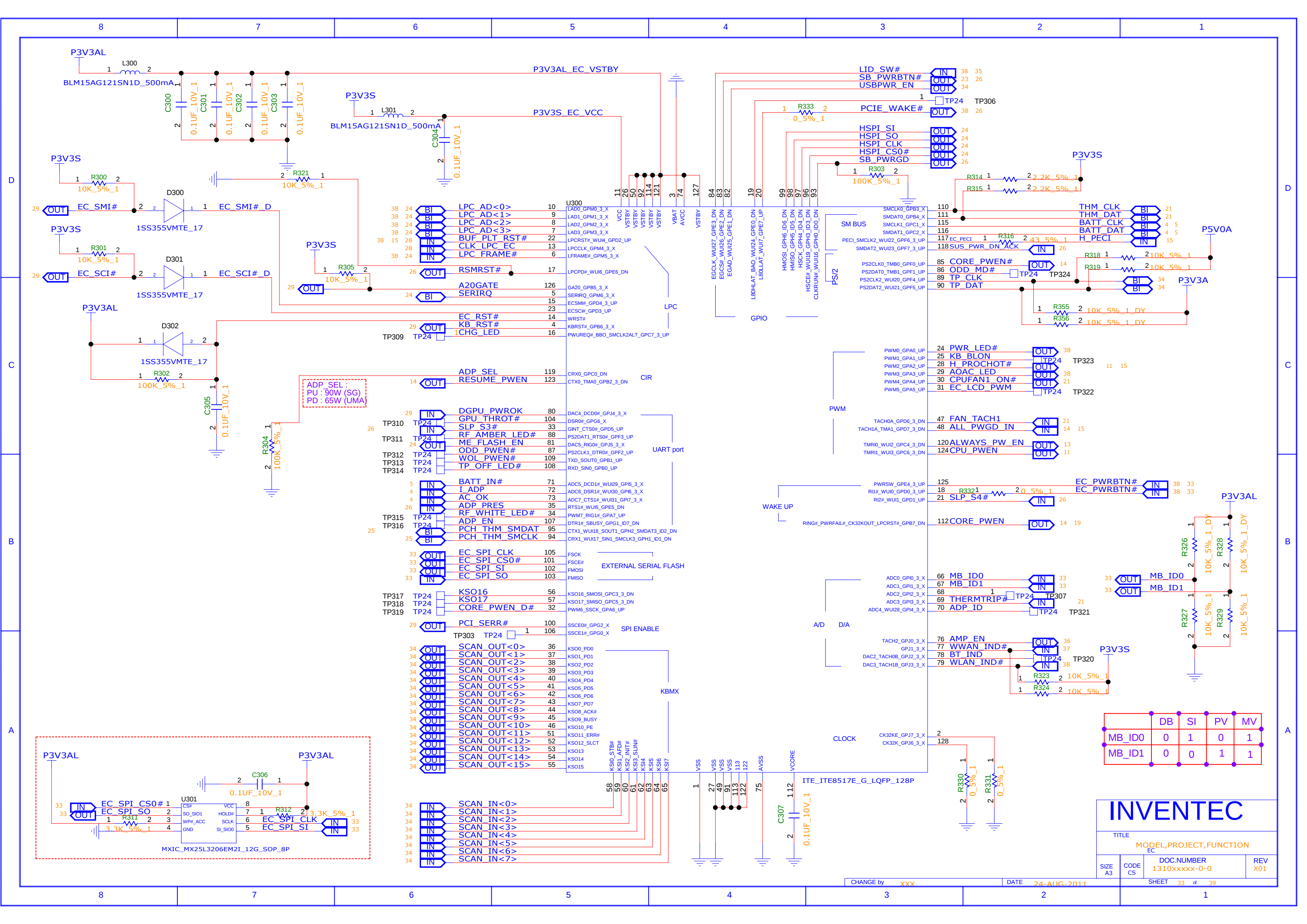
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 8

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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	DB	SI	PV	MV
MB_ID0	0	1	0	1
MB_ID1	0	0	1	1

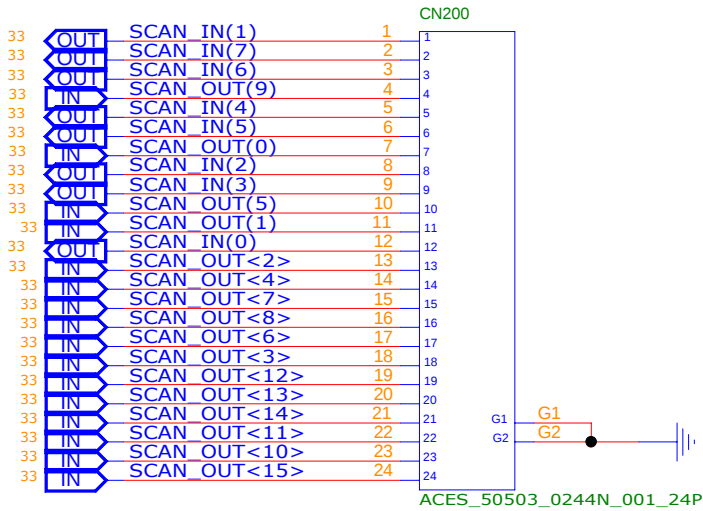
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

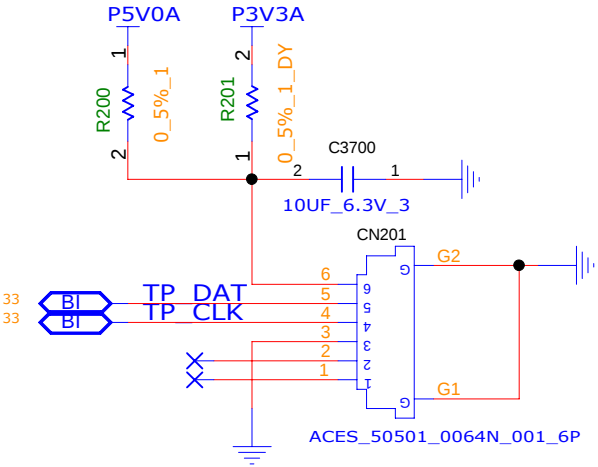
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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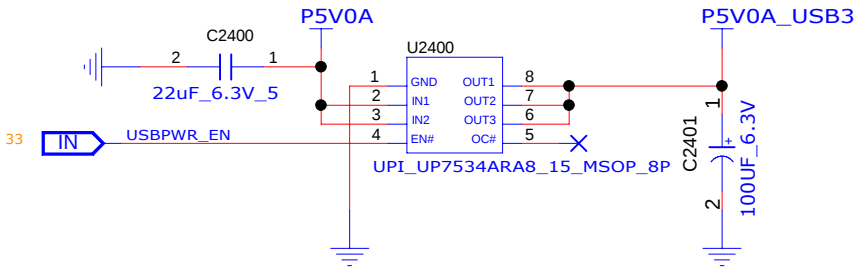
KEYBOARD CONN(26 PIN)



TouchPad Module CONN



USB 3.0 POWER SW



INVENTEC

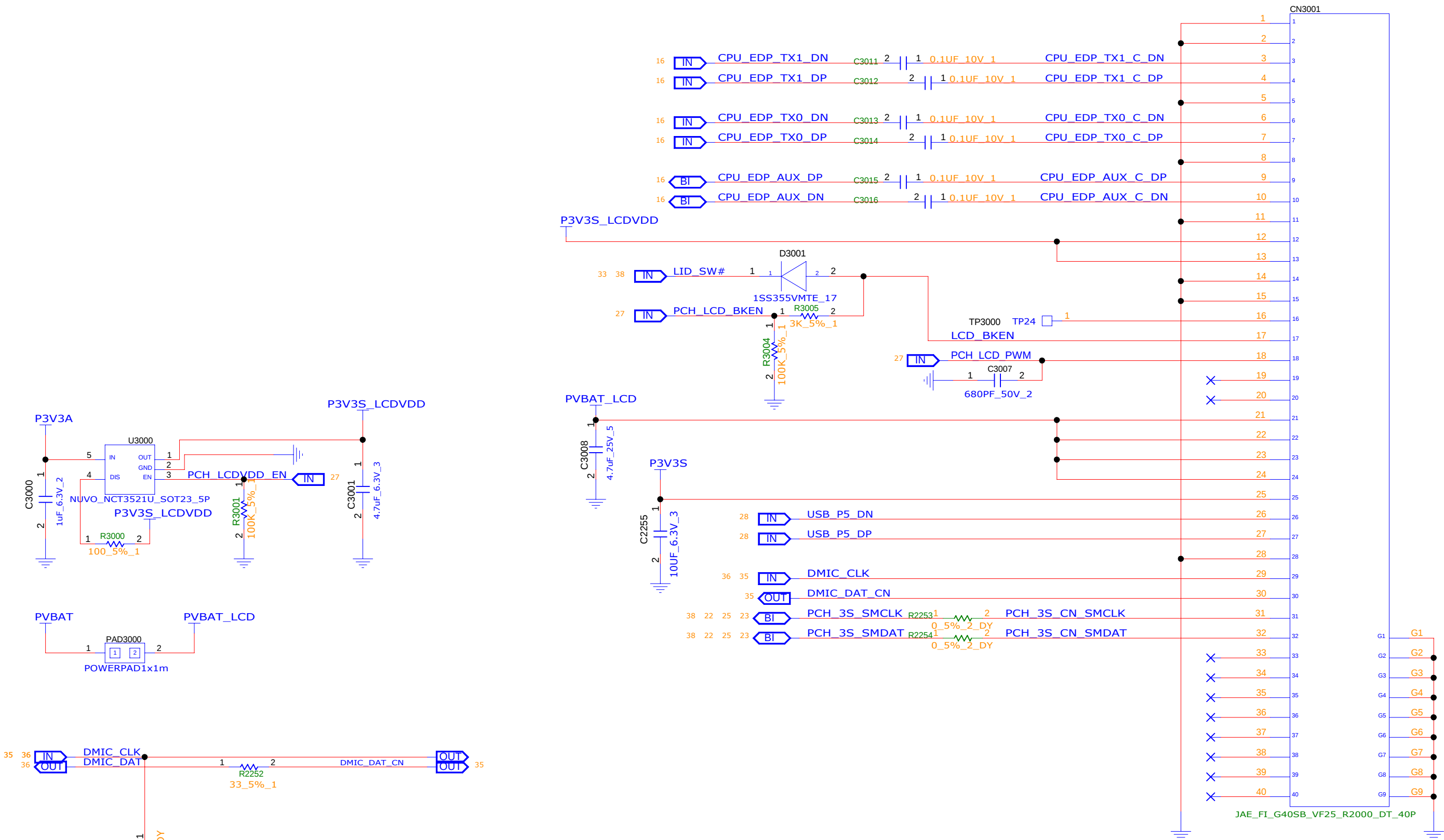
TITLE
MODEL,PROJECT,FUNCTION
KB & TP & USB PWR SW

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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EDP CONN



INVENTEC

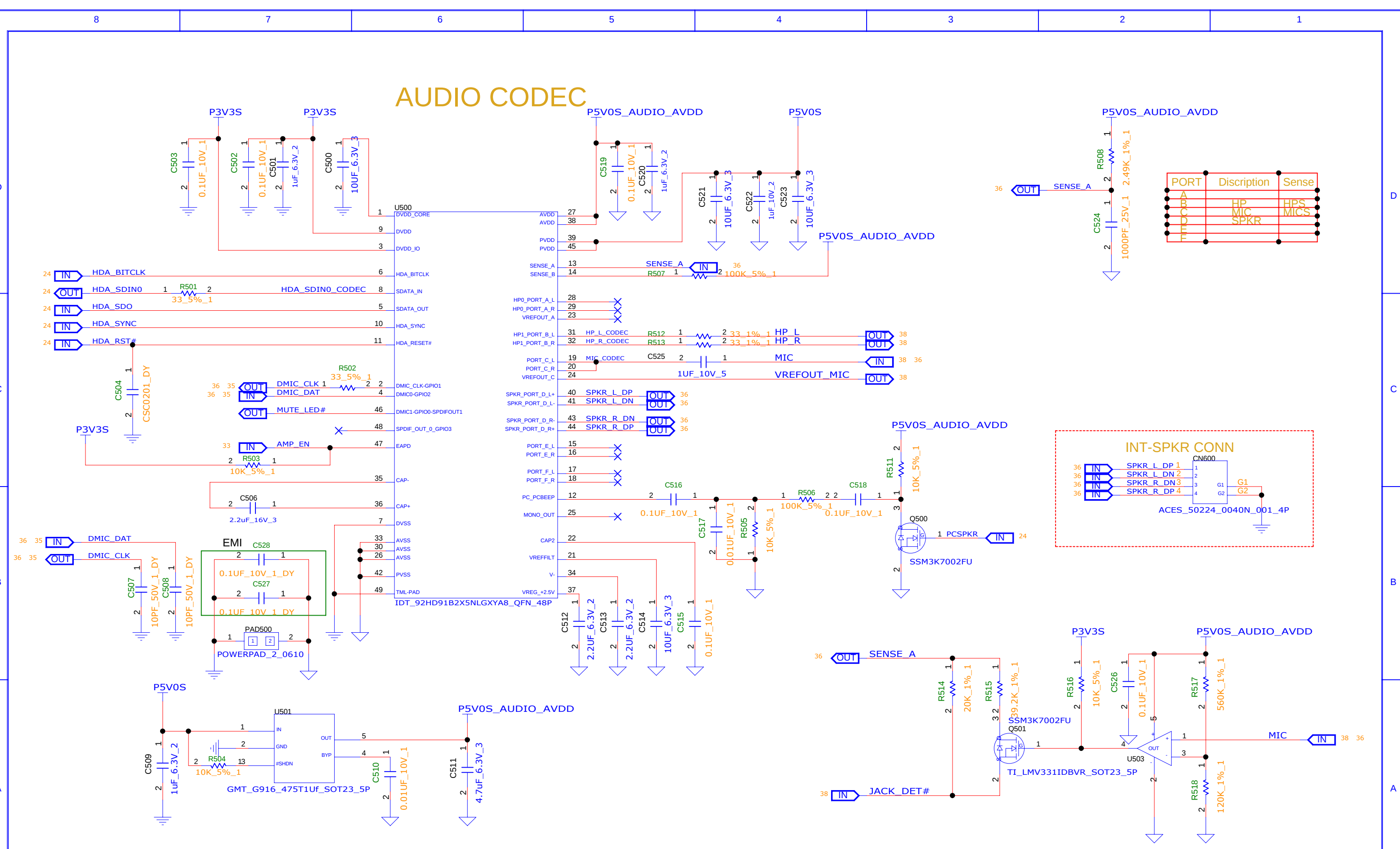
TITLE
MODEL,PROJECT,FUNCTION
EDP

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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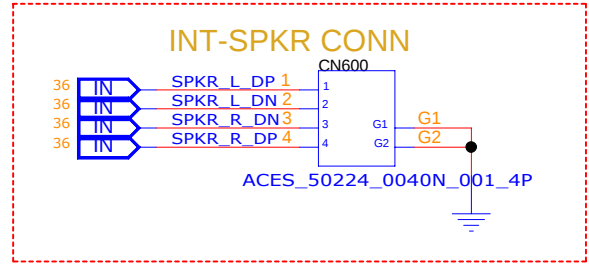
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AUDIO CODEC

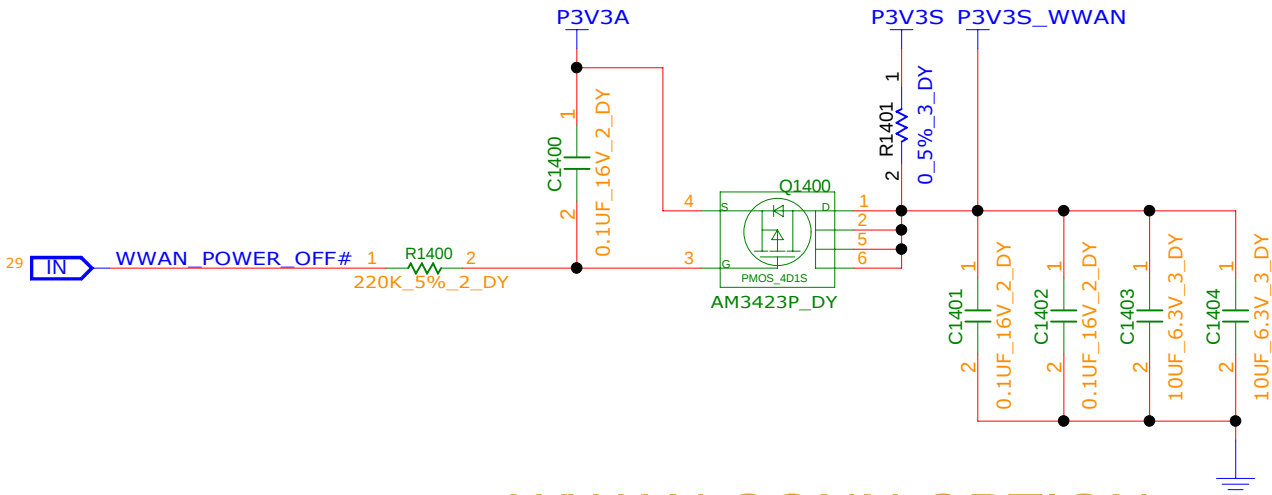


PORT	Discription	Sense
A	HP	HPS
B	MIC	MICS
C	SPKR	
D		

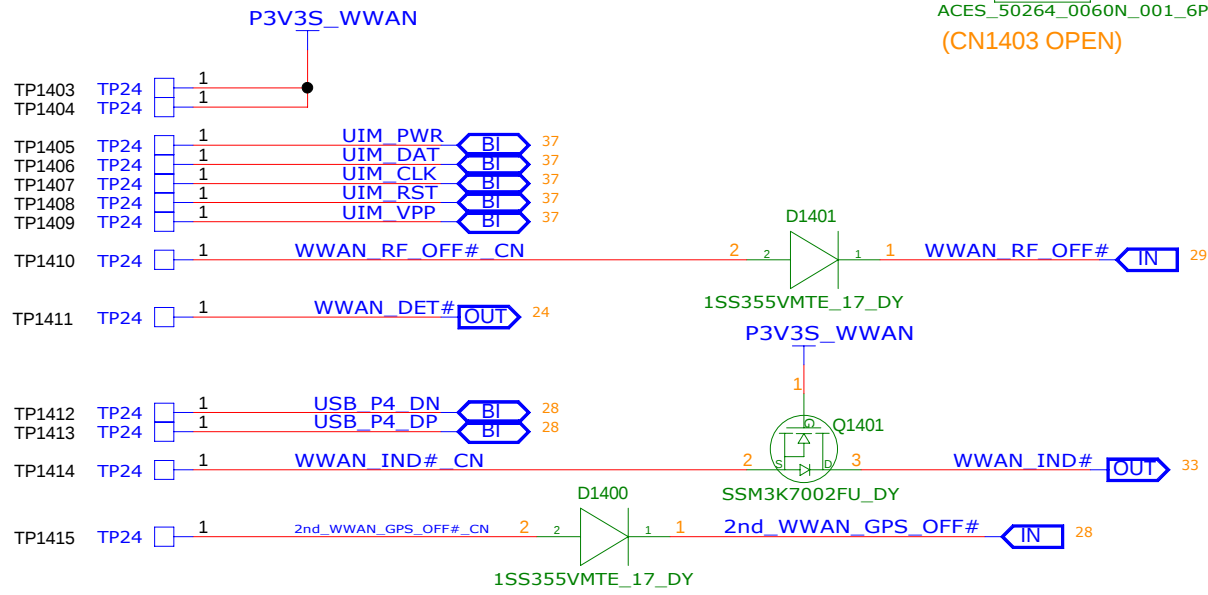
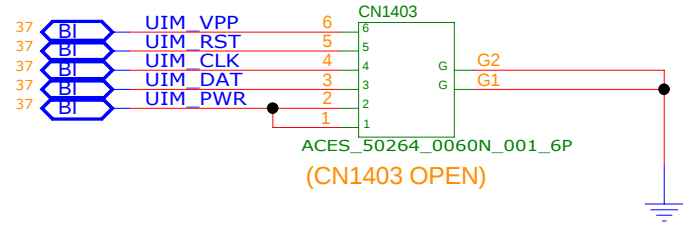


INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
AUDIO CODEC			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01



WWAN CONN OPTION



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
WWAN_OPTION

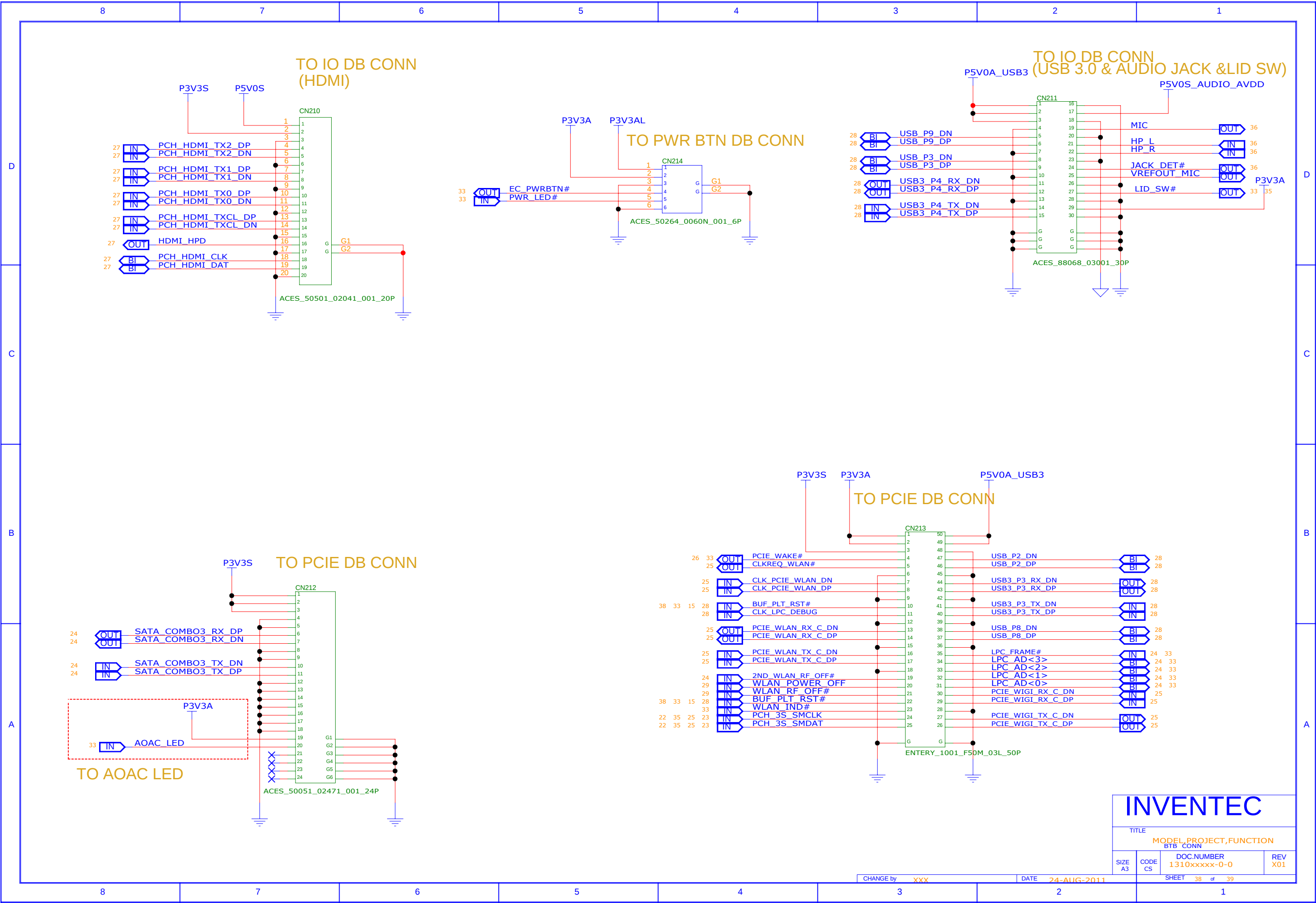
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CHANGE by XXX DATE 24-AUG-2011

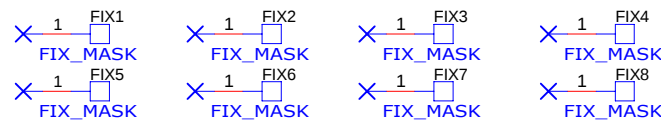
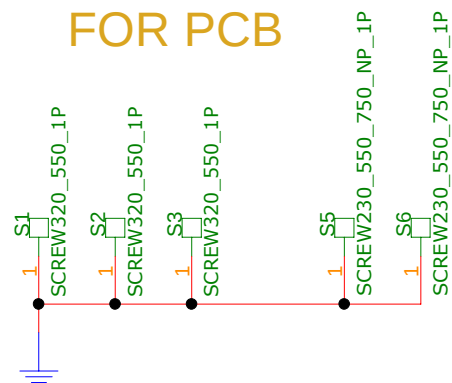
SHEET 37 of 39

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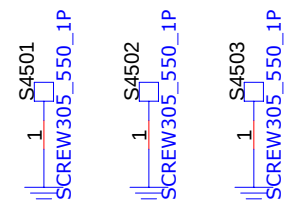
8 7 6 5 4 3 2 1



FOR PCB



FOR CPU



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION
SCREW

SIZE
A3

CODE	
CS	

DOC.NUMBER
1310xxxxx-0-0

REV
X01CHANGE by XXX

DATE	24-AUG-2011
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