

HSF Property:ROHS or Halogen-Free

2012.03.23

DRAWER		EE	DATE	POWER	DATE	INVENTEC	
LOCKEN		2012.03.23		ENGIN	2012.03.23		
CHECKIN		LOCKEN	2012.03.23	ENGIN	2012.03.23		
RECALL		RECALL	2012.03.23	BY	2012.03.23		
RESPONSIBLE		BEYAN	2012.03.23	BY	2012.03.23		
TITLE PROJECT/PROJECT FUNCTION DAKAR10F/IG MAIN BOARD							
SIZE=		VER:		SIZE	CODE	DOC NUMBER	REV
FILE NAME:		R/N		SHEET	CS	1310A-004901-G-MTR	X02

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D

D

C

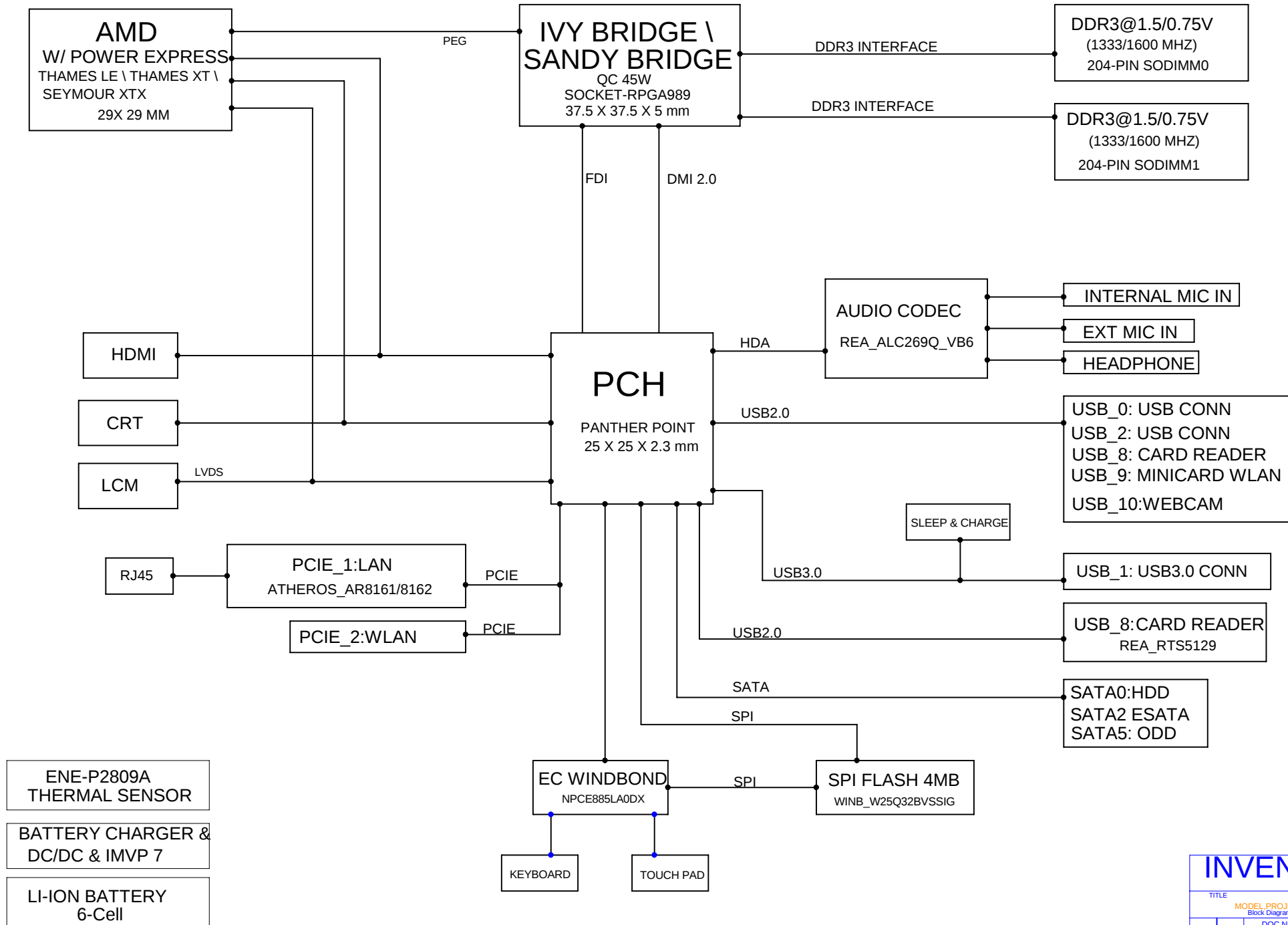
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A

A

**INVENTEC**

TITLE

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6

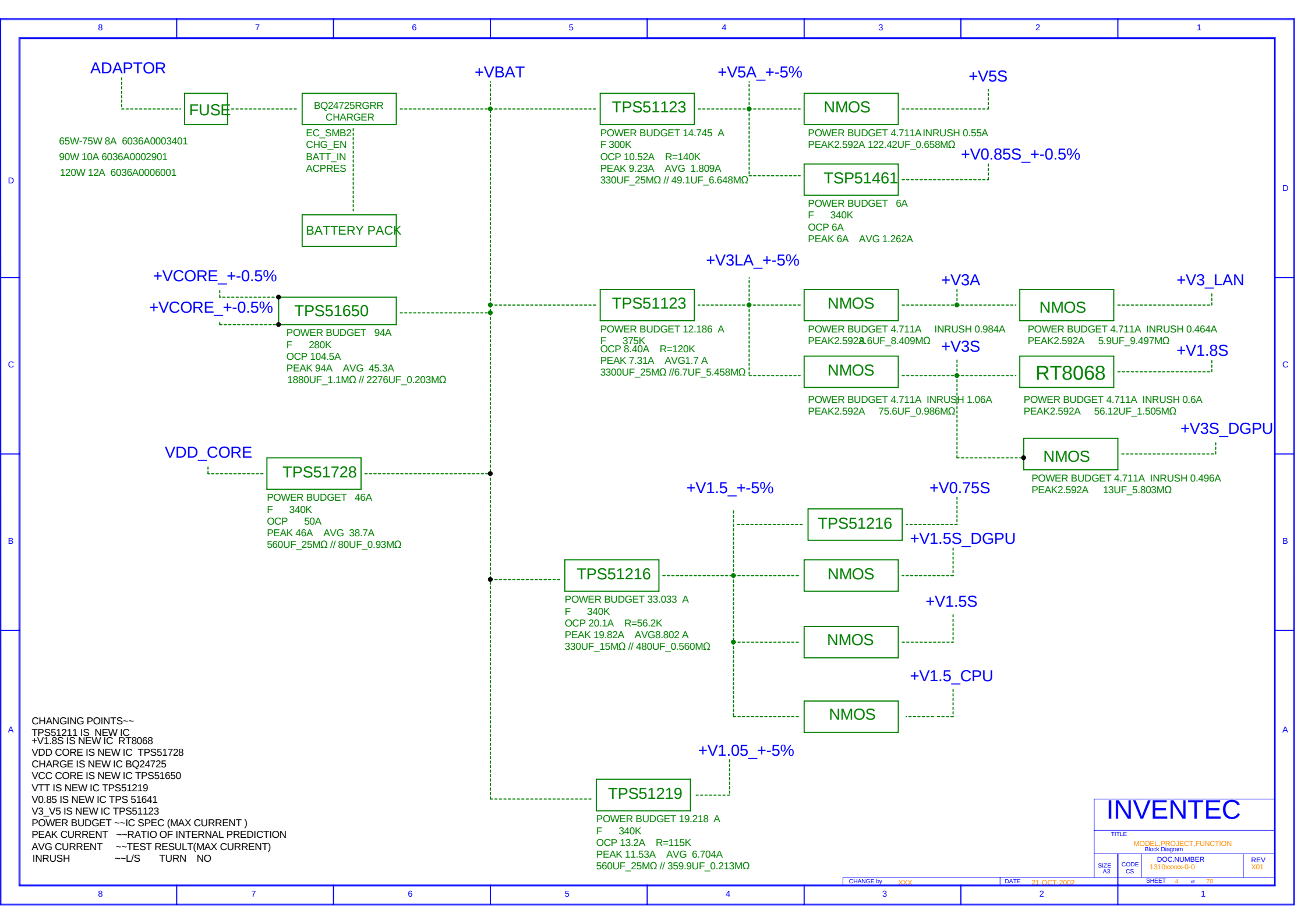
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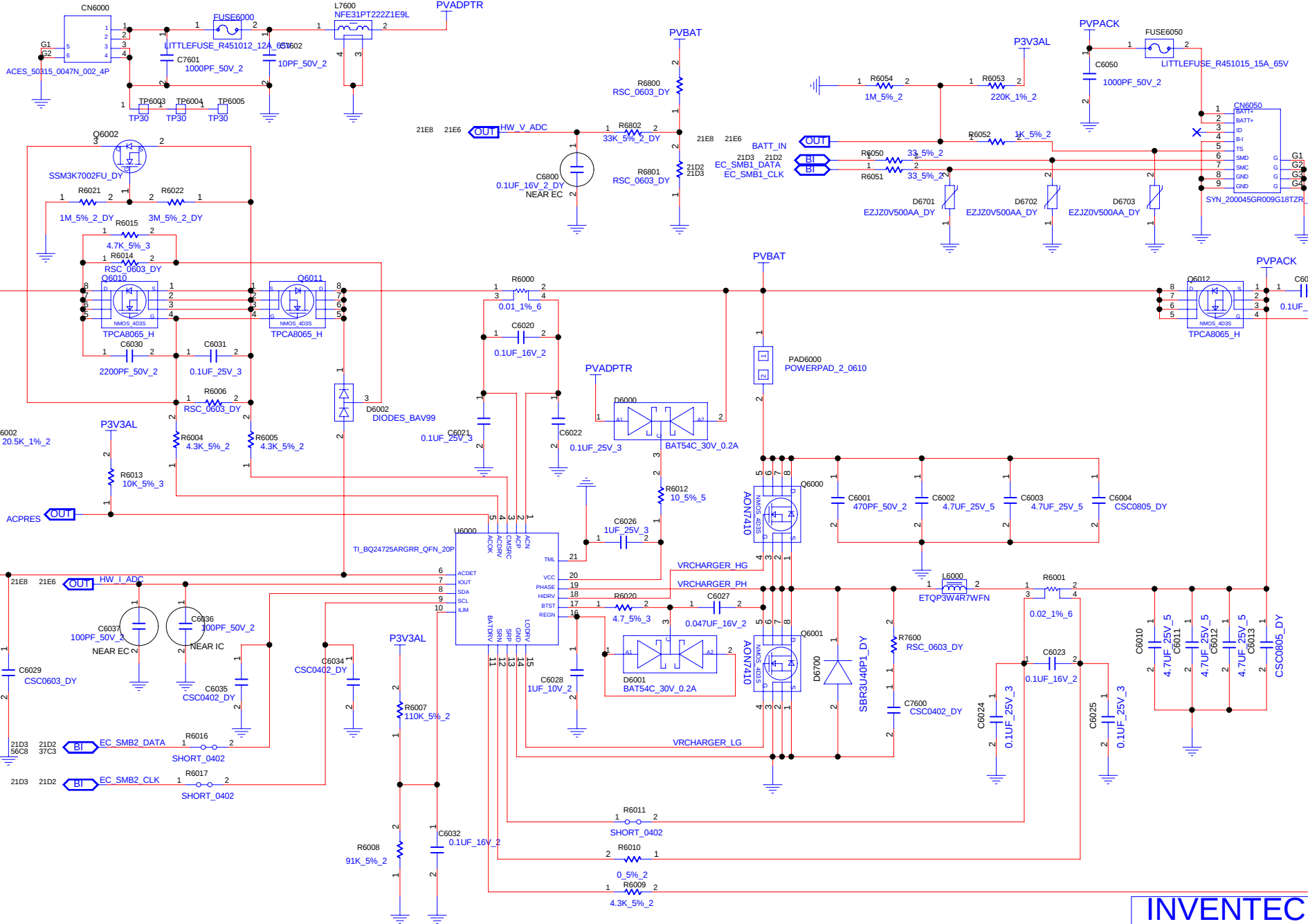
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2

1



FUSE6000
65W-75W 8A(6036A0003401)
90W 10A(6036A0002901)
120W 12A(6036A0006001)



INVENTEC

TITLE

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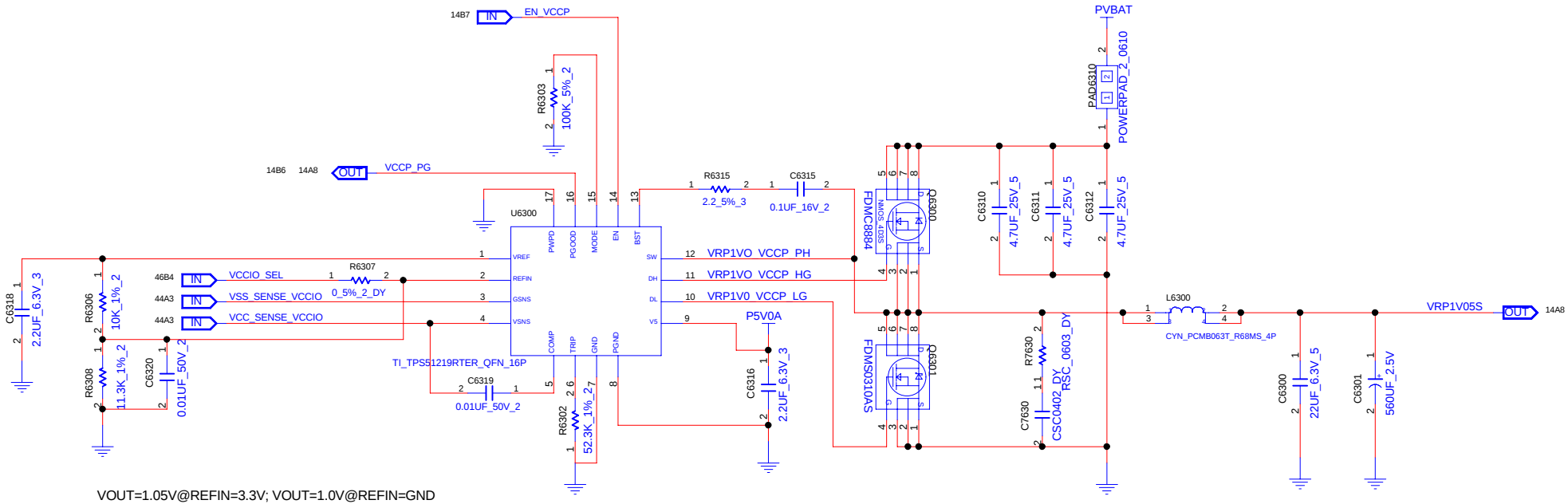
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DATE

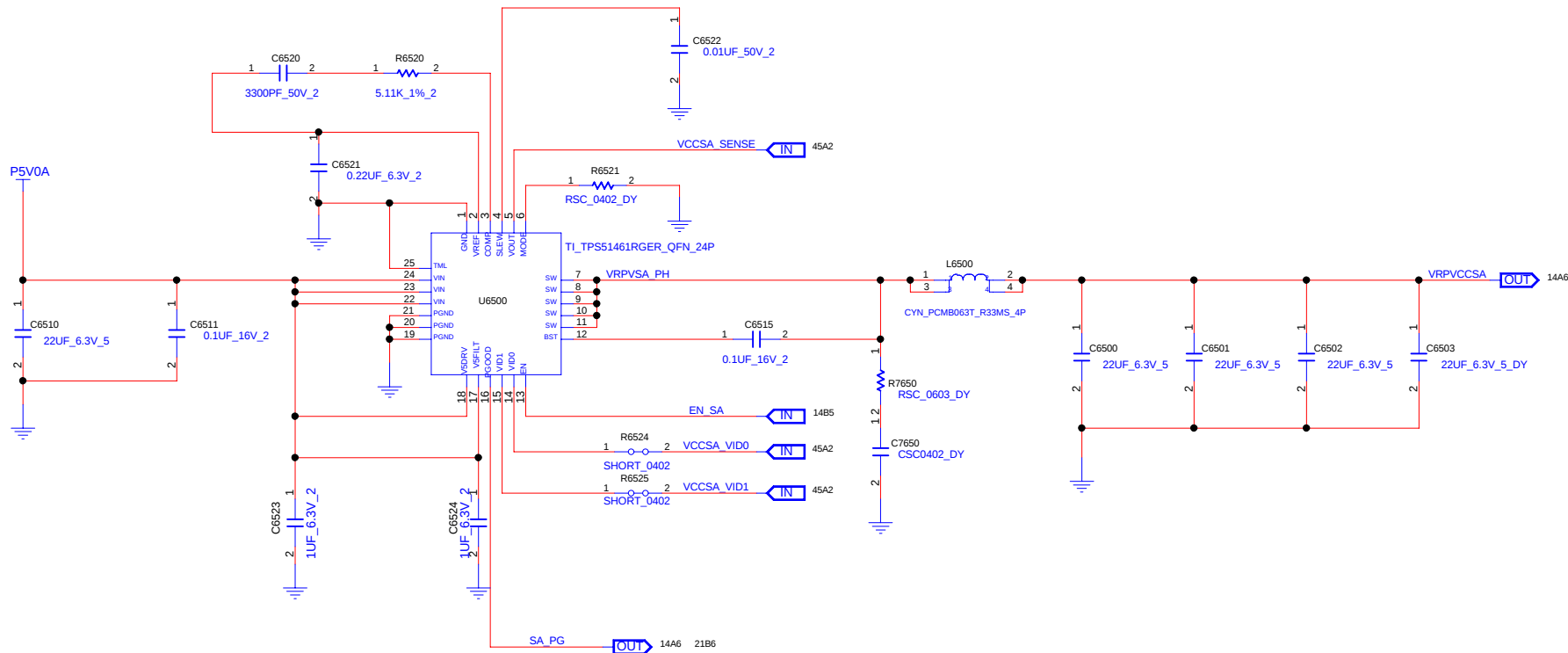
21-OCT-2002

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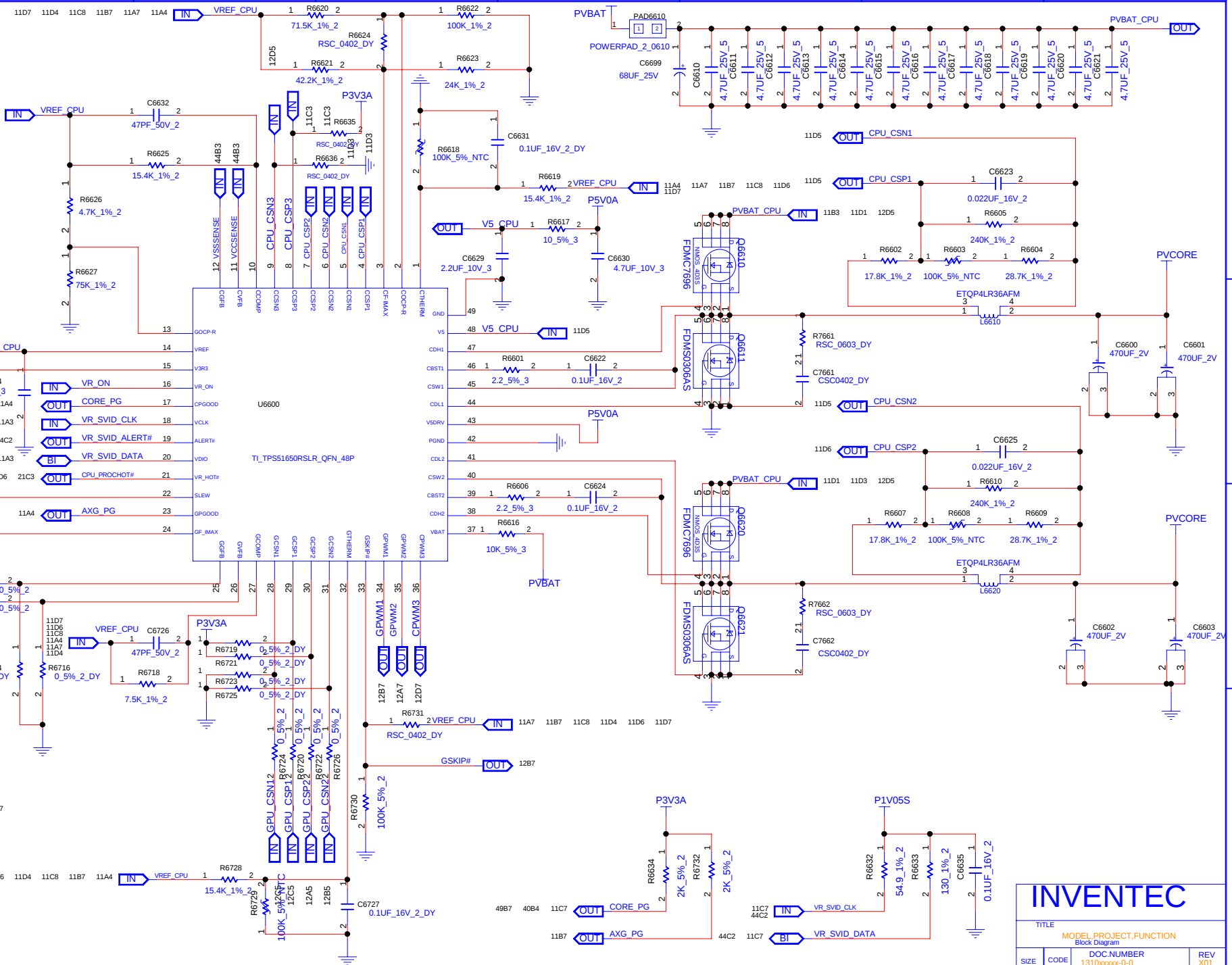
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	UMA/PX		DIS.	
	3+2	3+0	2+1	2+0
R6620	71.5K	71.5K	44.2K	44.2K
R6621	42.2K	42.2K	90.9K	90.9K
R6622	100K	100K	39K	39K
R6626	4.7K	DNP	3.3K	DNP
R6627	75K	DNP	56K	DNP
R6711	100K	DNP	200K	DNP
R6712	24K	DNP	30K	DNP
R6719	DNP	0	DNP	0
R6723	DNP	0	DNP	0
R6635	DNP	DNP	0	0
R6636	DNP	DNP	0	0
R6714	DNP	0	DNP	0
R6716	DNP	0	DNP	0
R6721	DNP	0	0	0
R6725	DNP	0	0	0
R6726	0	DNP	DNP	DNP
R6724	0	DNP	0	DNP
R6722	0	DNP	DNP	DNP
R6720	0	DNP	0	DNP
R6713	0	DNP	0	DNP
R6715	0	DNP	0	DNP

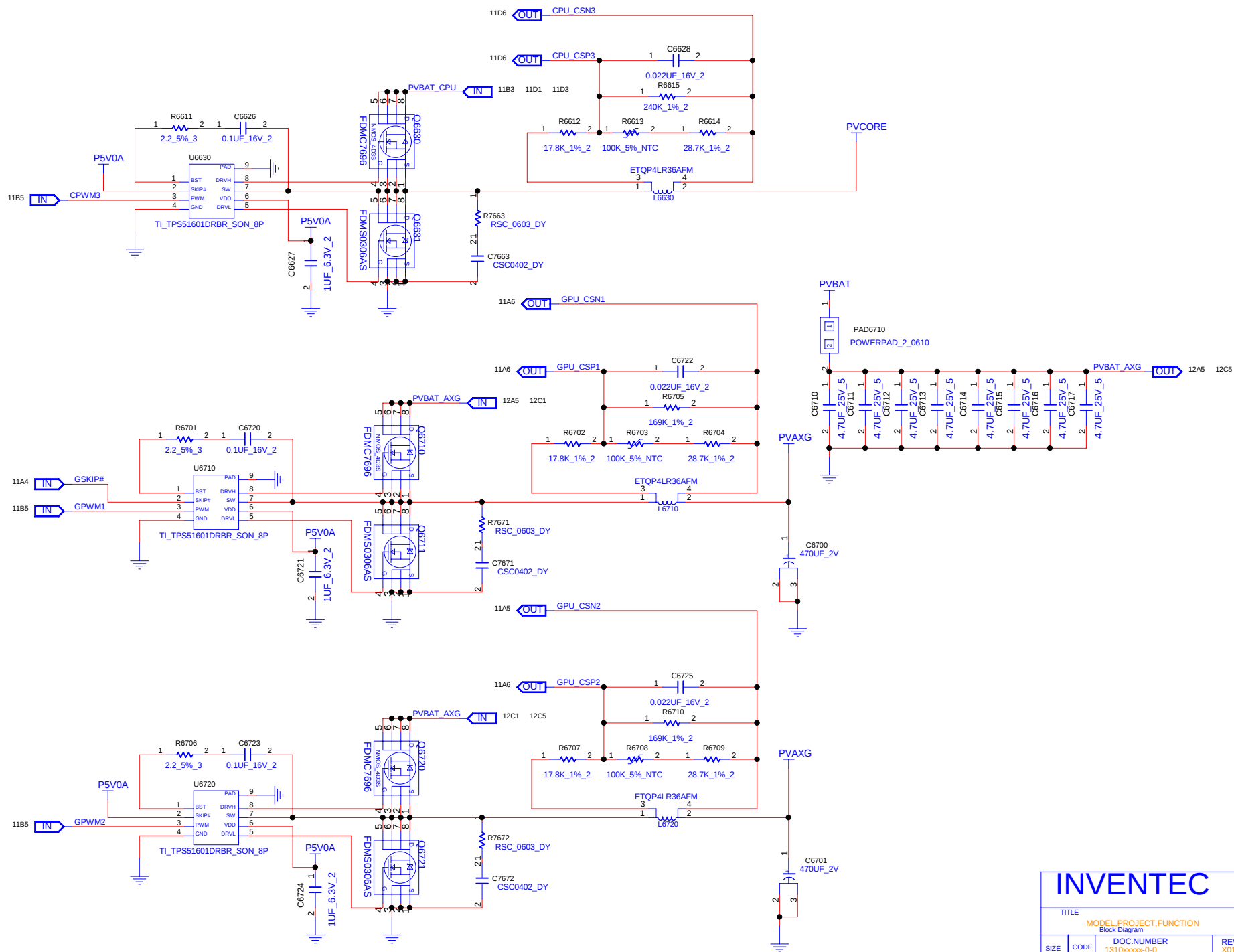


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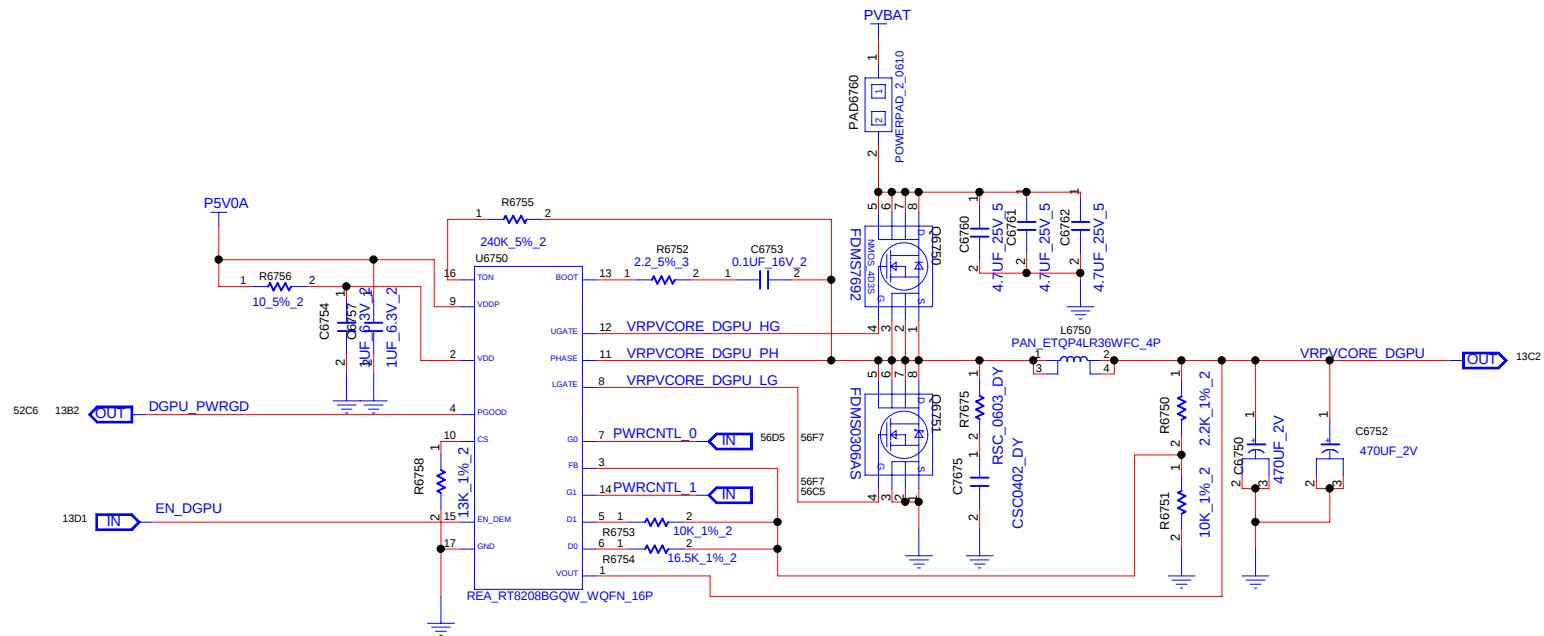
TITLE	MODEL,PROJECT,FUNCTION

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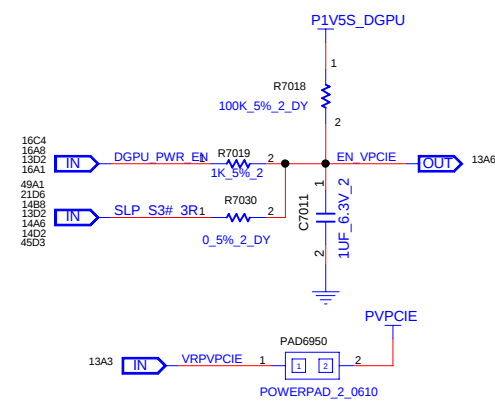
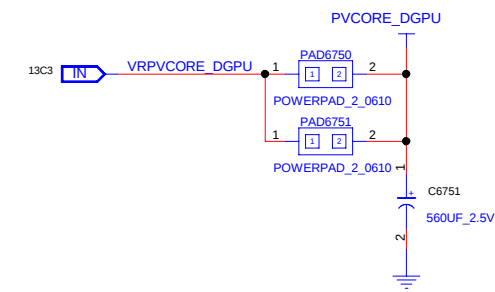
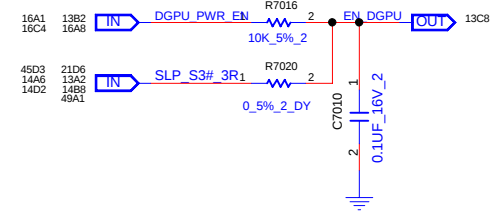
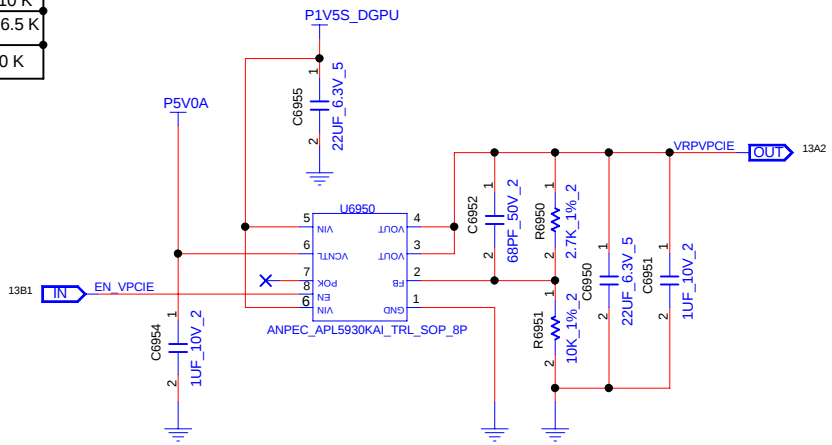
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VGA TYPE :	SEYMOUR GTX	THAMES XT	THAMES LE	
G0	G1	VOUT	VOUT	VOUT
0	0	1.15	N/A	N/A
0	1	1.05	N/A	N/A
1	0	1	1	1
1	1	0.9	0.9	0.9

P.S. R6750(R1)R6751(R2)R6753(R3)R6754(R4)

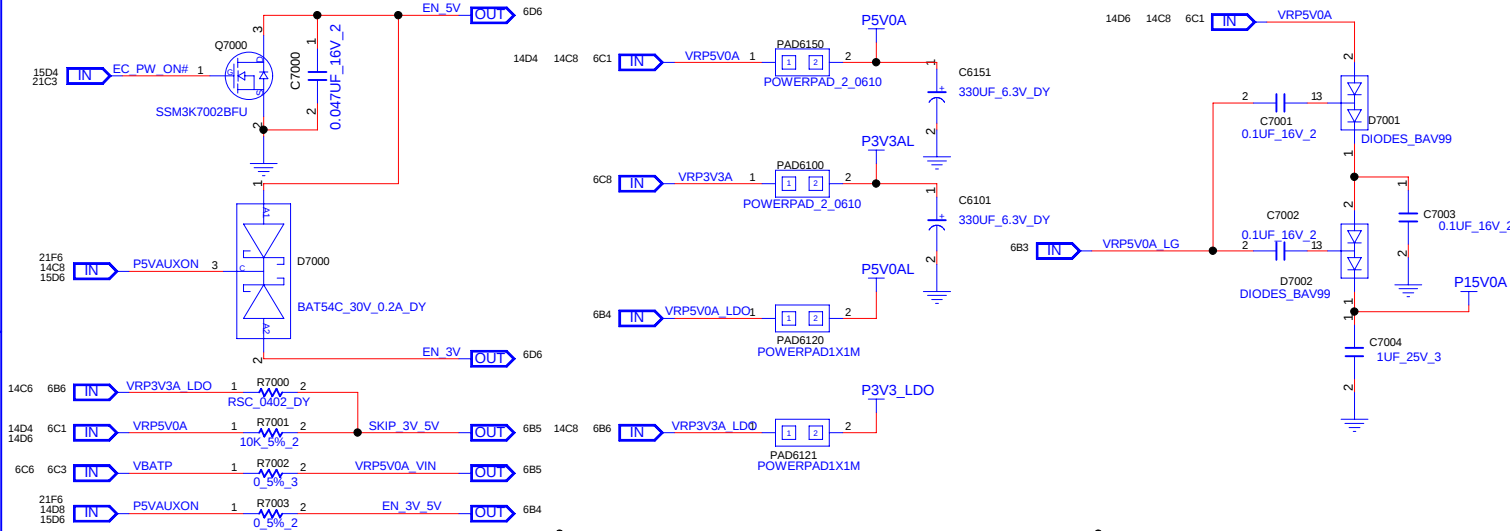
RT8208A	RT8208B	Output Voltage Equation	
G0	G1	G0	G1
0	0	1	1
1	0	0	1
0	1	1	0
1	1	0	0



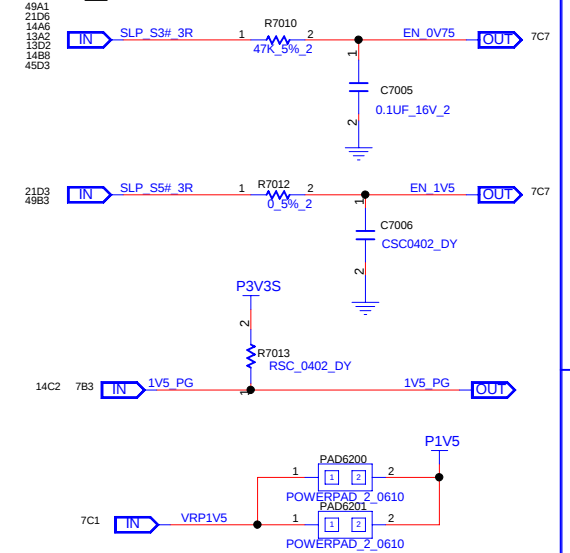
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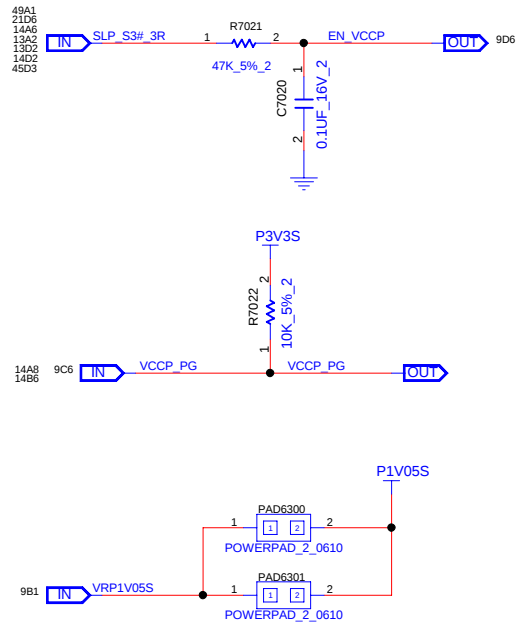
3V & 5V



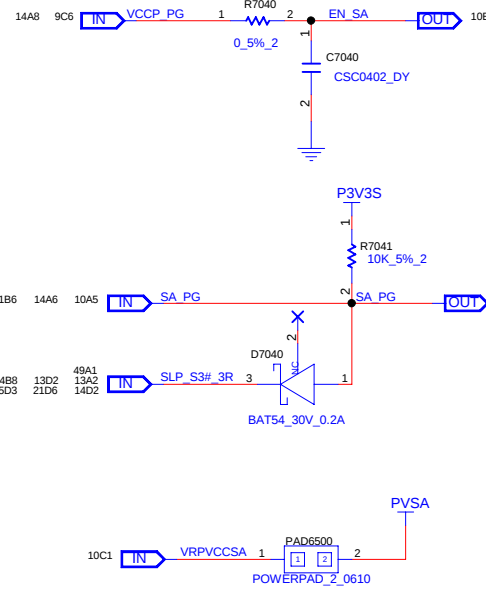
DDR_P1V5



VCCIO

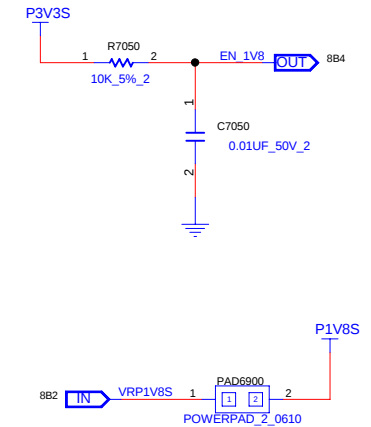


VCCSA



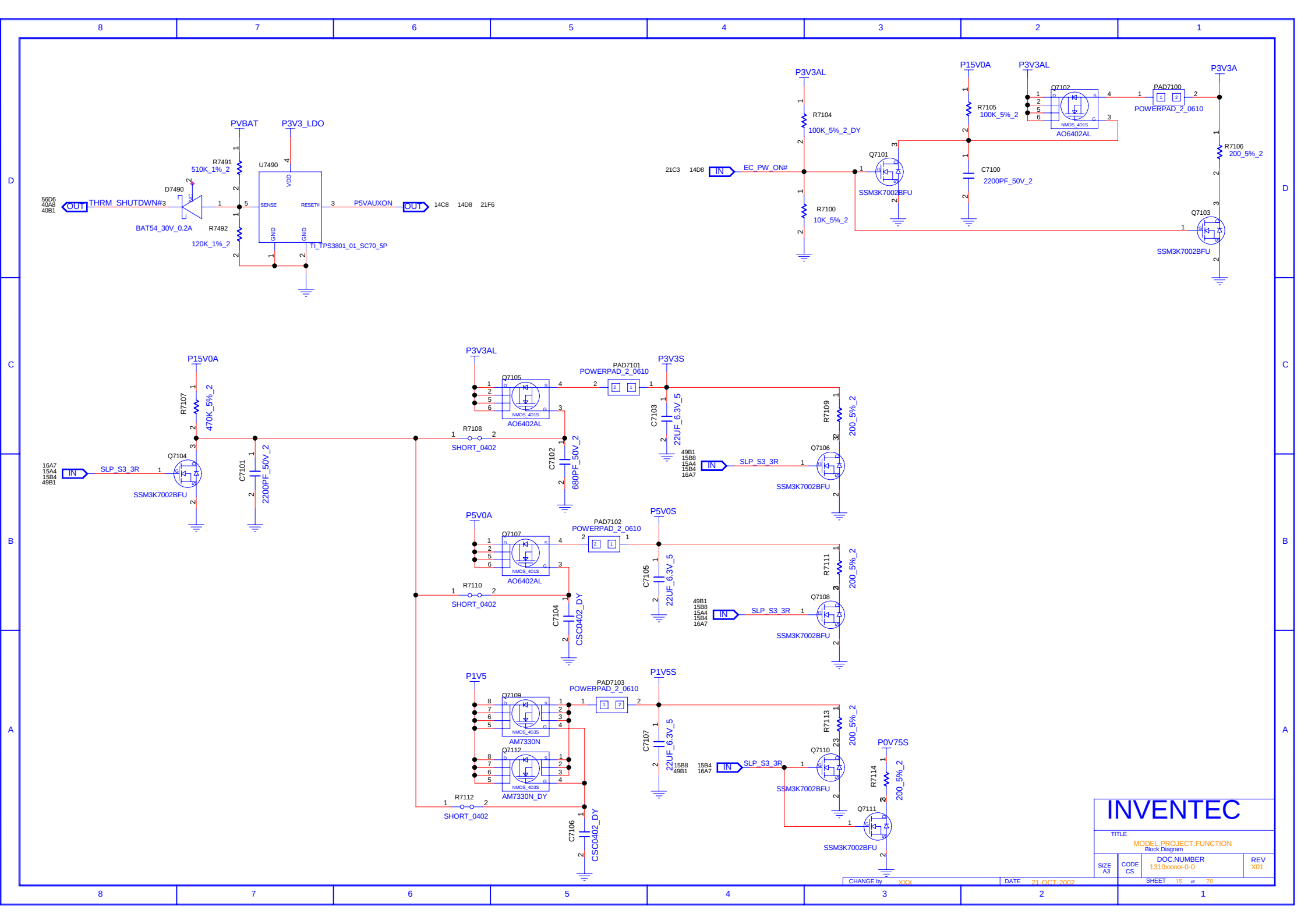
DGPU

P1V8S



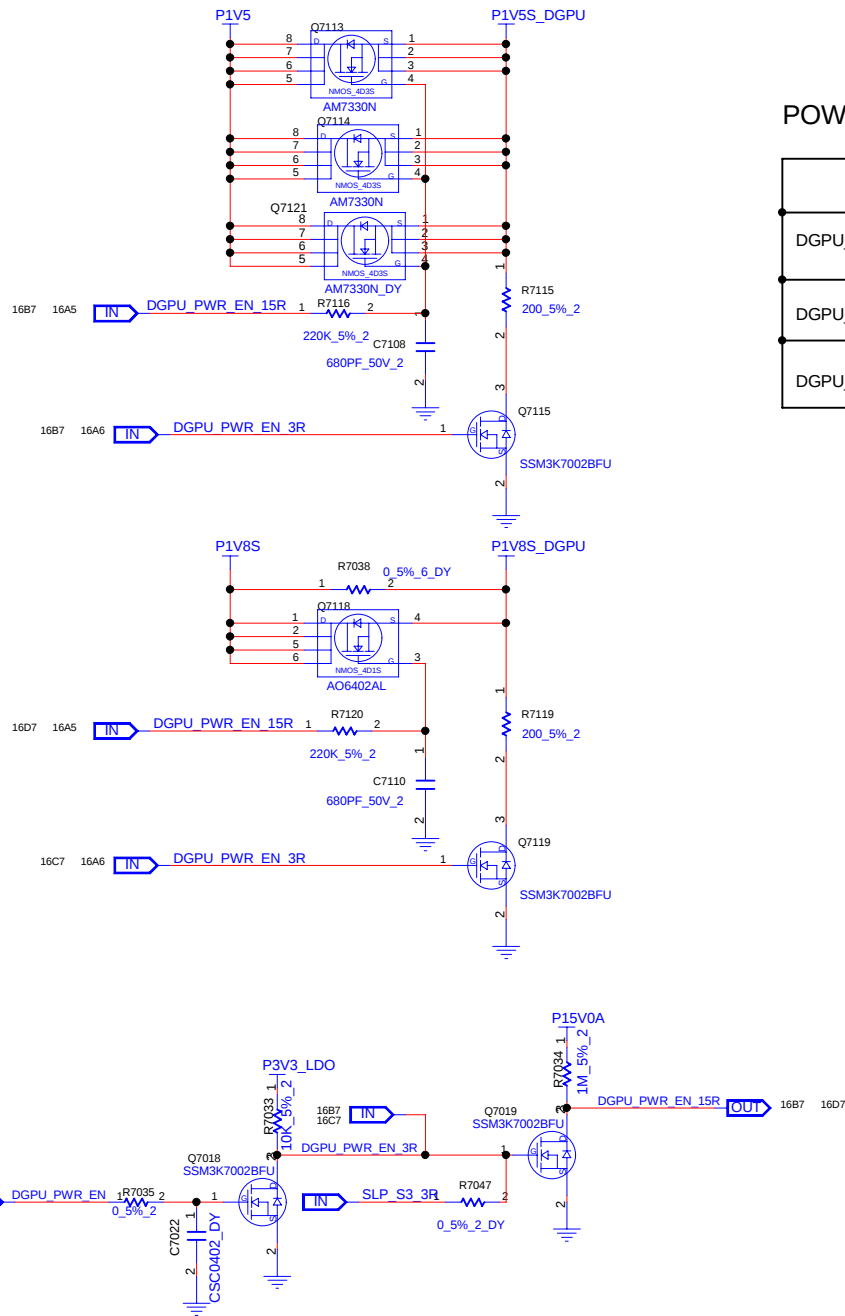
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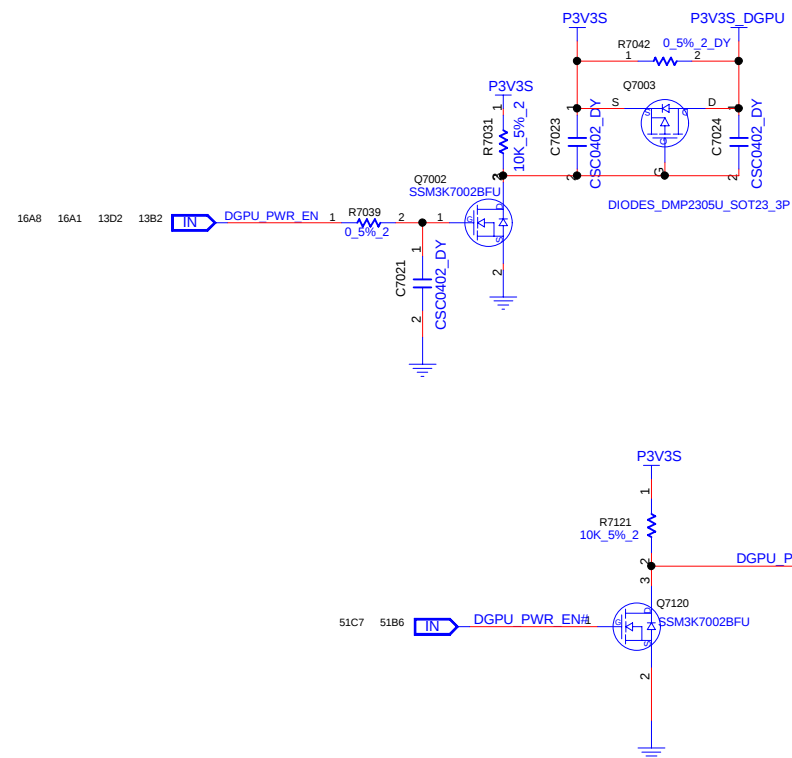
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TITLE			
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POWER EXPRESS

	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PWRGD			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED



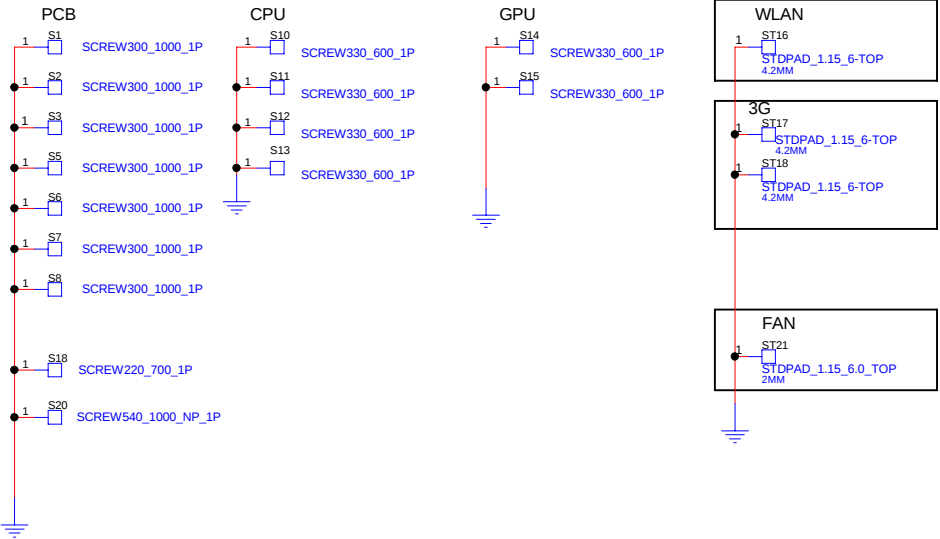
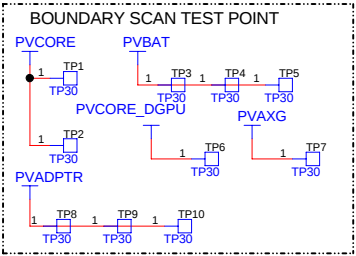
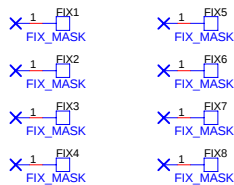
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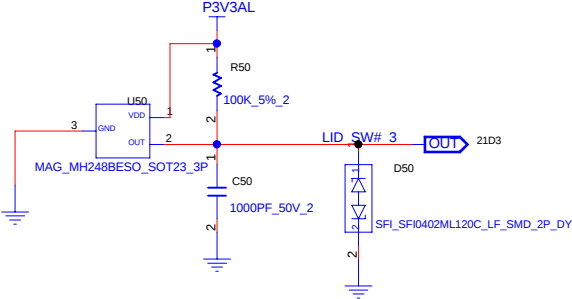
REFERENCE 0~49(PCB SCREW)



INVENTEC

TITLE			
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REFERENCE 50-99(HALL SENSOR)

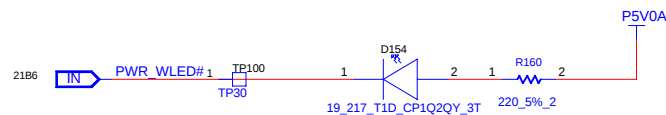


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TITLE MODEL,PROJECT,FUNCTION Block Diagram			
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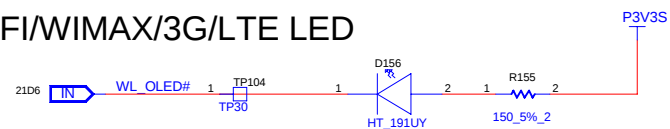
REFERENCE 100~199(LED)

POWER ON LED

D154 BRIGHT WHEN SYSTEM IS POWER ON
D154 BLINK WHEN SYSTEM GO TO SLEEP

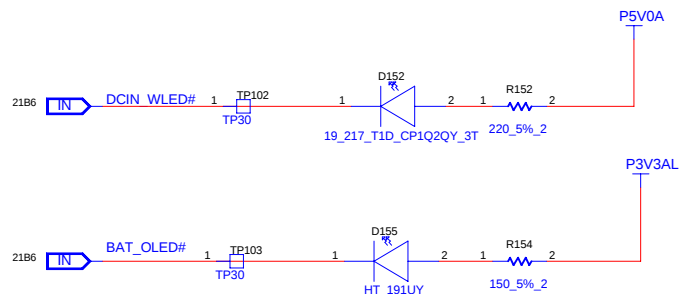


WIFI/WIMAX/3G/LTE LED



DC IN / BATTERY CHARGE LED

D152 BRIGHT: BOTH AC-ADAPTER IS PLUGGED IN AND BATTERY IS FULL CHARGED
D155 BRIGHT: WHILE CHARGING BATTERY FROM AC-ADAPTER
BLINK: LOW BATTERY



INVENTEC

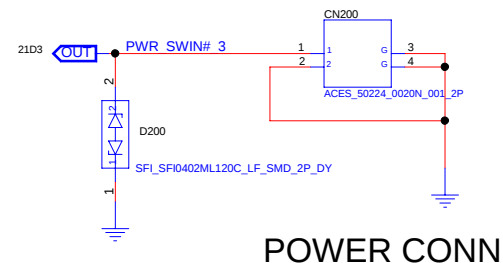
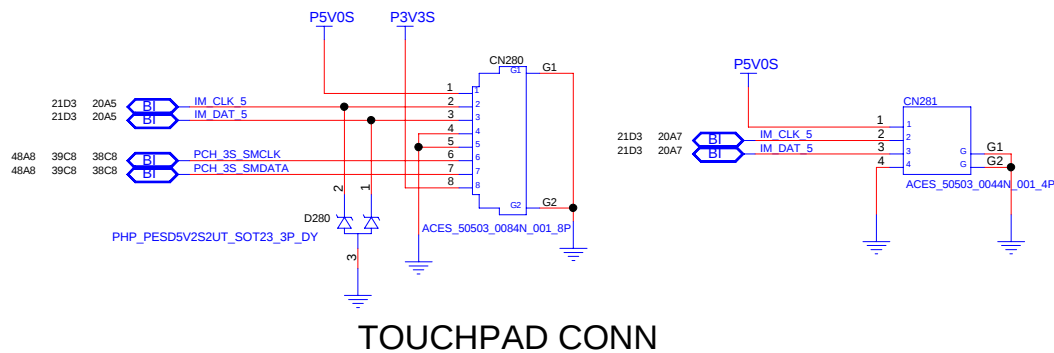
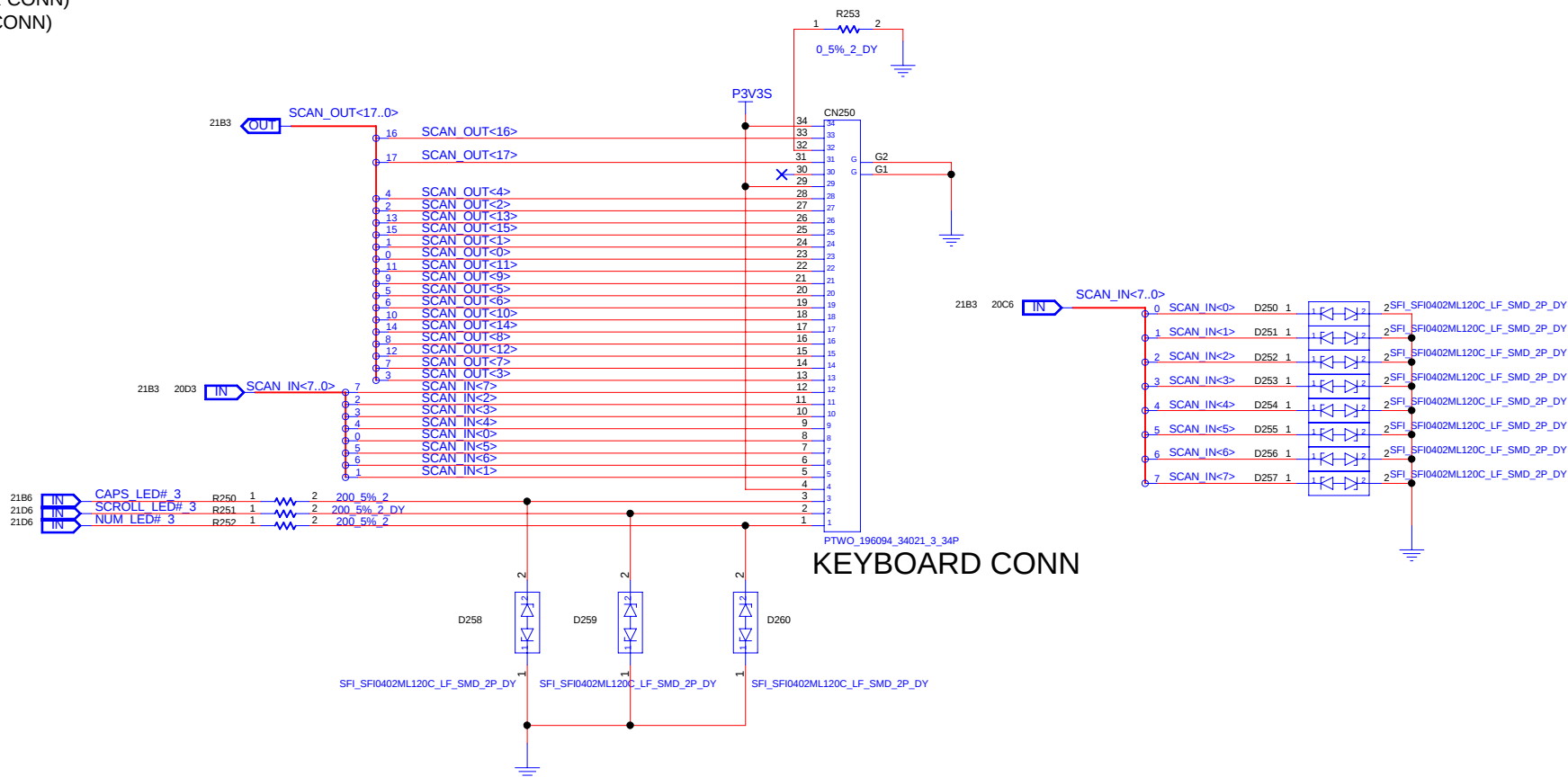
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MODEL PROJECT FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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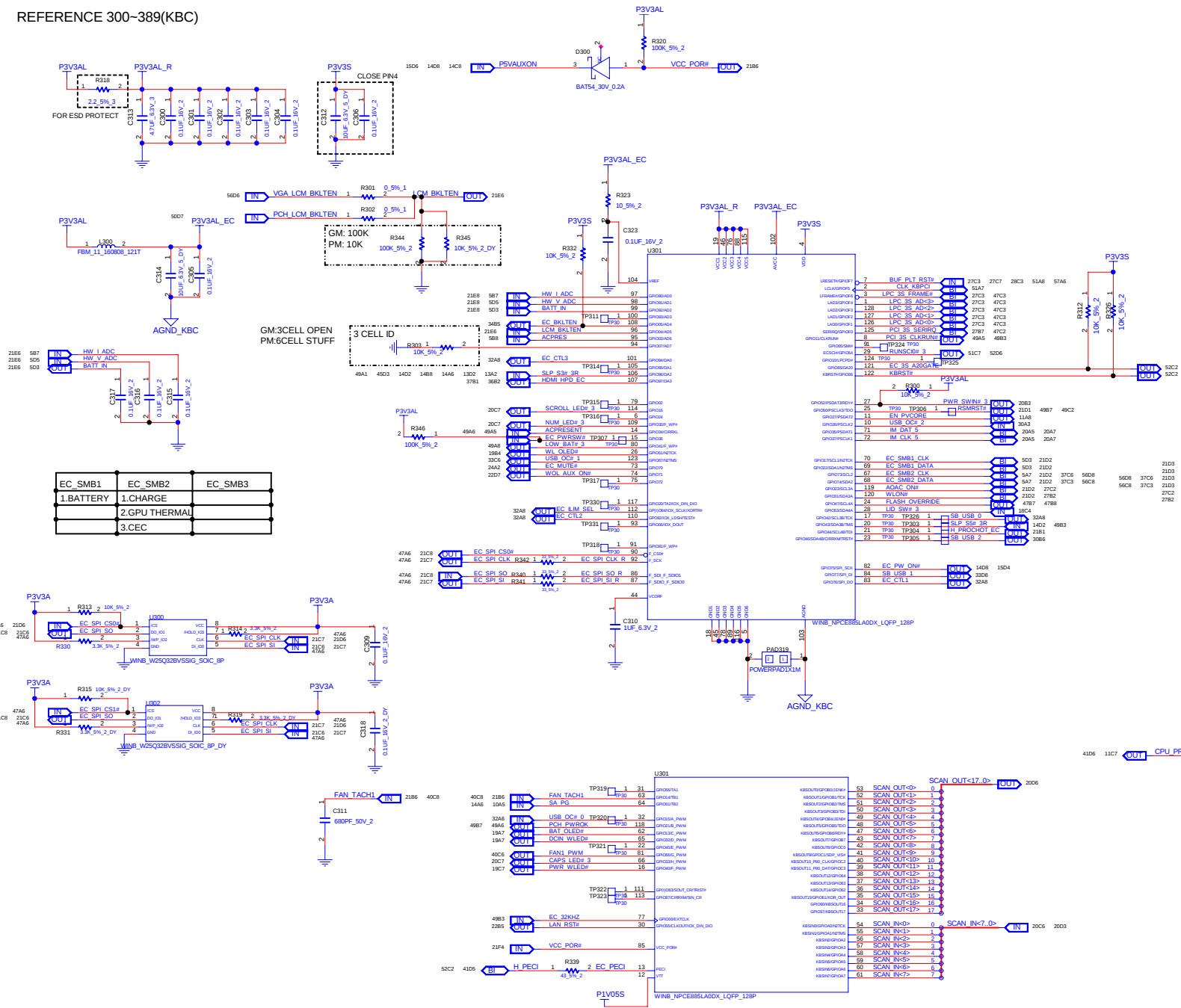
REFERENCE 200~249(POWER CONN)
REFERENCE 250~299(KB/TP CONN)



INVENTEC

TITLE			
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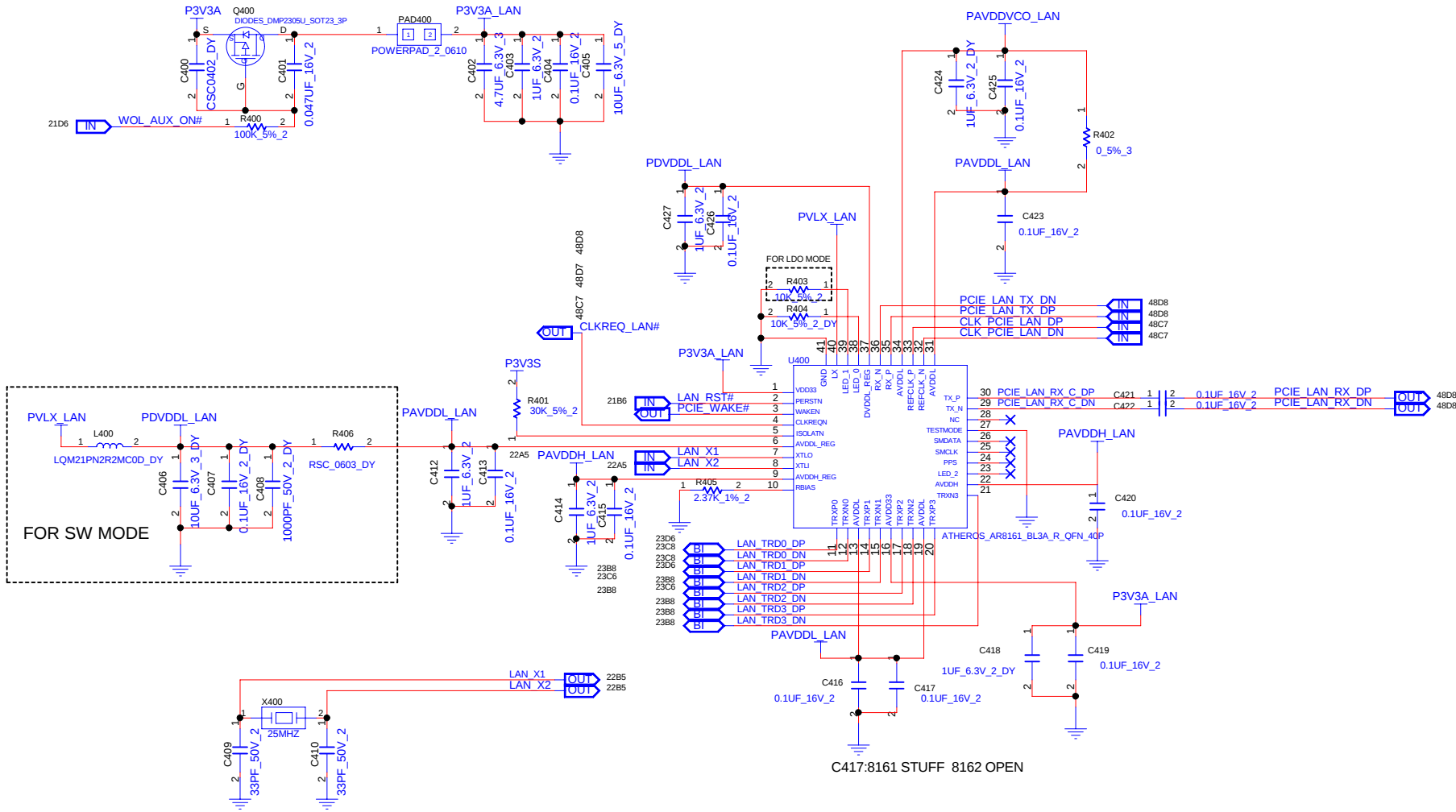
REFERENCE 300-389(KBC)



INVENTEC

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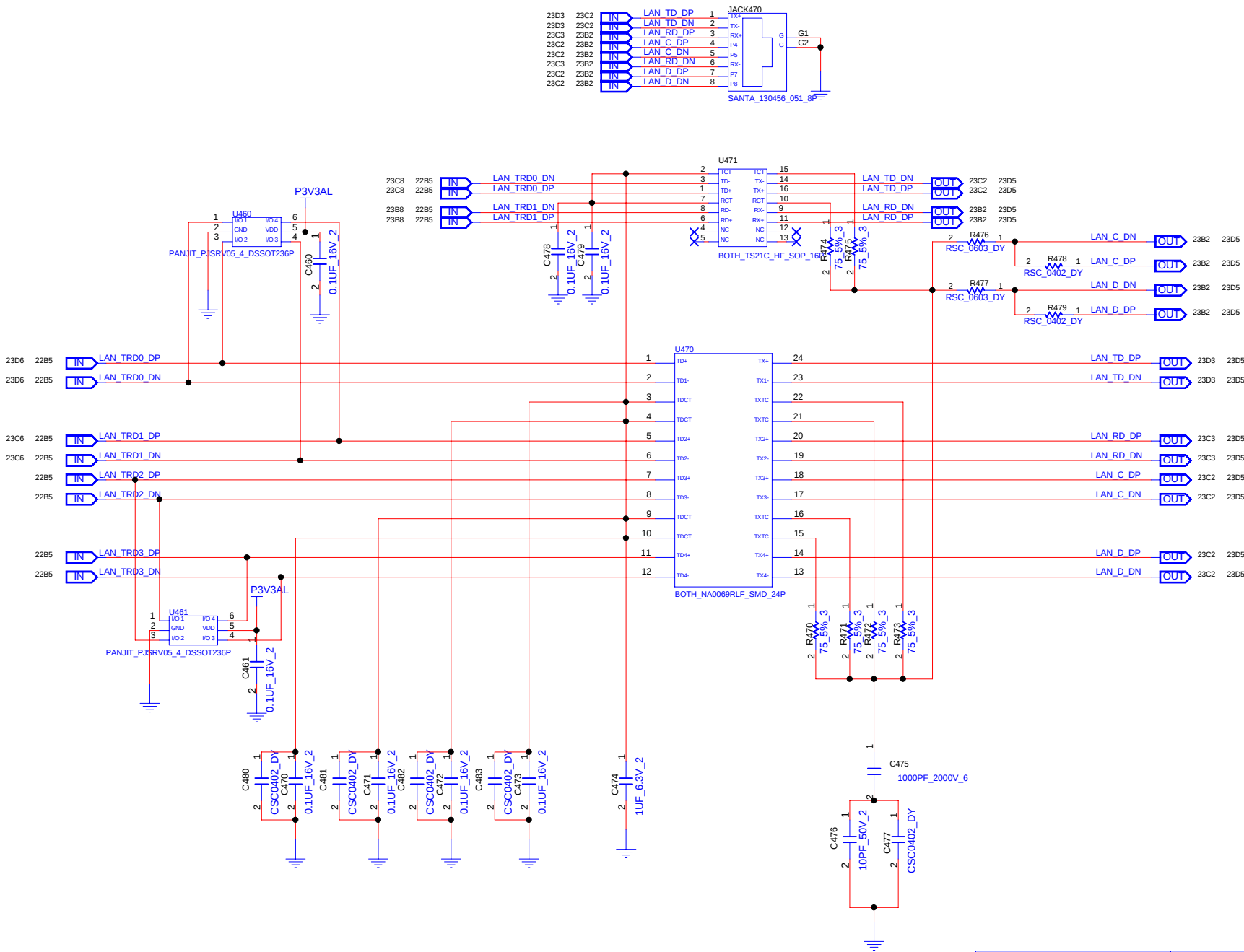


C417:8161 STUFF 8162 OPEN

INVENTEC

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Block Diagram			
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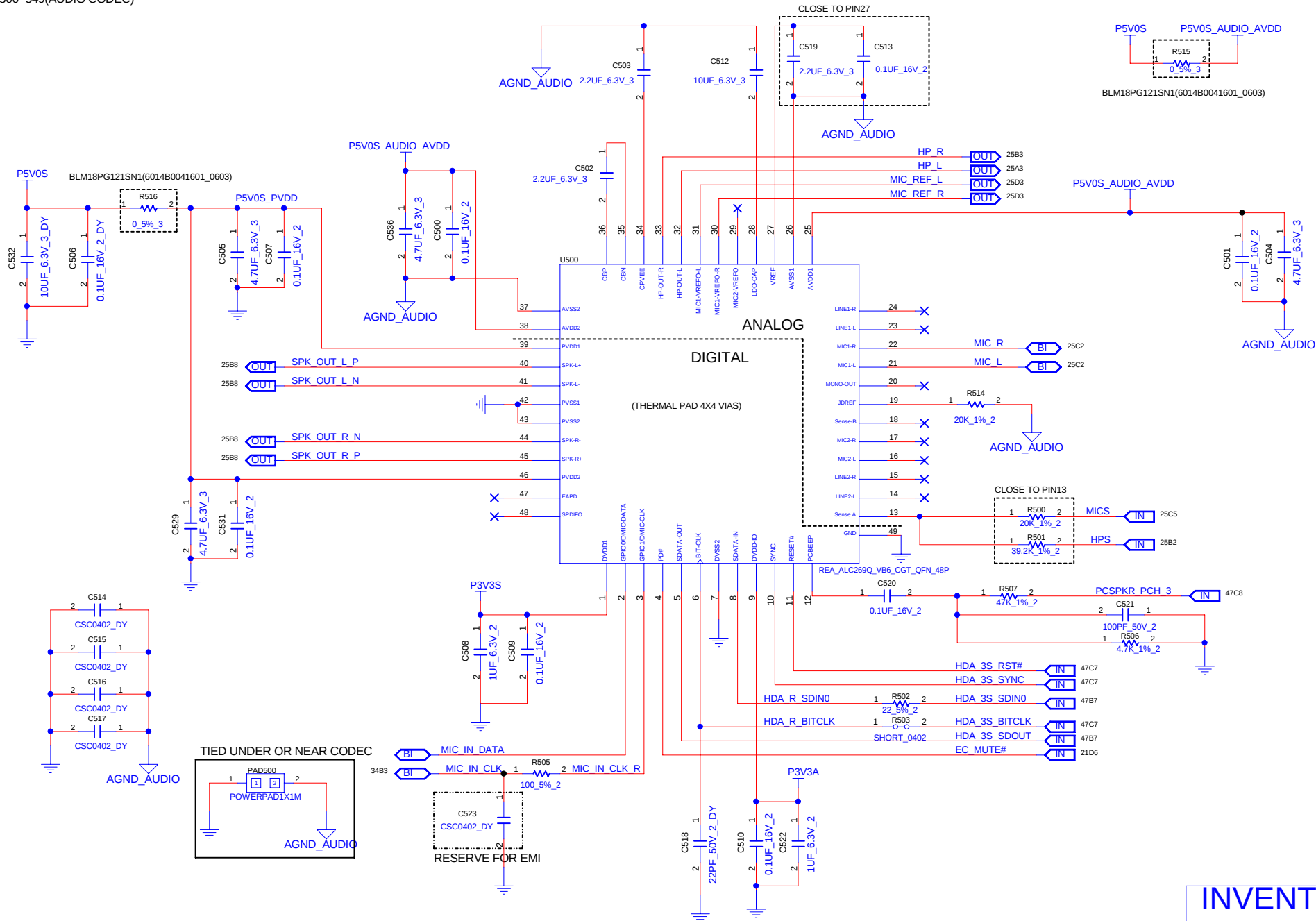
REFERENCE 400~499(LAN)



INVENTEC

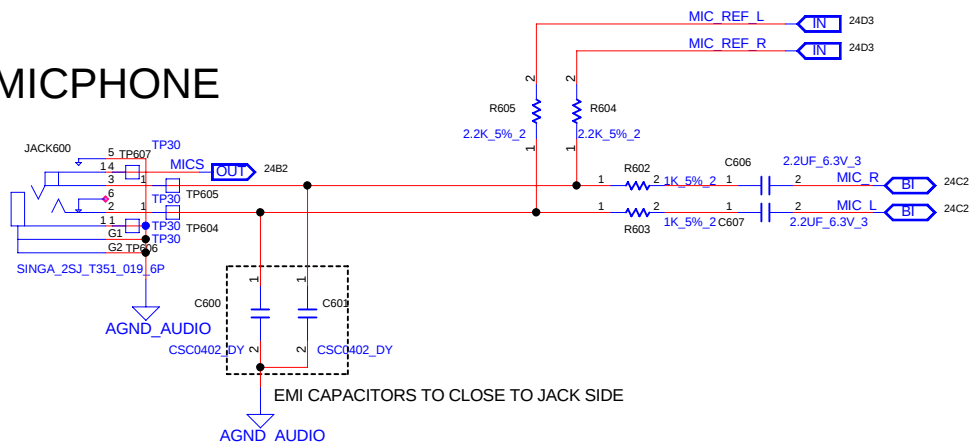
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Block Diagram			
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A3	CS	1310xxxx-0-0	X01

REFERENCE 500-549(AUDIO CODEC)

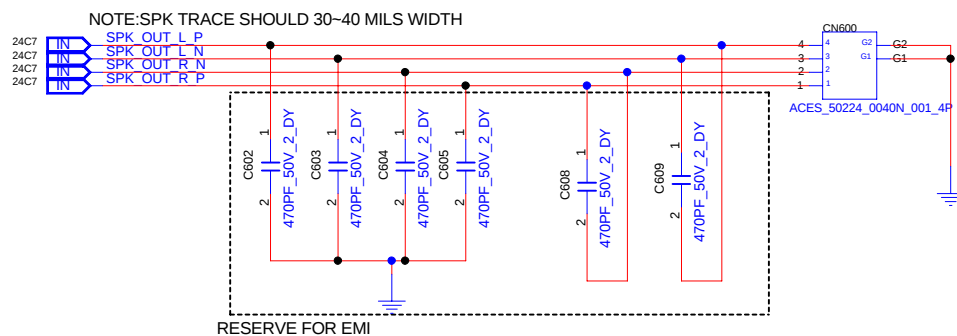


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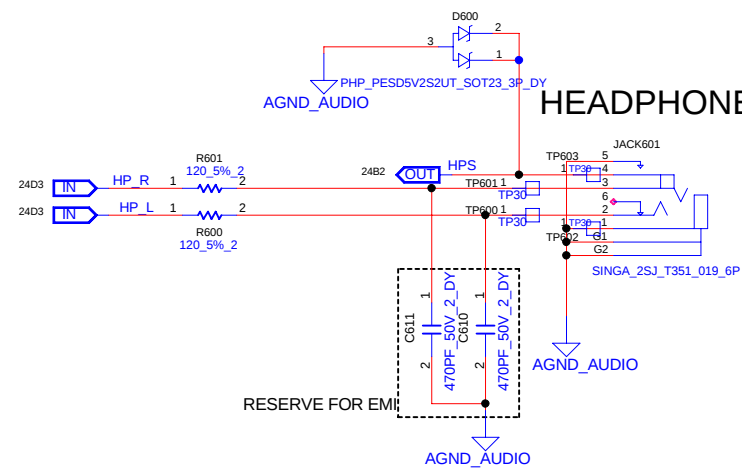
MICPHONE



INTERNAL SPEAKERS



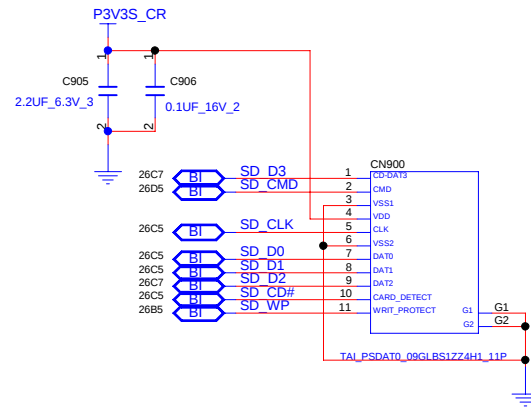
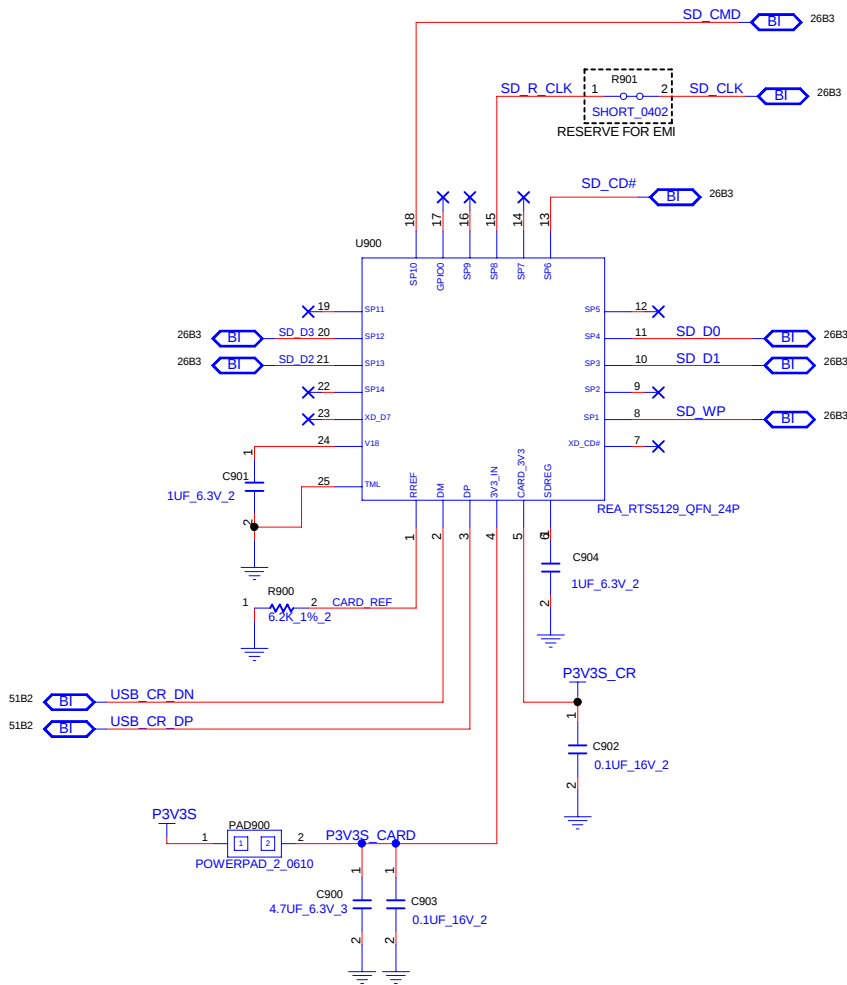
P-DY HEADPHONE



INVENTEC

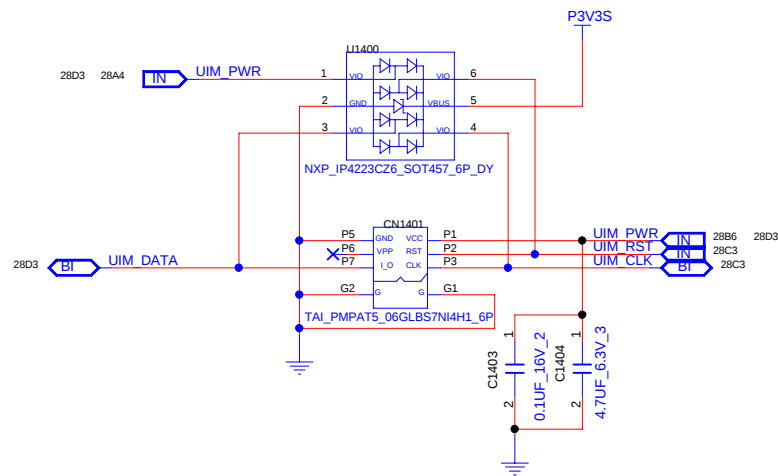
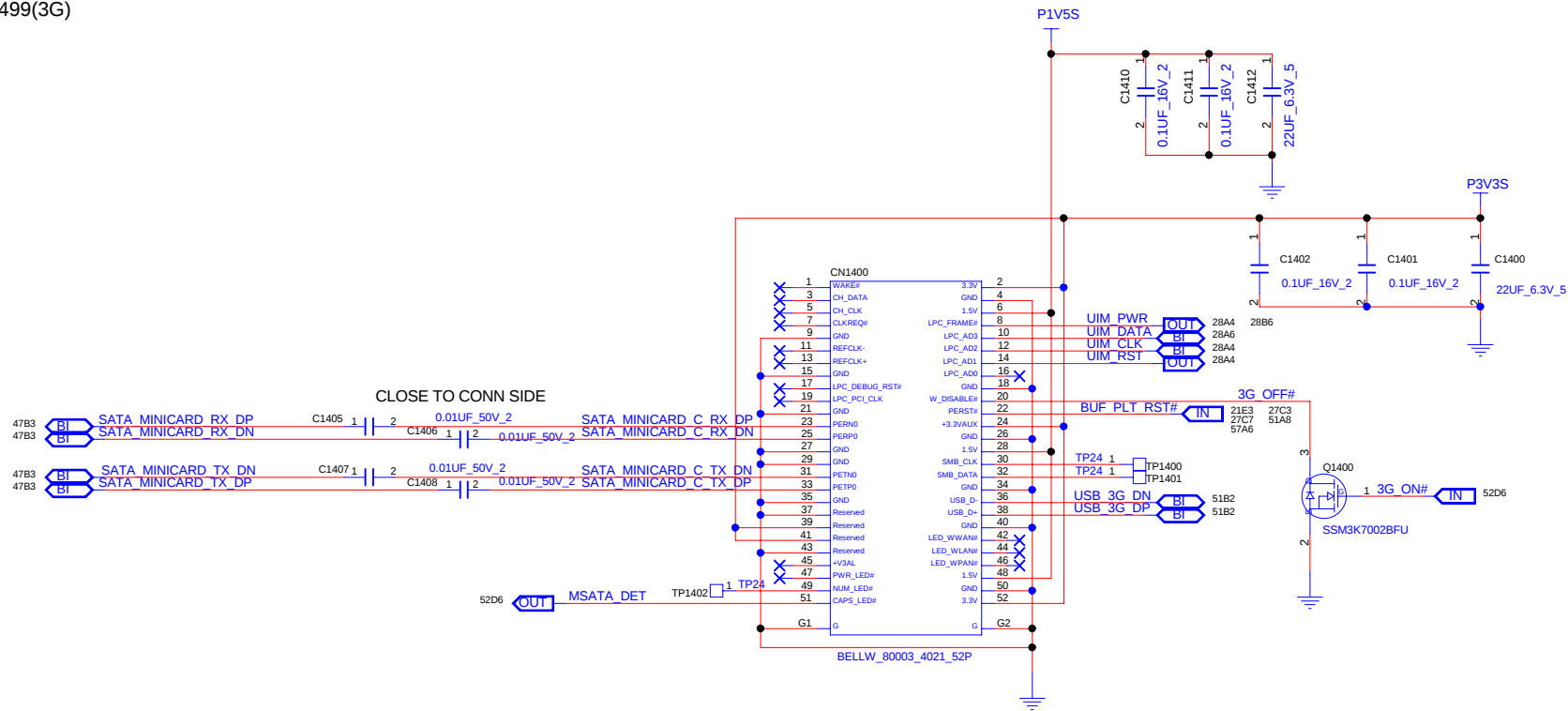
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SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0

REFERNCE 900~999(CARDREADER)



INVENTEC			
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REFERENCE 1400~1499(3G)



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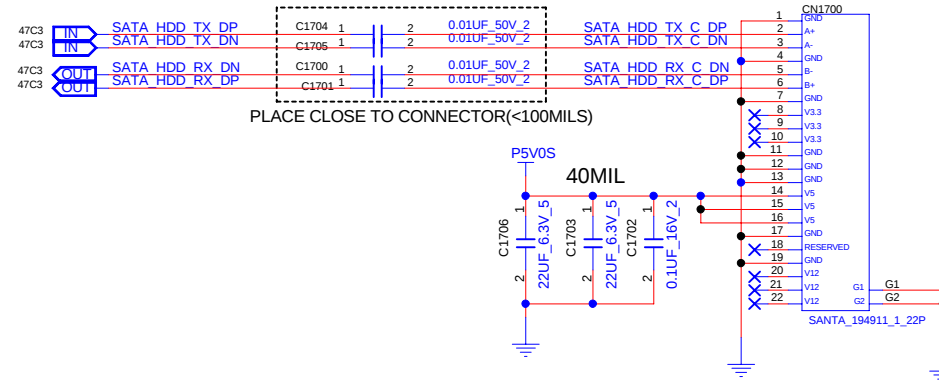
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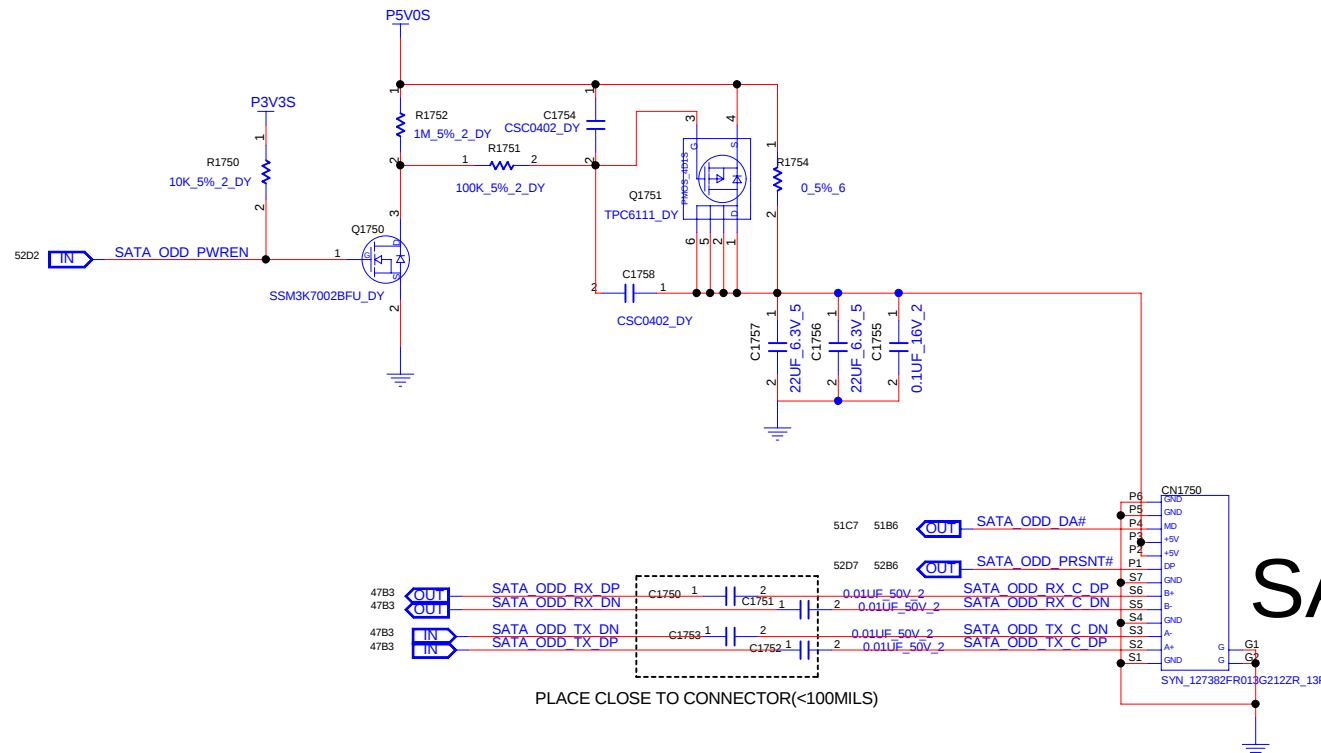
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REFERENCE 1700~1749(HDD)
REFERENCE 1750~1799(ODD)

SATA HDD



SATA ODD

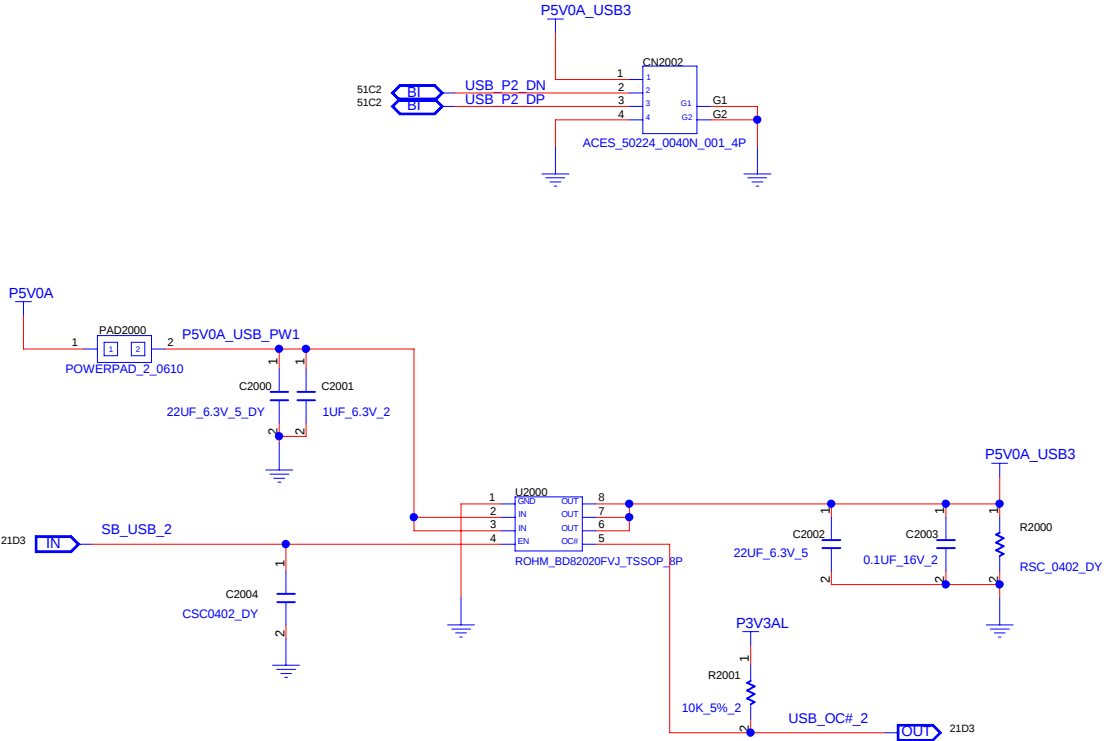


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INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risk Management	7. Project Risk Management
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

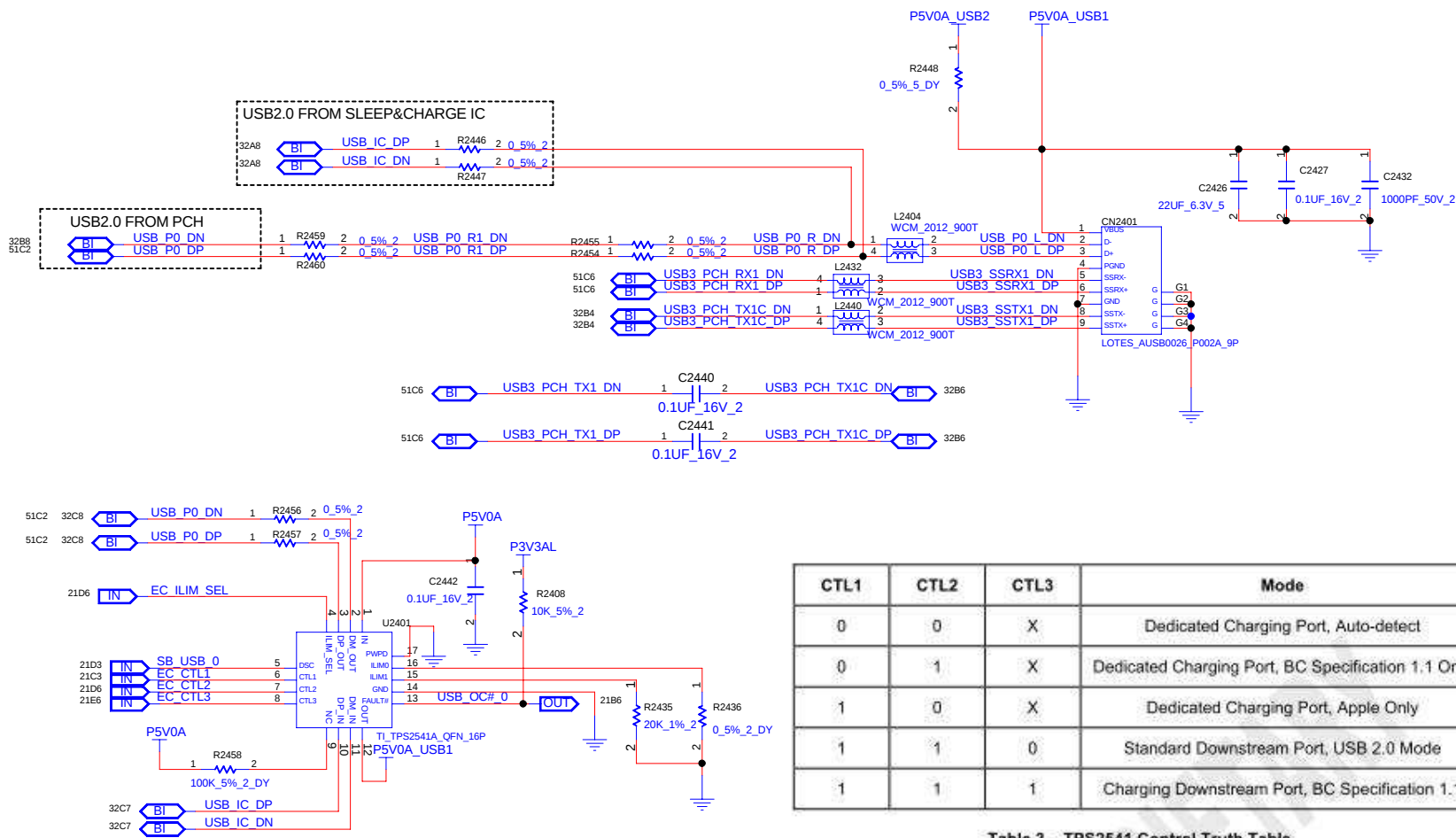
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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REFERENCE 2400~2499

REMOVE USB3.0 CONTROLLER IC

INVENTEC			
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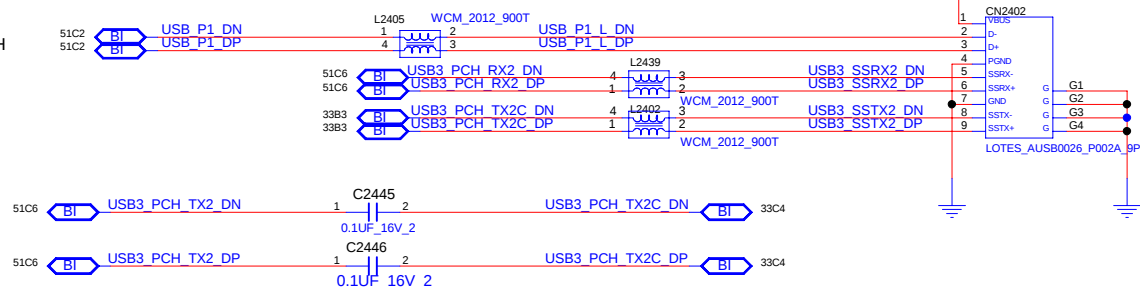
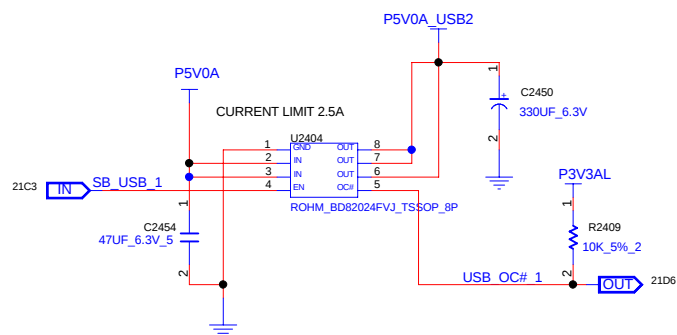
CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1.1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

Table 3 – TPS2541 Control Truth Table

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USB 3.0 CONNECTOR



INVENTEC

TITLE	MODEL, PROJECT, FUNCTION
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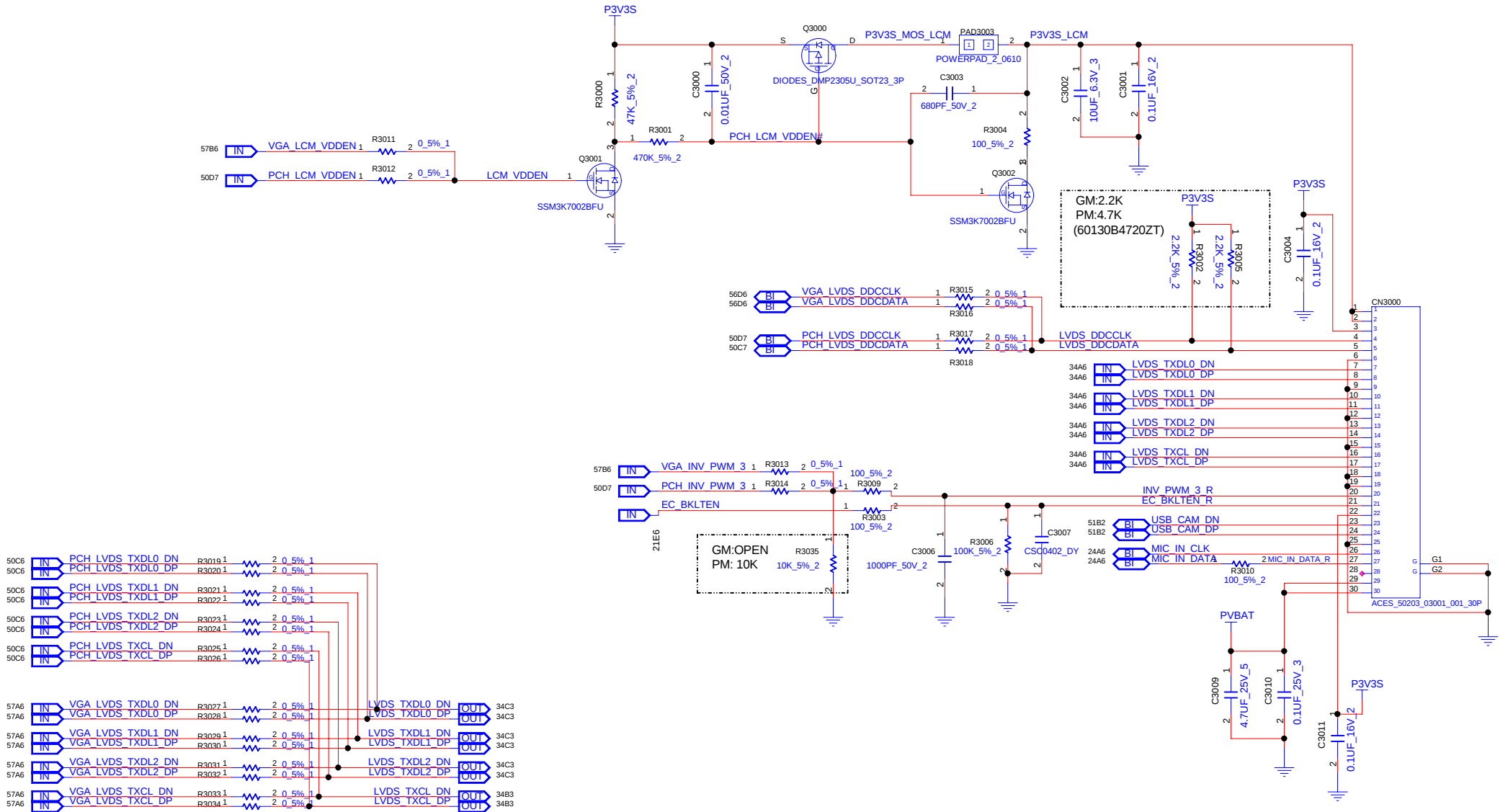
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REFERENCE 3000~3049(LCM)



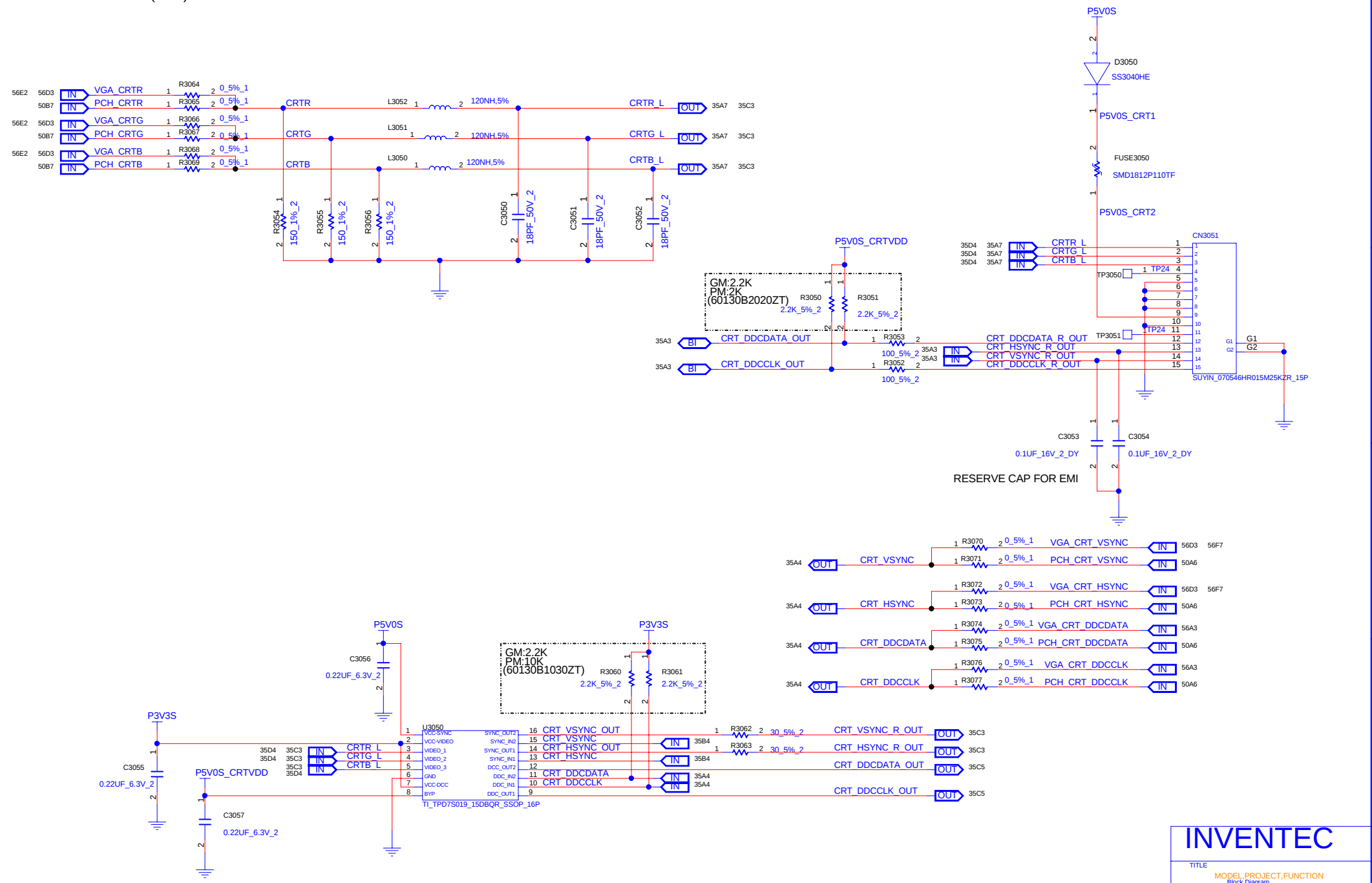
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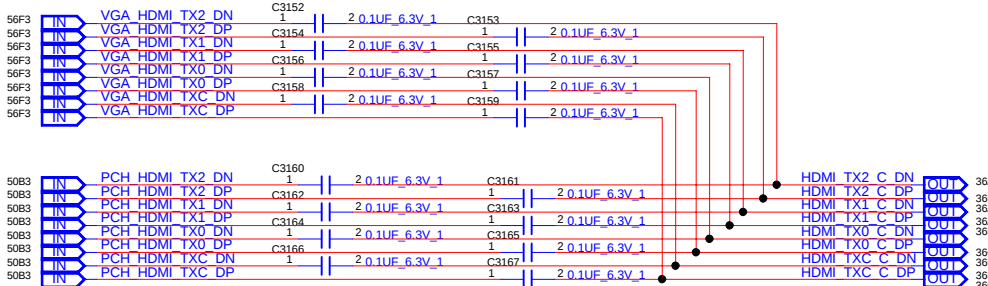
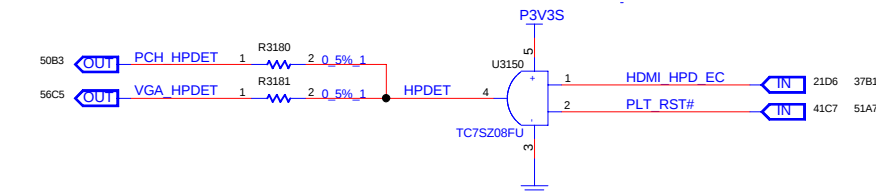
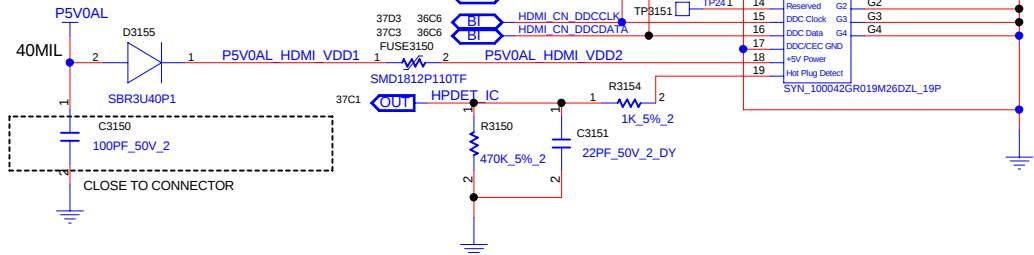
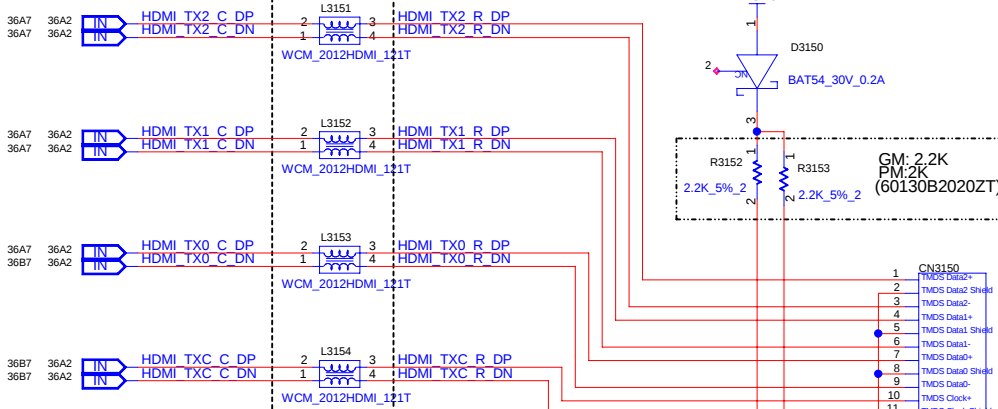
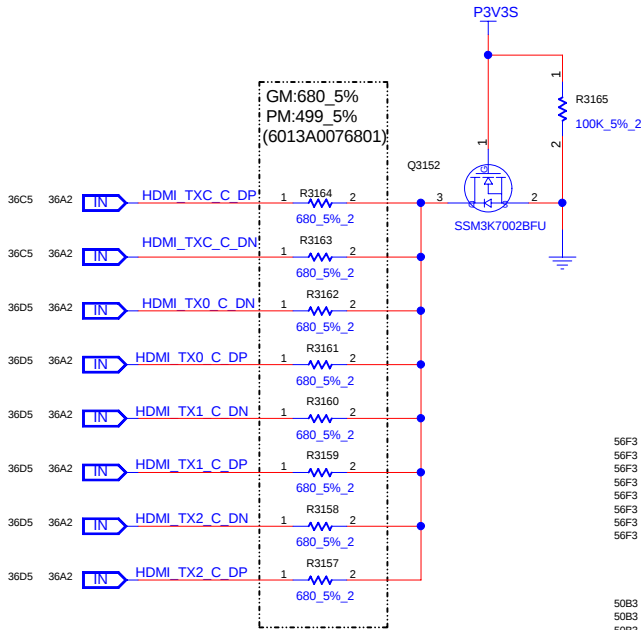
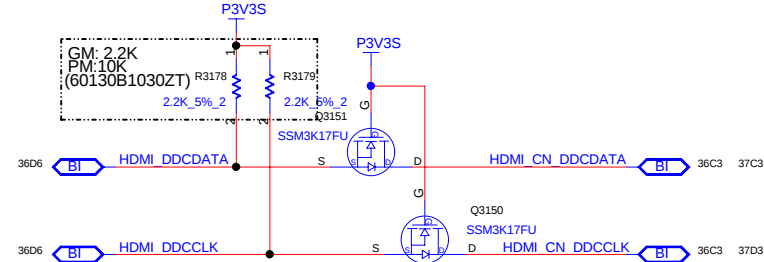
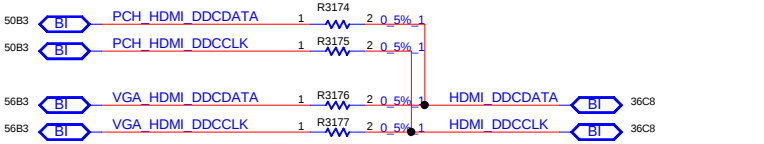
REFERENCE 3050~3099(CRT)



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 3150~3199(HDMI)

PLACE CLOSE TO CONNECTOR



INVENTEC

36A7 36D5

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

xxx

DATE

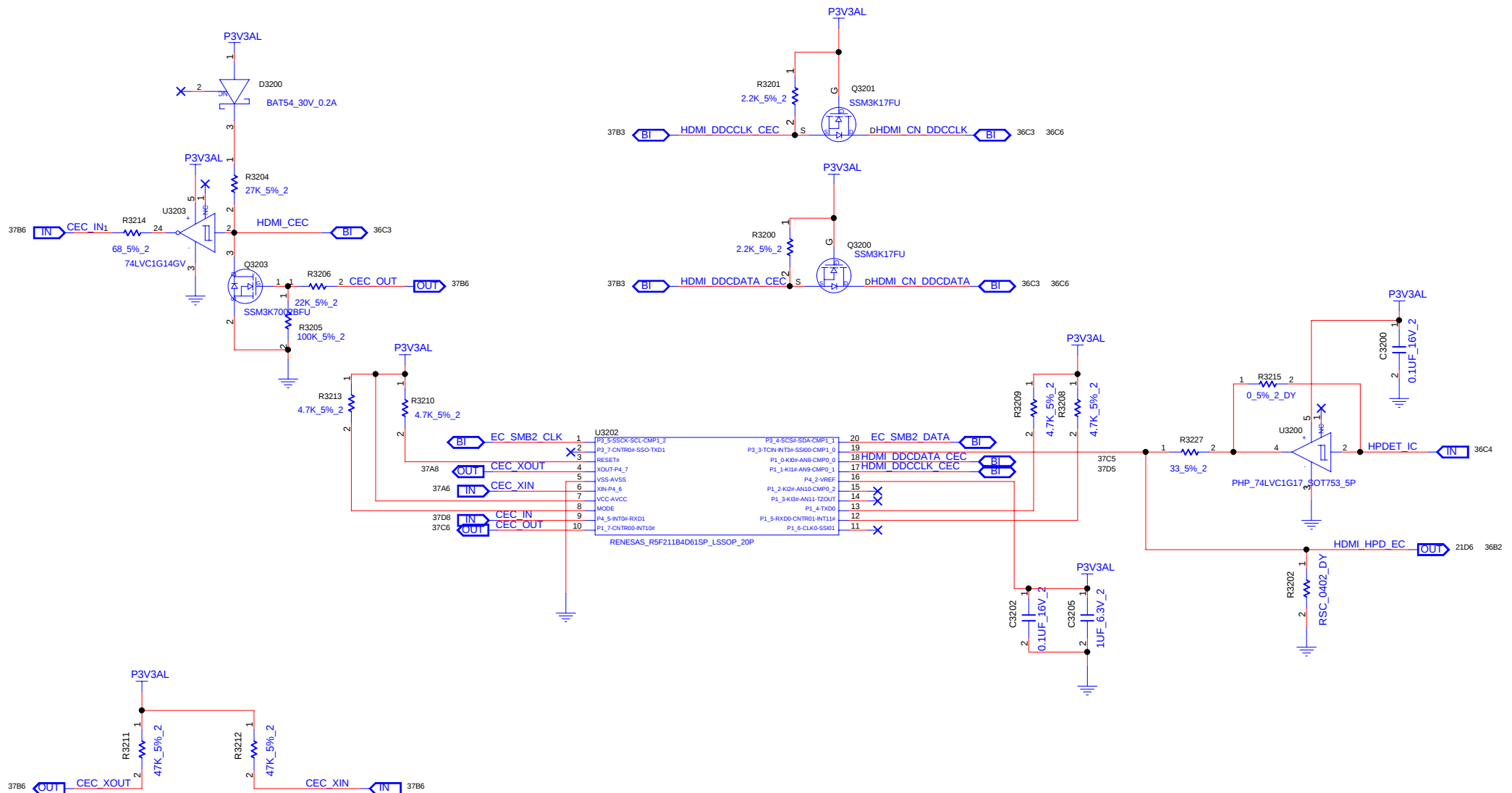
21-OCT-2002

SHEET

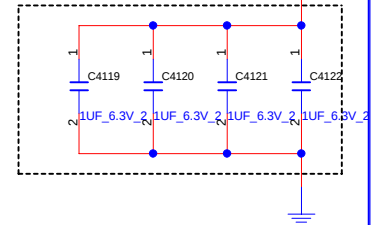
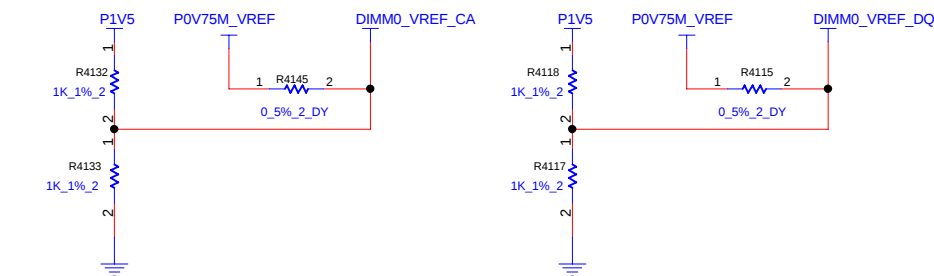
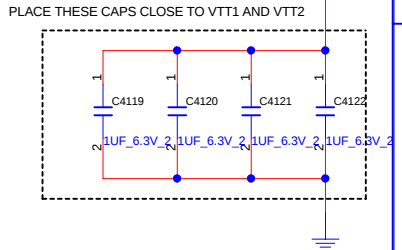
36

of

70

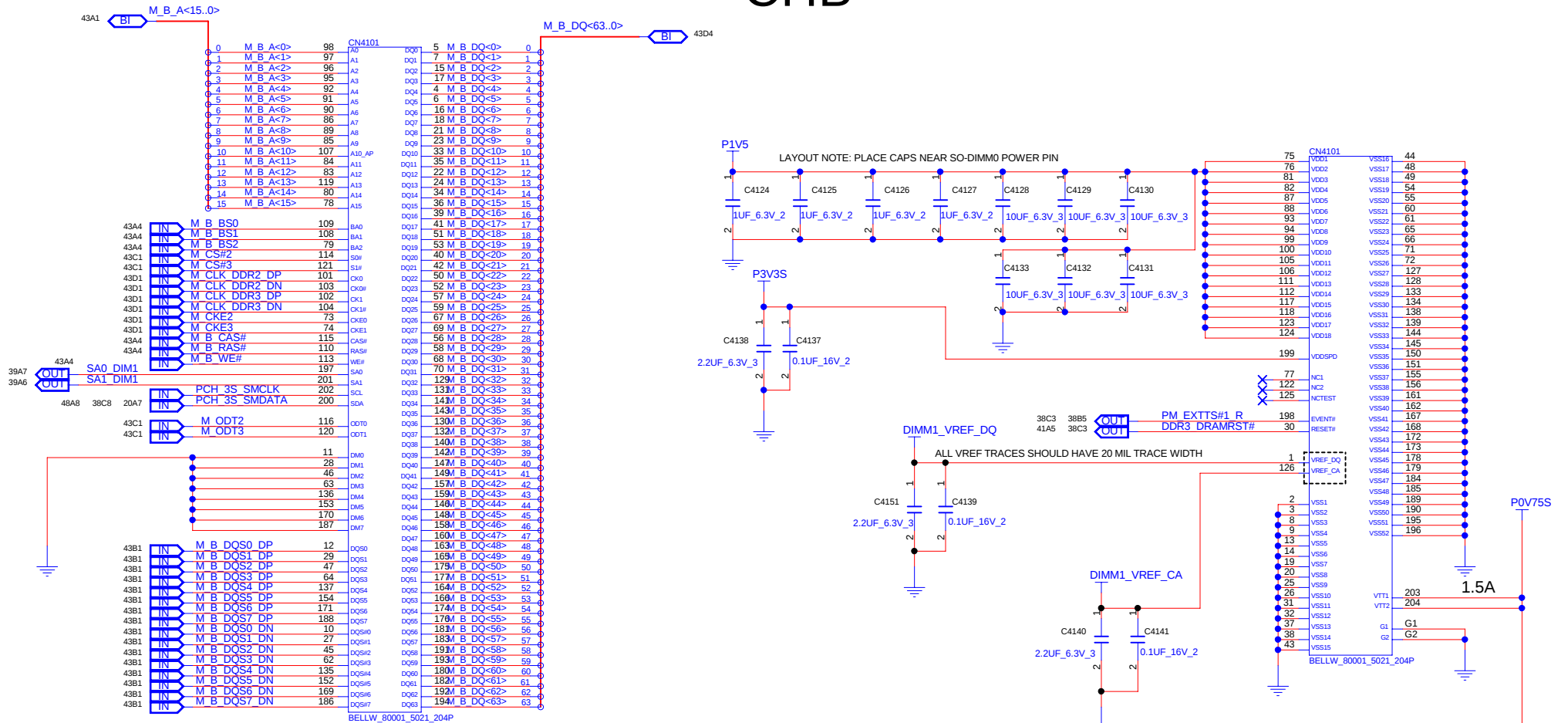


INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 37 of 70			

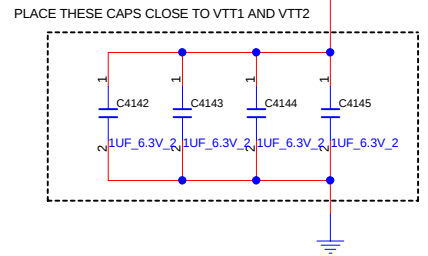
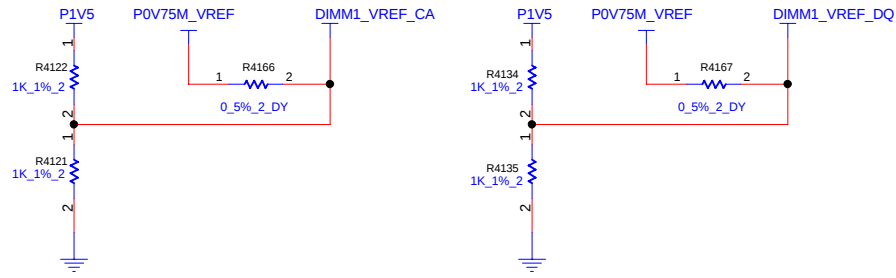
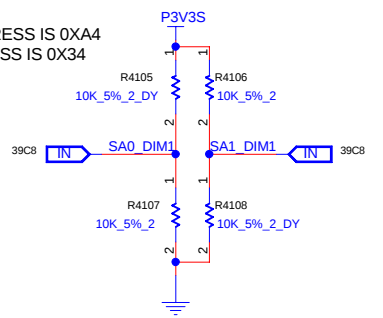


SHEET 38 of 70

CHB



NOTE:
SO-DIMMB SPD ADDRESS IS 0XA4
SO-DIMMB TS ADDRESS IS 0X34



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 4300~4349(FAN)
REFERENCE 4411~4449(THERMAL)

D

D

C

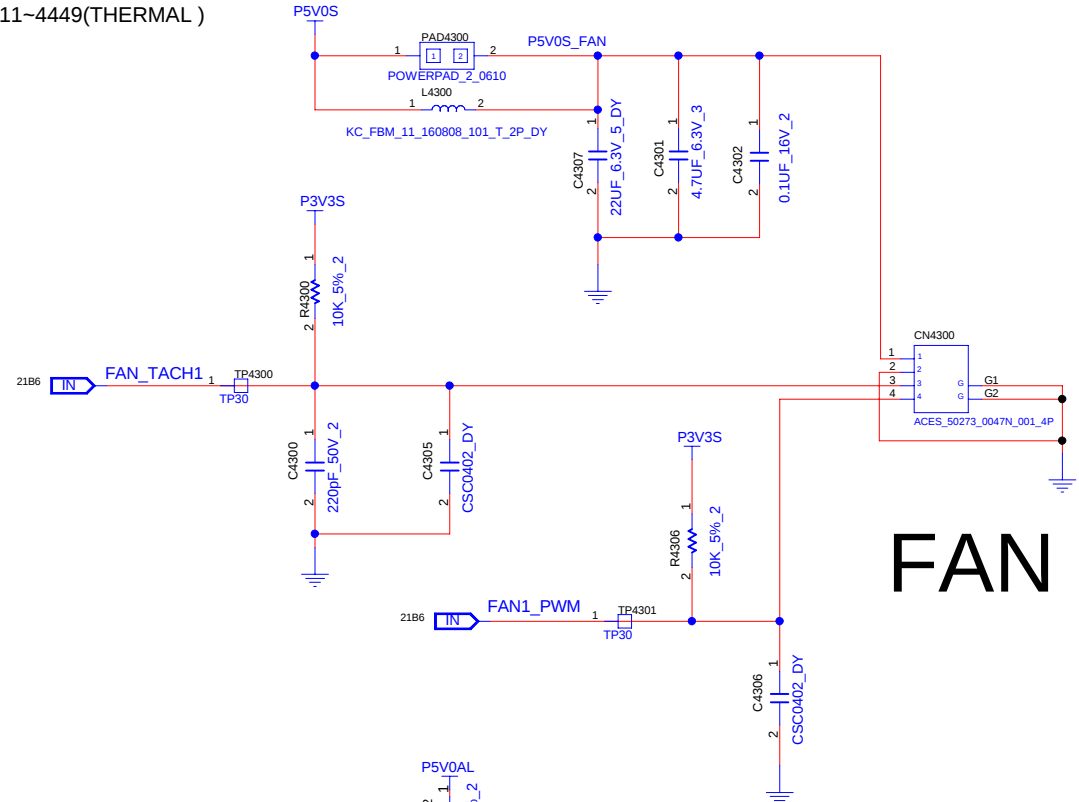
C

B

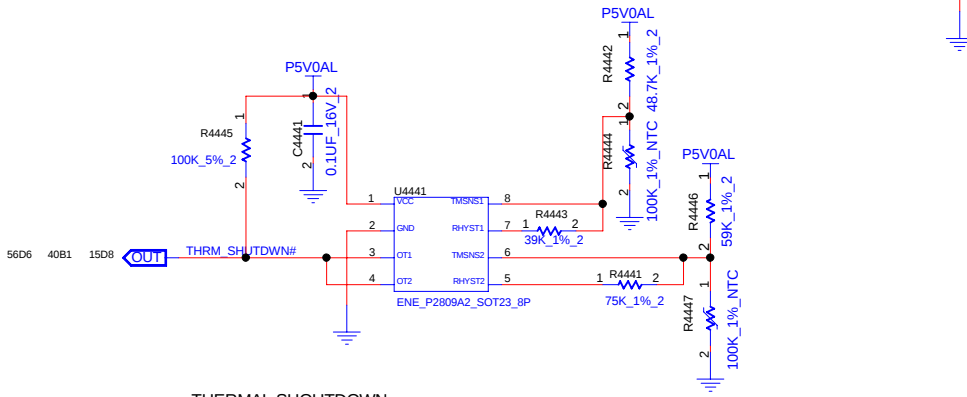
B

A

A



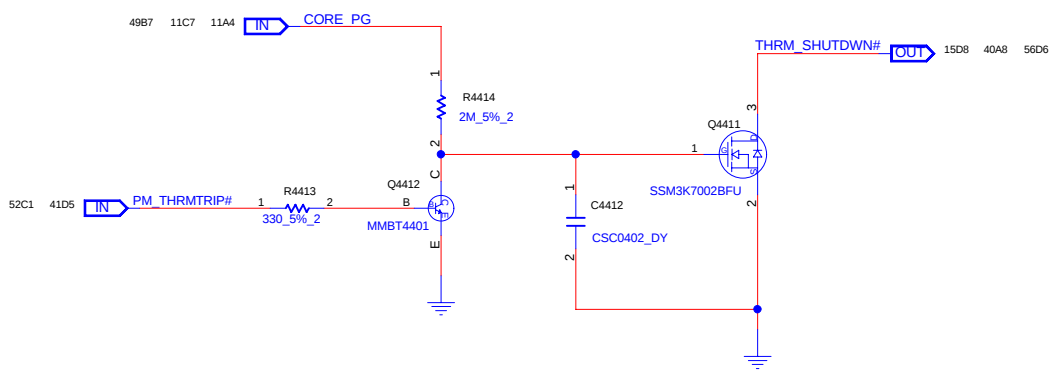
FAN CN



THERMAL SHUTDOWN

GM THERMAL SHUTDOWN CPU SIDE AT 68.6 C
GM THERMAL SHUTDOWN VGA SIDE AT 63.4 C
PM THERMAL SHUTDOWN CPU SIDE AT 71.9 C
PM THERMAL SHUTDOWN VGA SIDE AT 80.2 C

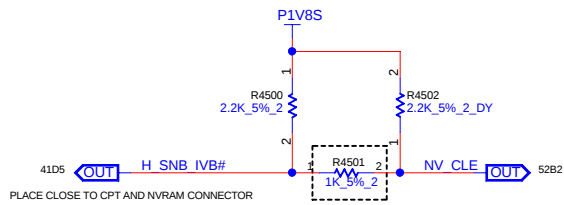
	10F	10FG
R4442	48.7K	43.2K
R4443	39K	30.1K
R4446	59K	32.4K
R4441	75K	18.2K



INVENTEC

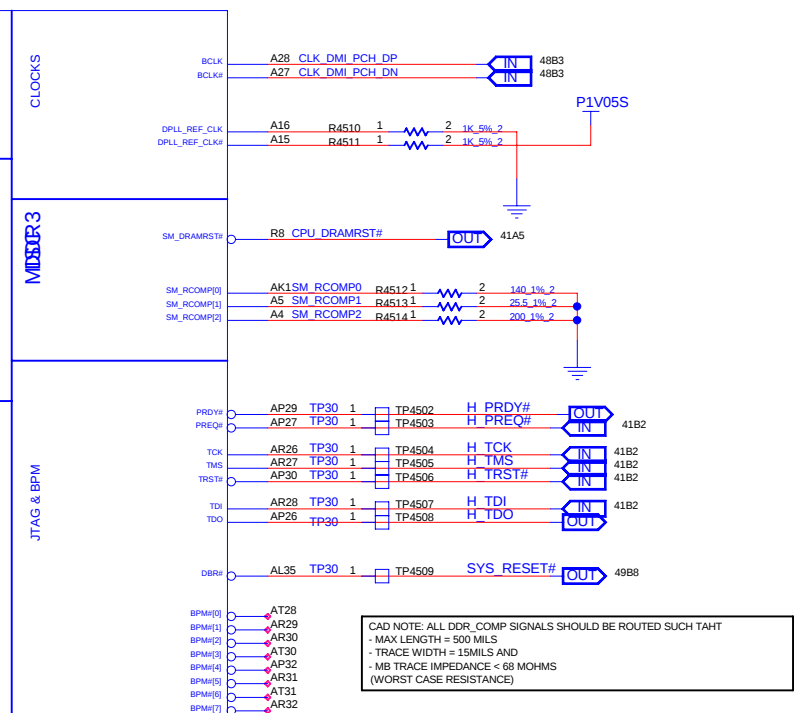
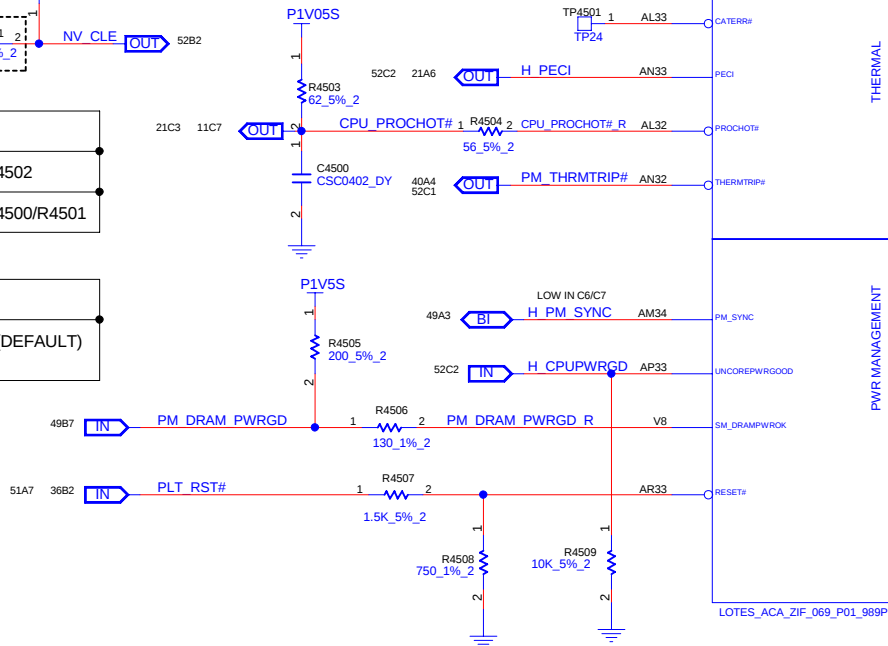
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 4500~4699(CPU)

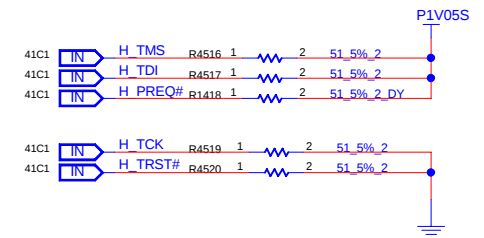


PROCESS STRAP SETTING	
SANDY BRIDGE ONLY	STUFF R4502
SANDY BRIDGE/IVY BRIDGE	STUFF R4500/R4501

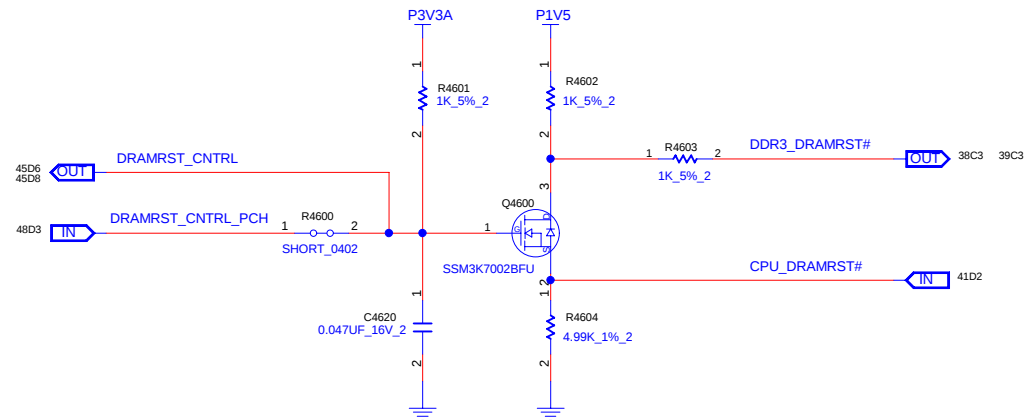
DMI&FDI TERMINATIONVOLTAGE	
NV_CLE	SET TOVSS WHEN LOW (DEFAULT) SET TOVCC WHEN HIGH



CAD NOTE: ALL DDR_COMP SIGNALS SHOULD BE ROUTED SUCH THAT
- MAX LENGTH = 500 MILS
- TRACE WIDTH = 15MILS AND
- MB TRACE IMPEDANCE < 68 MOHMS
(WORST CASE RESISTANCE)



S3 CIRCUIT: DRAM_RST# TO MEMORY SHOULD BE HIGH DURING S3



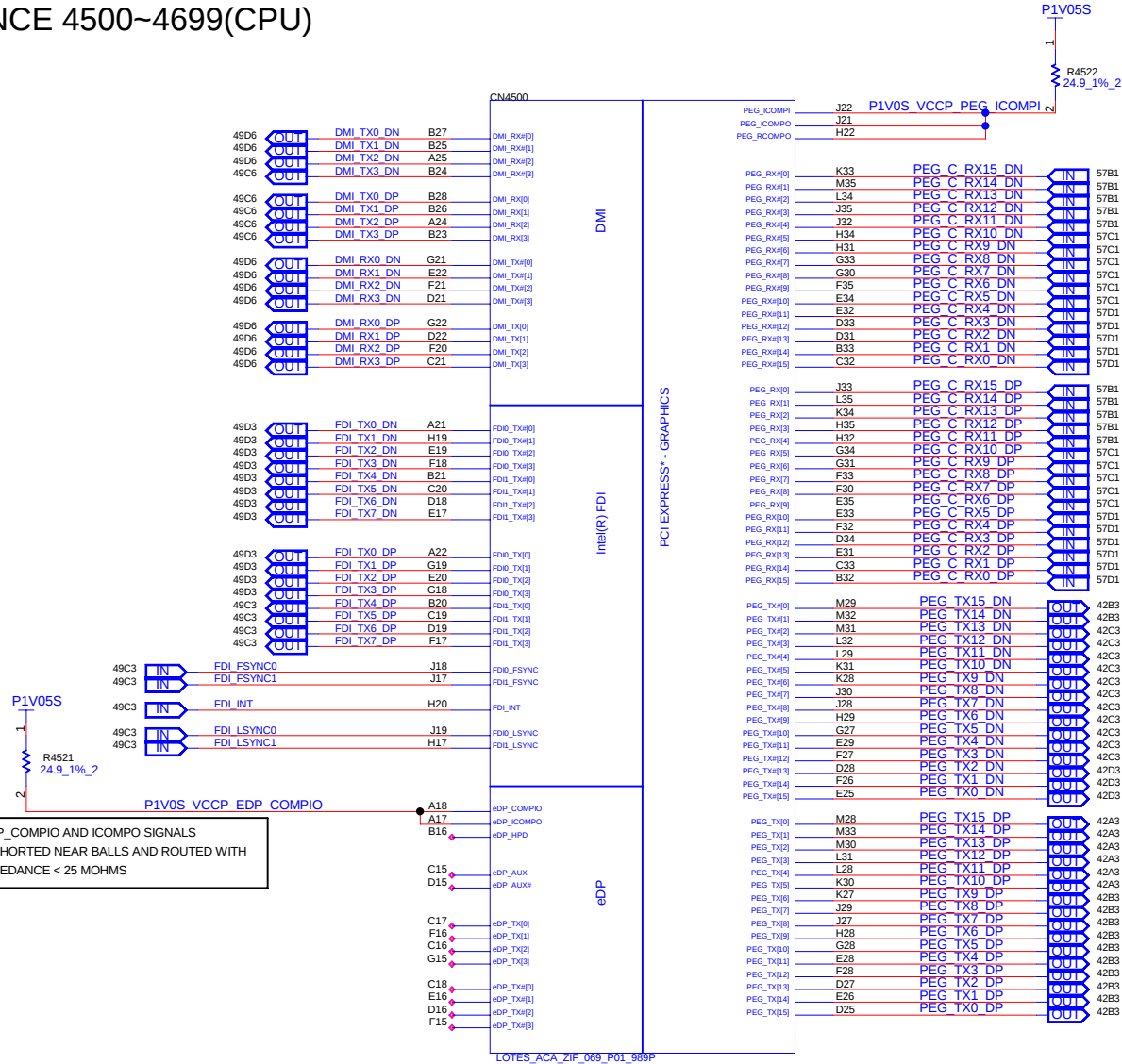
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	

CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 41 of 70

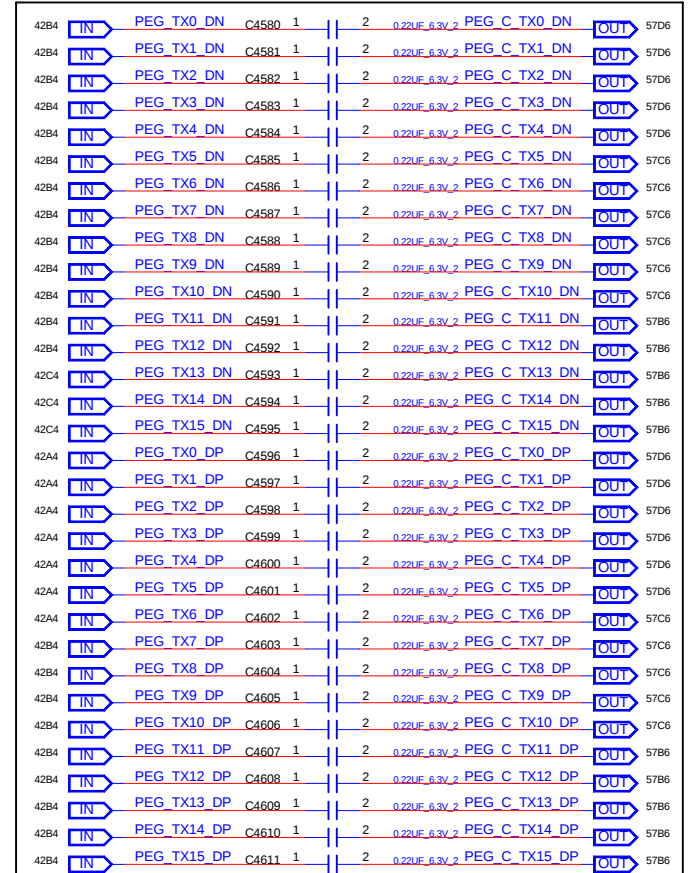
REFERENCE 4500~4699(CPU)



CAD NOTE: PEG_ICOMPI AND RCOMPO SIGNALS SHOULD BE SHORTED AND ROUTED WITH
- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 43 MOHMS

PEG_ICOMPO SIGNALS SHOULD BE ROUTED WITH
- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 14.5 MOHMS

CLOSE TO CPU



INVENTEC

TITLE
MODEL PROJECT FUNCTION
Block Diagram

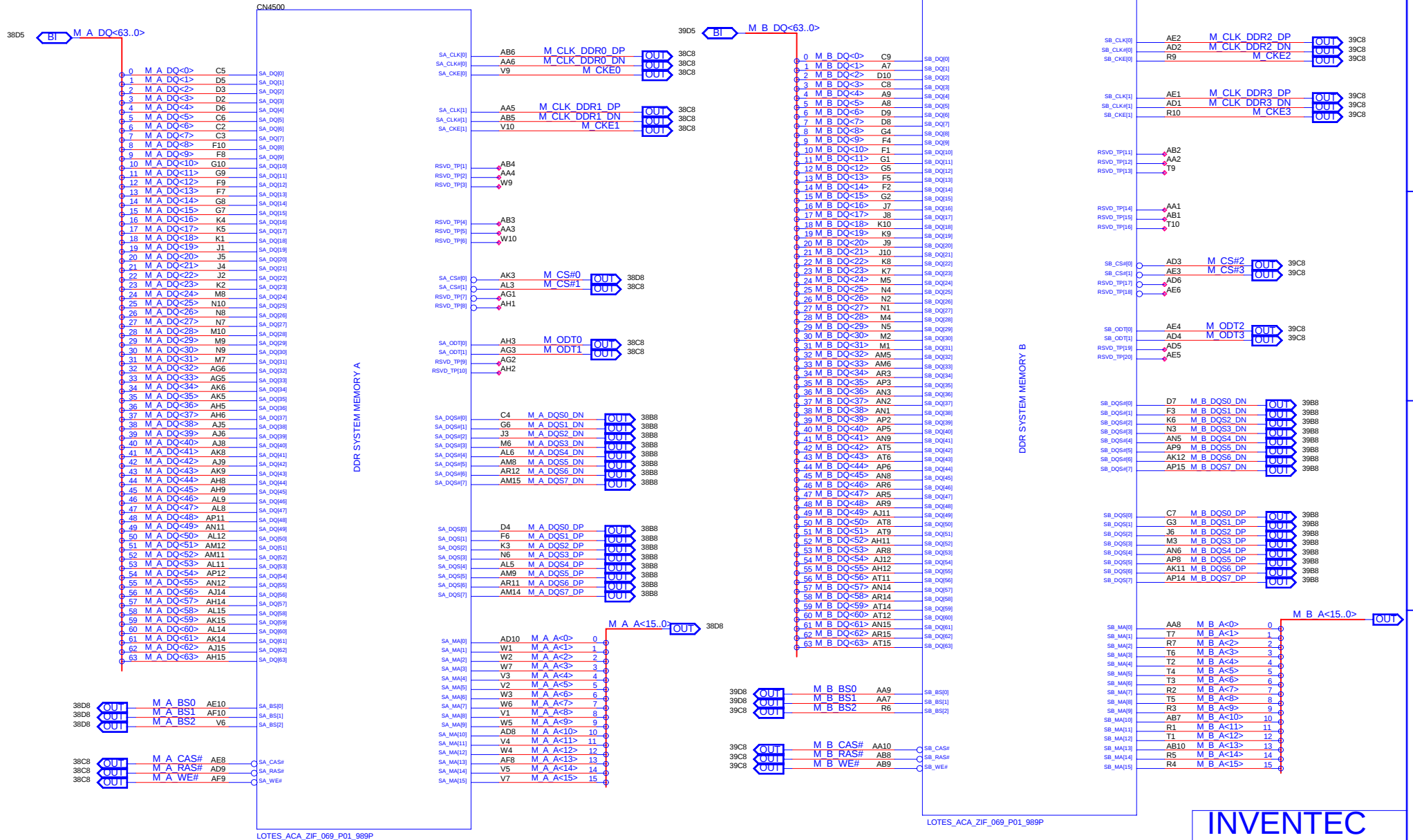
SIZE CODE DOC NUMBER REV
A3 CS 1310xxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 42 of 70

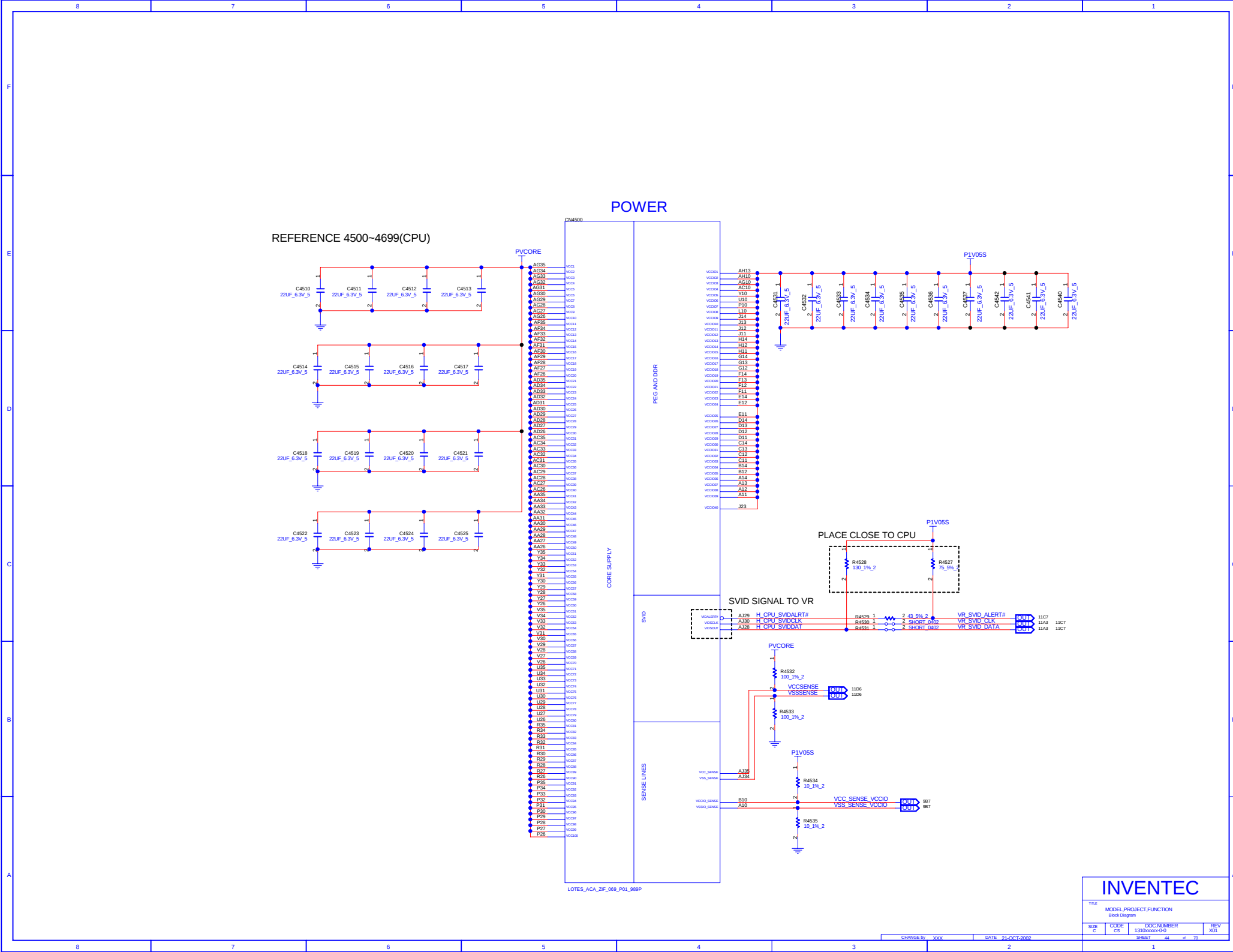
REFERENCE 4500~4699(CPU)

SOCKET,CPU,989P,TIN,3.0MM,STR,SMD,TR



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SHEET	43	of	70



POWER

REFERENCE 4500~4699(CPU)

- AG35
- AG34
- AG33
- AG32
- AG31
- AG30
- AG29
- AG28
- AG27
- AF35
- AF34
- AF33
- AF32
- AF31
- AF30
- AF29
- AF28
- AF27
- AF26
- AD35
- AD34
- AD33
- AD32
- AD31
- AD30
- AD29
- AD28
- AD27
- AD26
- AC35
- AC34
- AC33
- AC32
- AC31
- AC30
- AC29
- AC28
- AC27
- AA35
- AA34
- AA33
- AA32
- AA31
- AA30
- AA29
- AA28
- AA27
- Y35
- Y34
- Y33
- Y32
- Y31
- Y30
- Y29
- Y28
- Y27
- Y26
- V35
- V34
- V33
- V32
- V31
- V30
- V29
- V28
- V27
- V26
- U35
- U34
- U33
- U32
- U31
- U30
- U29
- U28
- U27
- U26
- R35
- R34
- R33
- R32
- R31
- R30
- R29
- R28
- R27
- R26
- P35
- P34
- P33
- P32
- P31
- P30
- P29
- P28
- P27
- P26

PNVORE

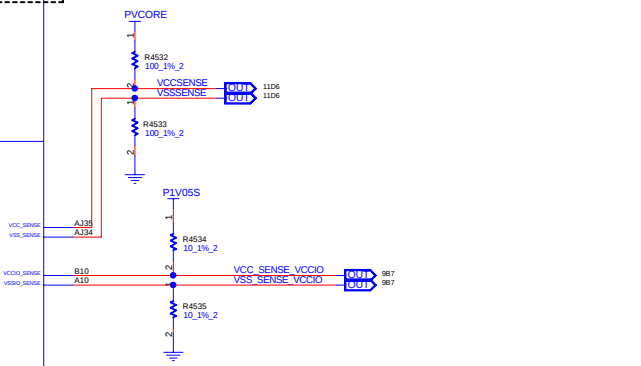
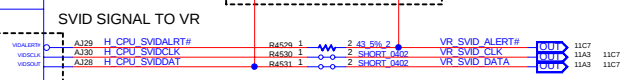
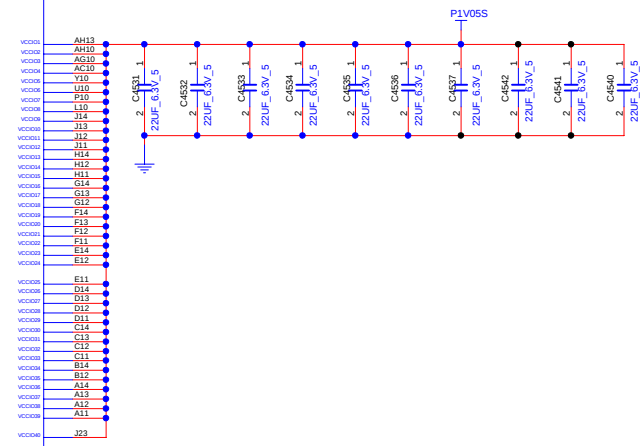
LOTES_ACA_ZIF_069_P01_989P

REG AND DDR

CORE SUPPLY

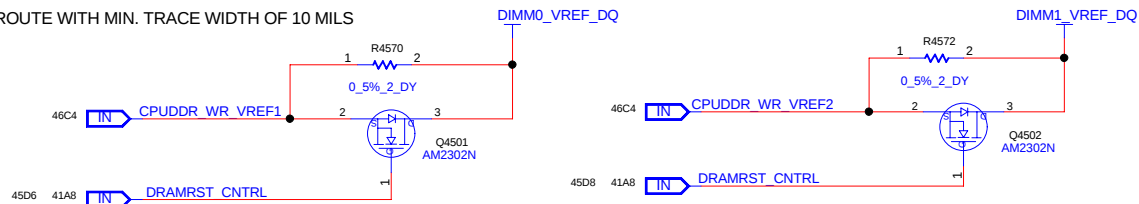
SVID

SENSE LINES

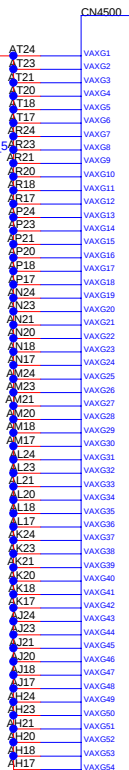
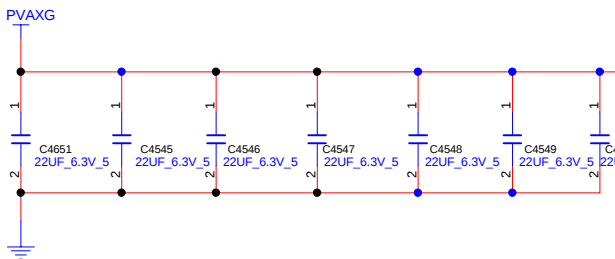


INVENTEC				
MODEL_PROJECT/FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
C	CS	131000000-05	X01	
SHEET			44	of 50

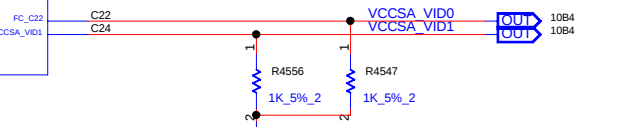
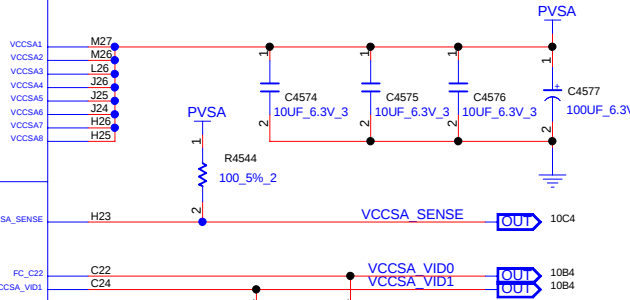
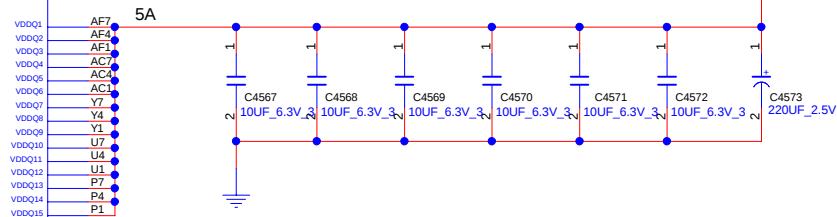
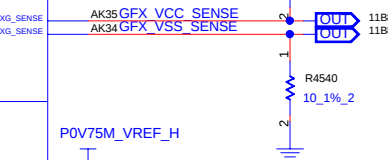
PROCESSOR DRIVEN VREF PATH WAS STUFFED BY DEFAULT:
ROUTE WITH MIN. TRACE WIDTH OF 10 MILS



POWER



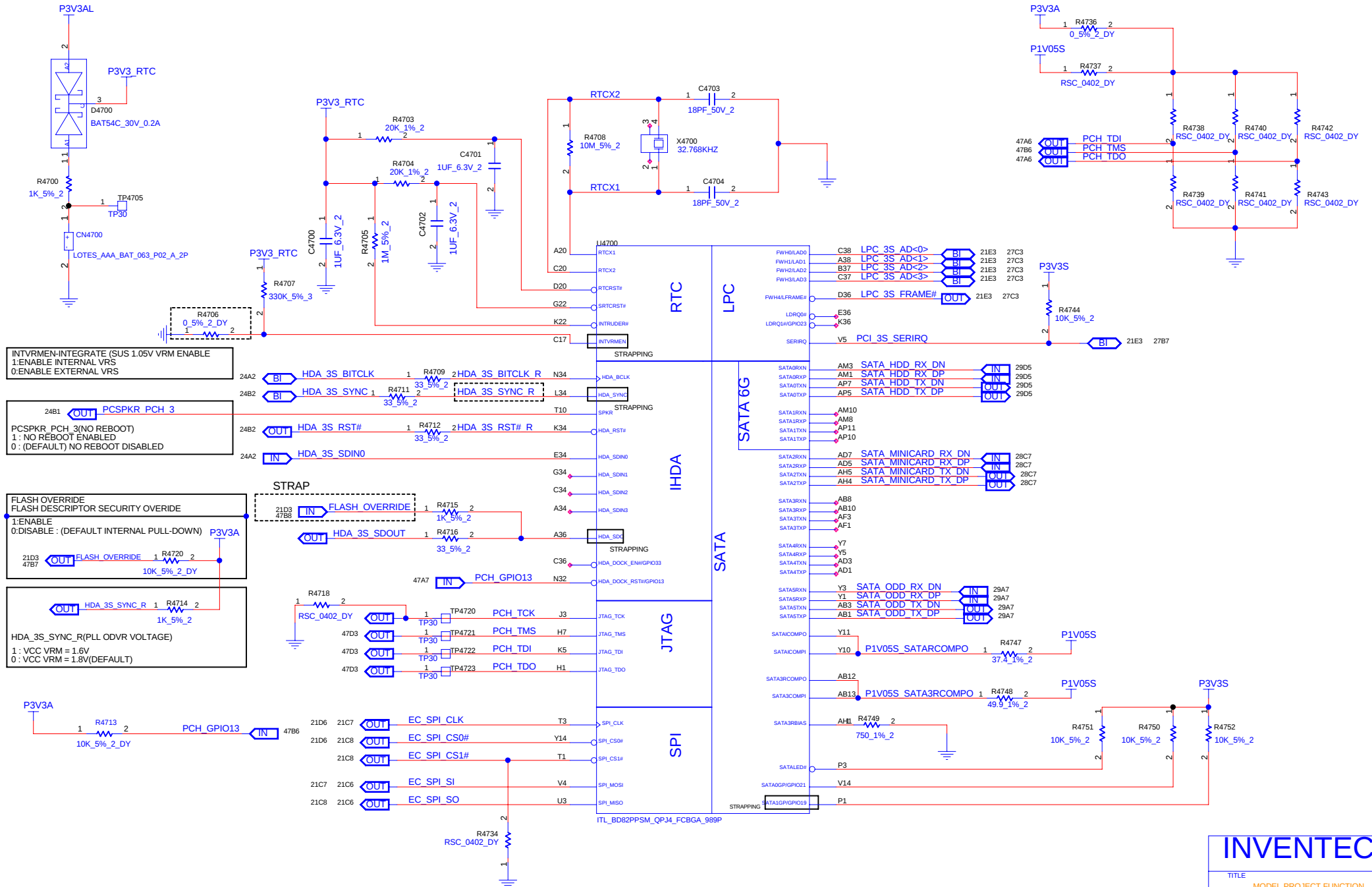
LOTES_ACA_ZIF_069_P01_989P



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 45 of 70			

REFERENCE 4700~4949(PCH)

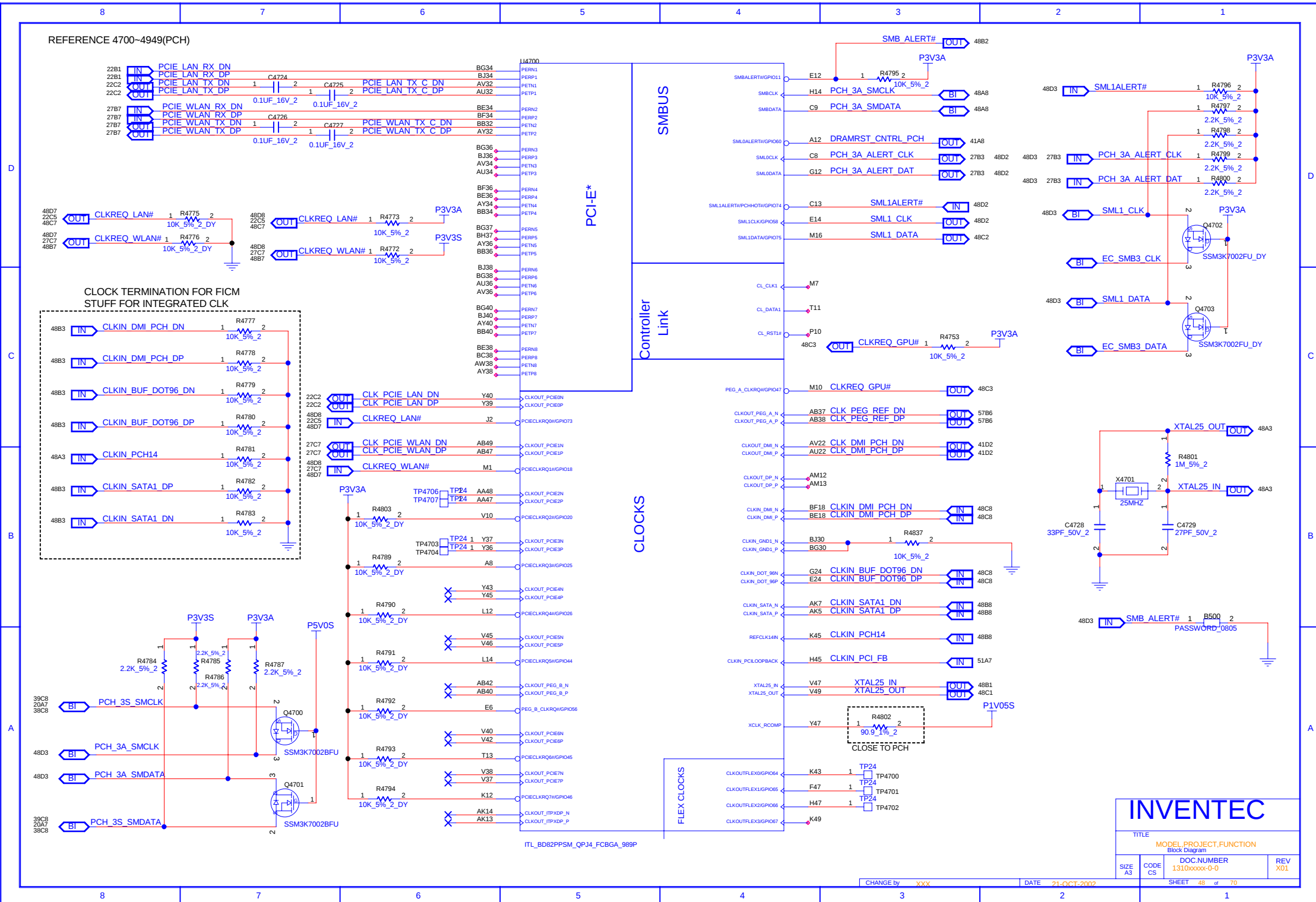


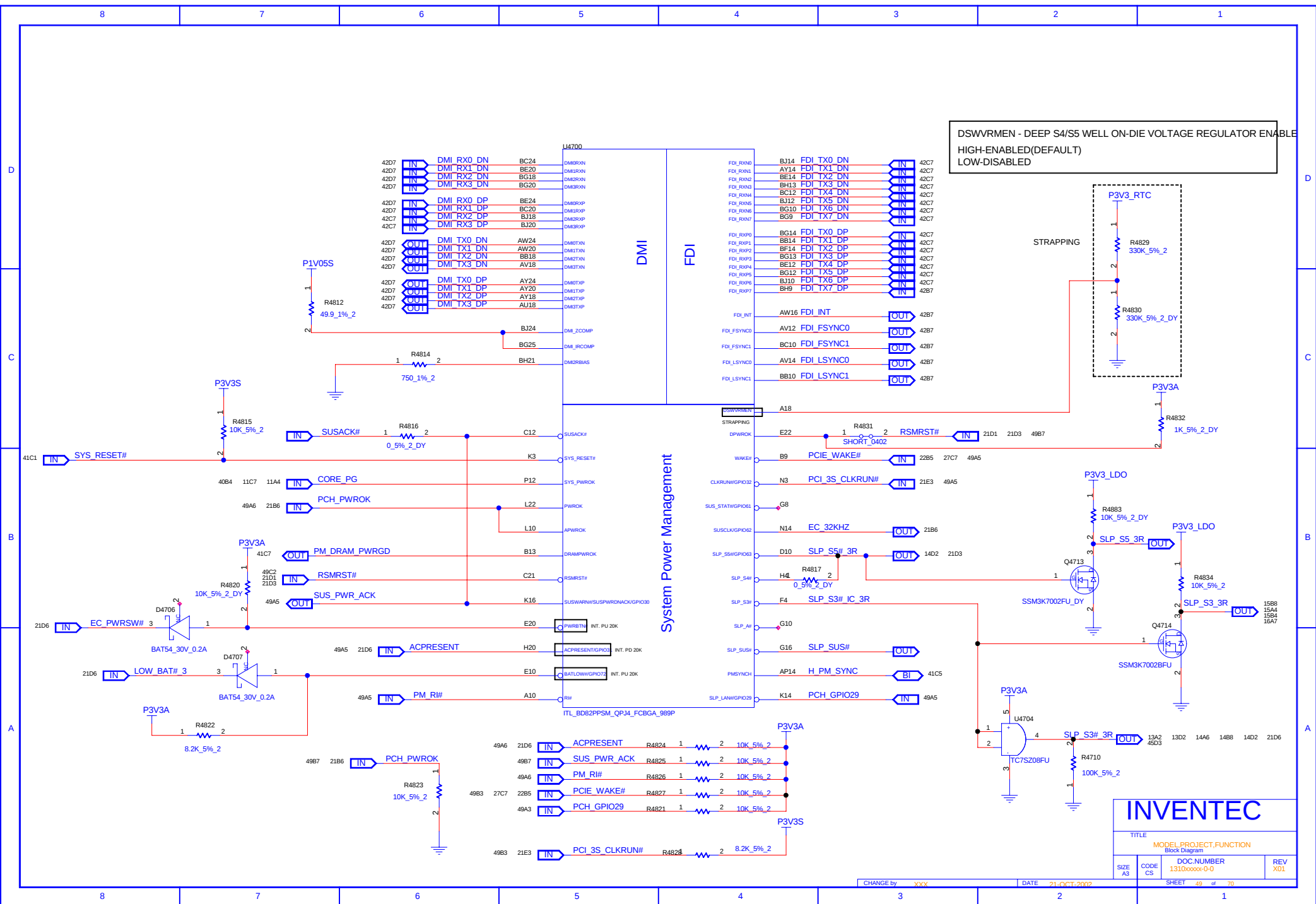
INVENTEC

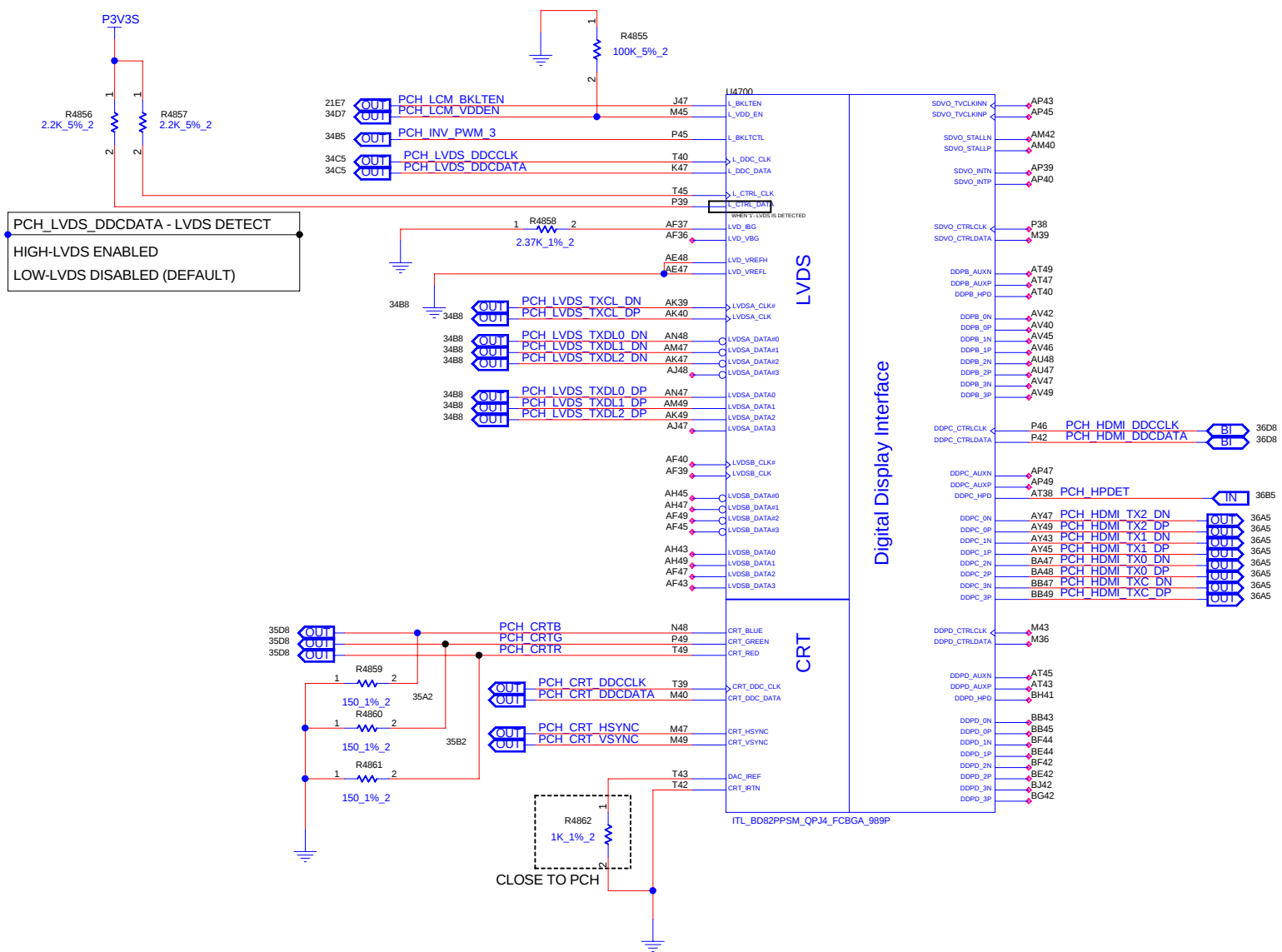
TITLE	MODEL PROJECT FUNCTION

SIZE	CODE	DOC.NUMBER	
		1310xxxxx-0-0	

SHEET 47 of 70







INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

[illegible]

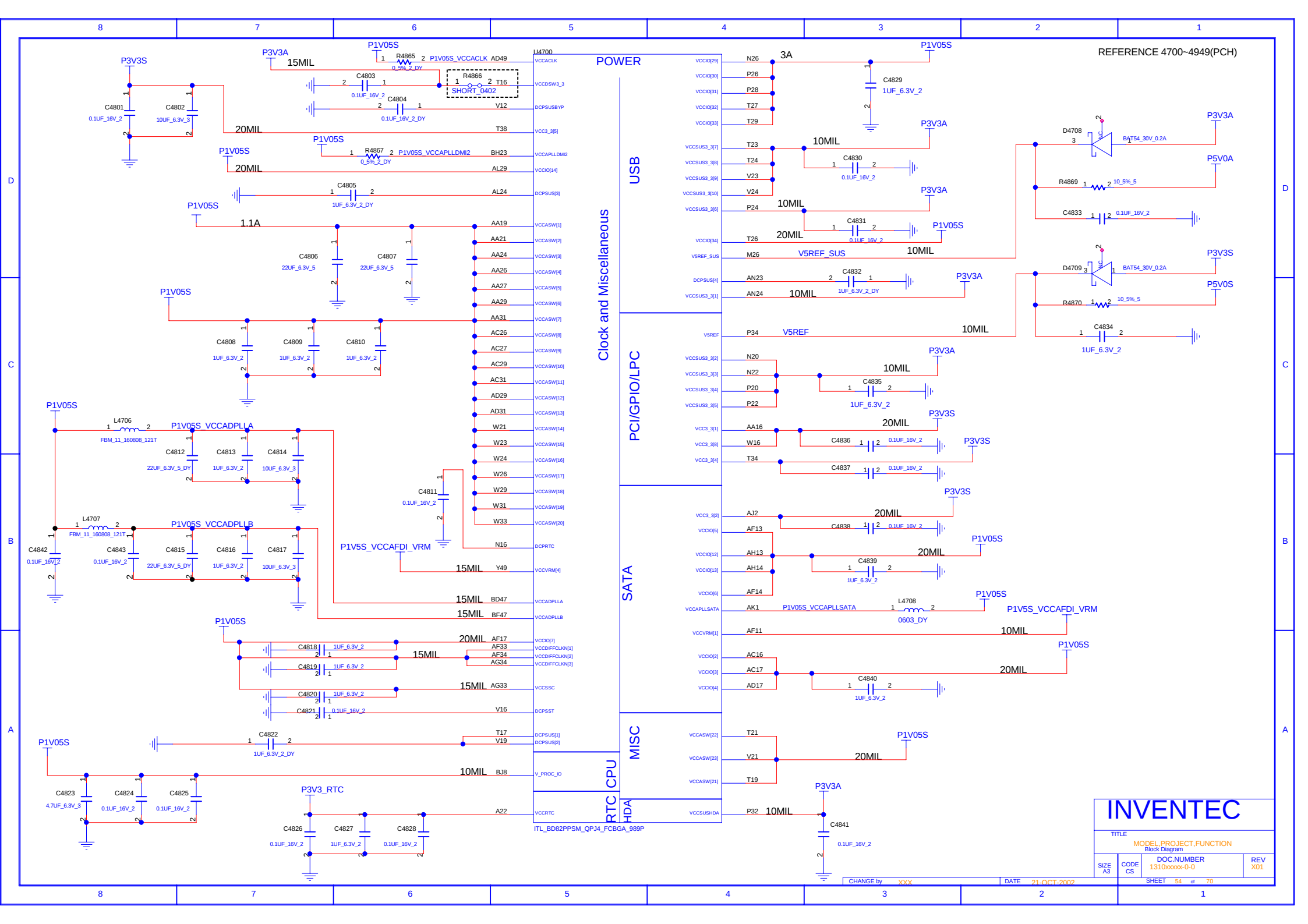
TITLE _____

MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	
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SHEET 53 of 70

CHANGE by	XXX	DATE	21-OCT-2002
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8

7

6

5

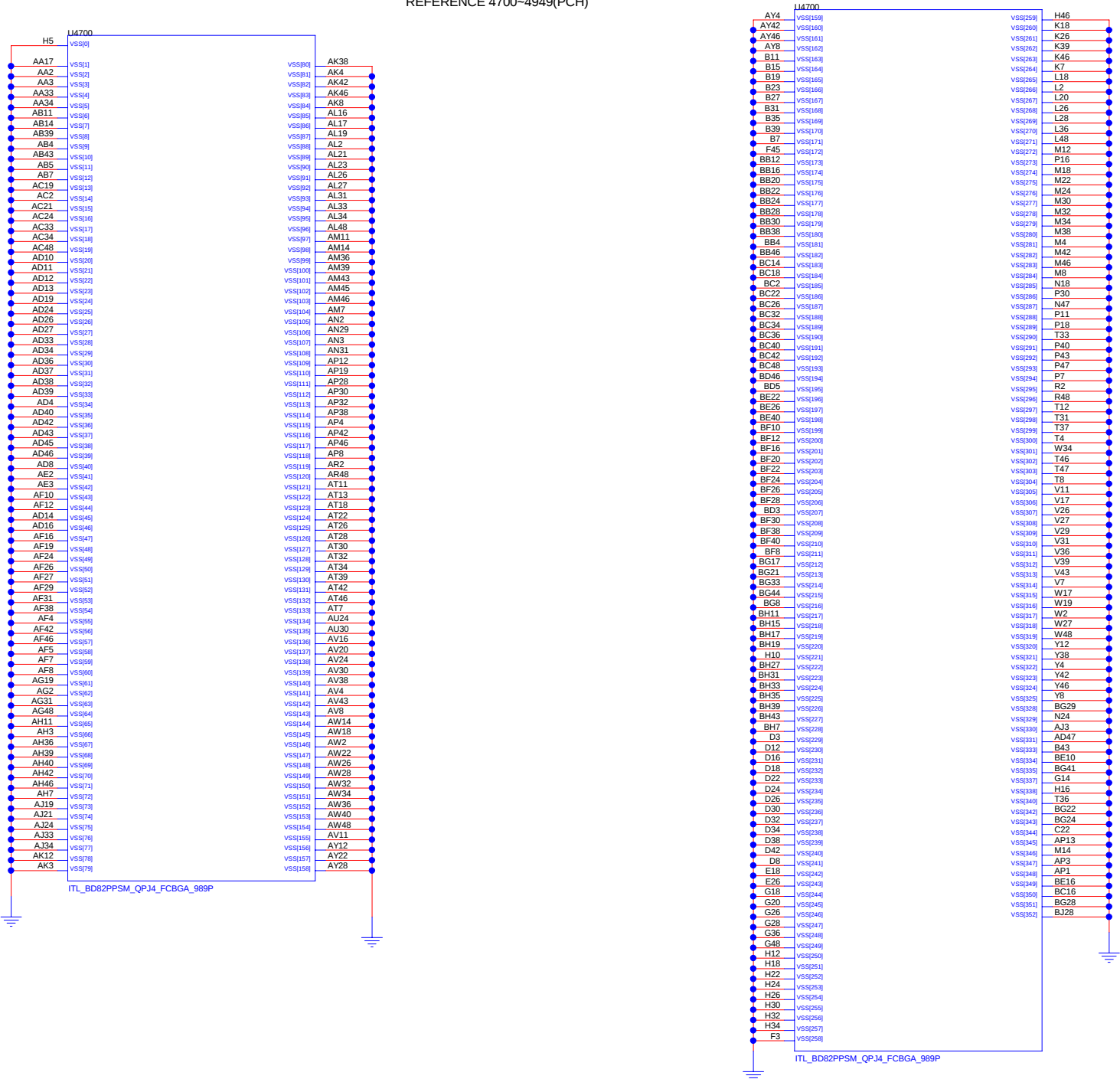
4

3

2

1

REFERENCE 4700~4949(PCH)



INVENTEC

TITLE

MODEL PROJECT FUNCTION
Block DiagramSIZE
A3CODE
CSDOC NUMBER
1310xxxx-0-0REV
X01

DATE 21-OCT-2002

CHANGE by XXX

SHEET 55 of 70

8

7

6

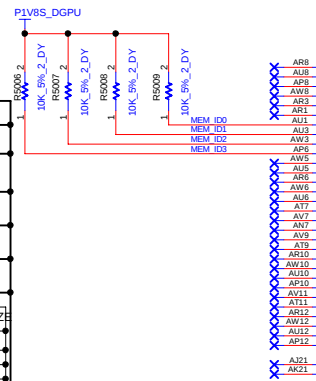
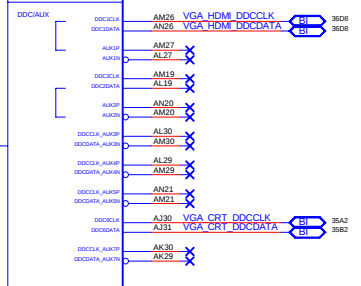
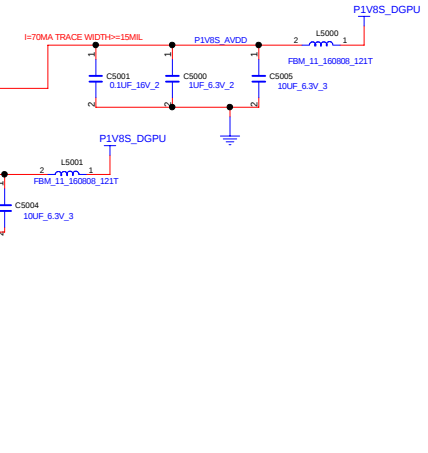
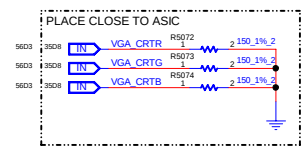
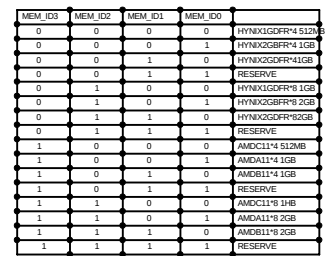
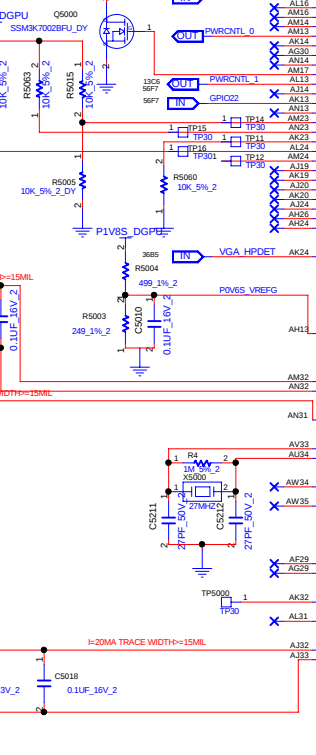
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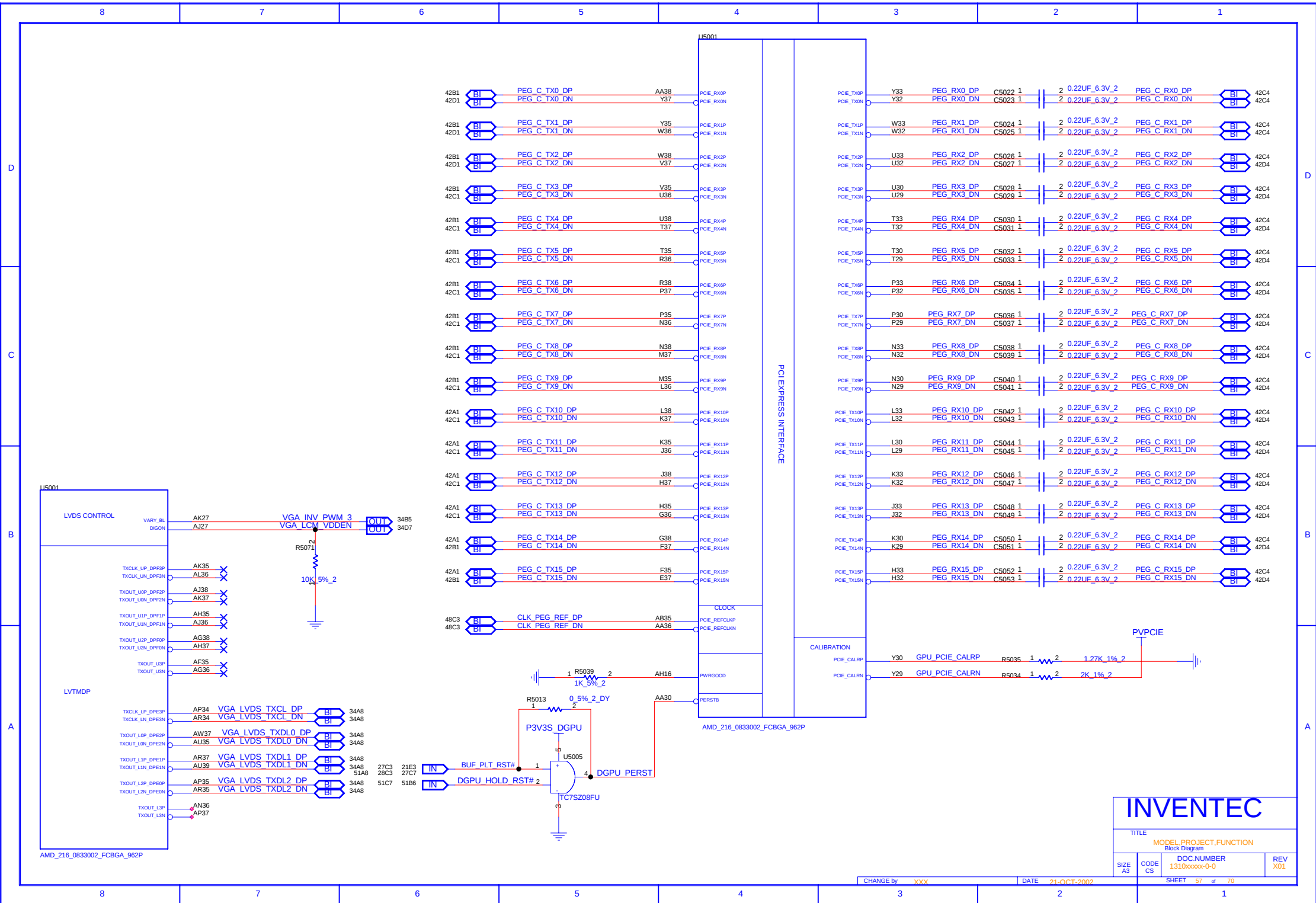
4

3

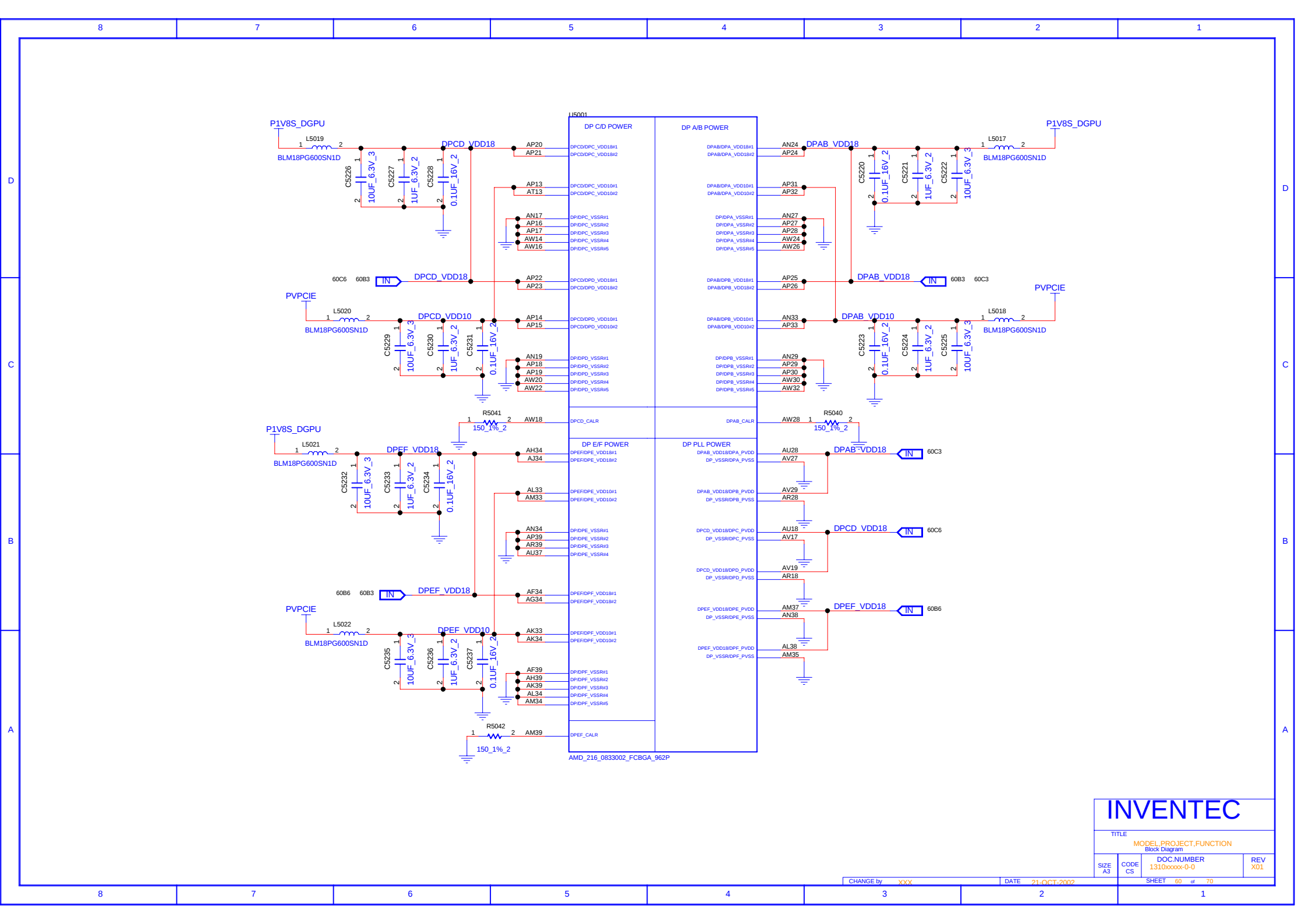
2

1

[illegible]





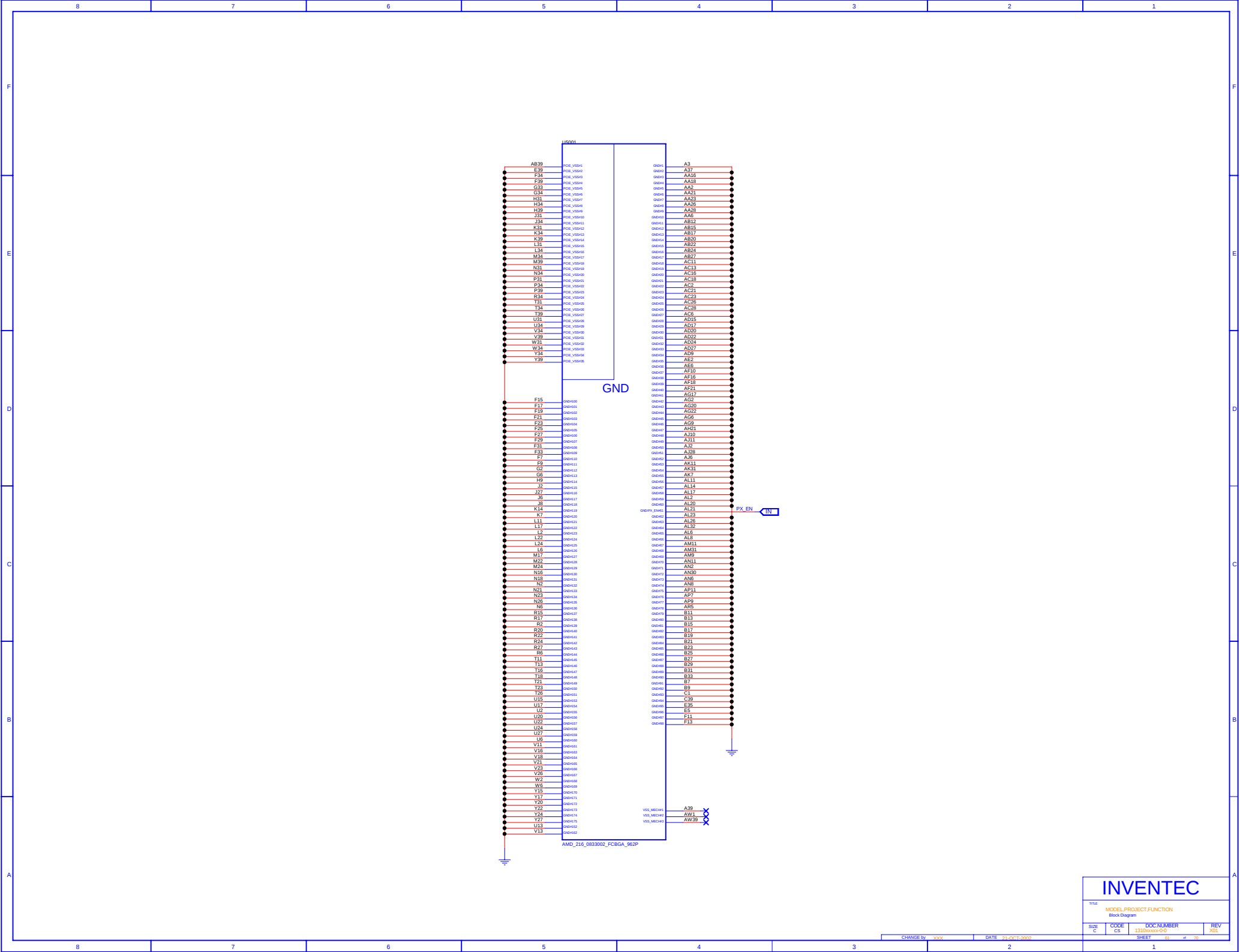


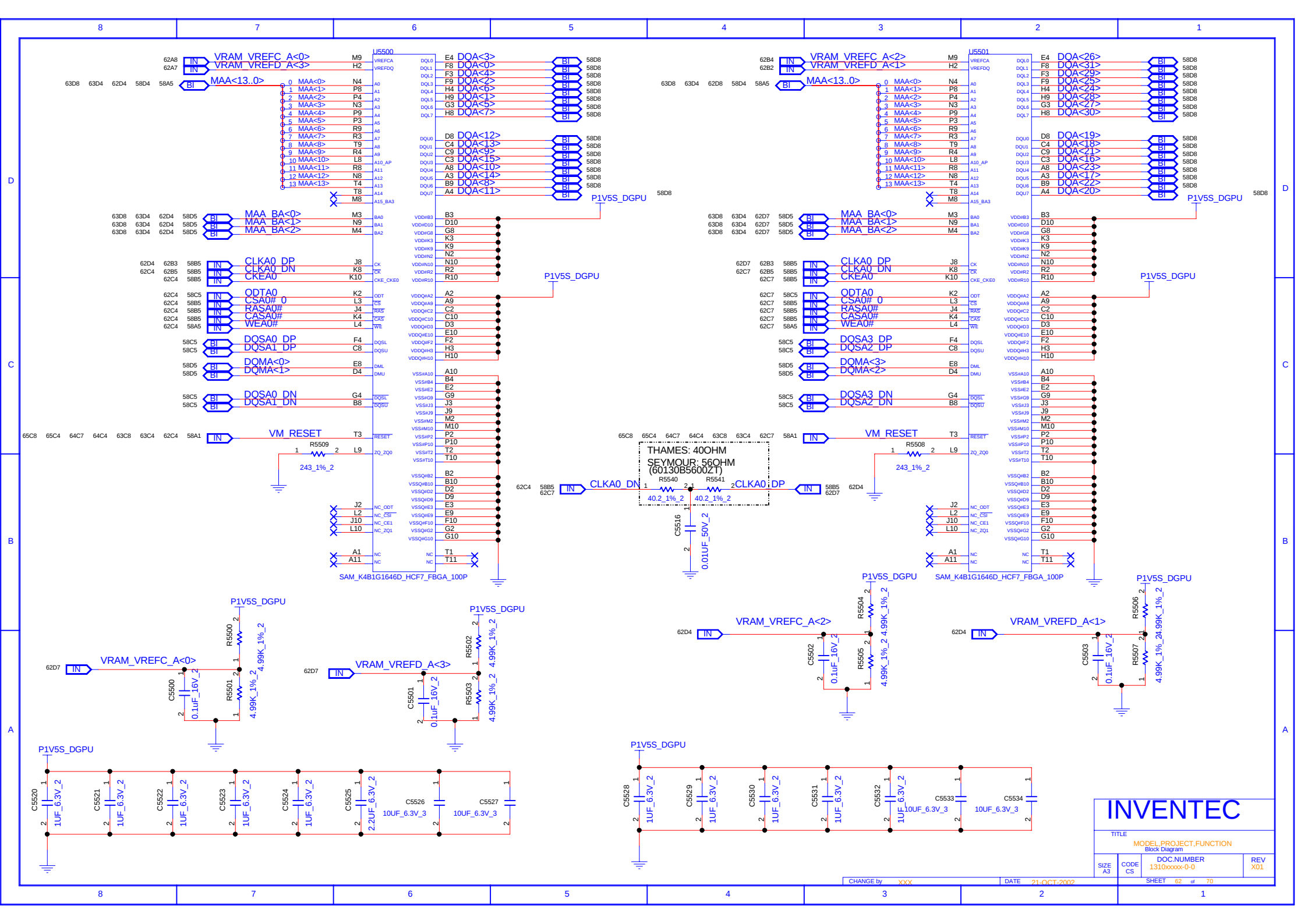
INVENTEC

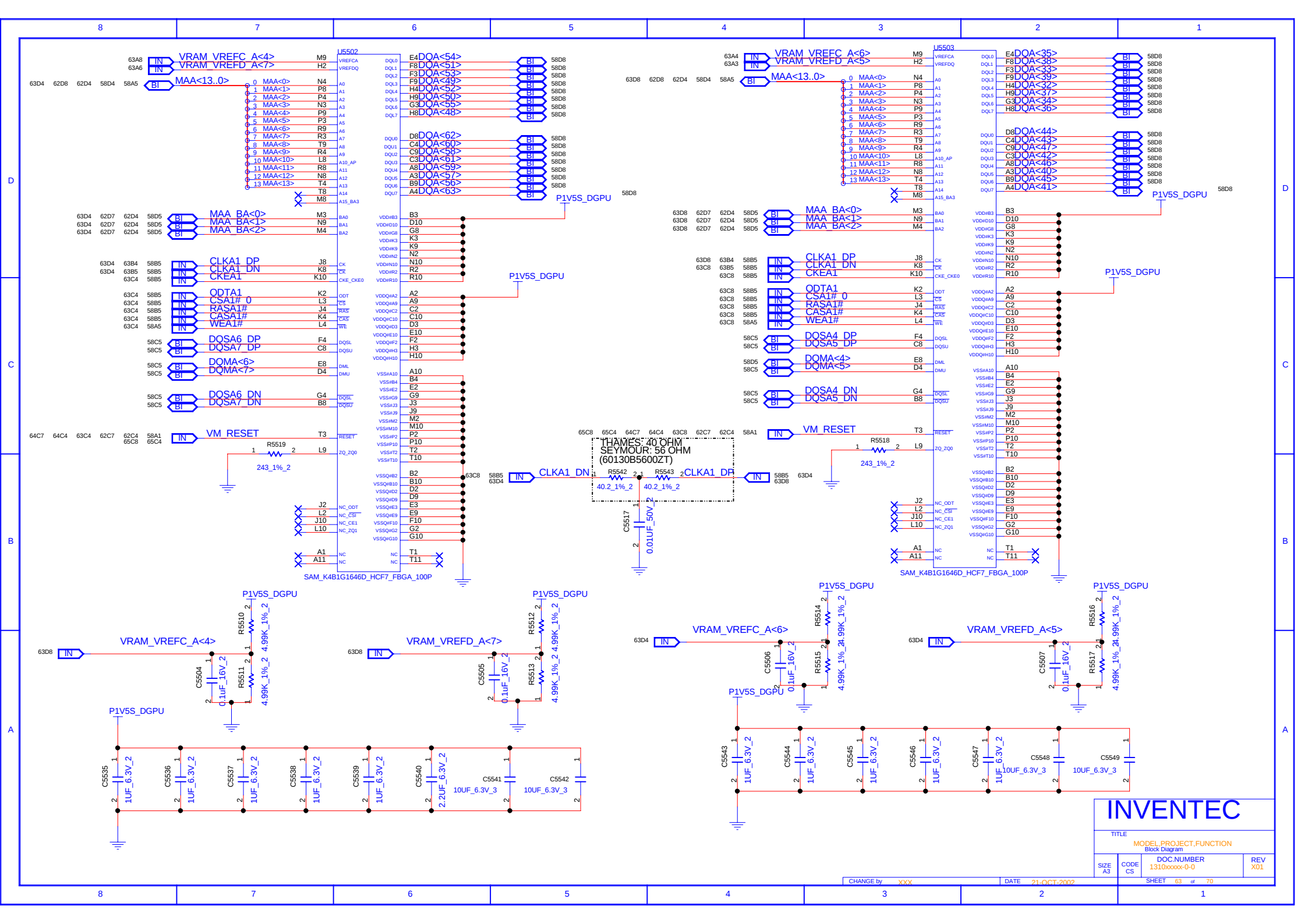
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

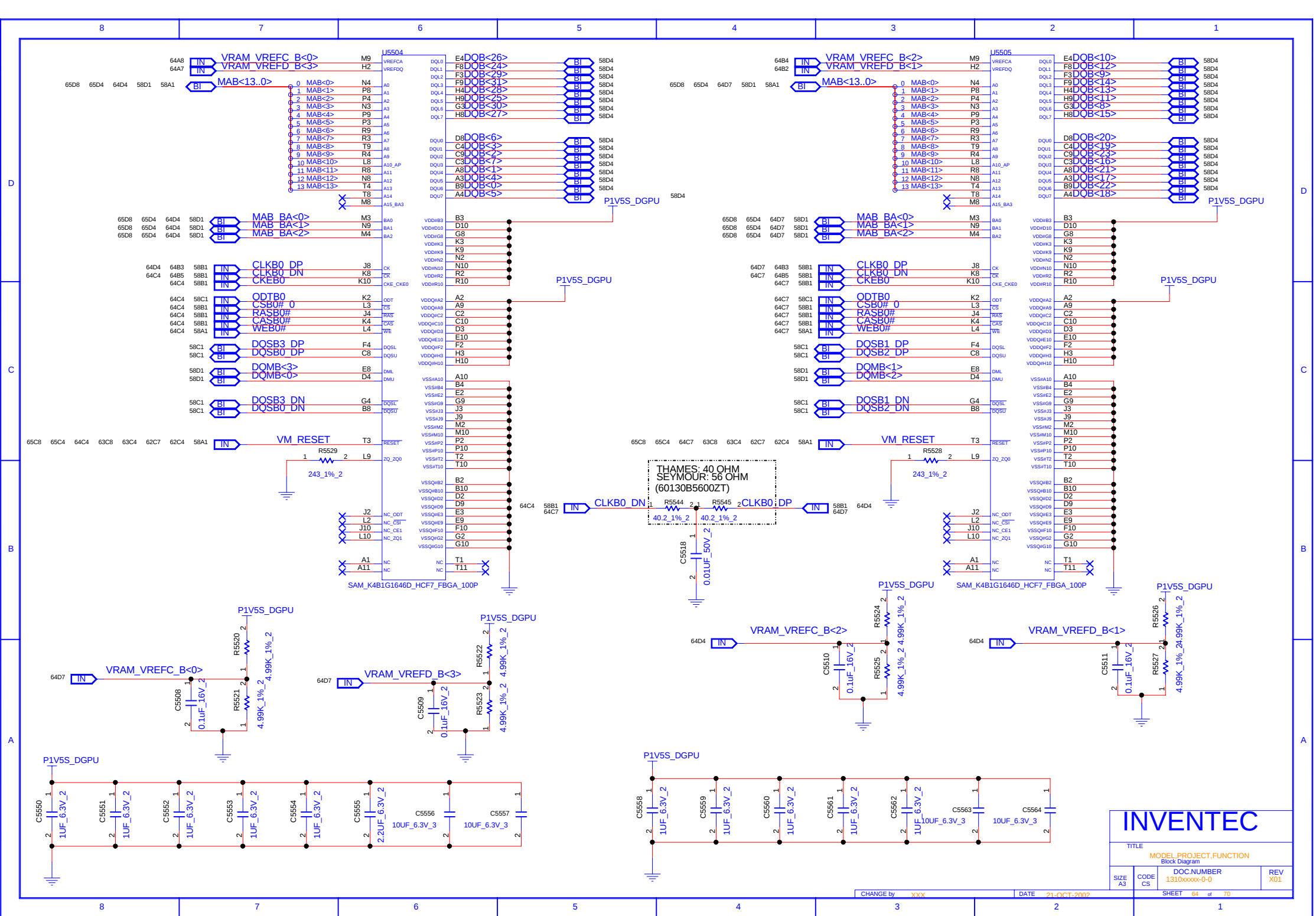
CHANGE by XXX DATE 21-OCT-2002

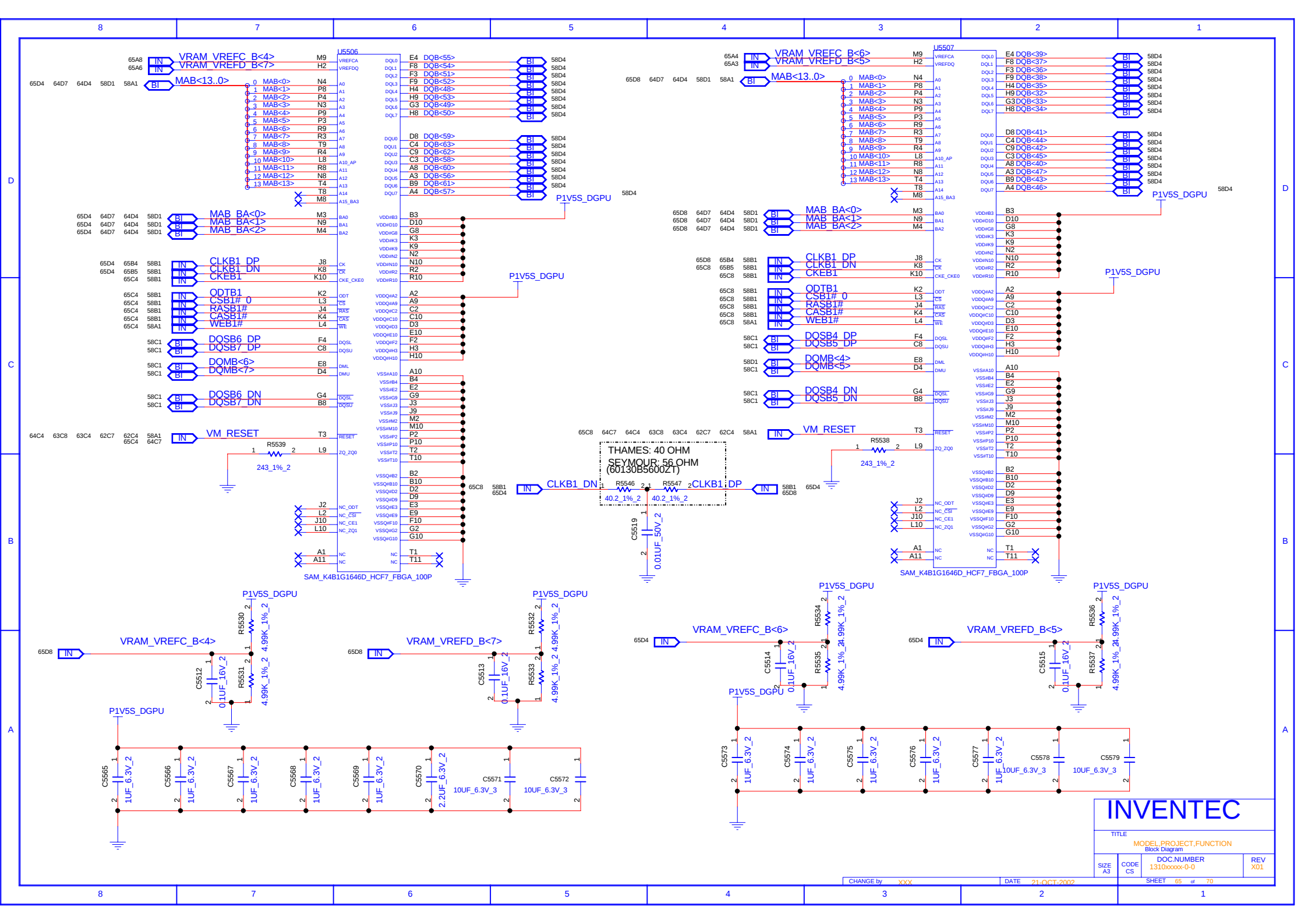
SHEET 60 of 70





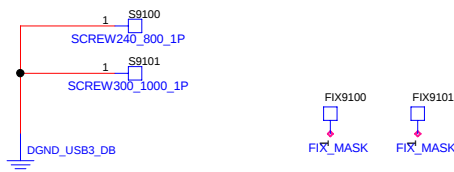
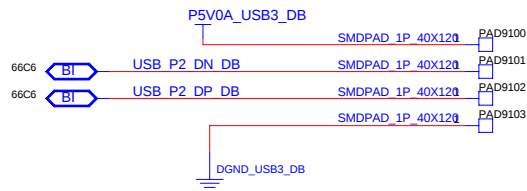
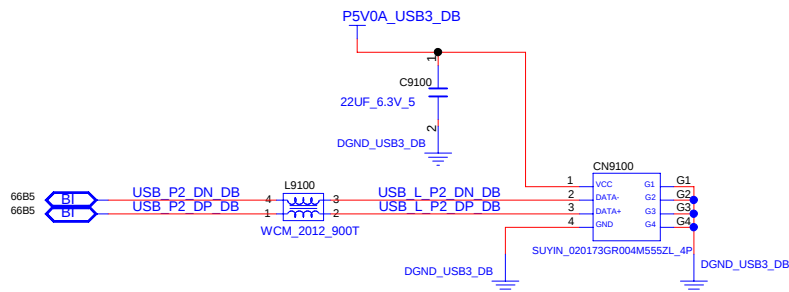






INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
SHEET 65 of 70			

USB BOARD



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

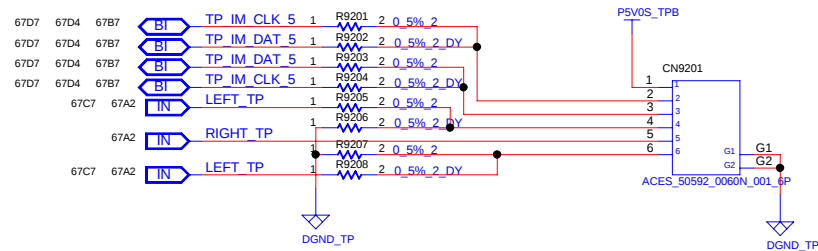
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CHANGE by XXX DATE 21-OCT-2002

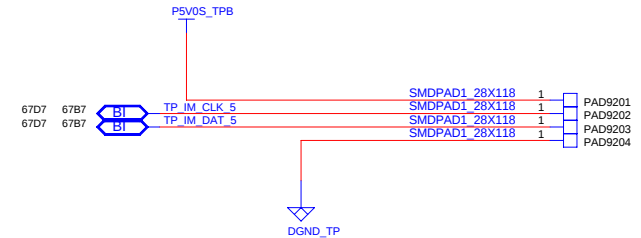
SHEET 66 of 70

TOUCH PAD BOARD 4 PIN

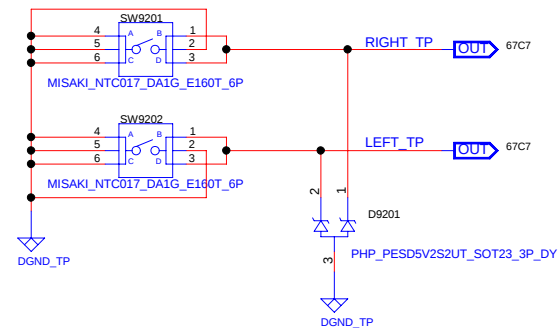
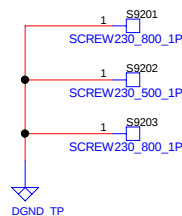
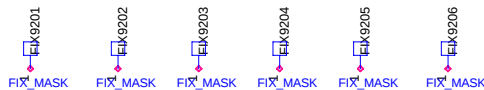
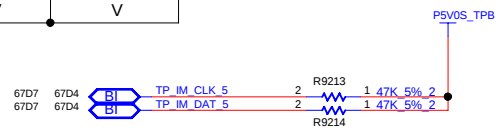
TOUCHPAD TO MODULE CONN



TOUCHPAD TO MB CONN



4PIN	IMR/METAL	TEXTURE
R9201		V
R9202	V	
R9203		V
R9204	V	
R9205		V
R9206	V	
R9207		V
R9208	V	
R9213	V	V
R9214	V	V

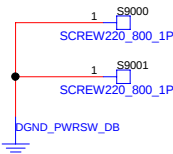
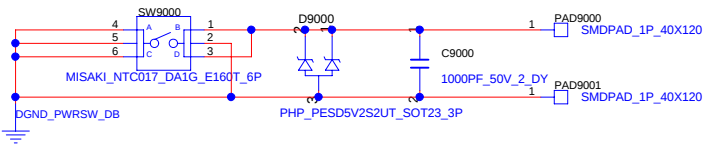


INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

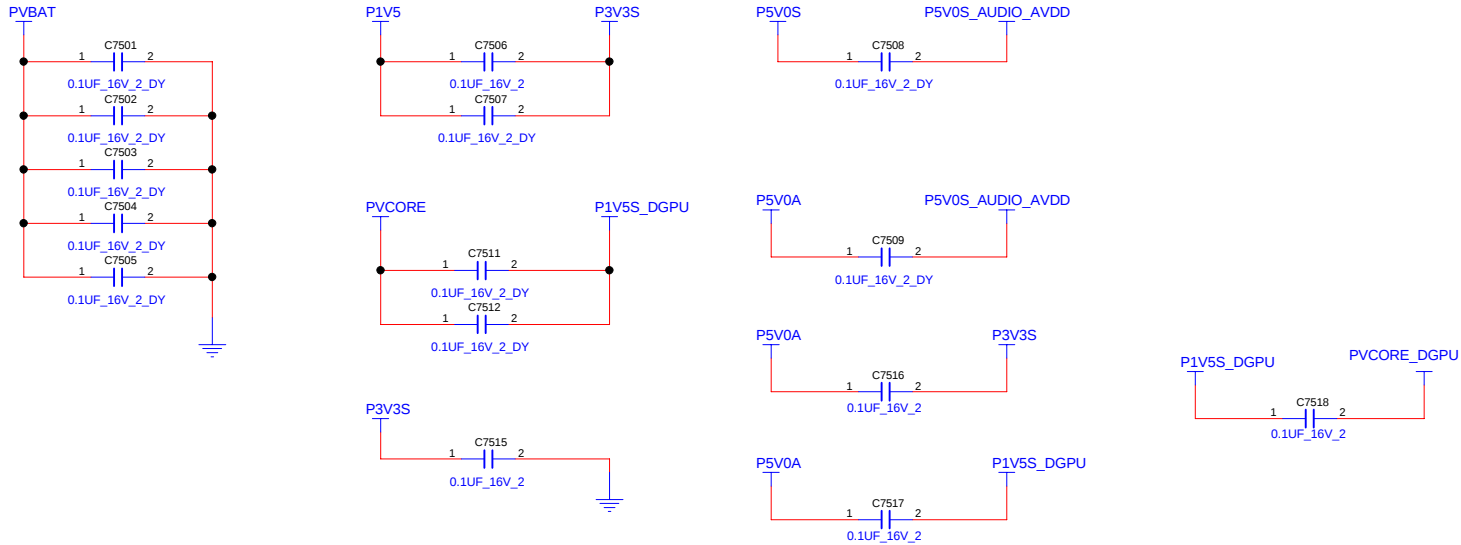
REFERENCE 9000~9999(SMALL BOARD)

POWER BUTTON



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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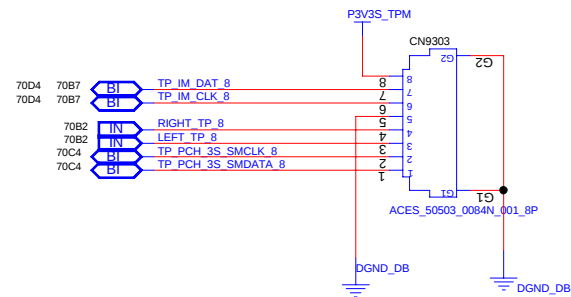


INVENTEC

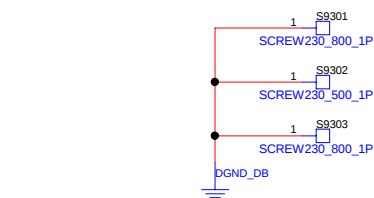
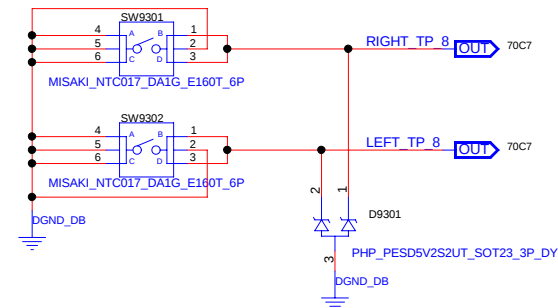
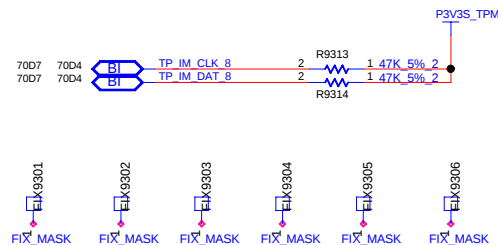
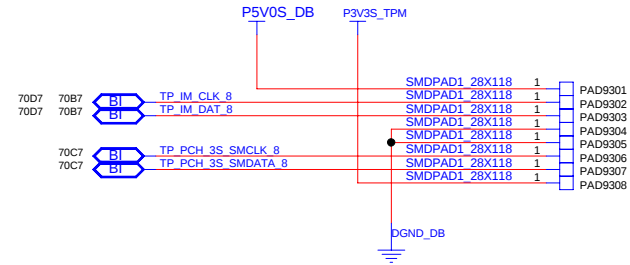
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SIZE	CODE	DOC NUMBER	REV
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TOUCH PAD BOARD 8 PIN

TOUCHPAD TO MODULE CONN



TOUCHPAD TO MB CONN



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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