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ASPEN DISCRETE PV(Final) 11/09/2004																																																																																														
<table border="1"><thead><tr><th>DATE</th><th>CHANGE NO.</th><th>REV</th></tr></thead><tbody><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr><tr><td> </td><td> </td><td> </td></tr></tbody></table>			DATE	CHANGE NO.	REV																												<table border="1"><thead><tr><th>DRAWER</th><th>EE</th><th>DATE</th><th>POWER</th><th>DATE</th></tr></thead><tbody><tr><td>DESIGN</td><td> </td><td> </td><td> </td><td> </td></tr><tr><td>CHECK</td><td> </td><td> </td><td> </td><td> </td></tr><tr><td>RESPONSIBLE</td><td> </td><td> </td><td> </td><td> </td></tr><tr><td colspan="2">SIZE = 3</td><td colspan="3">VER :</td></tr><tr><td colspan="5">FILE NAME : XXXX-XXXXXX-XX</td></tr><tr><td>P/N</td><td colspan="4">XXXXXXXXXXXX</td></tr></tbody></table>					DRAWER	EE	DATE	POWER	DATE	DESIGN					CHECK					RESPONSIBLE					SIZE = 3		VER :			FILE NAME : XXXX-XXXXXX-XX					P/N	XXXXXXXXXXXX				<table border="1"><thead><tr><th colspan="4">INVENTEC</th></tr><tr><th colspan="4">TITLE ASPEN</th></tr><tr><th>SIZE</th><th>CODE</th><th>DOC. NUMBER</th><th>REV</th></tr></thead><tbody><tr><td>A3</td><td>CS</td><td>Model No</td><td>A01</td></tr><tr><td colspan="2">SHEET 1</td><td colspan="2">OF 69</td></tr></tbody></table>		INVENTEC				TITLE ASPEN				SIZE	CODE	DOC. NUMBER	REV	A3	CS	Model No	A01	SHEET 1		OF 69	
DATE	CHANGE NO.	REV																																																																																												
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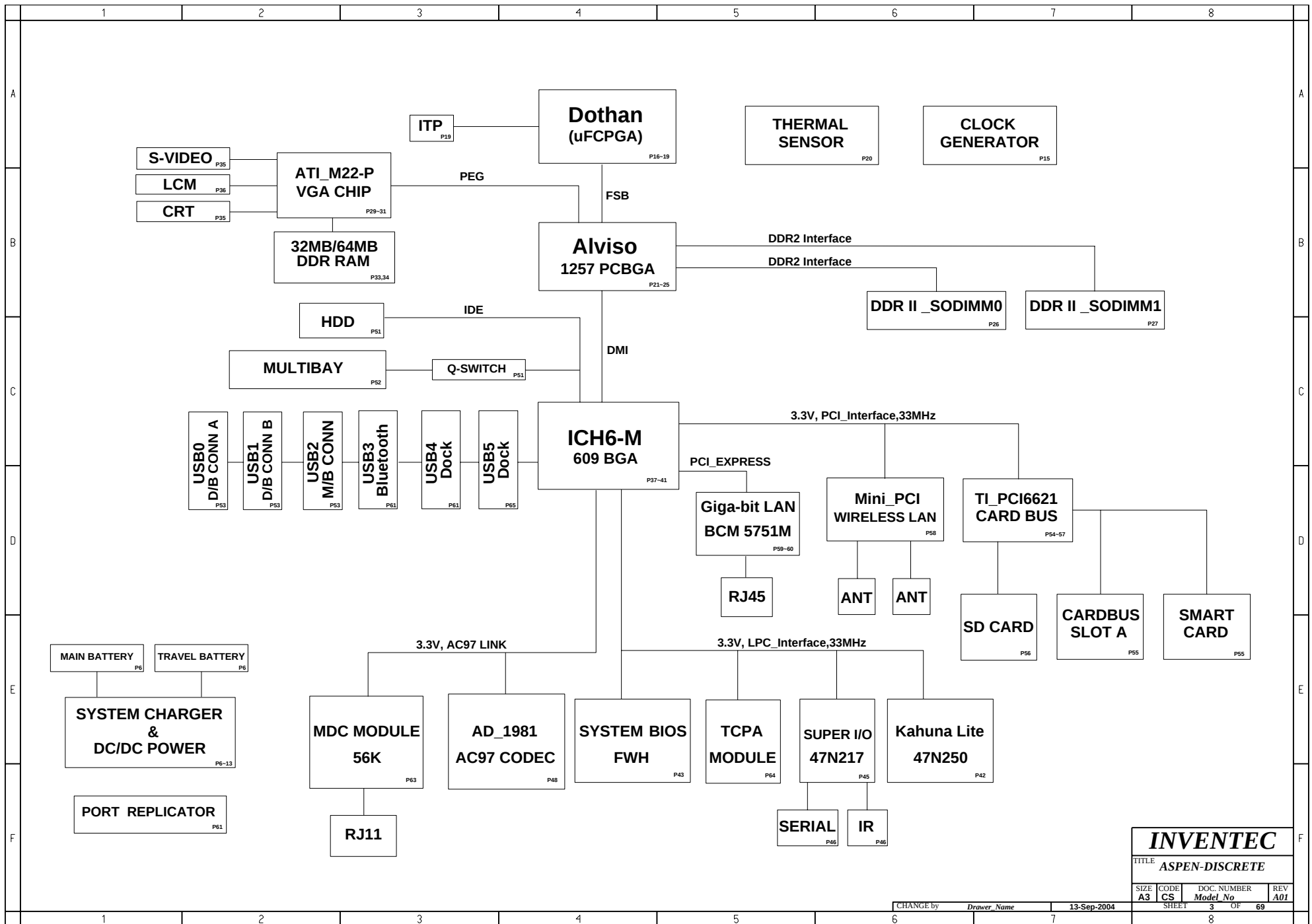
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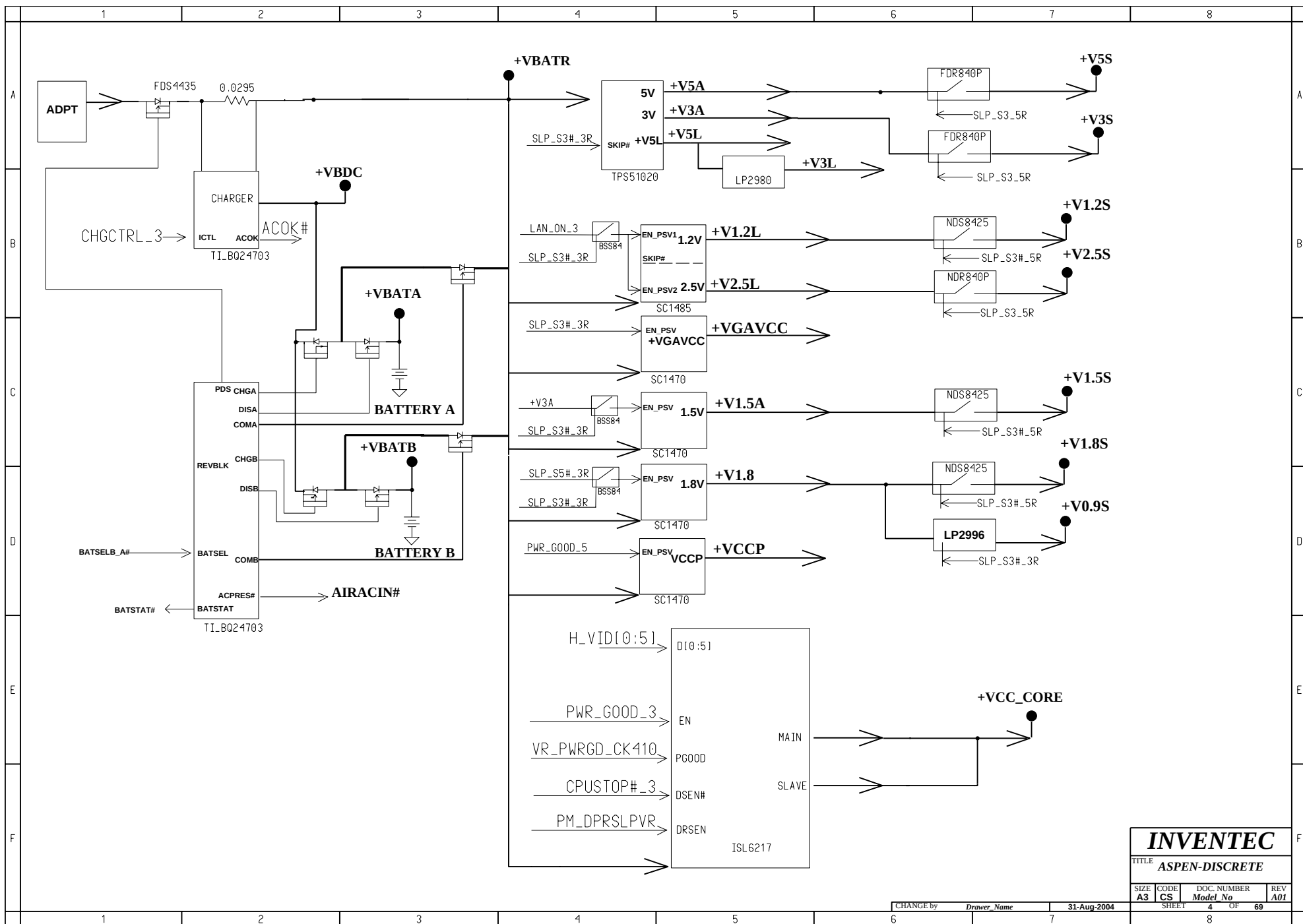
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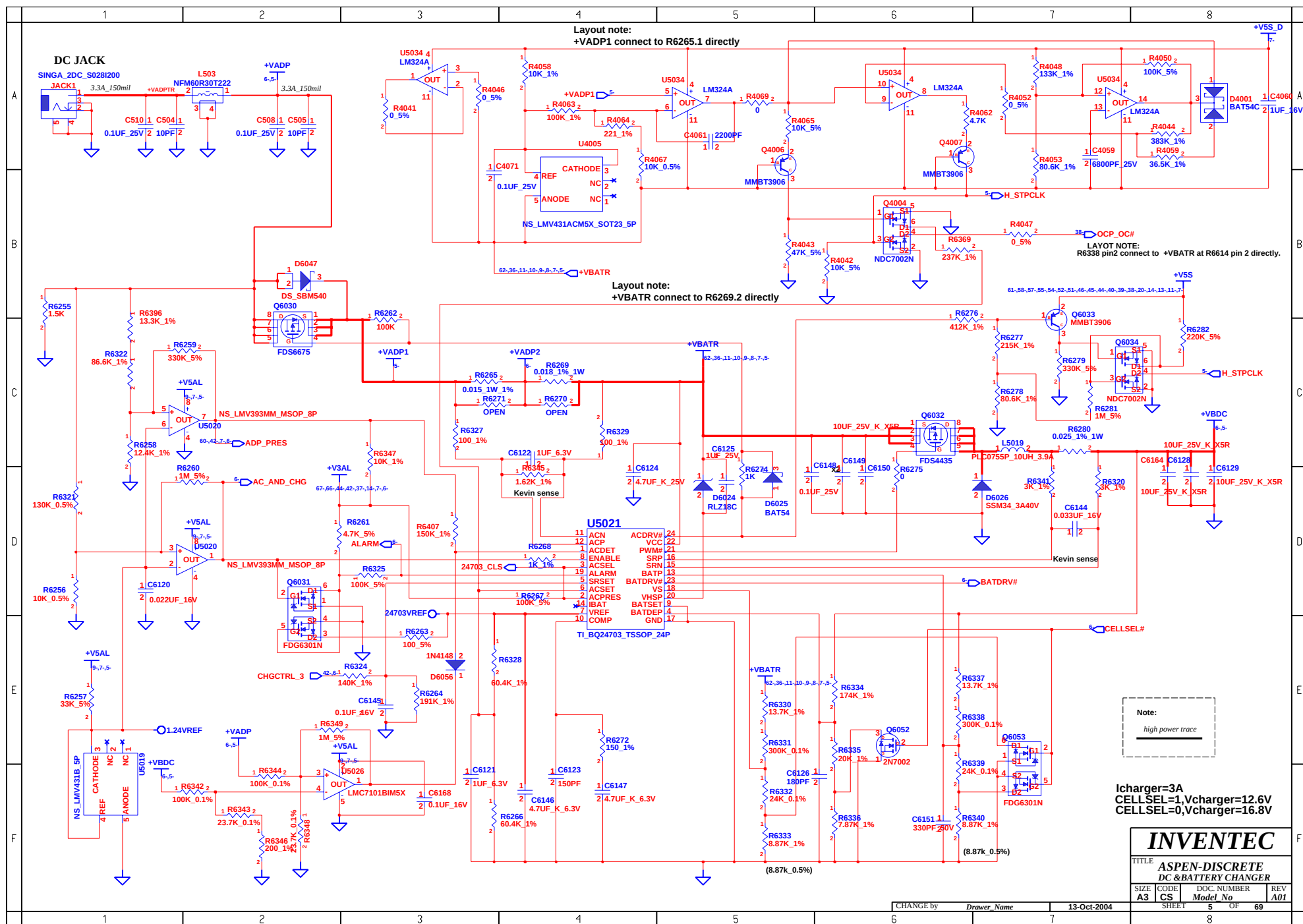
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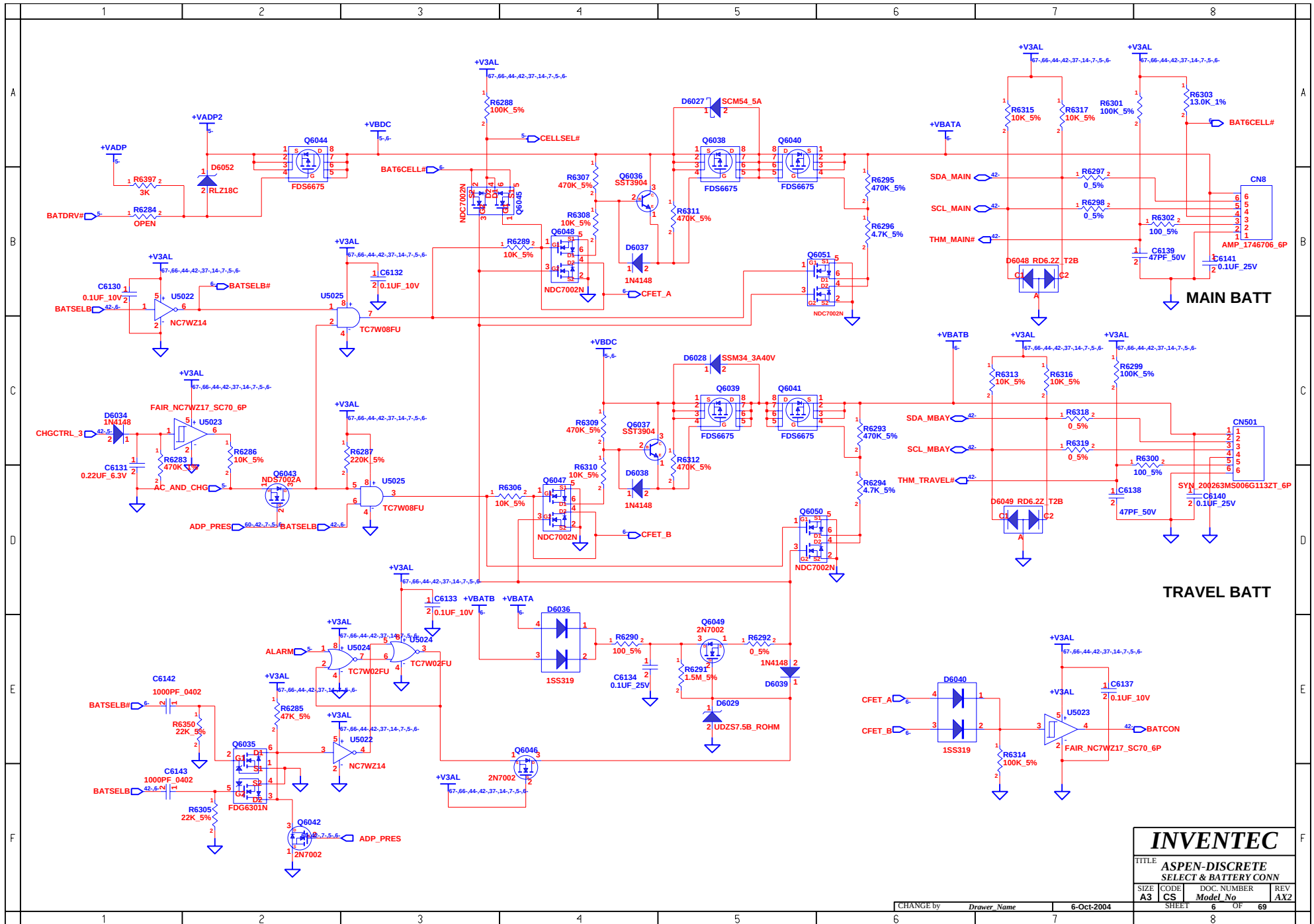
CHANGE by Drawn Name 31-Aug-2004

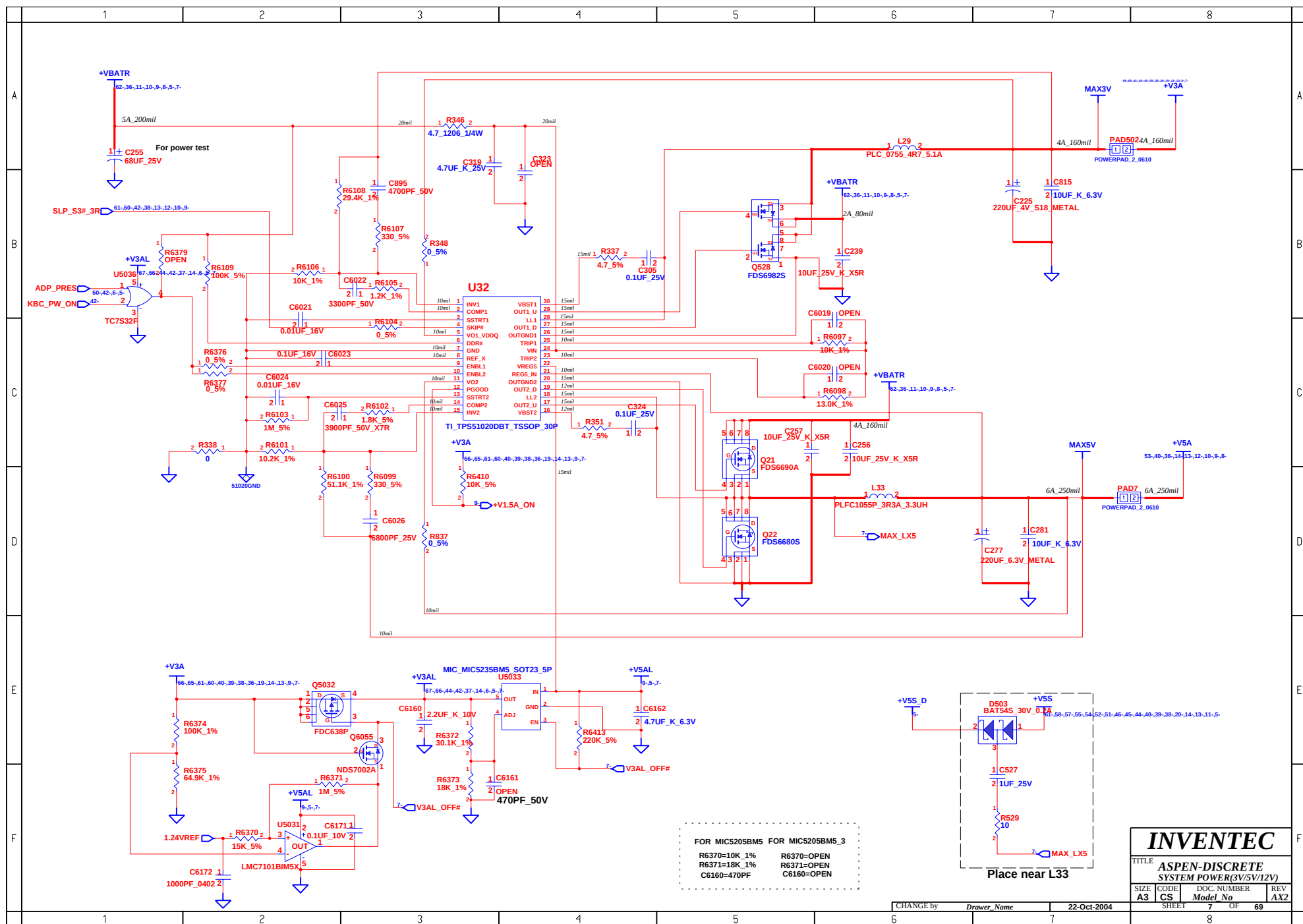
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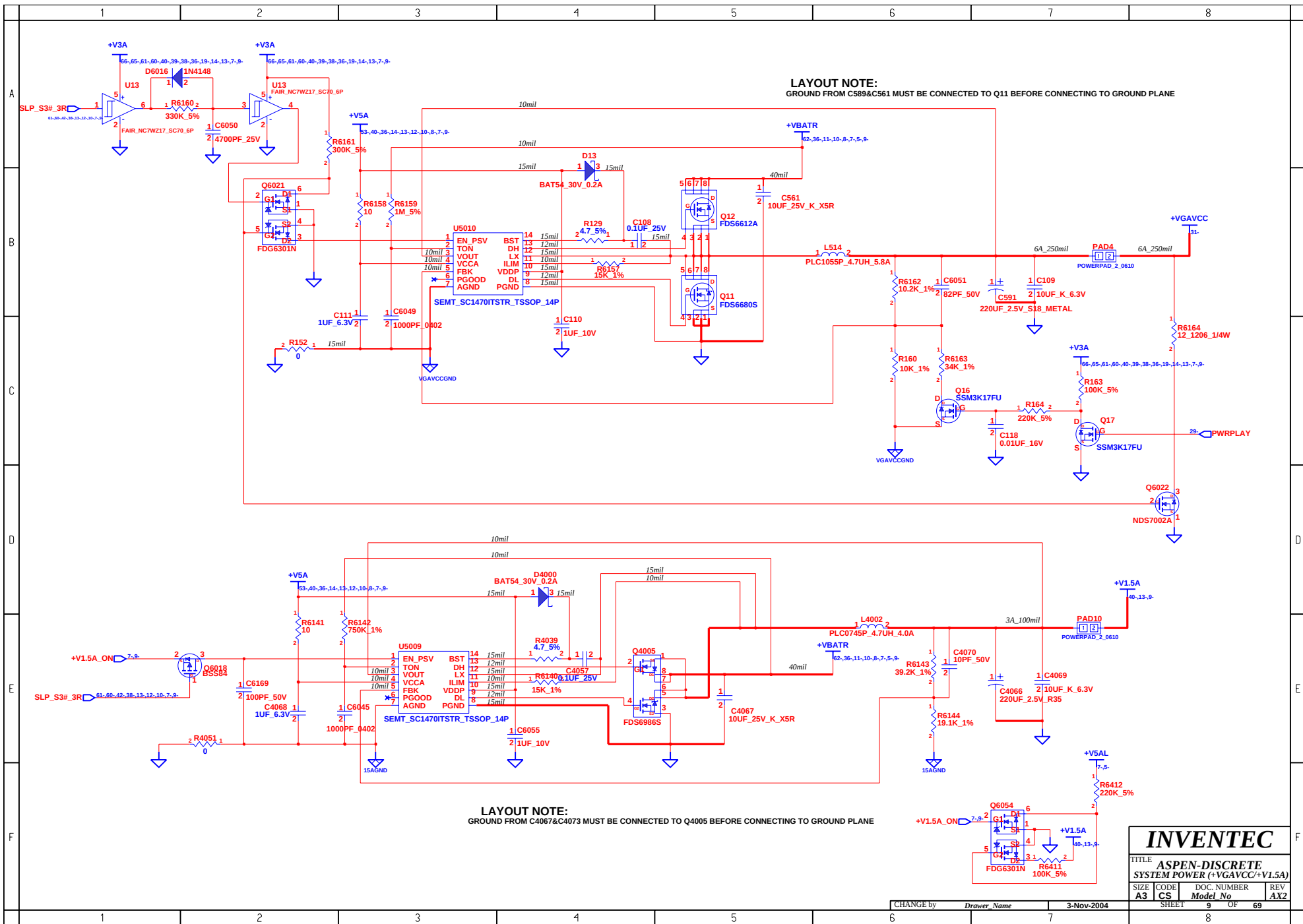


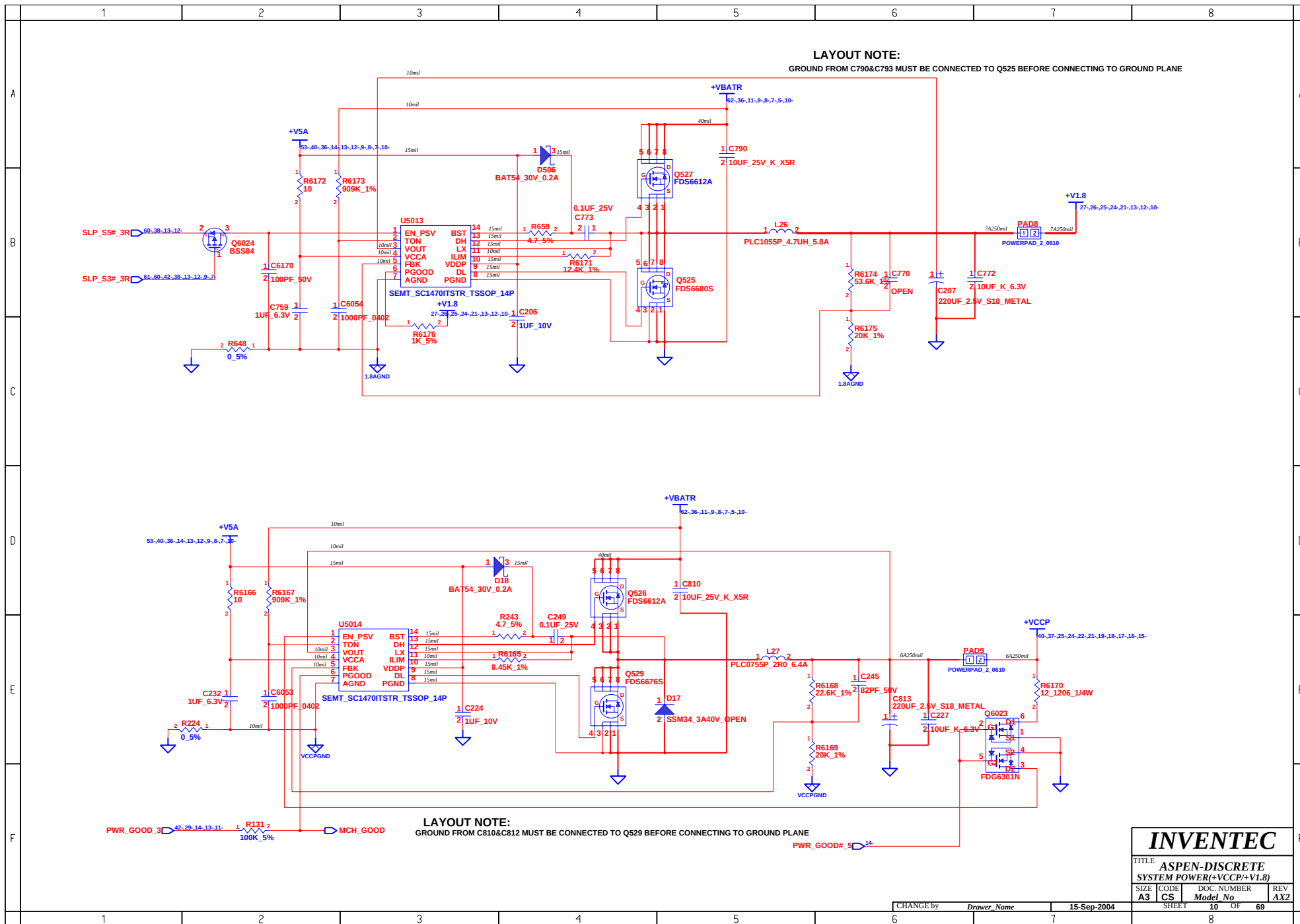


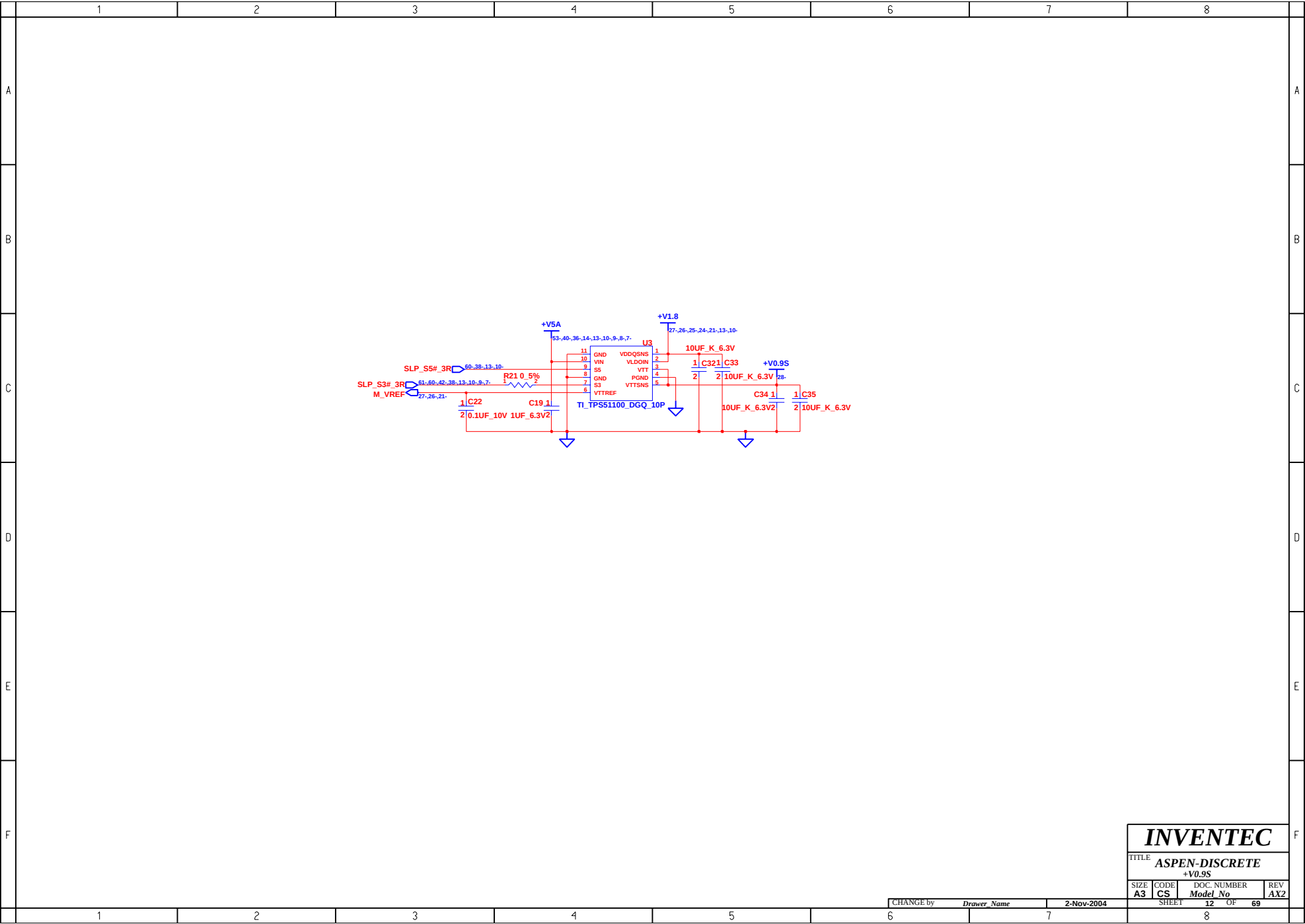












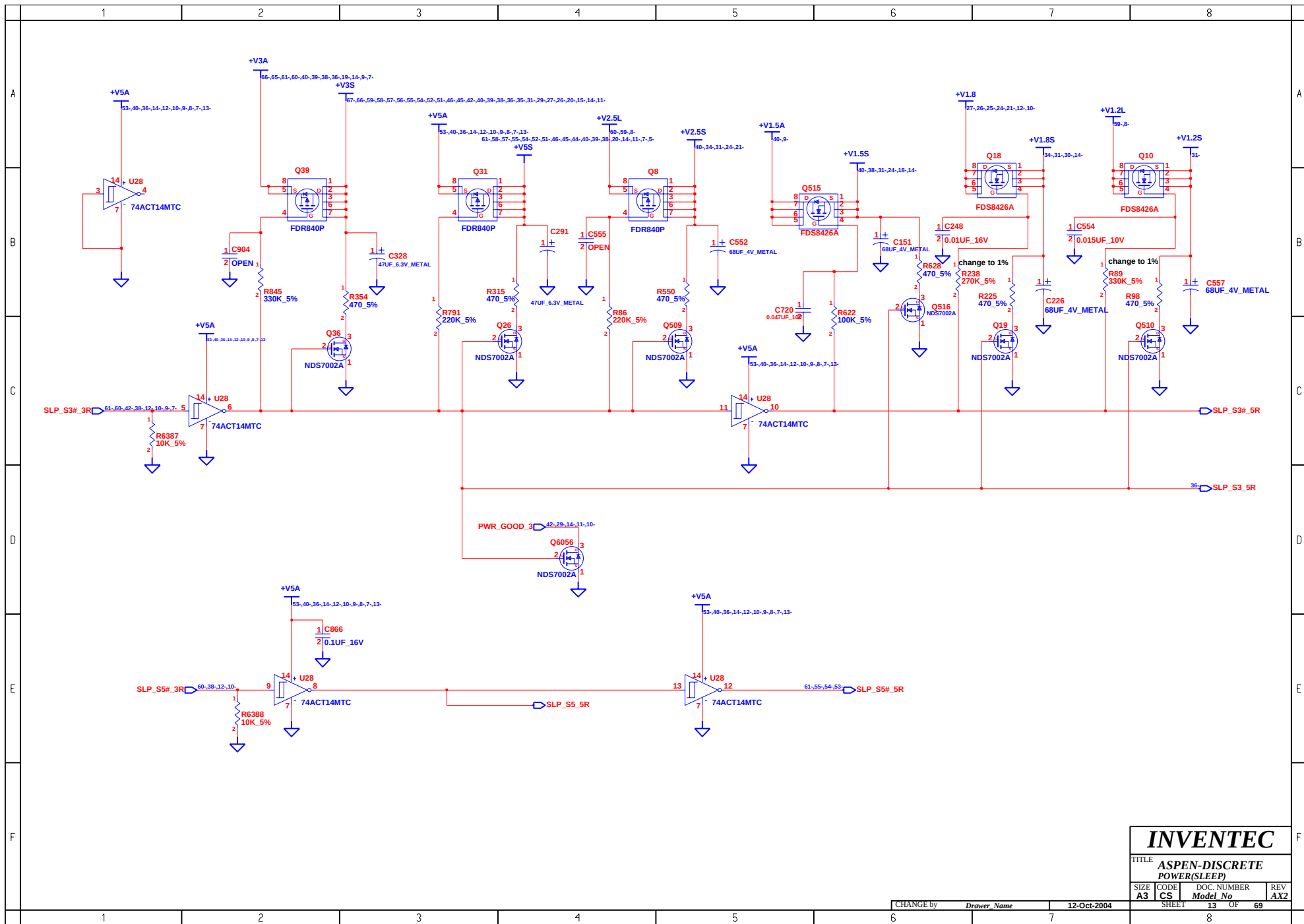
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TITLE
ASPEN-DISCRETE
+V0.9S

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

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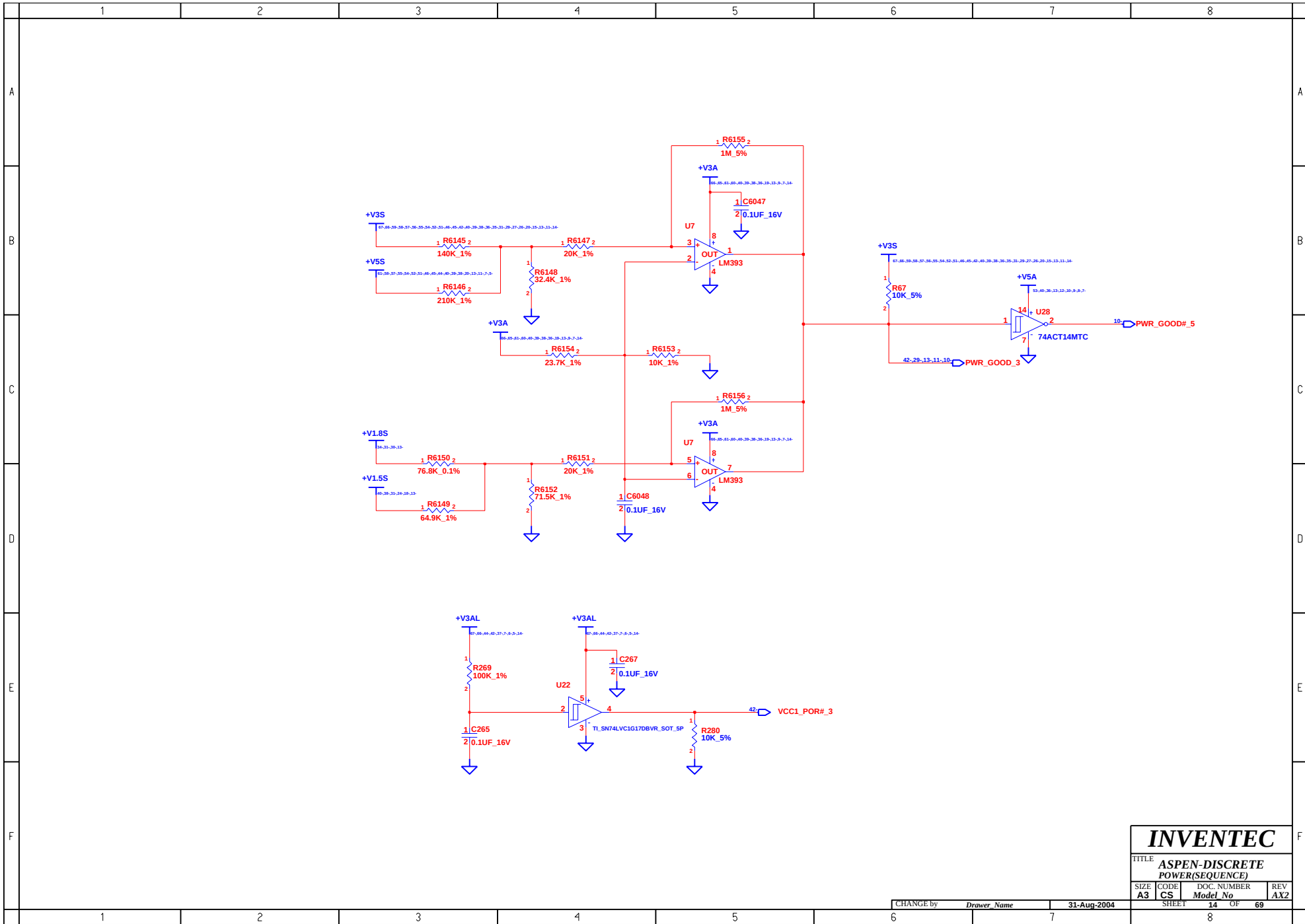


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A3	CS	Model No	AX2

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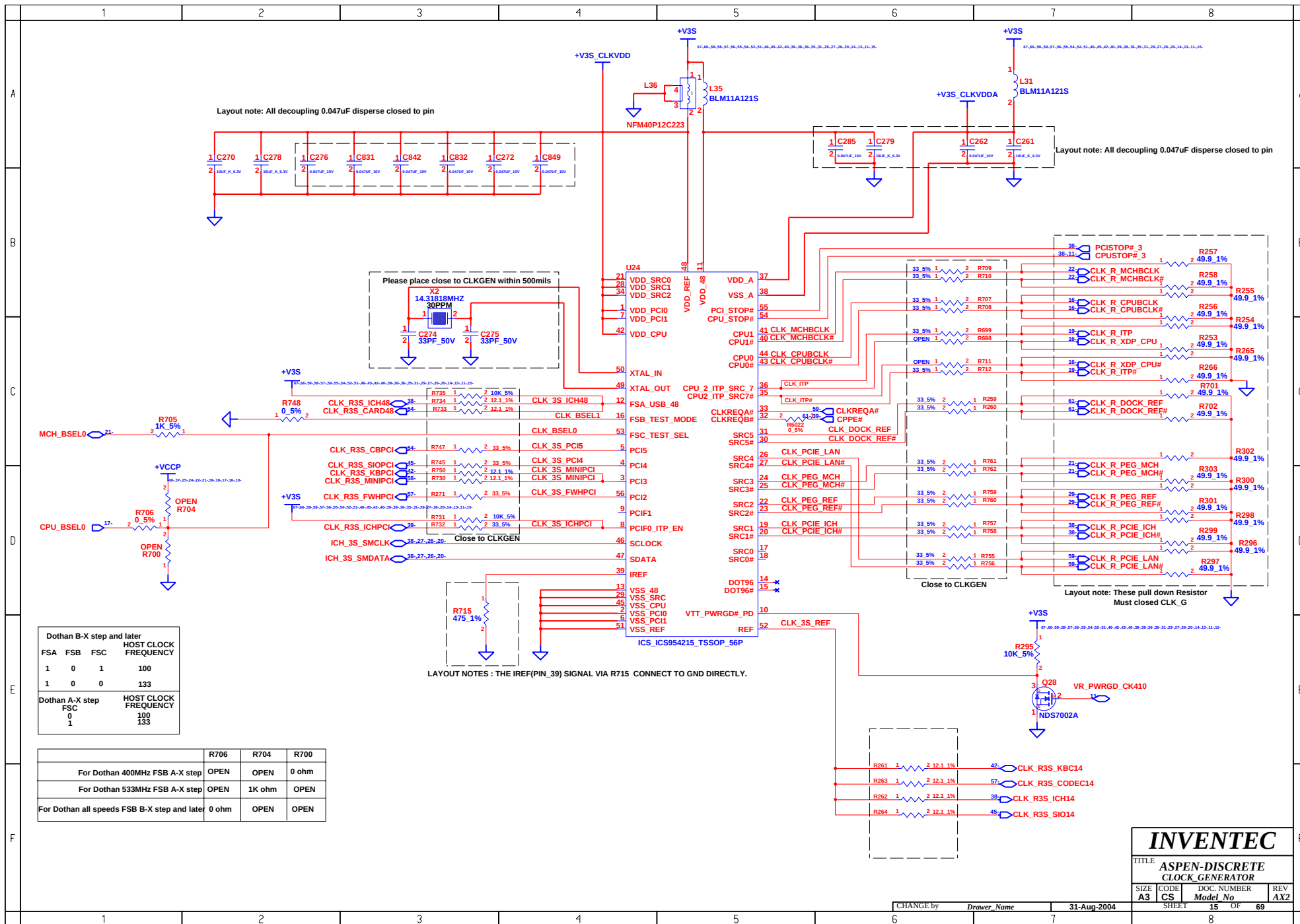


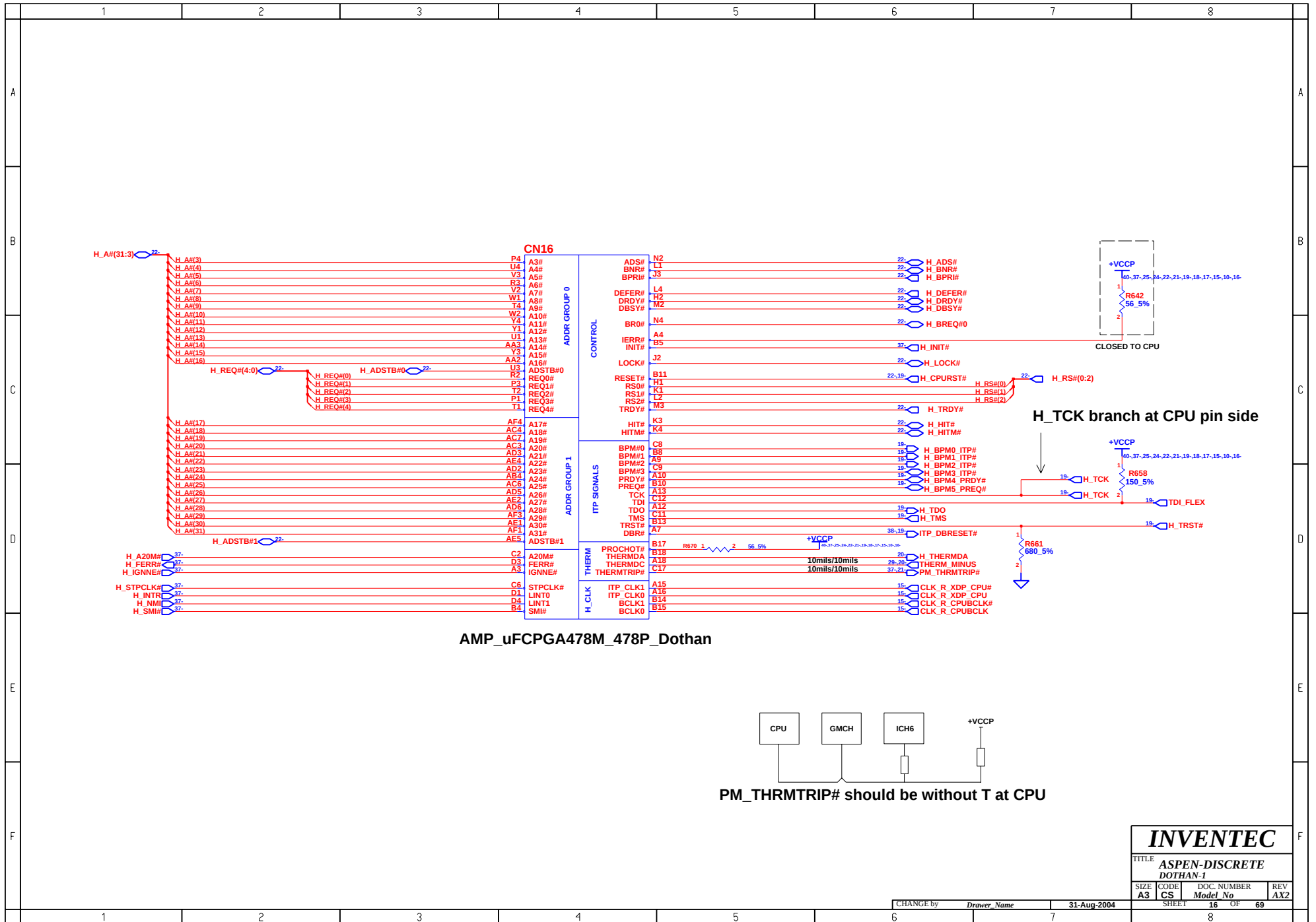
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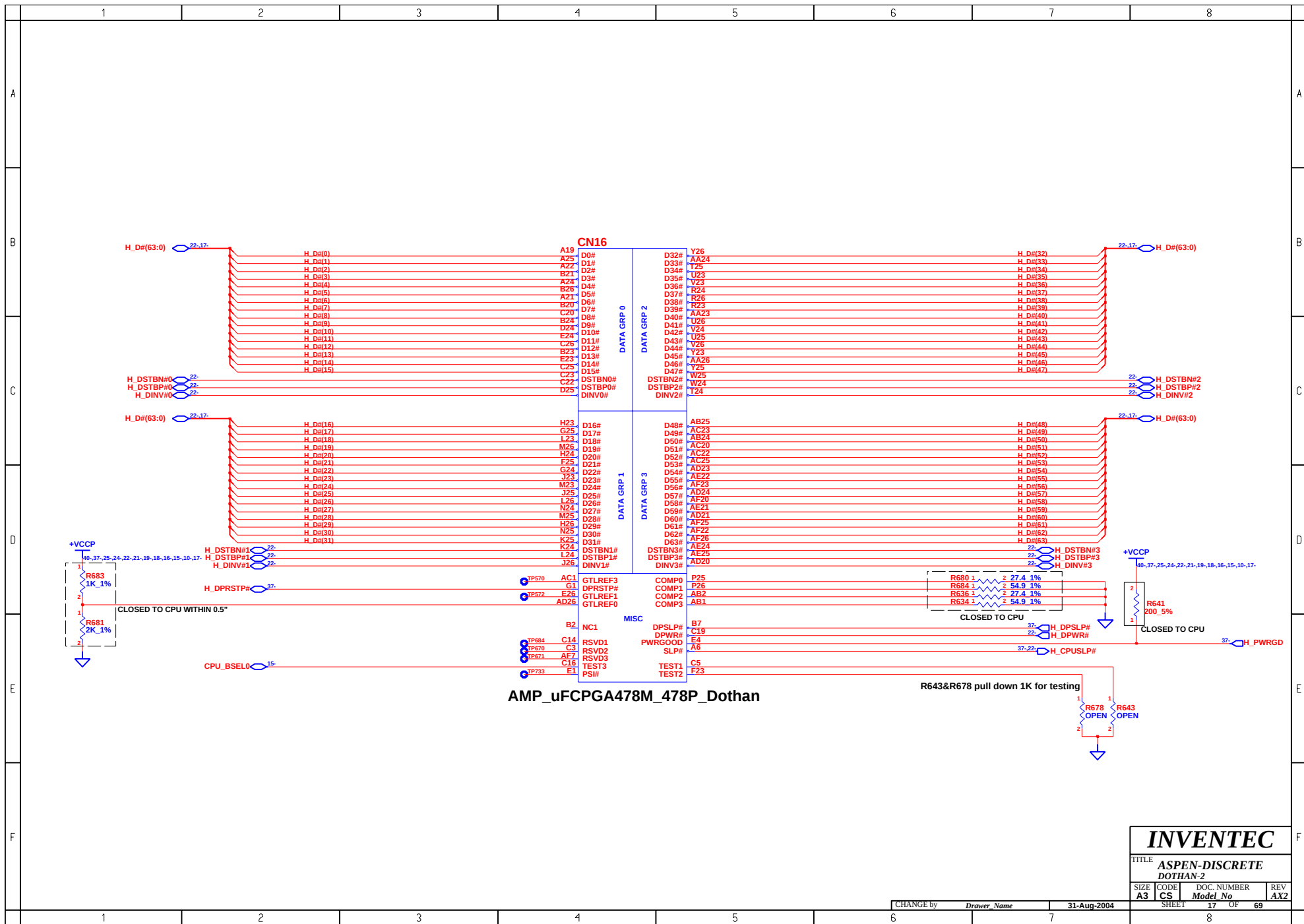
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A3	CS	Model No	AX2

CHANGE by *Drawn Name* 31-Aug-2004

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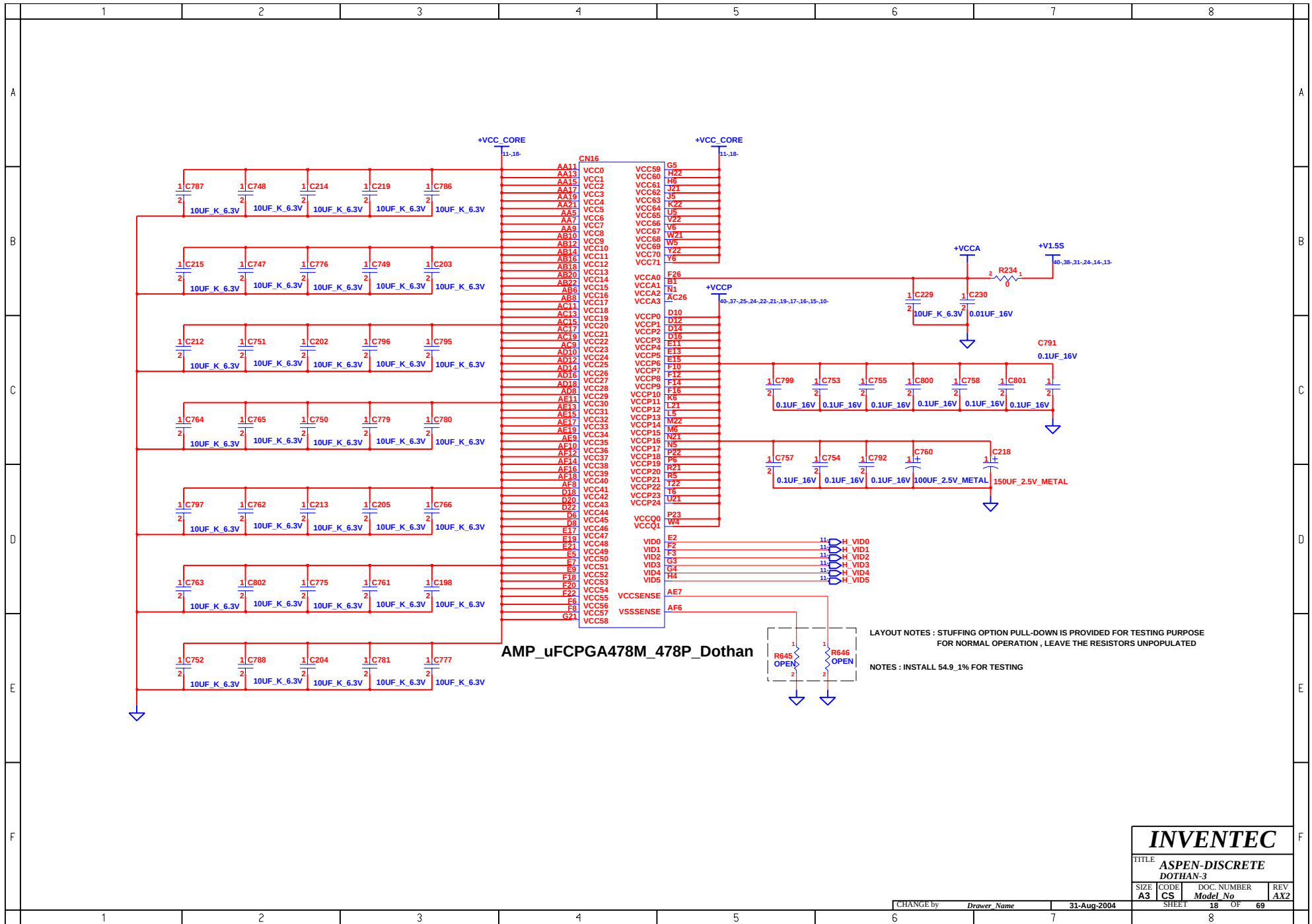
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TITLE
**ASPEN-DISCRETE
DOTHAN-2**

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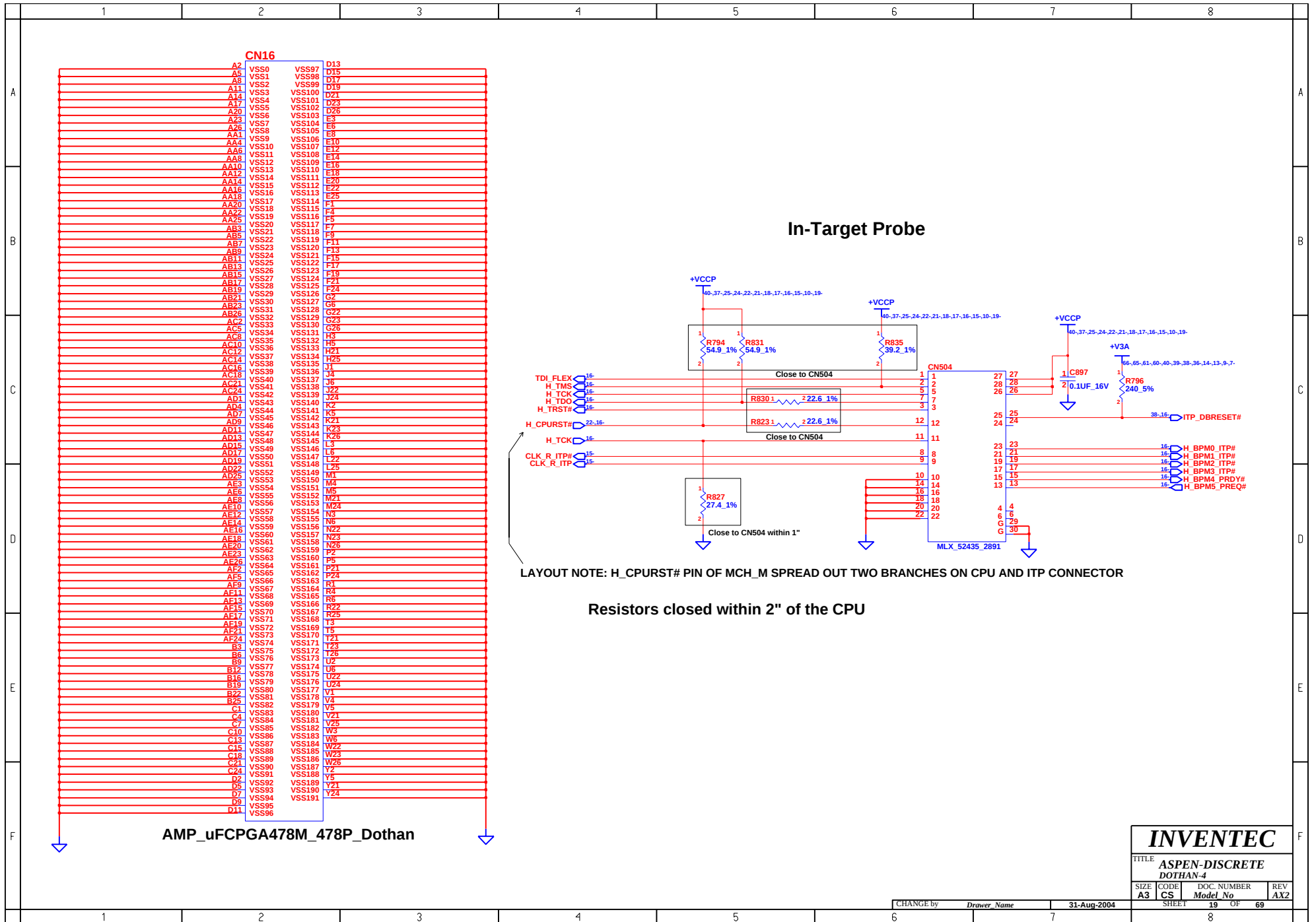
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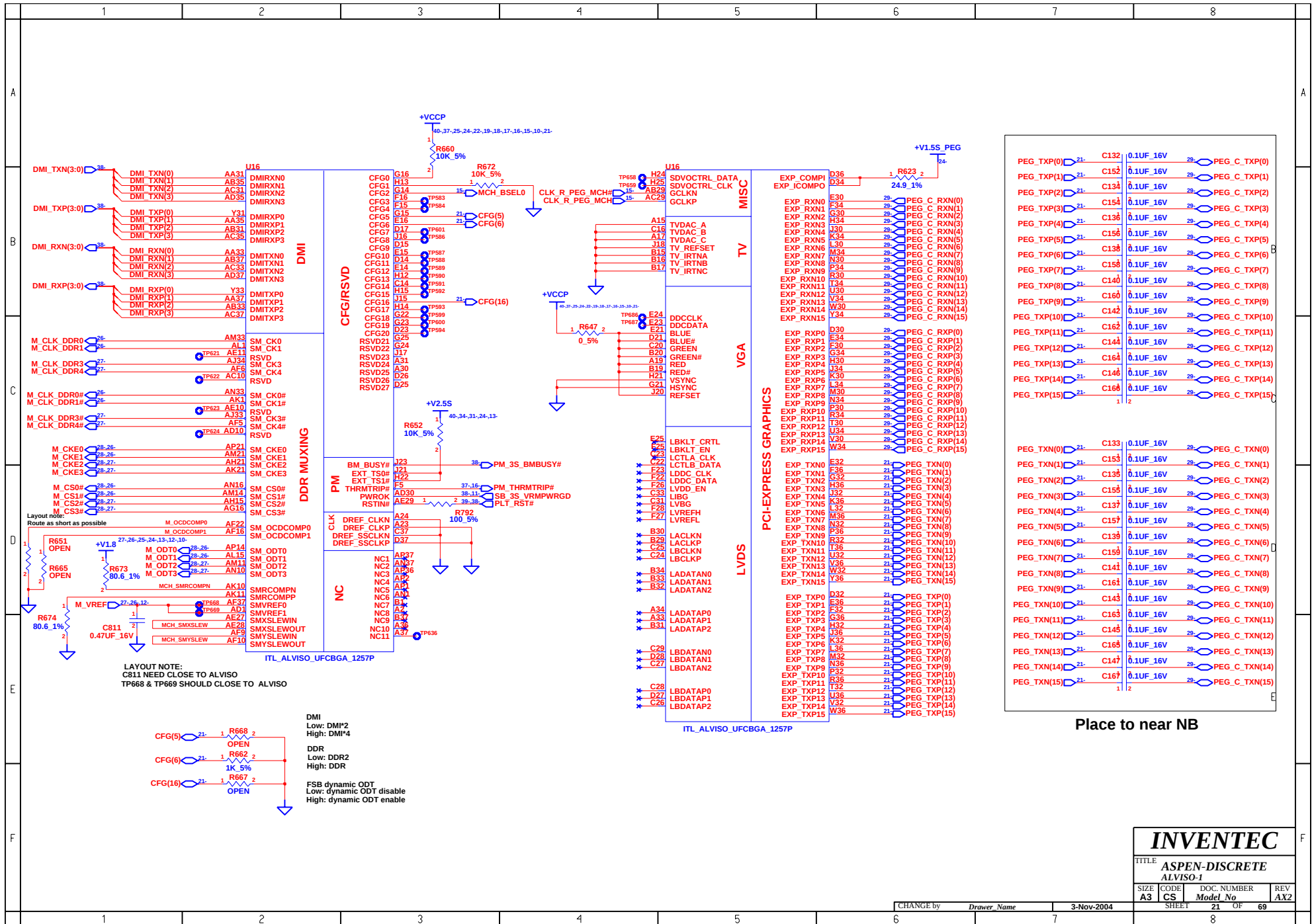


INVENTEC

TITLE ASPEN-DISCRETE DOTHAN-3			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
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CHANGE by *Drawer Name* 31-Aug-2004





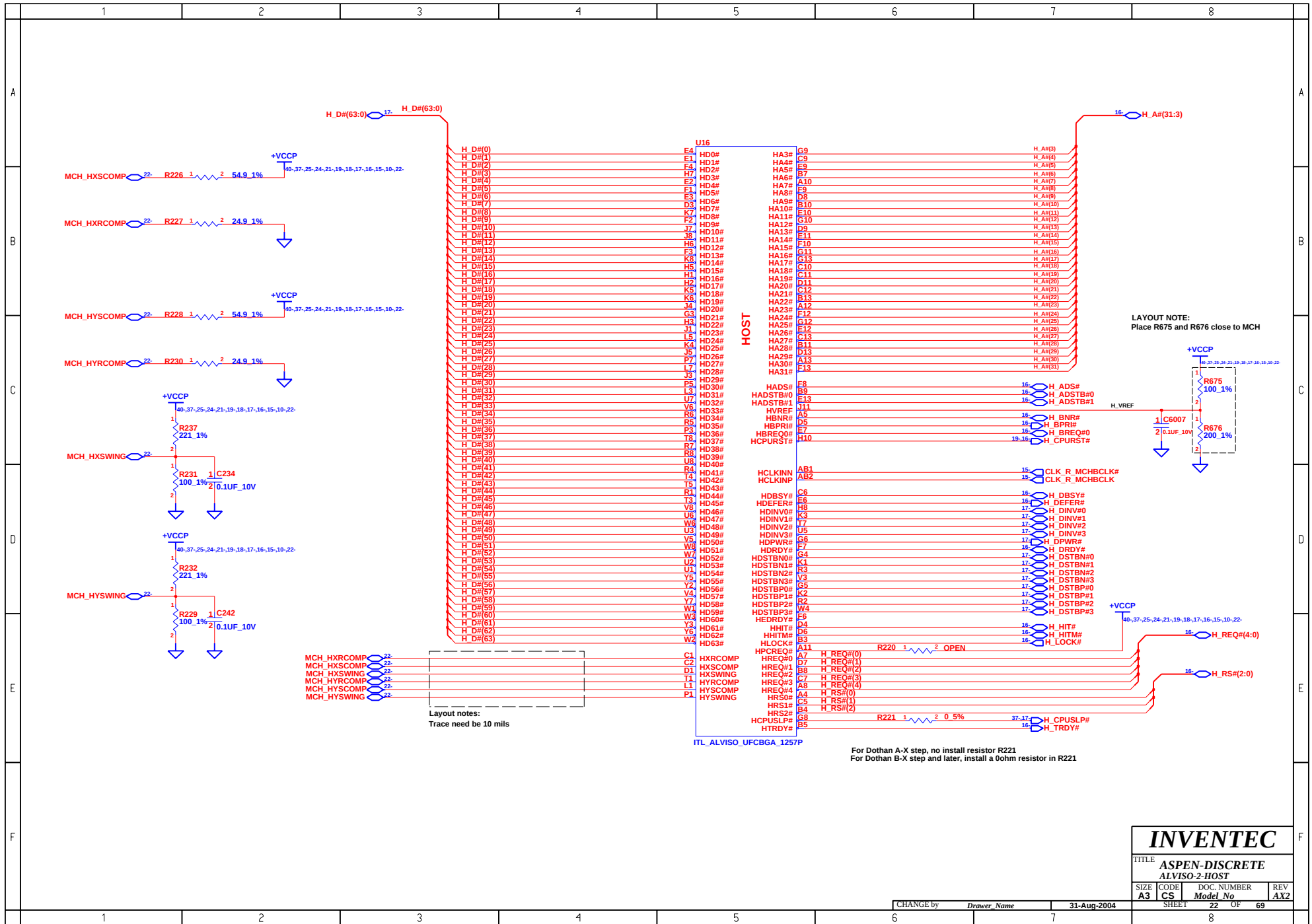
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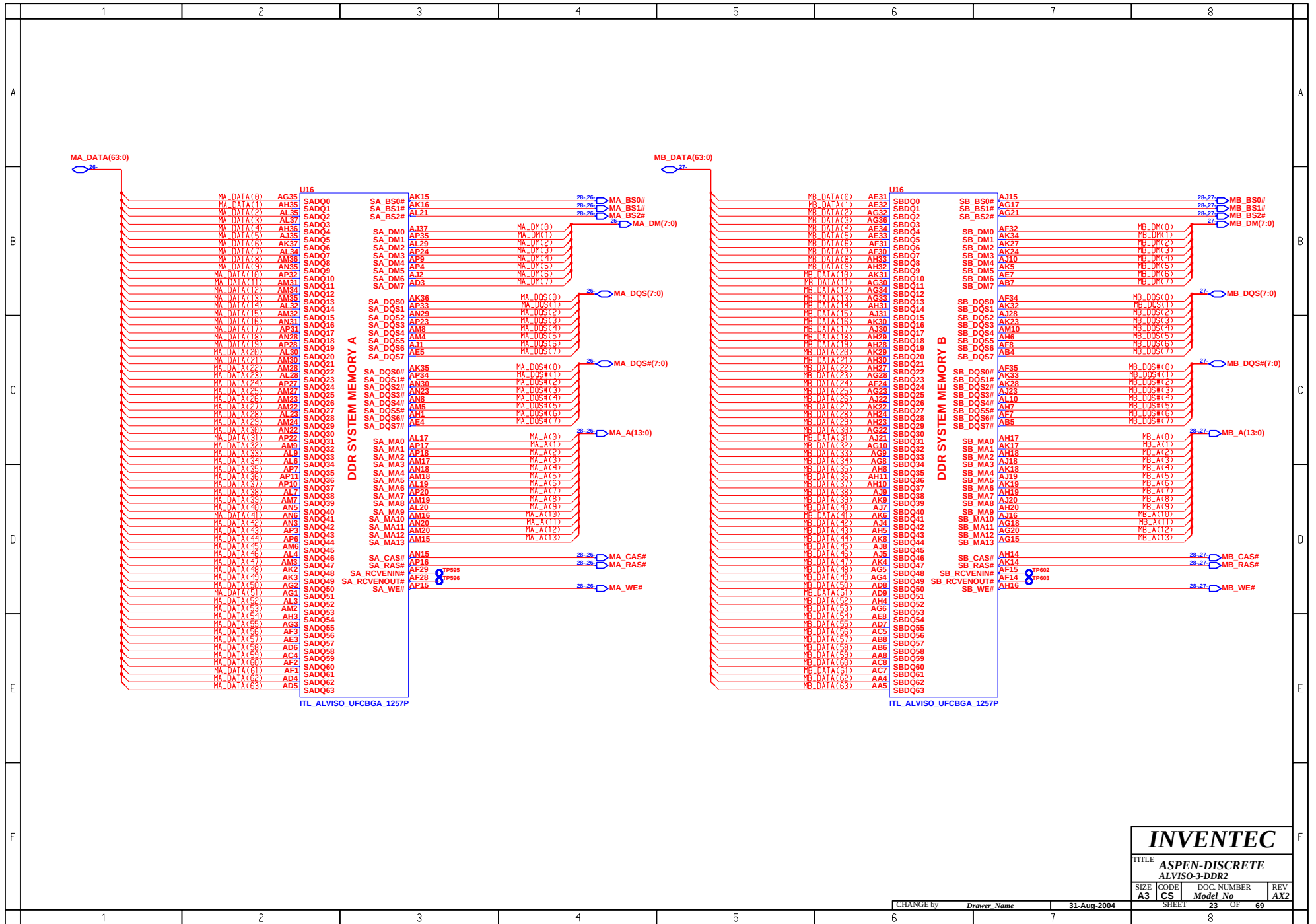
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ALVISO-1

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A3	CS	Model No	AX2

CHANGE by *Drawer Name* 3-Nov-2004

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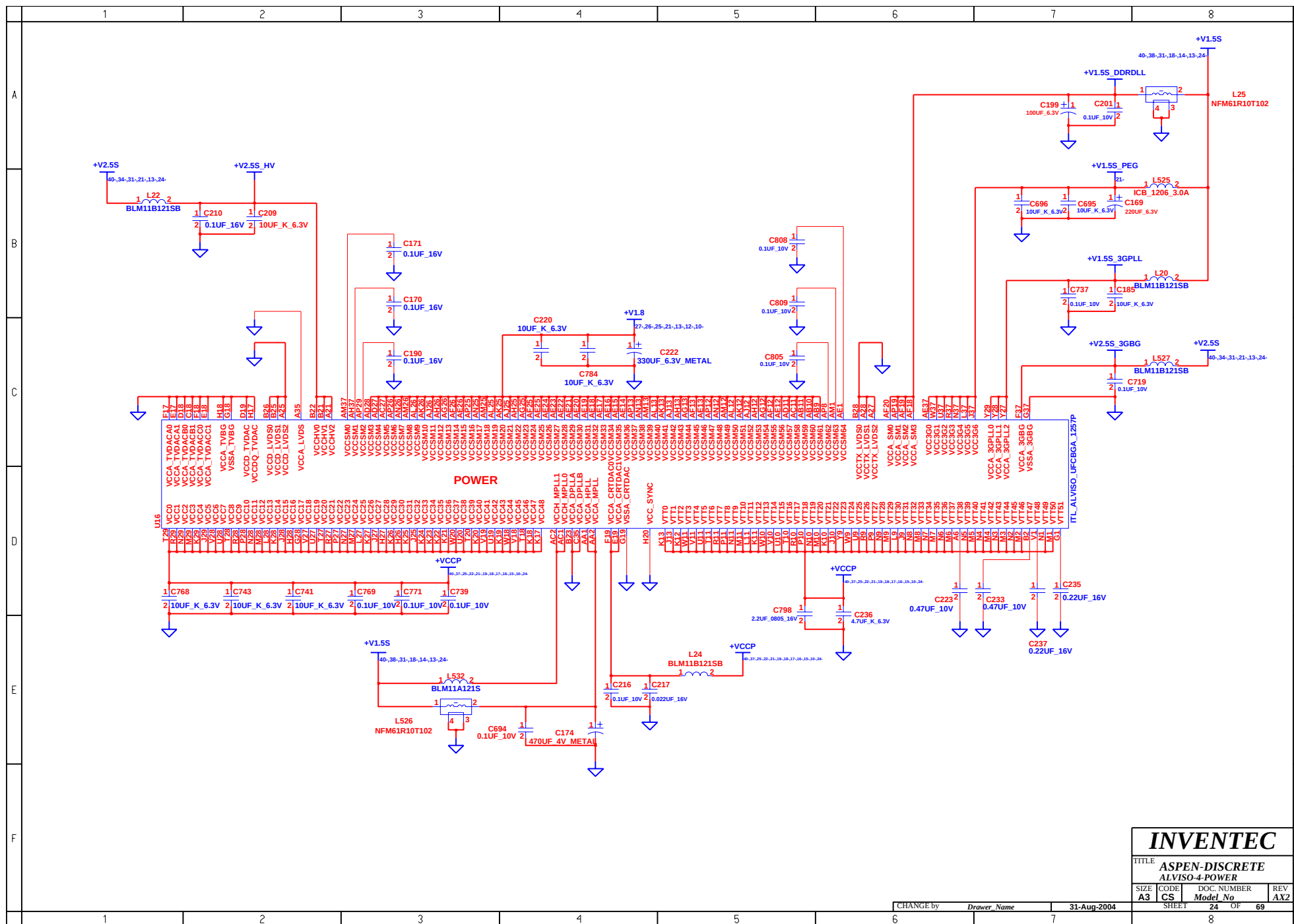
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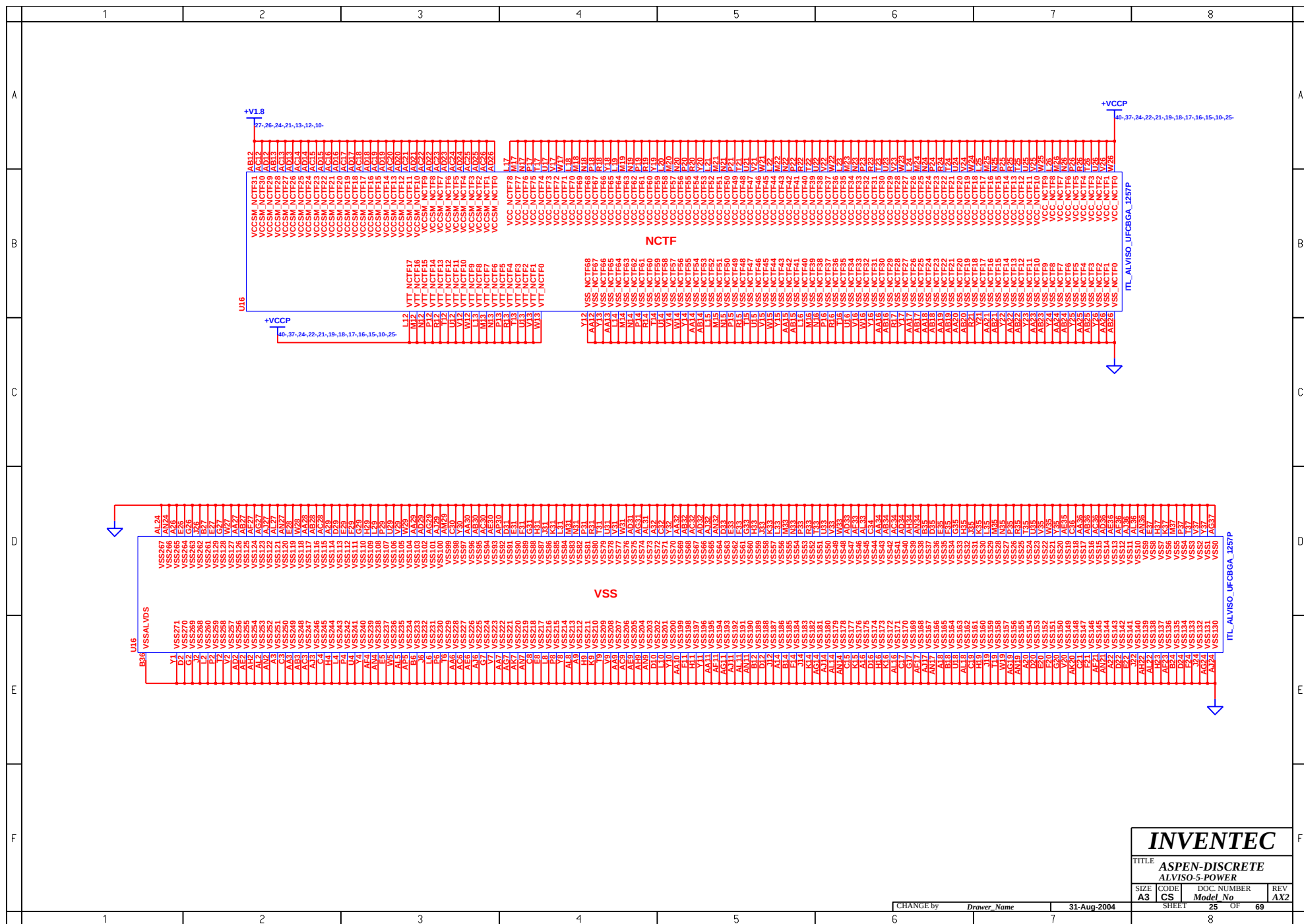
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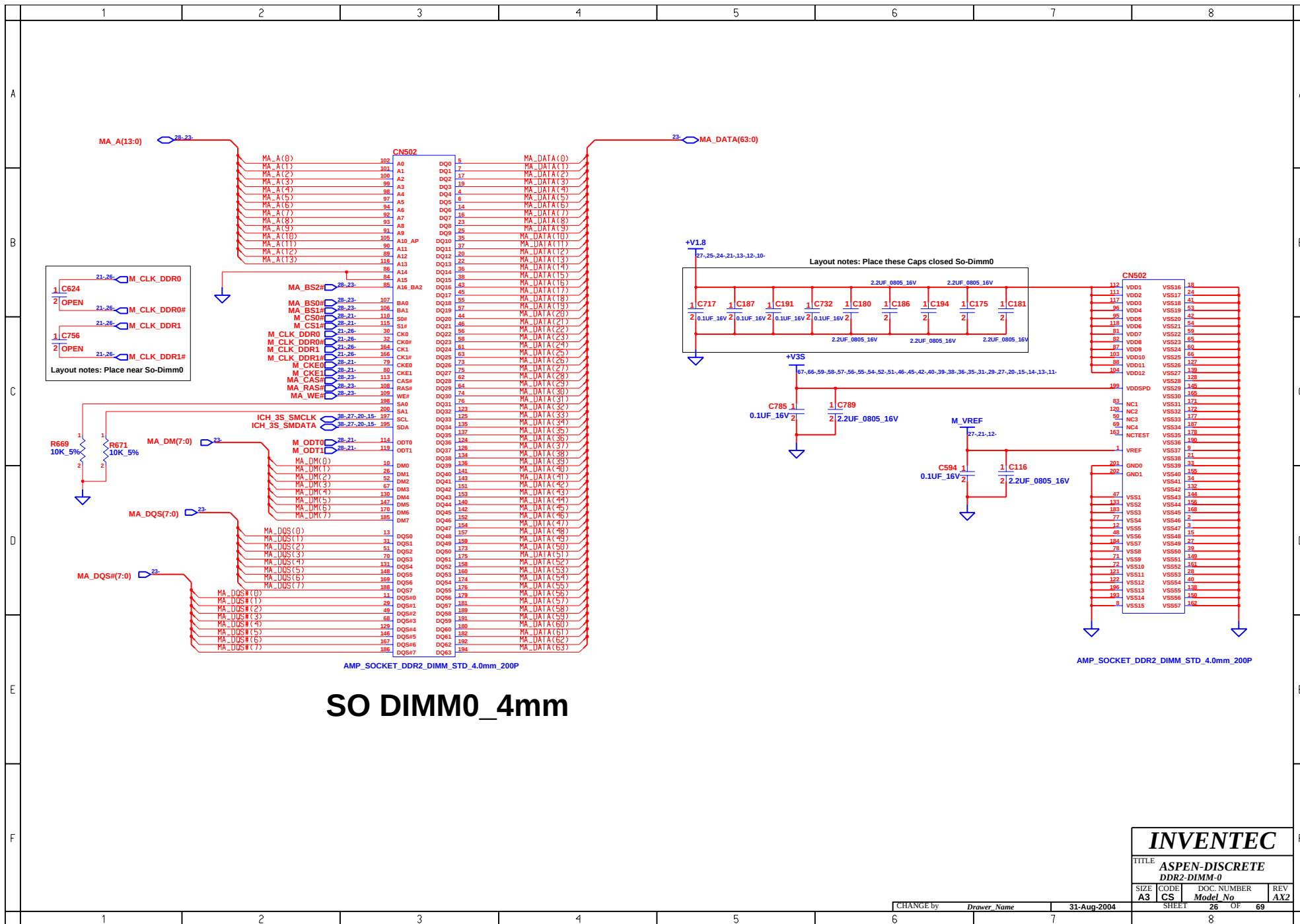
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A3	CS	Model No	AX2

CHANGE by Drawer Name 31-Aug-2004

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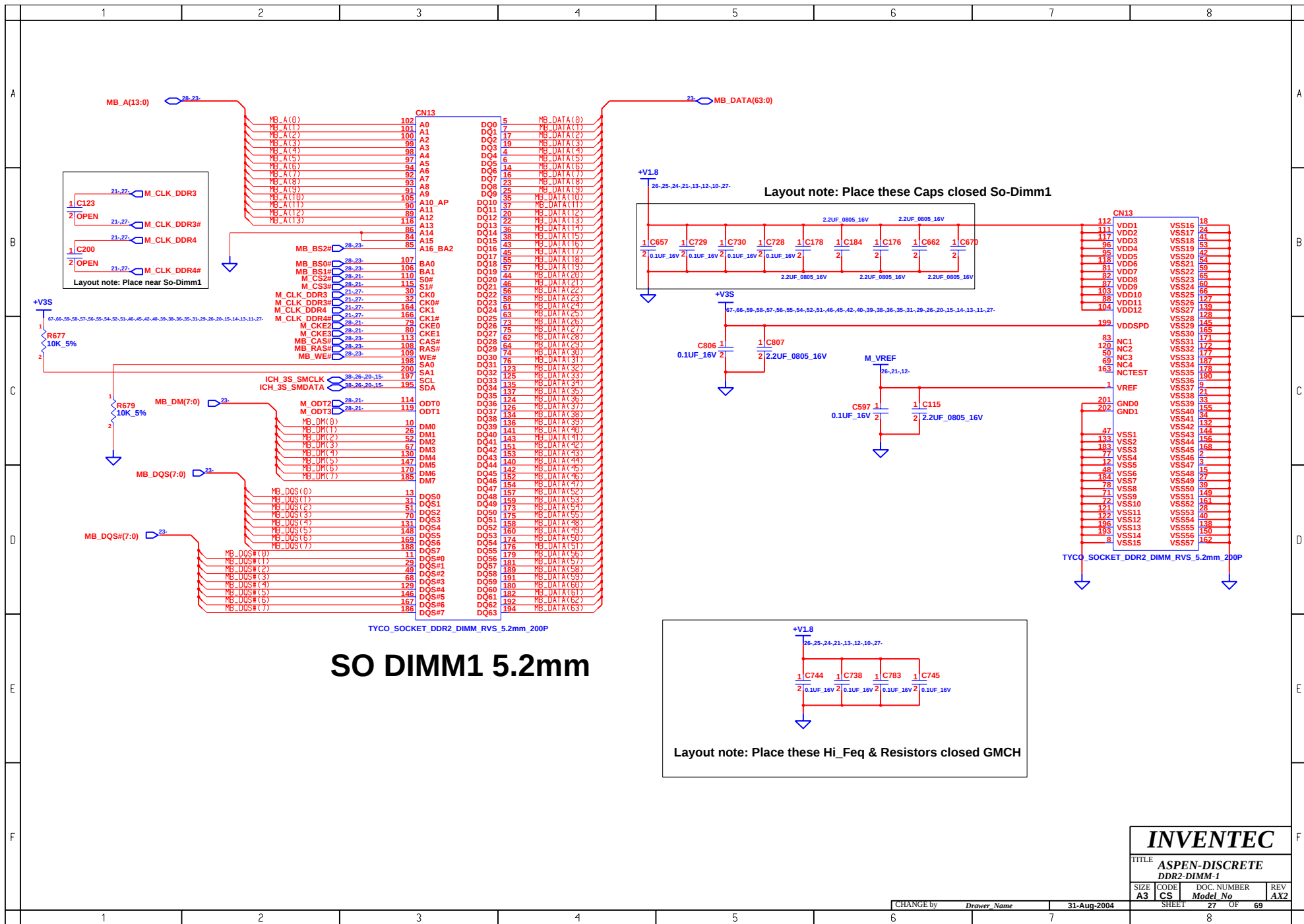
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TITLE
ASPEN-DISCRETE
DDR2-DIMM-0

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawer Name* 31-Aug-2004

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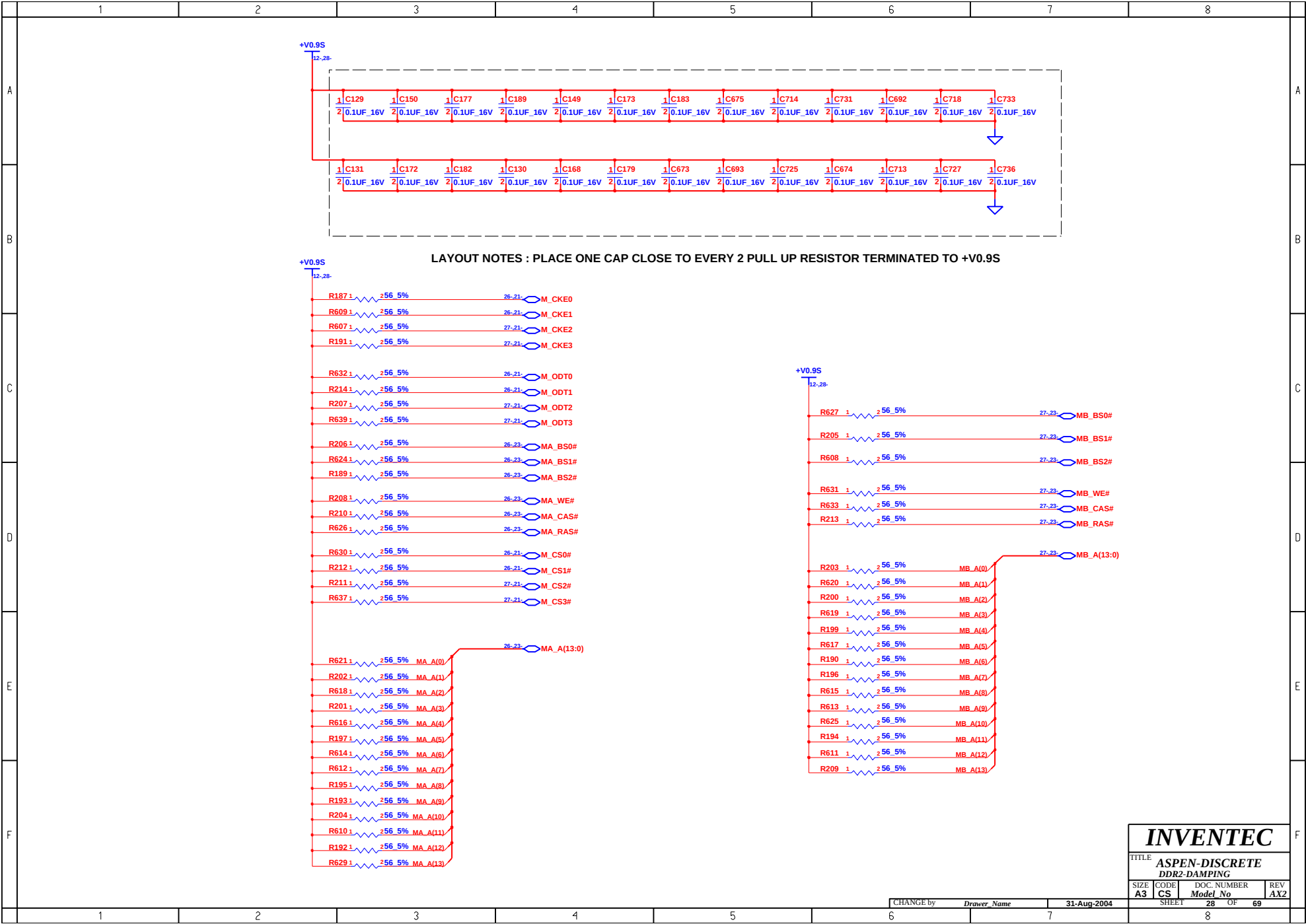
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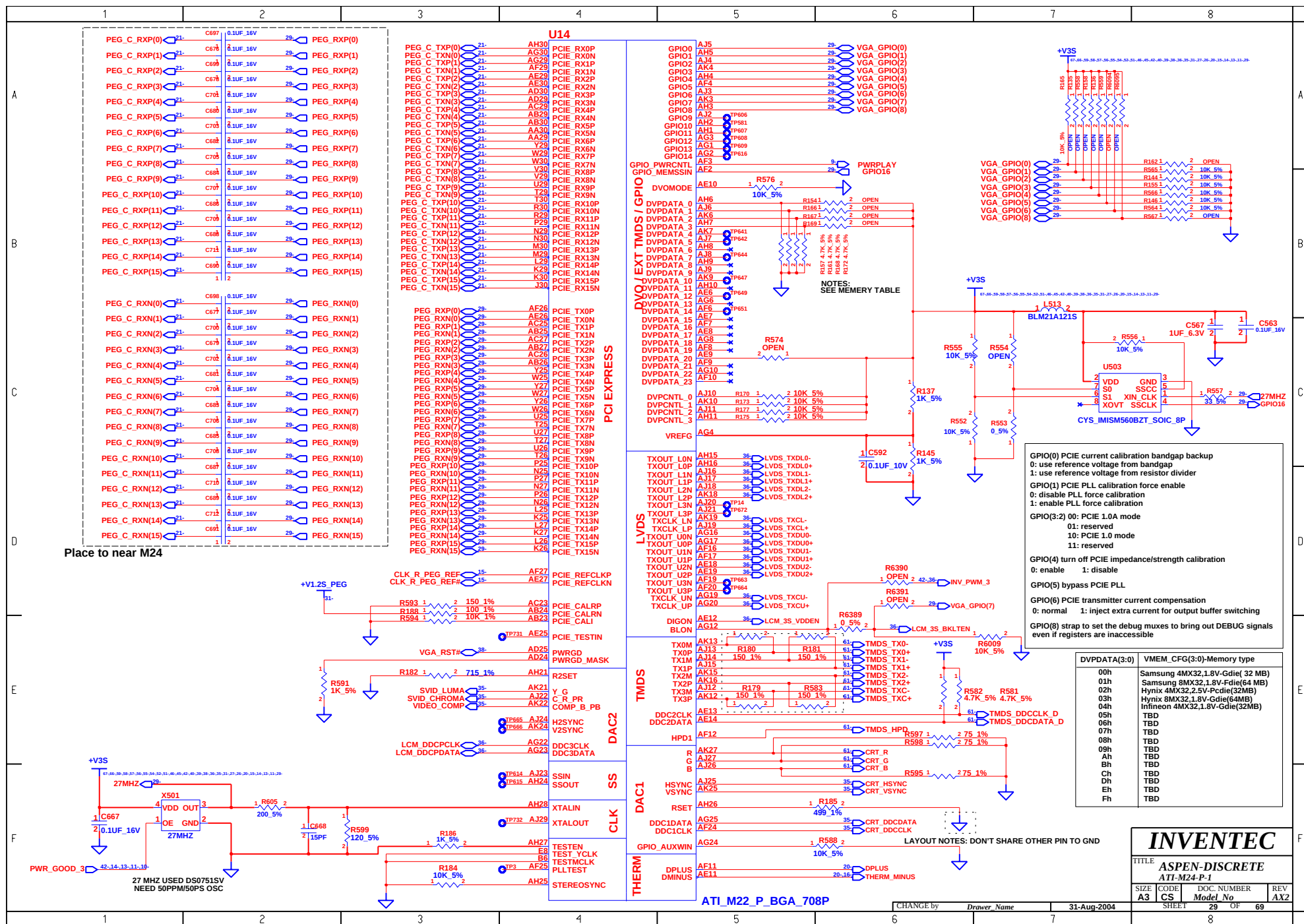
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DDR2-DIMM-1**

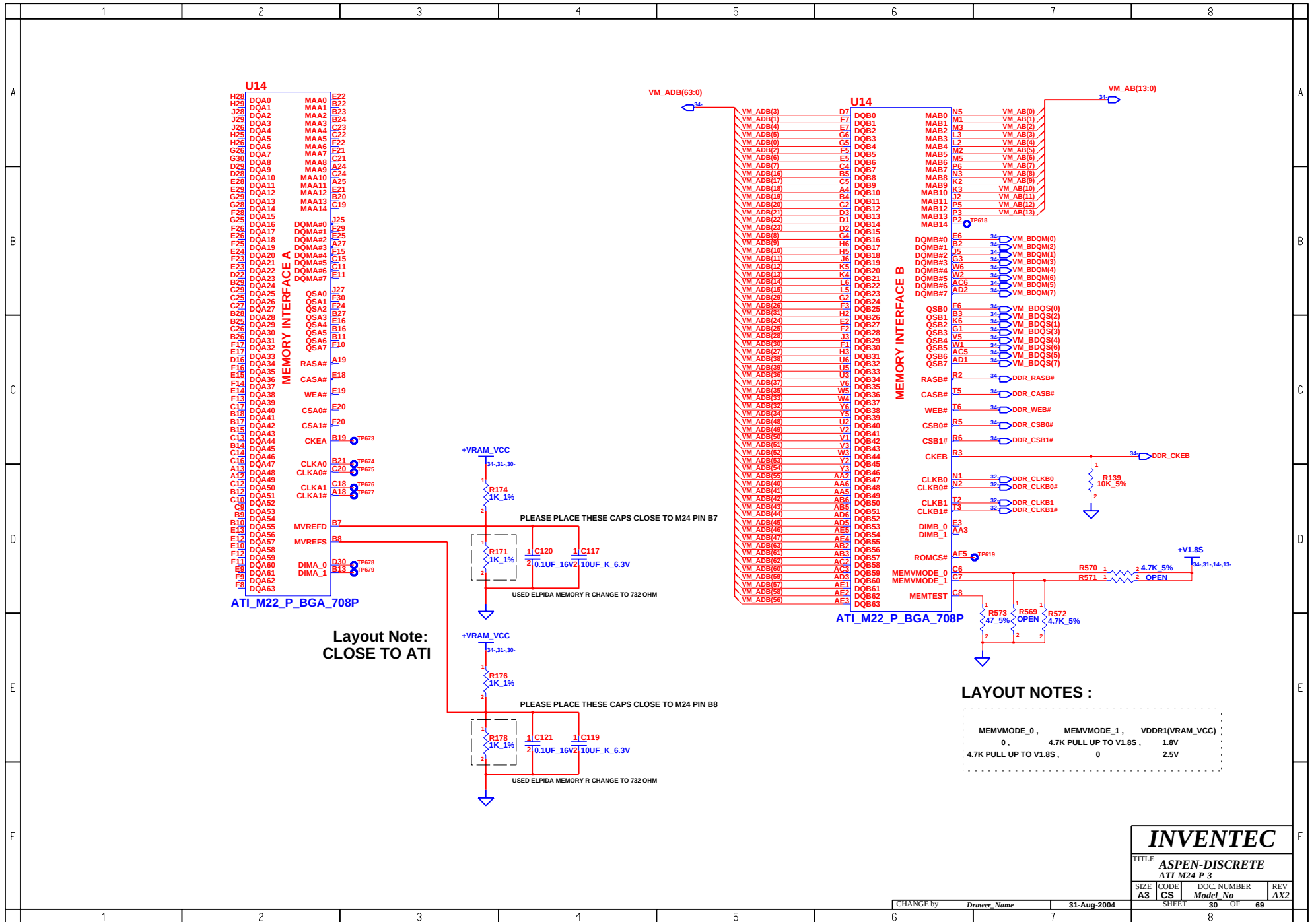
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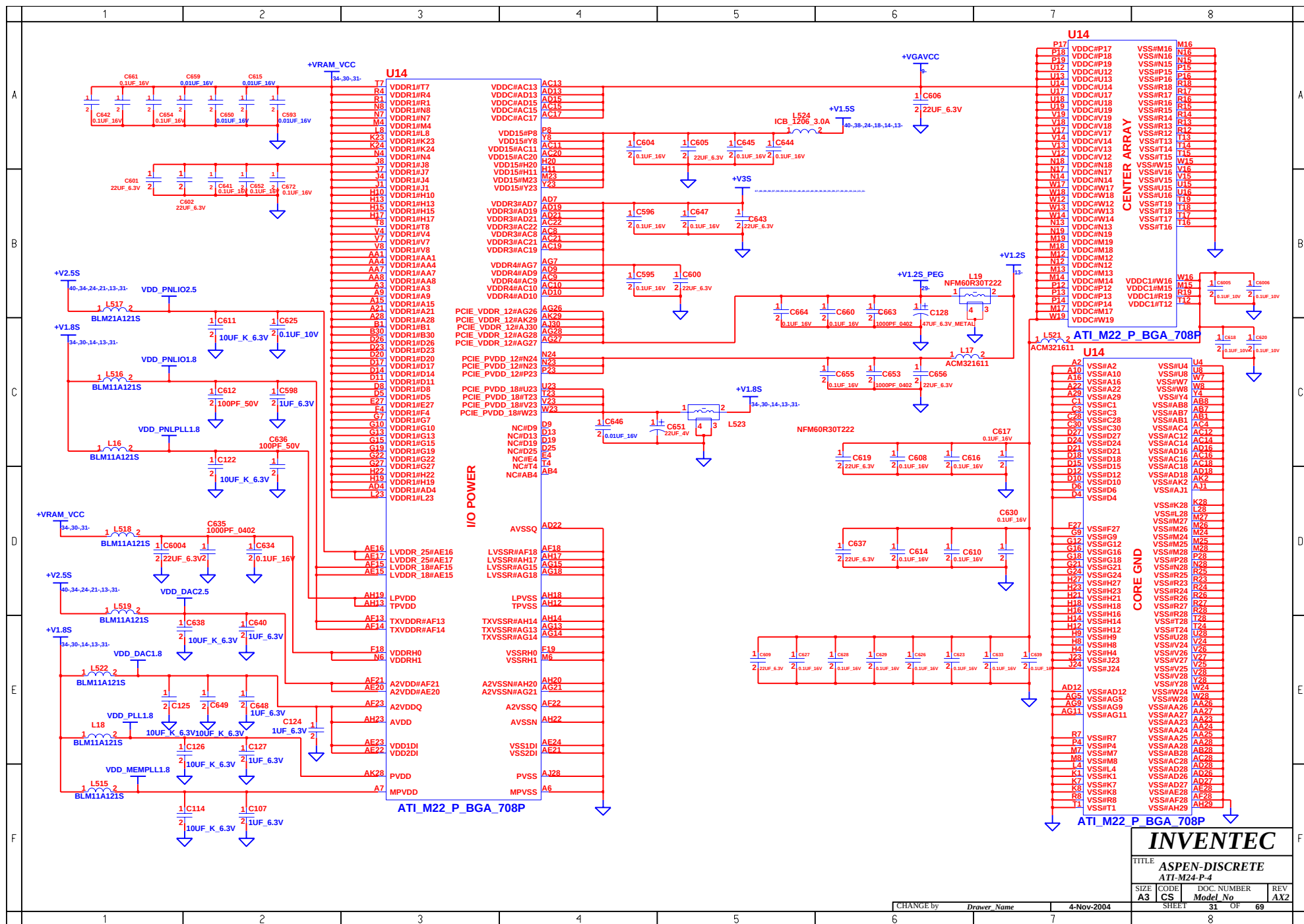
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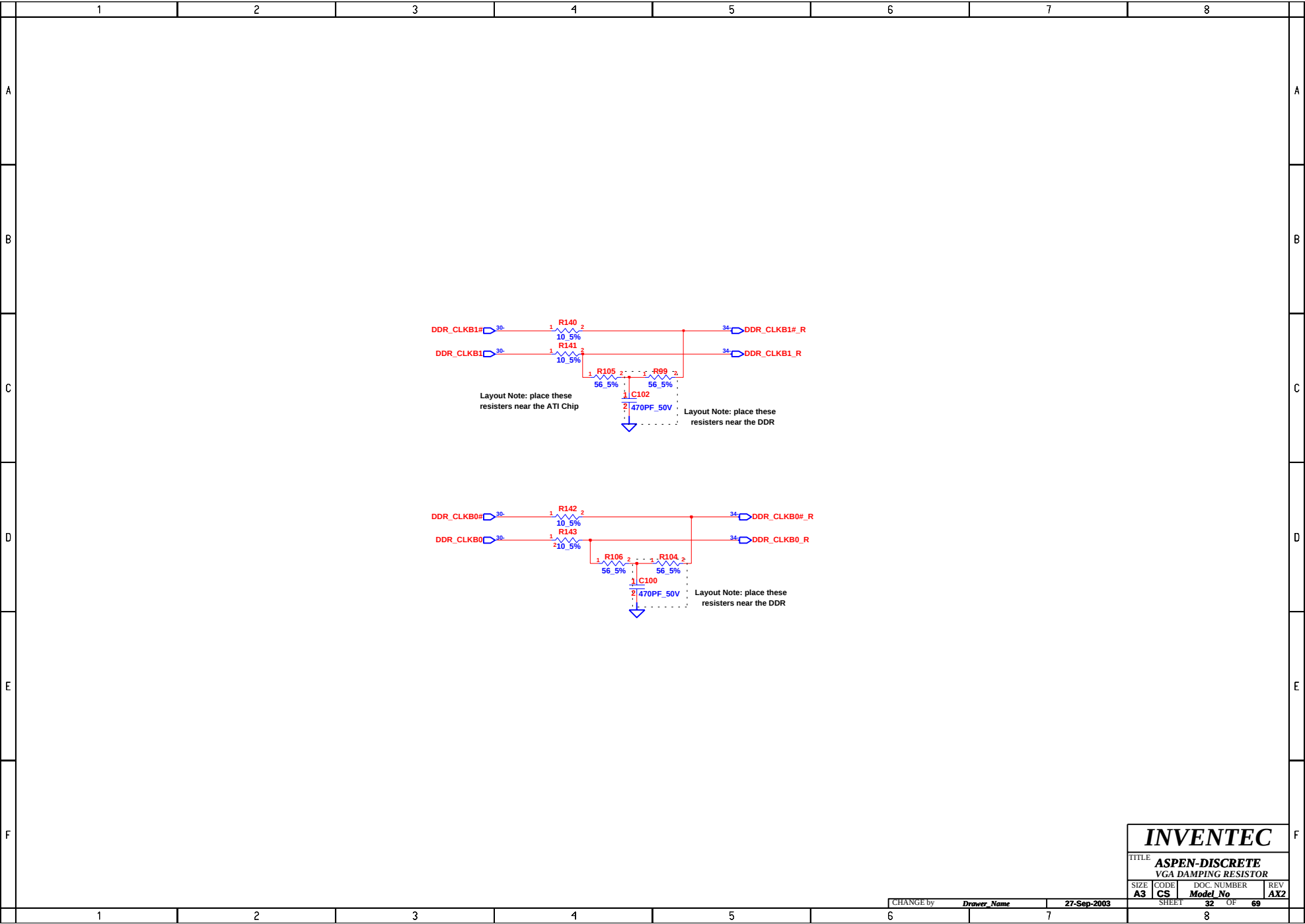
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INVENTEC

TITLE
ASPEN-DISCRETE
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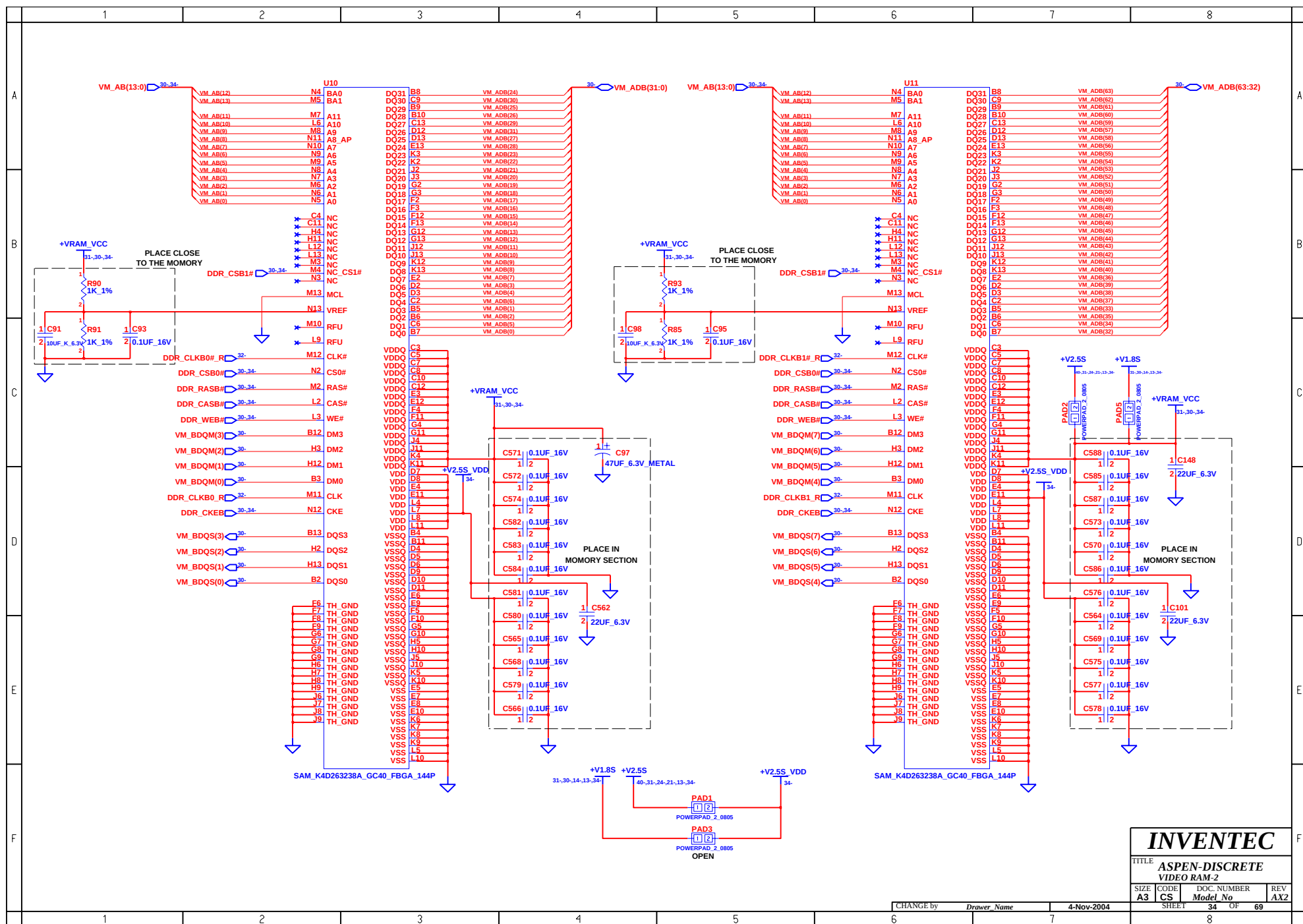
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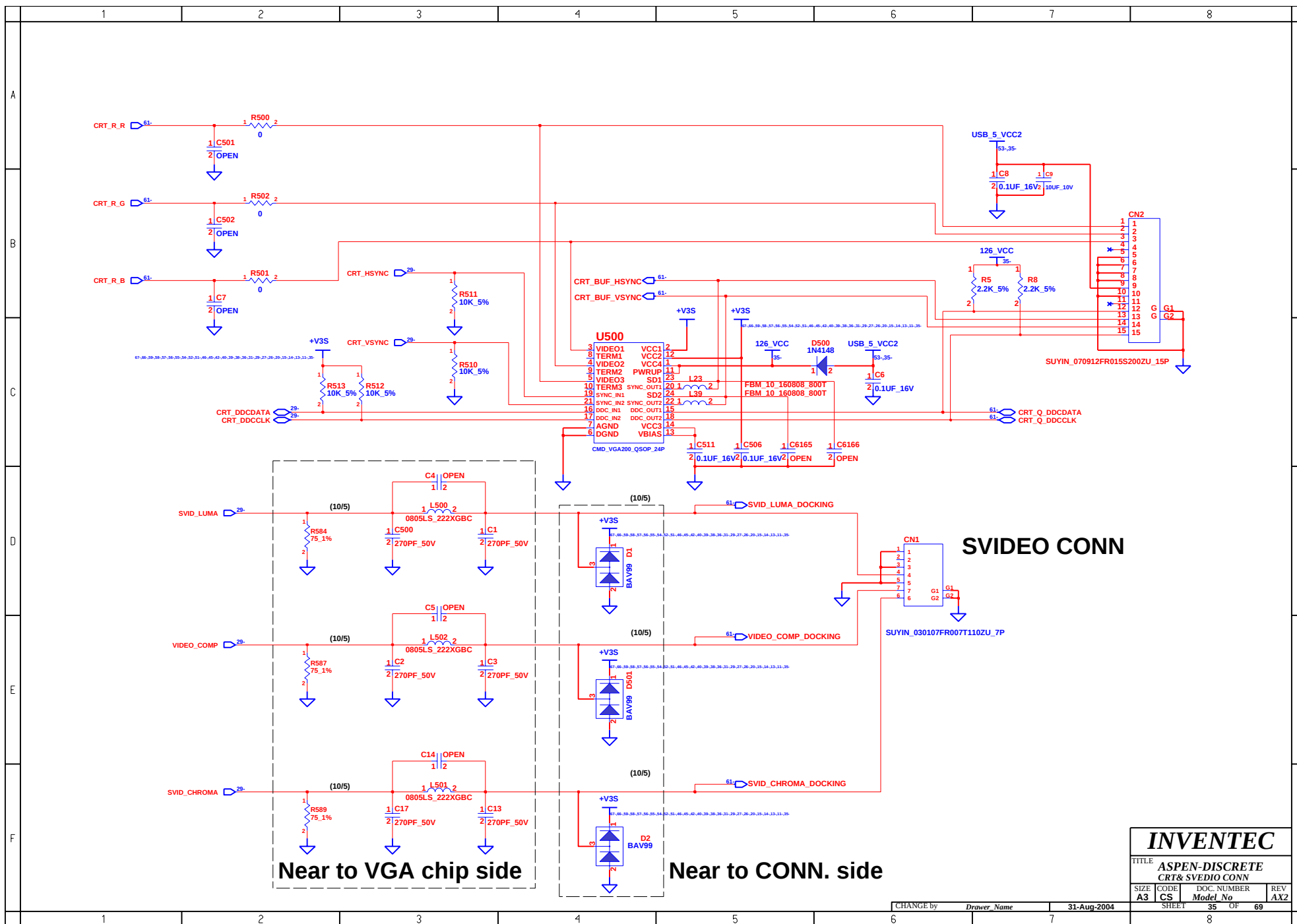
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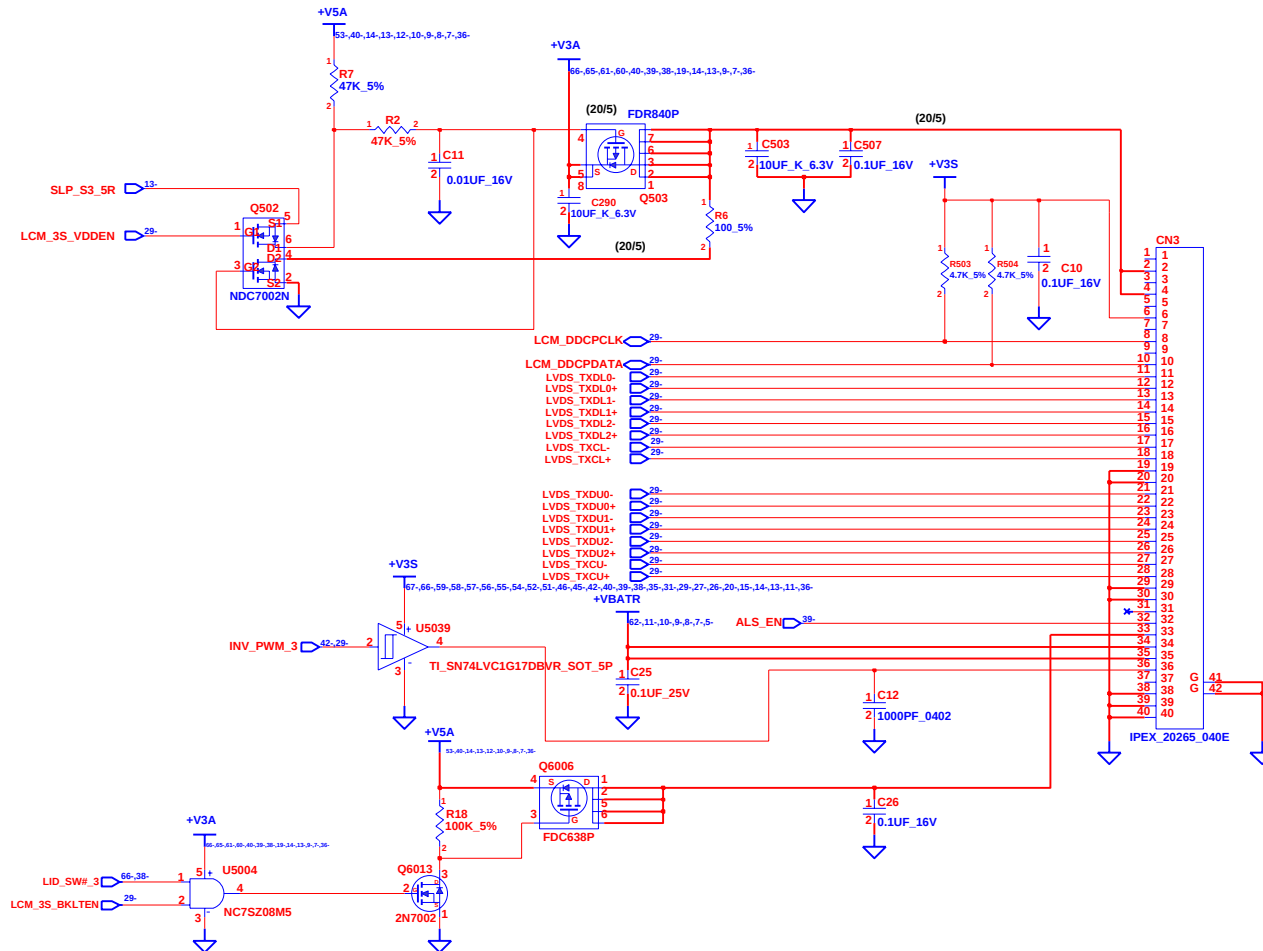
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D									D
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INVENTEC			
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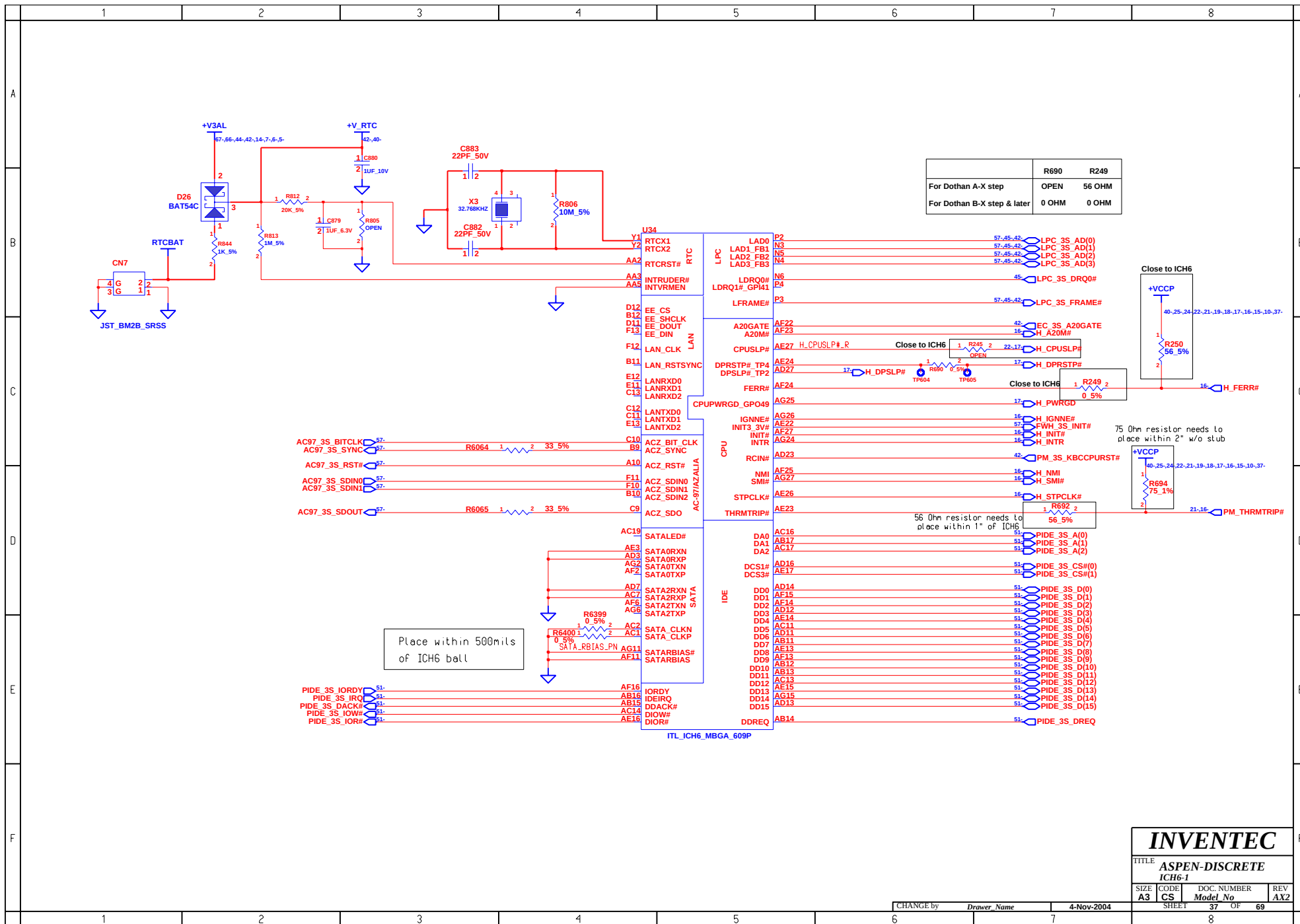




Place closed to connector



INVENTEC			
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CHANGE by Drawer Name		31-Aug-2004	SHEET 36 OF 69



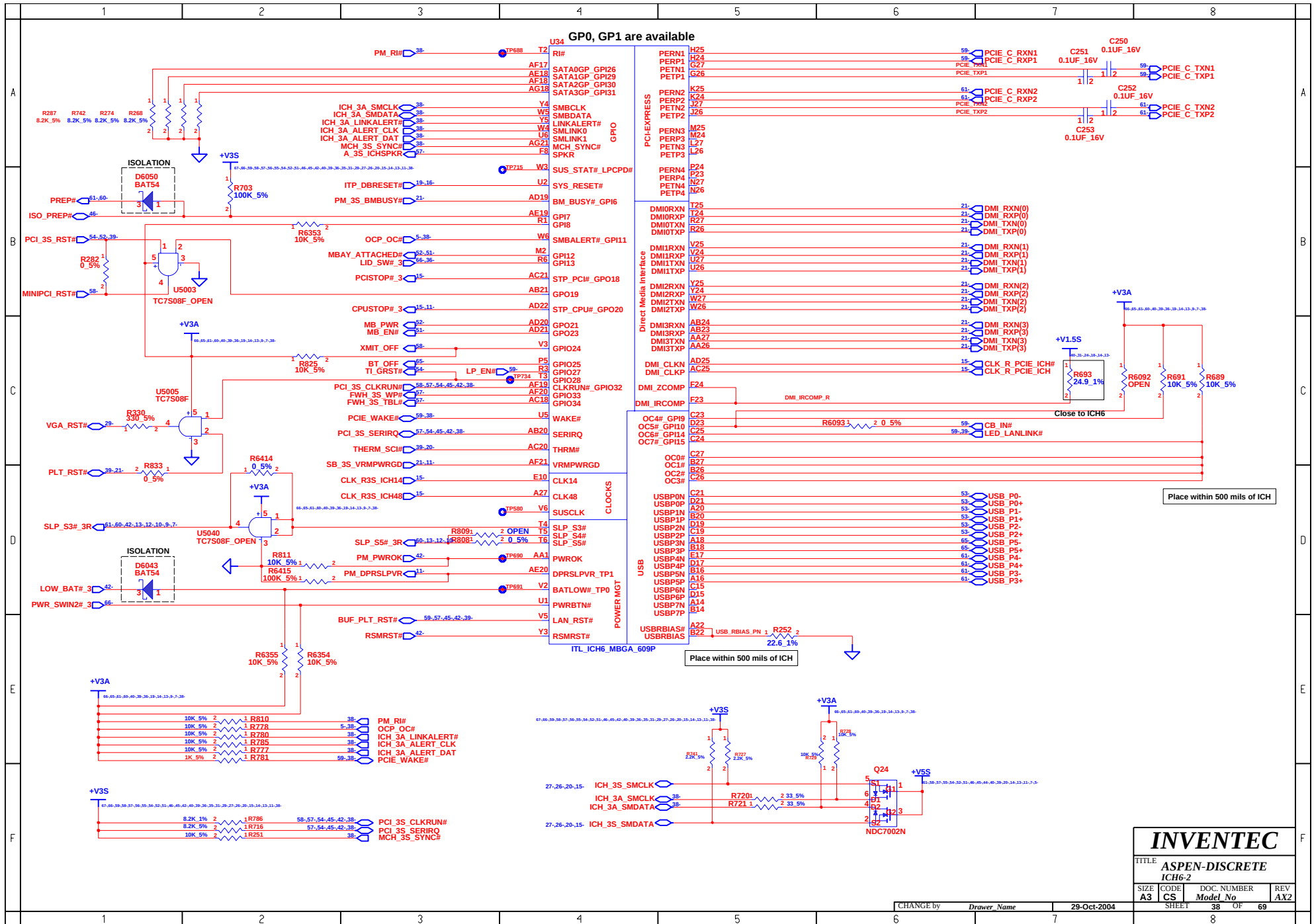
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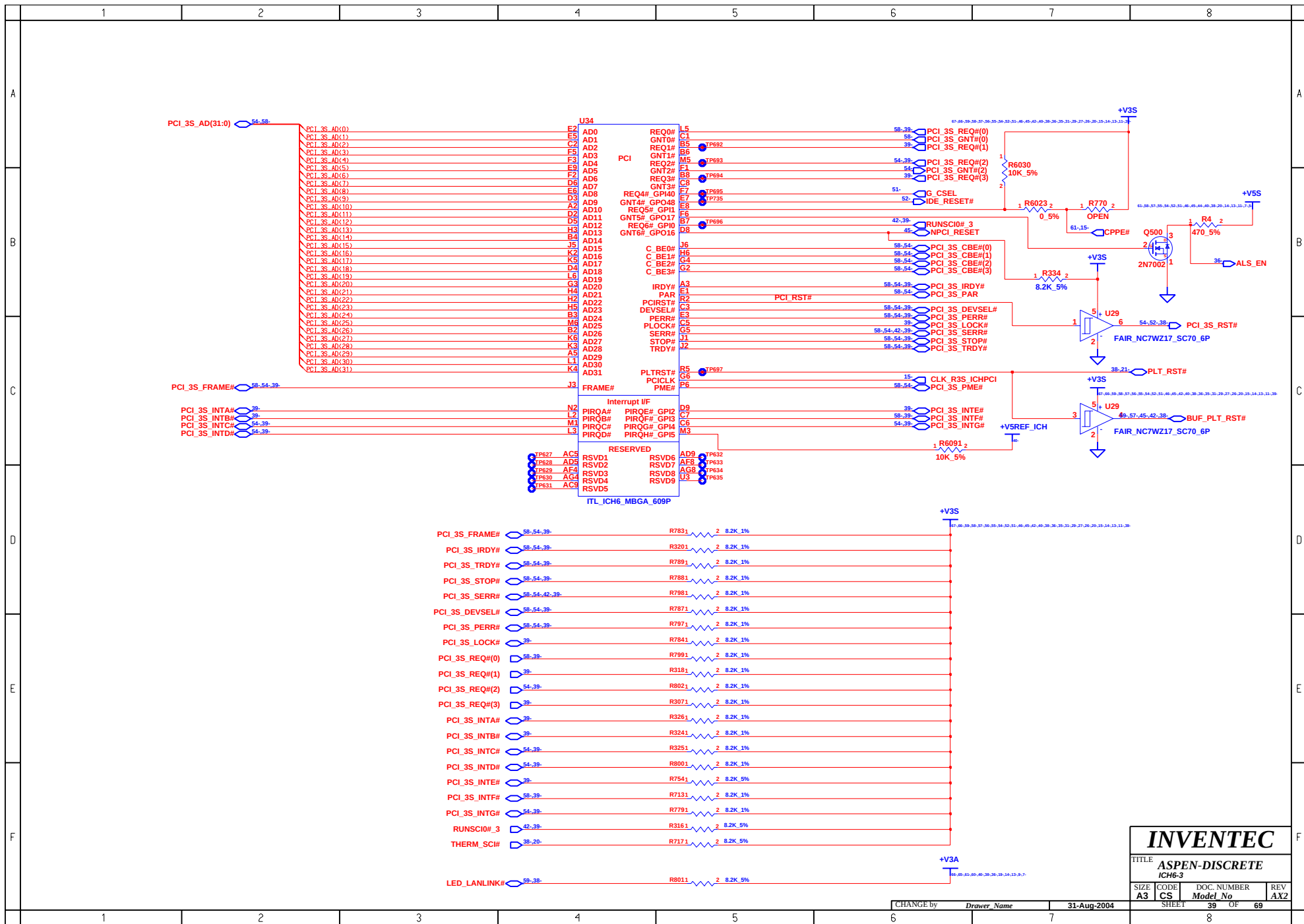
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**ASPEN-DISCRETE
ICH6-1**

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
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CHANGE by *Drawer Name* 4-Nov-2004

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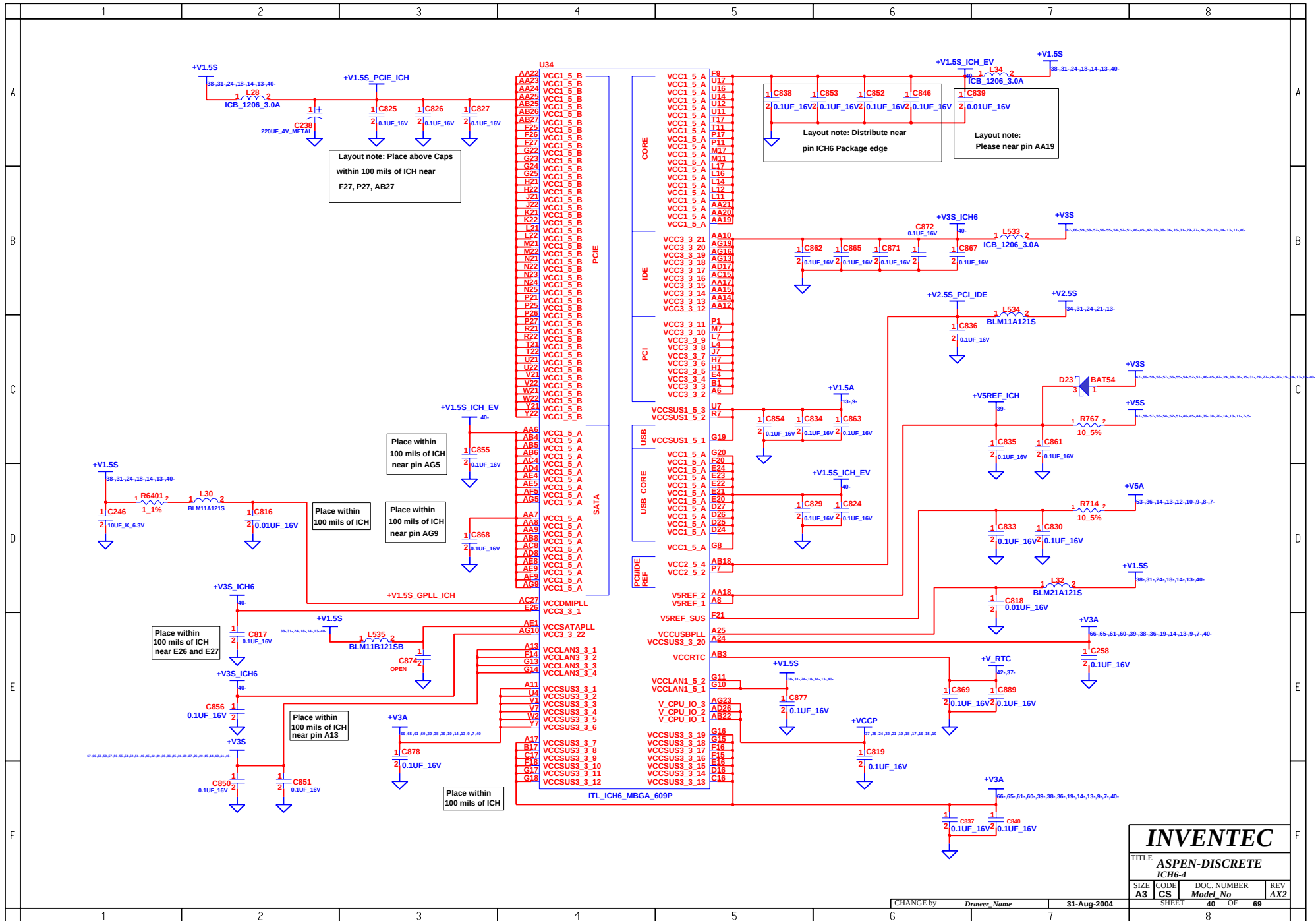
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TITLE
ASPEN-DISCRETE
ICH6-3

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Draver Name* 31-Aug-2004

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INVENTEC

TITLE
ASPEN-DISCRETE
ICH6-4

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawer Name* 31-Aug-2004

SHEET 40 OF 69

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B									B
C									C
D									D
E									E
F									F

U34		
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Y26	VSS171	VSS85
Y27	VSS170	VSS84
Y26	VSS169	VSS83
Y23	VSS168	VSS82
W7	VSS167	VSS81
W25	VSS166	VSS80
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W23	VSS164	VSS78
W1	VSS163	VSS77
V4	VSS162	VSS76
V27	VSS161	VSS75
V26	VSS160	VSS74
V23	VSS159	VSS73
U25	VSS158	VSS72
U24	VSS157	VSS71
U23	VSS156	VSS70
U15	VSS155	VSS69
U13	VSS154	VSS68
T7	VSS153	VSS67
T27	VSS152	VSS66
T26	VSS151	VSS65
T23	VSS150	VSS64
T16	VSS149	VSS63
T14	VSS148	VSS62
T13	VSS147	VSS61
T12	VSS146	VSS60
R1	VSS145	VSS59
R4	VSS144	VSS58
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R23	VSS141	VSS55
R17	VSS140	VSS54
R16	VSS139	VSS53
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R14	VSS137	VSS51
R13	VSS136	VSS50
R12	VSS135	VSS49
R11	VSS134	VSS48
P22	VSS133	VSS47
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M26	VSS116	VSS30
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H27	VSS96	VSS10
H26	VSS95	VSS9
H23	VSS94	VSS8
G9	VSS93	VSS7
G7	VSS92	VSS6
G21	VSS91	VSS5
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ITL_ICH6_MBG6_609P



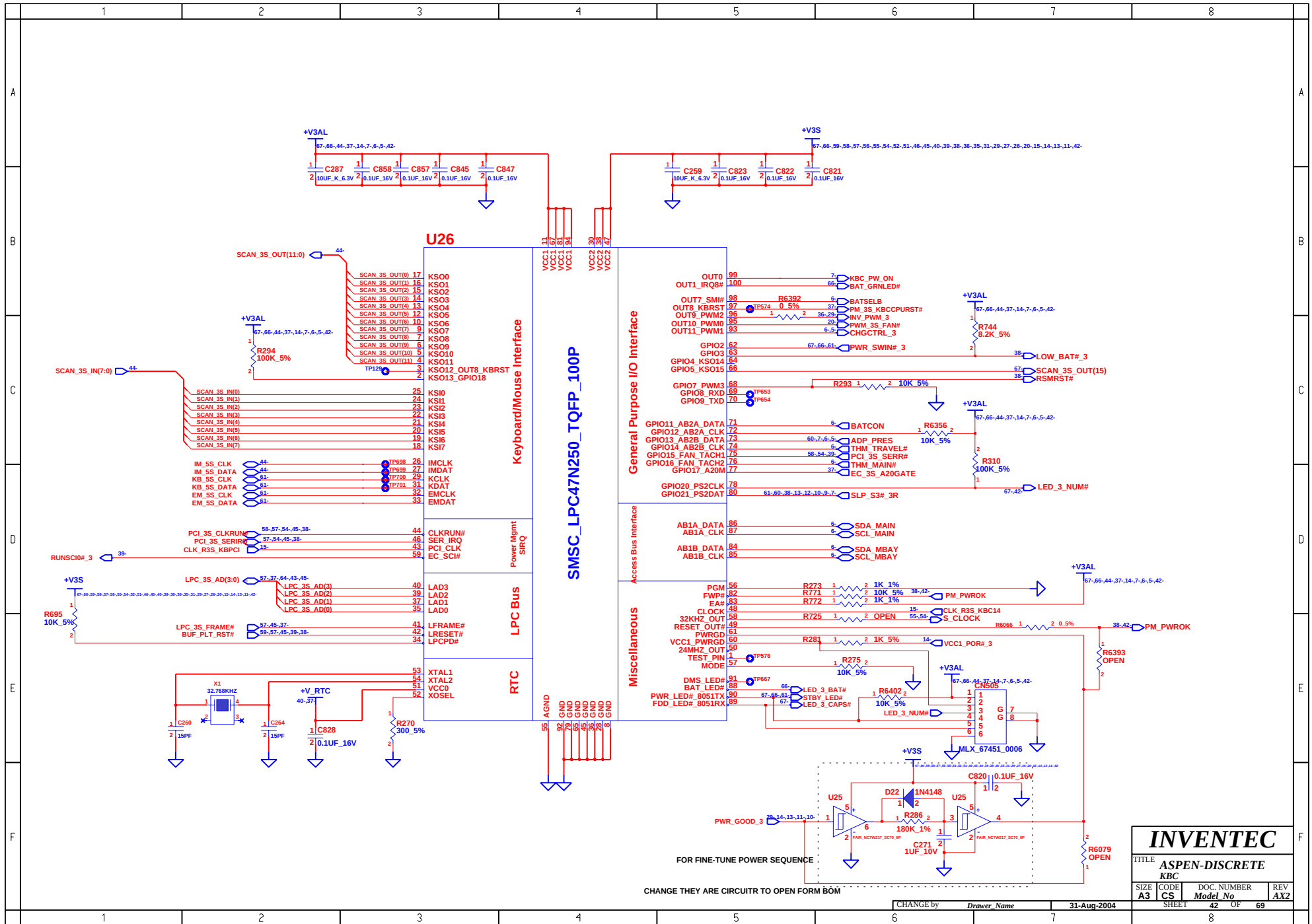
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TITLE
ASPEN-DISCRETE
ICH6-5

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
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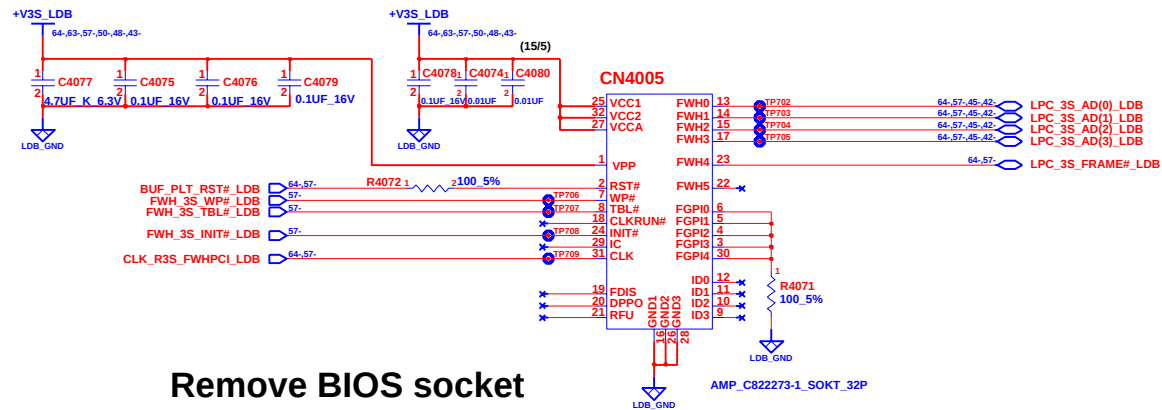
SHEET	41	OF	69
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CHANGE by Drawer Name 31-Aug-2004



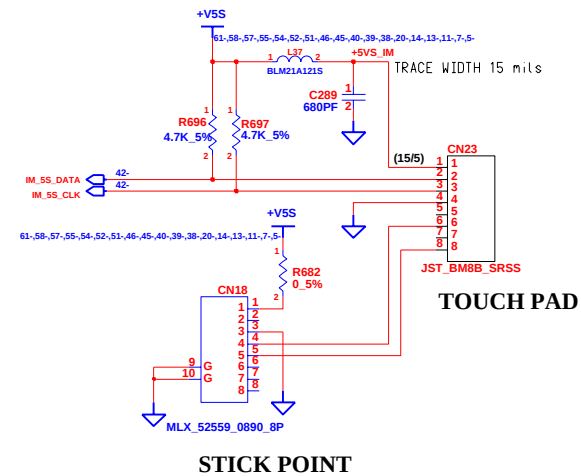
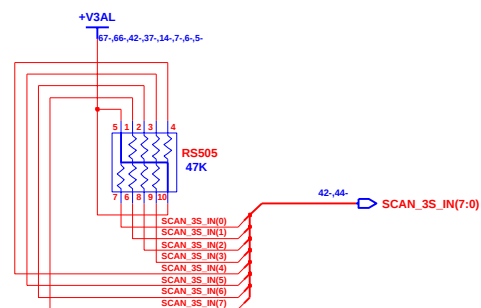
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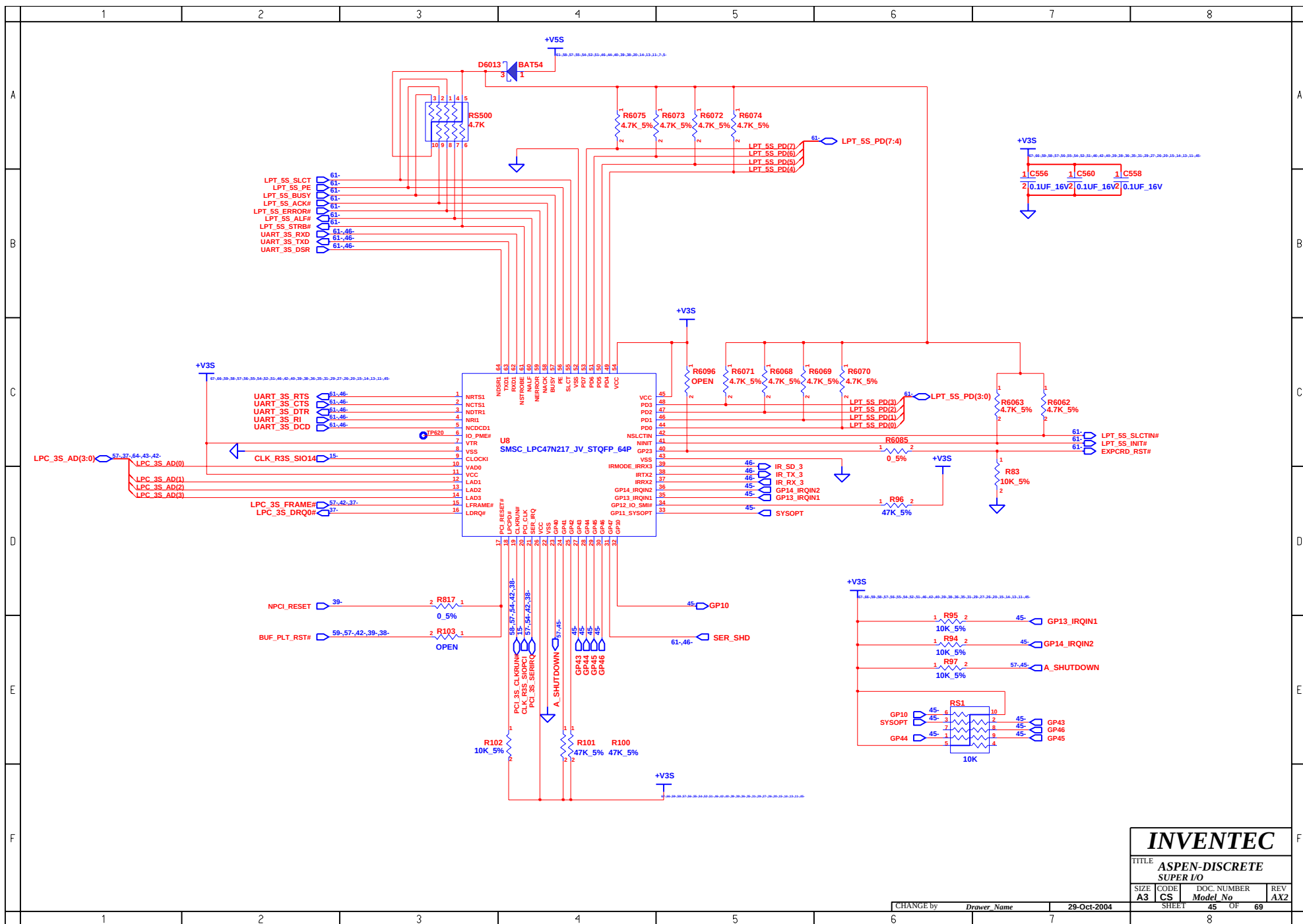
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TITLE ASPEN-DISCRETE				
KBC				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	Model No	42	AX2
SHEET		OF 69		

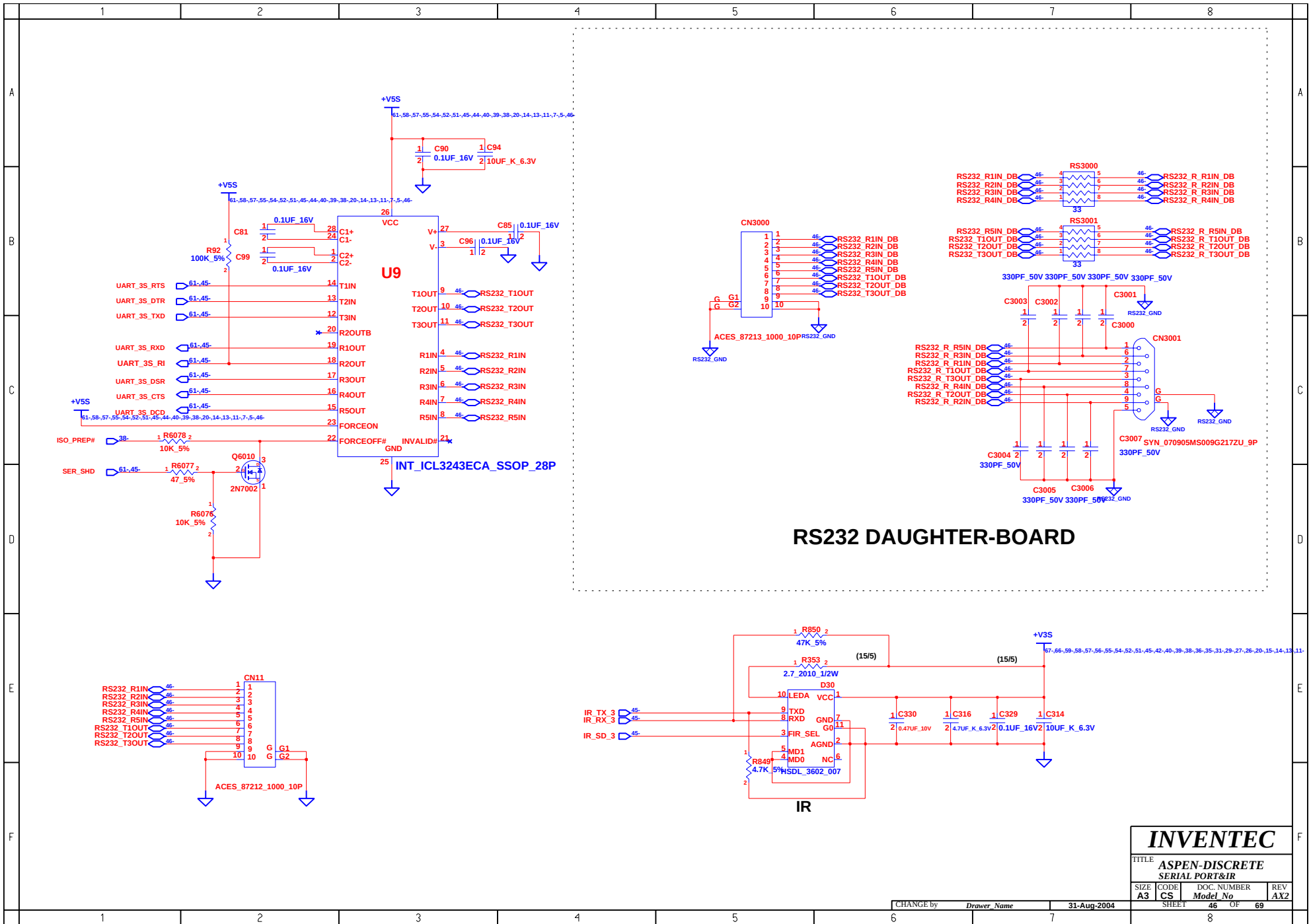


MDC , TCPA , OCP , FWH , AC97 B/B DAUGHTER BOARD

INVENTEC			
TITLE ASPEN-DISCRETE			
FWH			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by		Dravner Name	4-Nov-2004
SHEET		43	OF 69







	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE ASPEN-DISCRETE PARALLER PORT			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drafter Name		31-Aug-2004	
SHEET		47	OF 69

1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

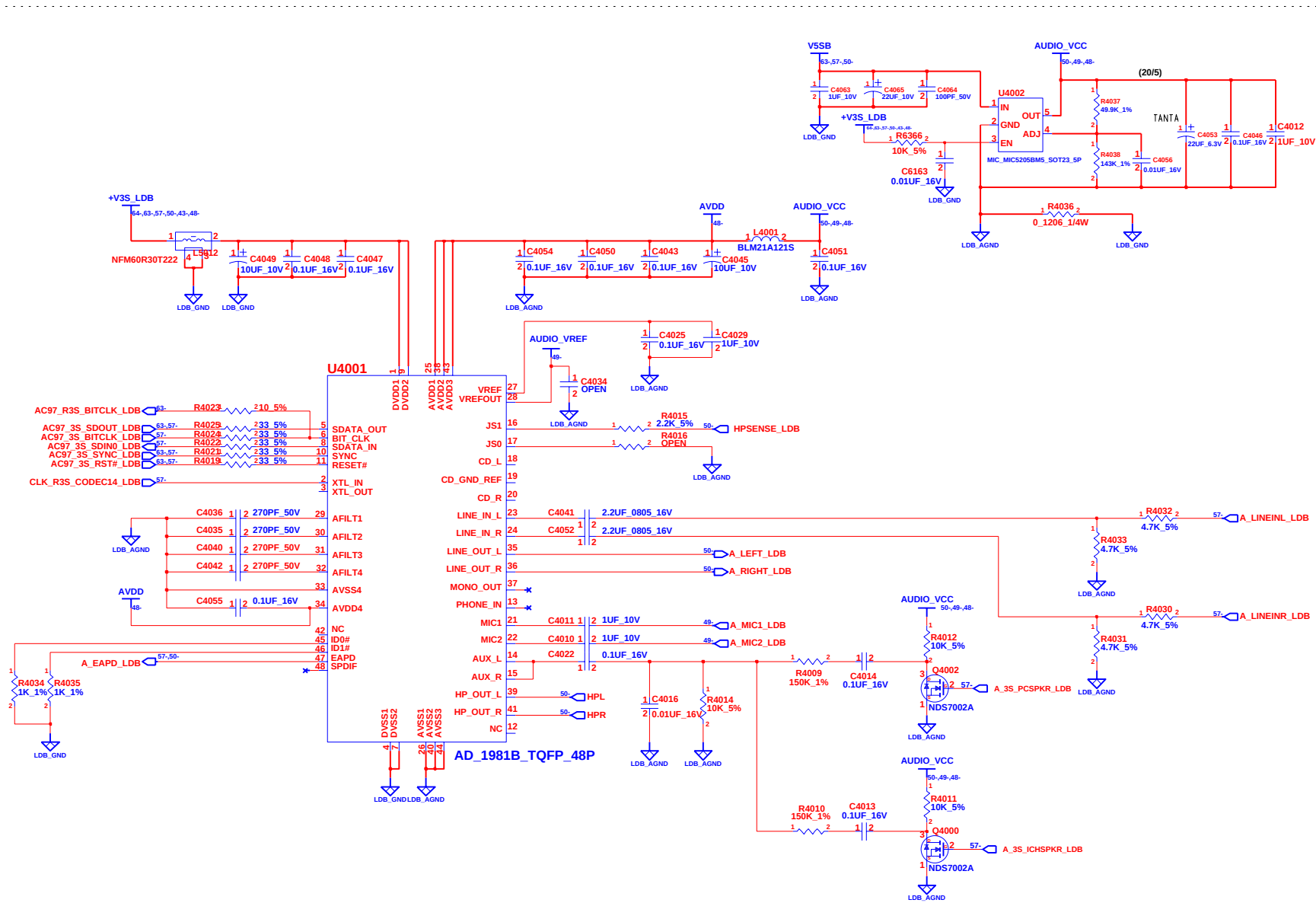
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C

D

E

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MDC , TCPA , OCP , FWH , AC97 B/B DAUGHTER BOARD

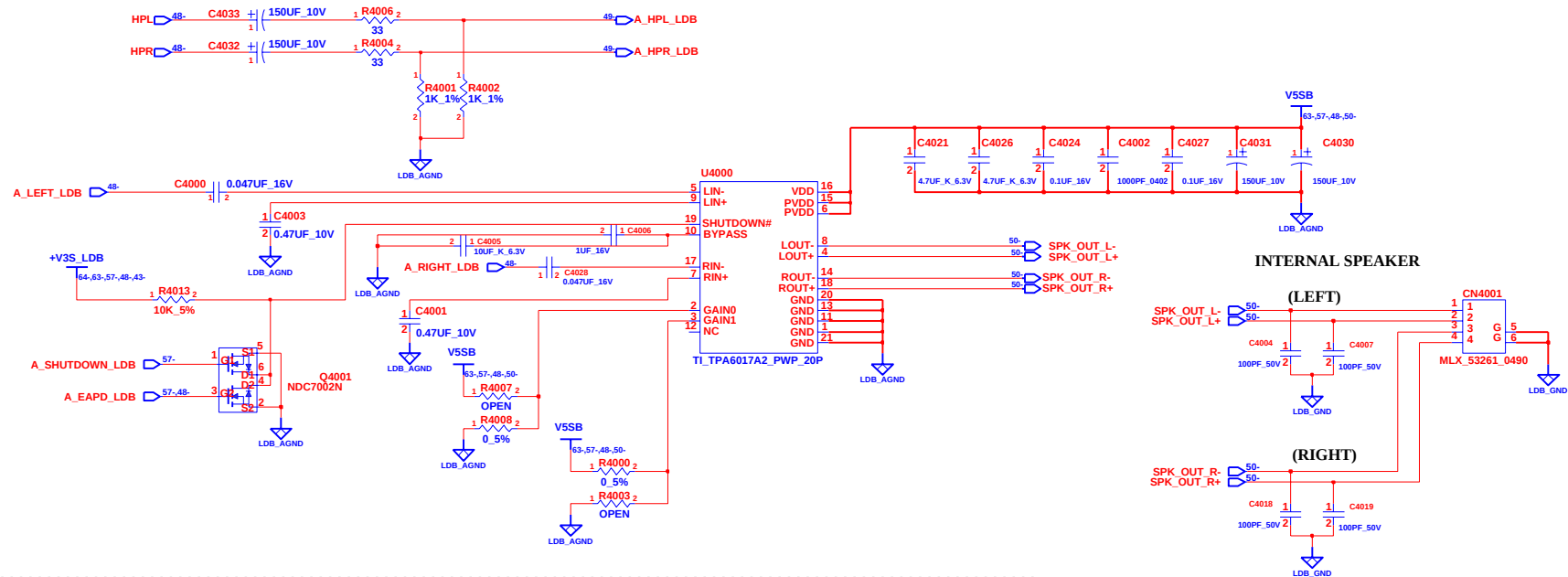
INVENTEC

TITLE			
ASPEN-DISCRETE			
AC97 CODEC			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

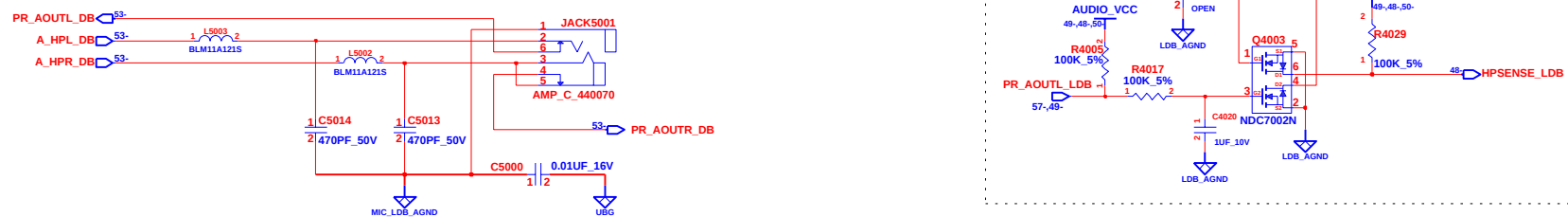
CHANGE by *Drawn Name* 31-Aug-2004

SHEET 48 OF 69

MDC , TCPA , OCP , FHW , AC97 B/B DAUGHTER BOARD



MIC AND HEADPHONE DAUGHTER BOARD (REFER TO SHEET 49)



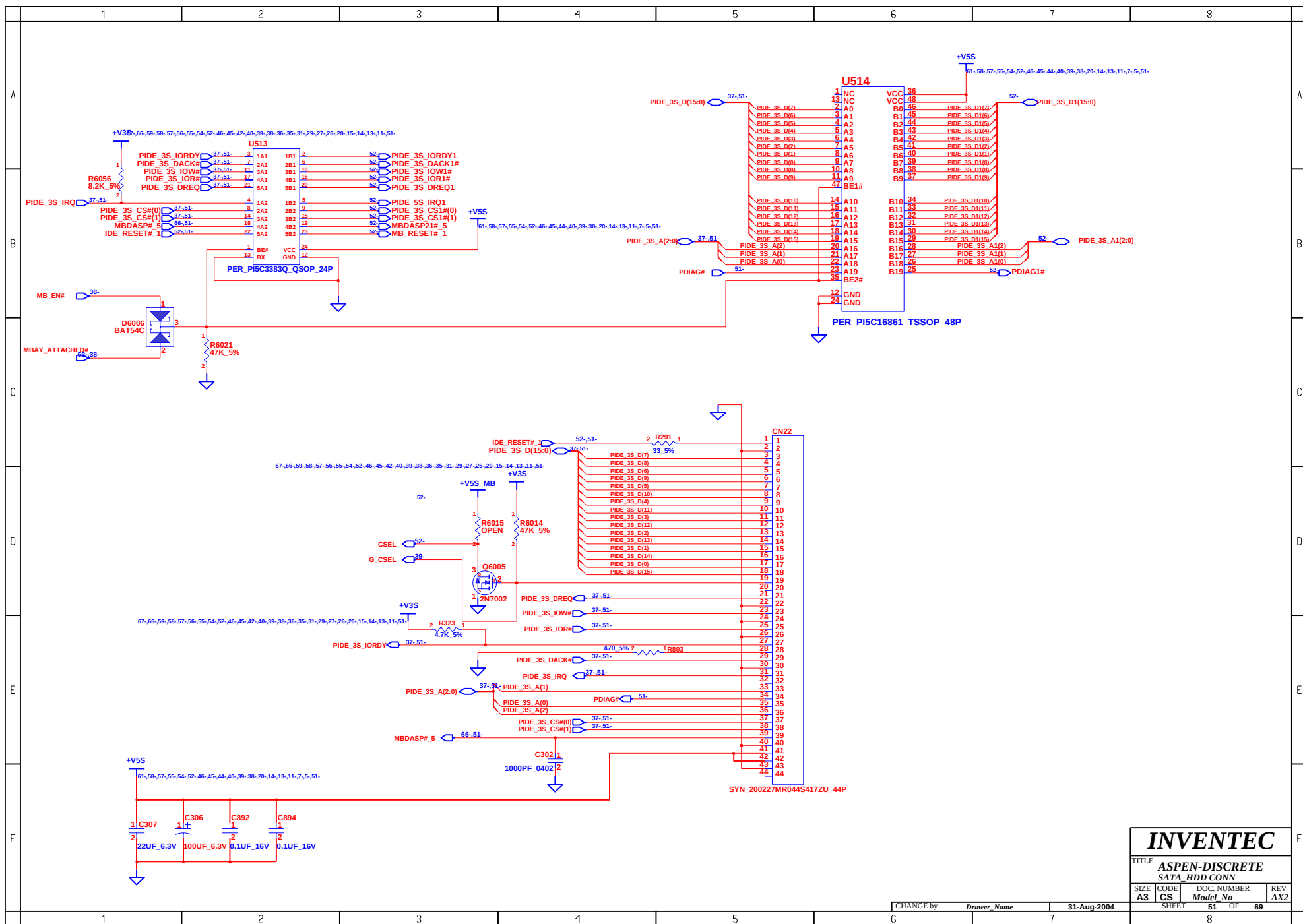
INVENTEC

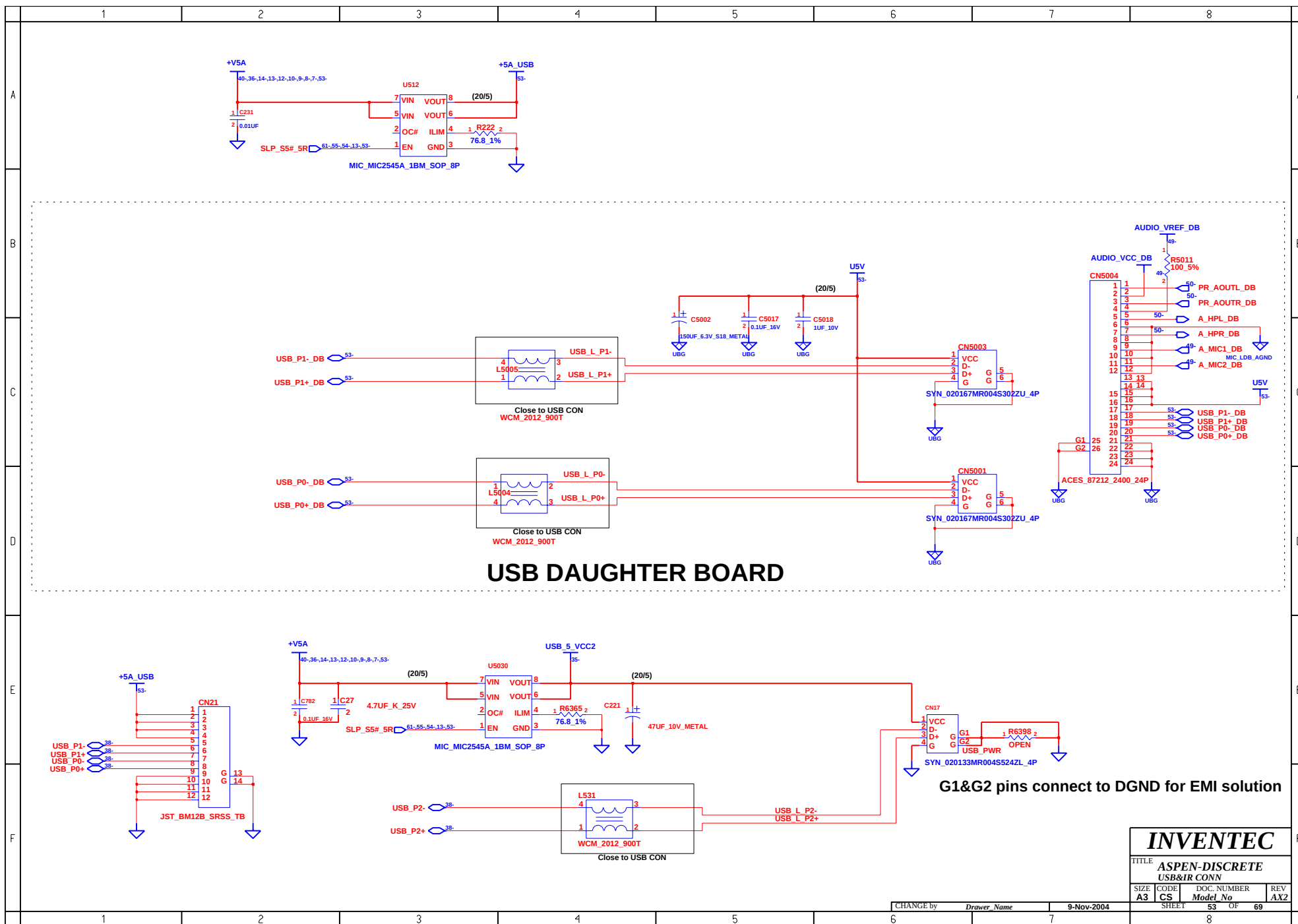
TITLE
**ASPEN-DISCRETE
AUDIO AMP & HP JACK**

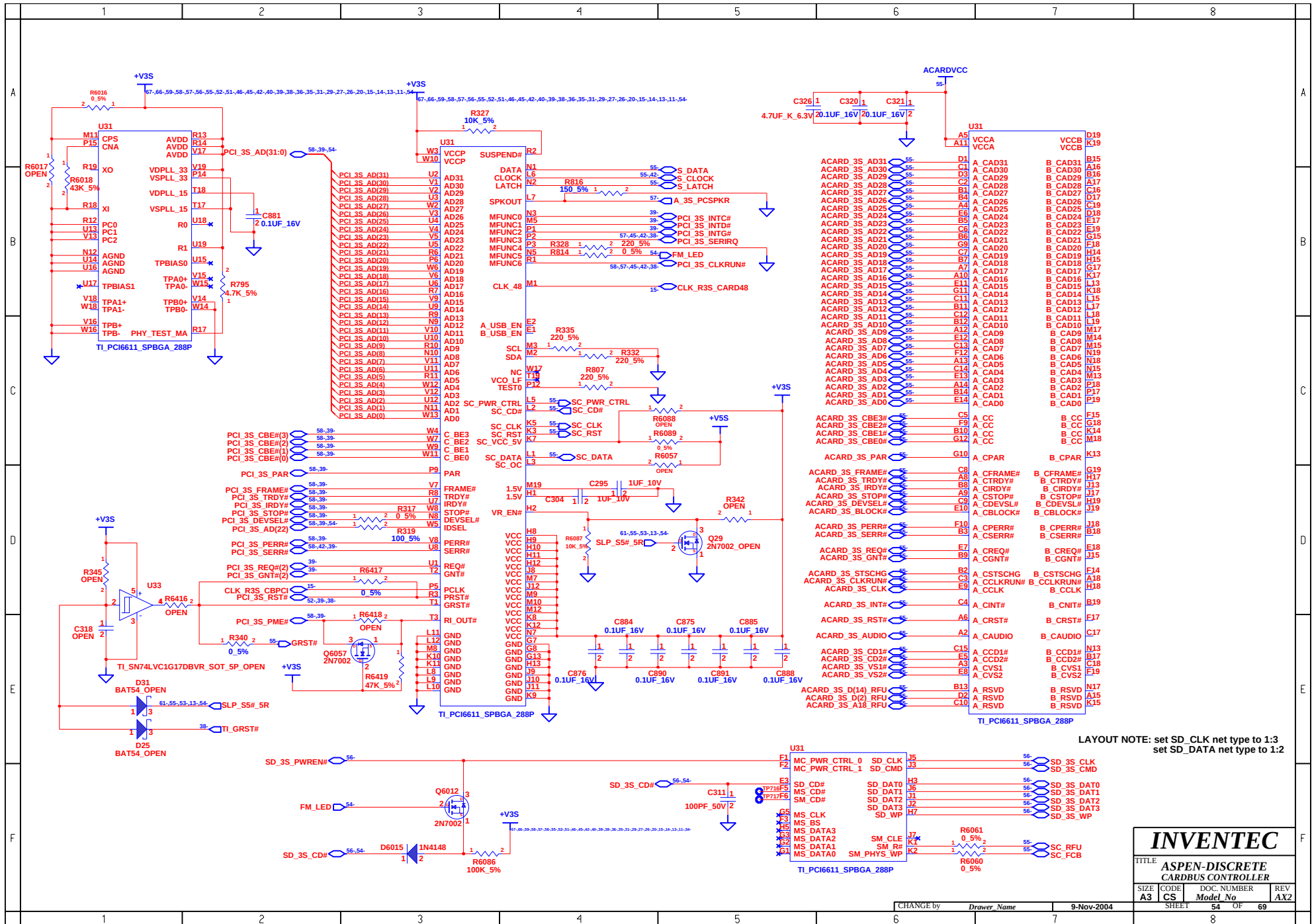
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

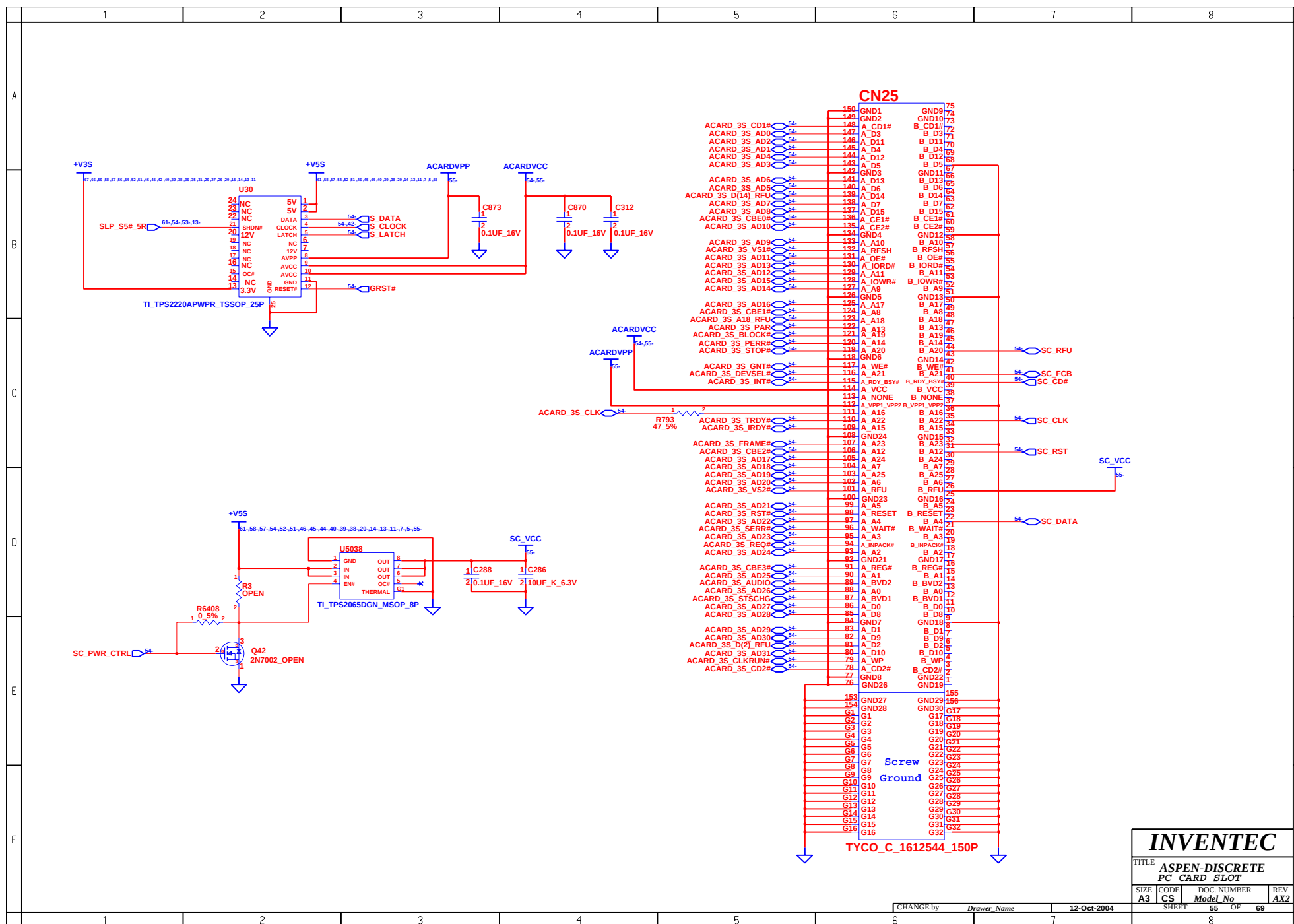
CHANGE by *Drawer Name* 12-Oct-2004

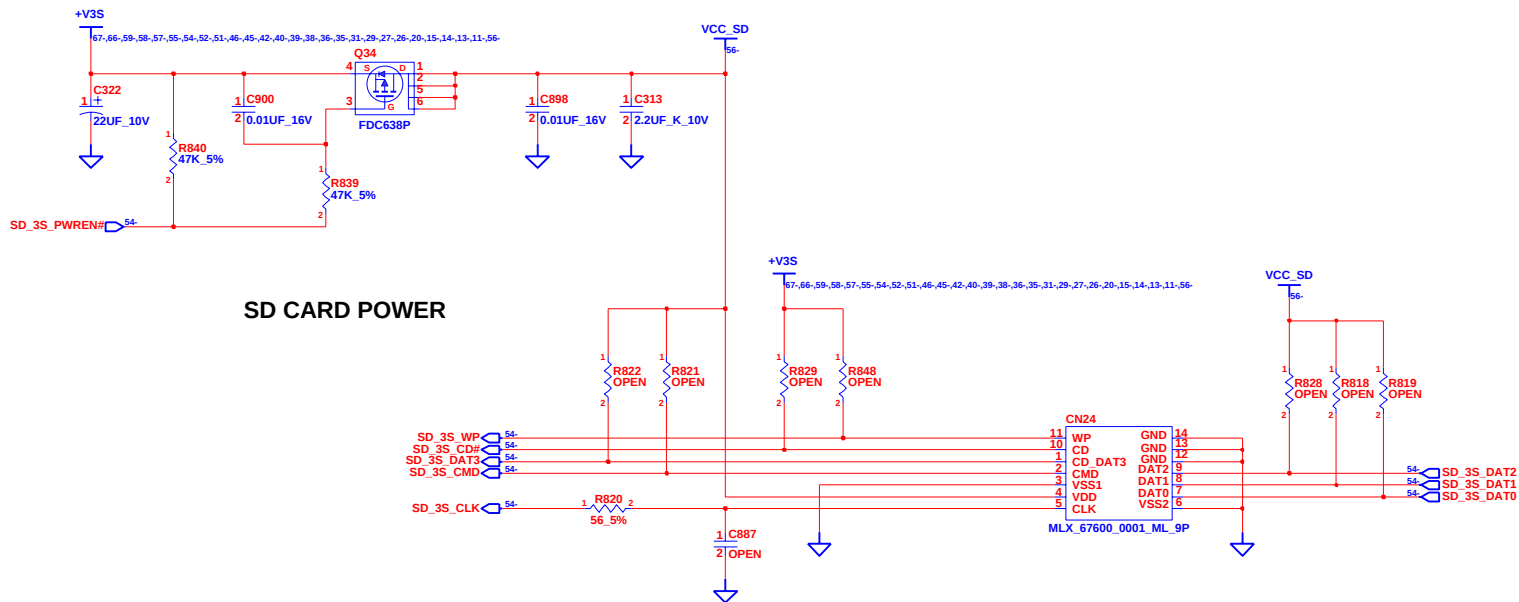
SHEET 50 OF 69











INVENTEC			
TITLE ASPEN-DISCRETE SD CARD CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drawer Name		31-Aug-2004	SHEET 56 OF 69

FM CONN.

CN6001

Signal	Pin
PCI_3S_SERIRQ	1
LPC_3S_AD(1)	2
PCI_3S_CLKRUN#	3
LPC_3S_AD(3)	4
FWH_3S_WP#	5
LPC_3S_AD(0)	6
FWH_3S_TBL#	7
LPC_3S_AD(2)	8
FWH_3S_INIT#	9
LPC_3S_FRAME#	10
CLK_R3S_FWHPC1	11
BUF_PLT_RST#	12
	13
	14
AC97_3S_SDO	15
AC97_3S_SDIO	16
AC97_3S_BITCLK	17
AC97_3S_SDI	18
AC97_3S_SYNC	19
AC97_3S_RST#	20
	21
	22
CLK_R3S_CODEC14	23
PR_AOUTR	24
A_3S_ICHSPKR	25
PR_AOUTL	26
A_3S_PCSPKR	27
A_LINEINR	28
A_SHUTDOWN	29
A_LINEINL	30
	31
PR_HPSENSE#	32
	33
A_EAPD	34
	35
	36
+VSS	37
	38
AUDIO_AGND	39
	40

SYN_100026FA040S200ZL_40P

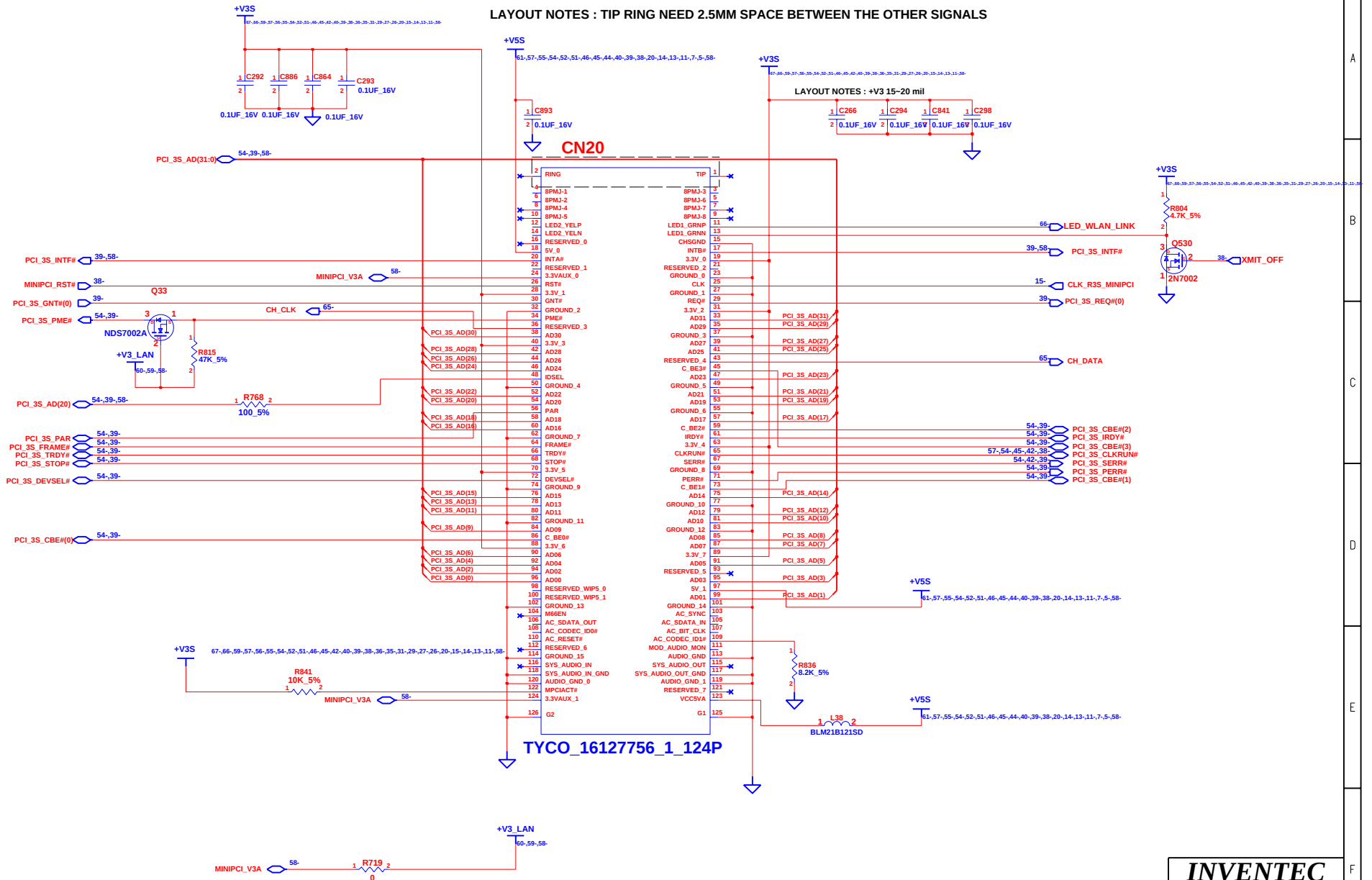
[illegible]

B/B DAUGHTER BOARD

INVENTEC			
TITLE ASPEN-DISCRETE DAUGHTER BOARD CNN			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX2
SHEET 57		OF 69	

LAYOUT NOTES : TIP RING NEED 2.5MM SPACE BETWEEN THE OTHER SIGNALS

LAYOUT NOTES : +V3 15-20 mil

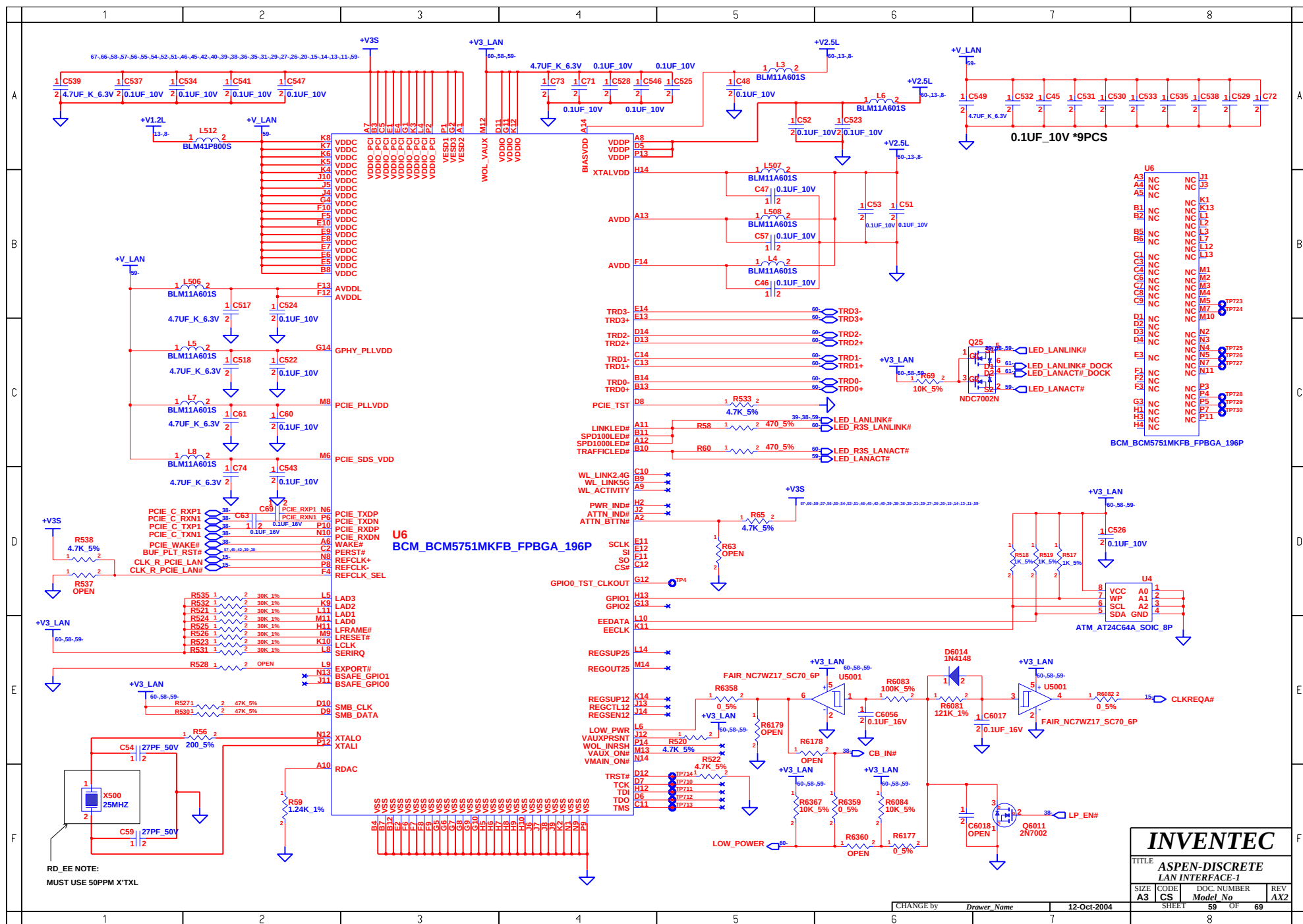


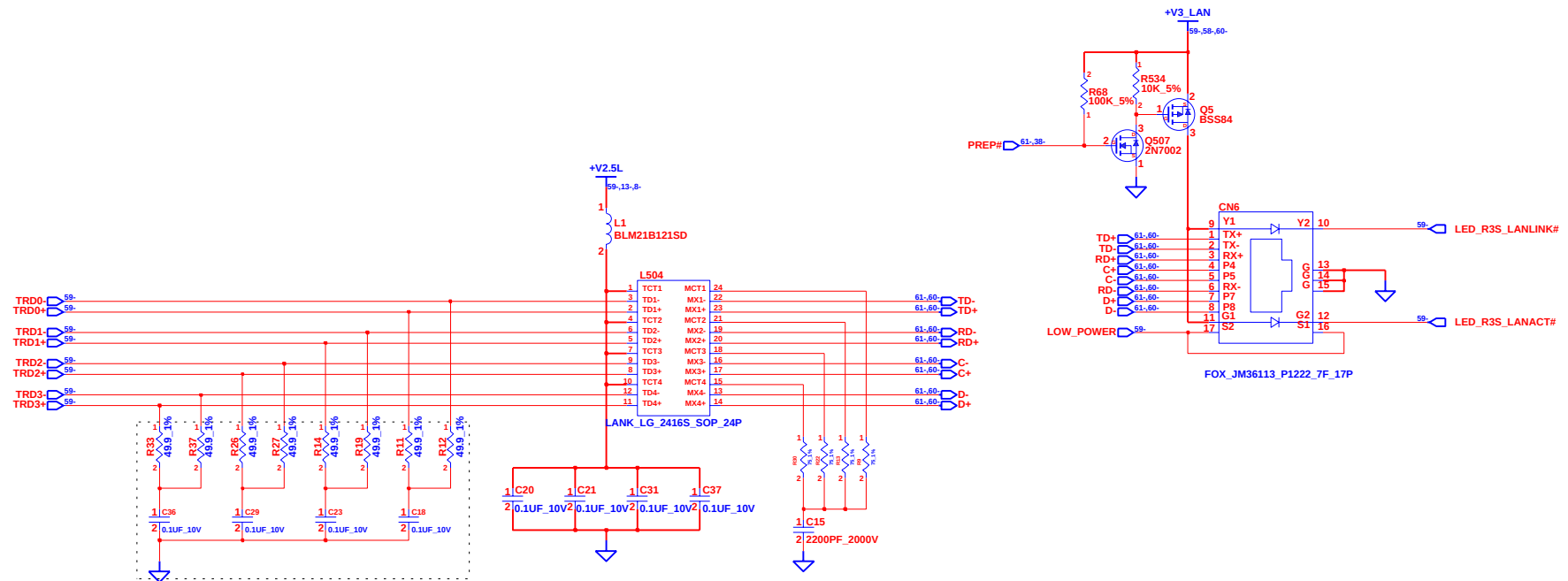
INVENTEC

TITLE			
ASPEN-DISCRETE			
MINIPCI CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

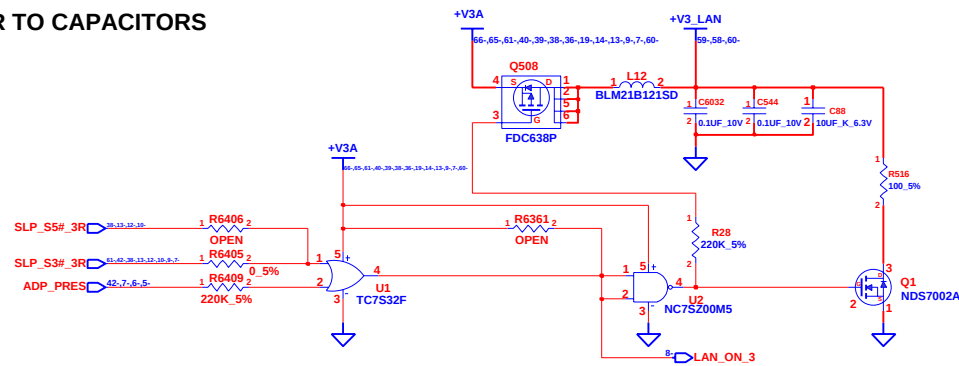
CHANGE by *Drawer Name* 31-Aug-2004

SHEET 58 OF 69

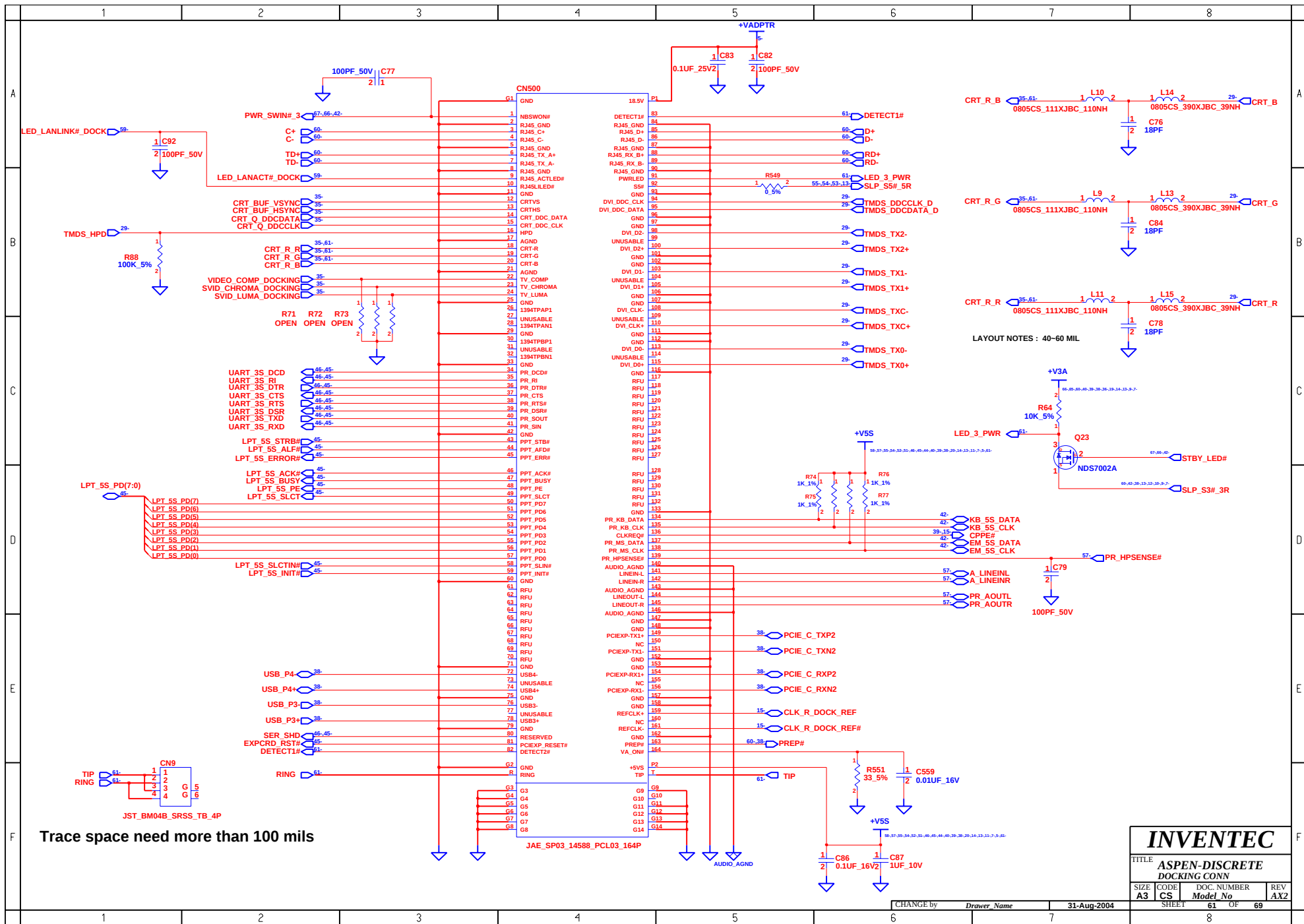


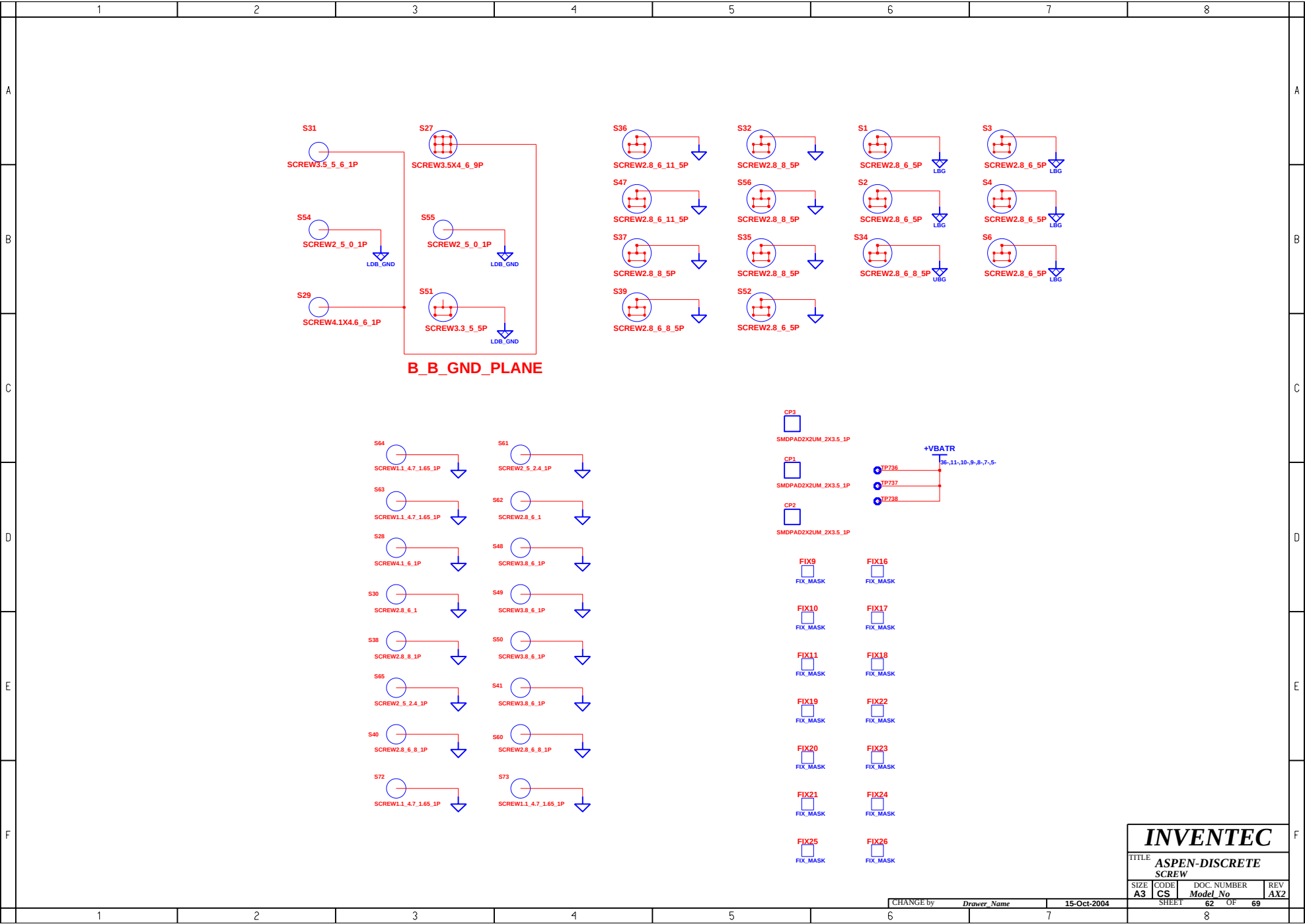


LAYOUT NOTE: THERE ARE RESISTORS NEAR TO CAPACITORS



INVENTEC			
TITLE ASPEN-DISCRETE LAN R45 CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by		20-Oct-2004	SHEET 60 OF 69



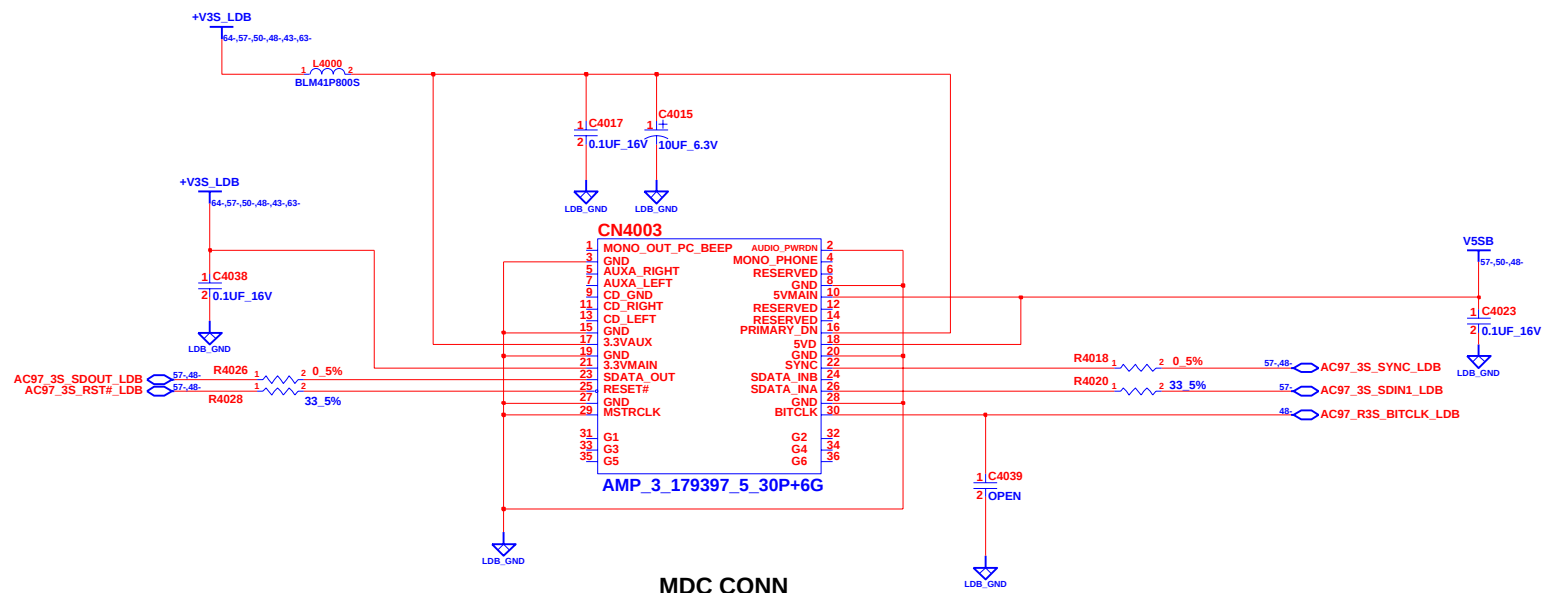


INVENTEC

TITLE
**ASPEN-DISCRETE
SCREW**

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX2
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CHANGE by *Drawer Name* 15-Oct-2004 SHEET 62 OF 69



MDC , TCPA , OCP , FHW , AC97 B/B DAUGHTER BOARD

INVENTEC			
TITLE ASPEN-DISCRETE			
MDC CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by		31-Aug-2004	SHEET 63 OF 69
Drawer Name		7	8

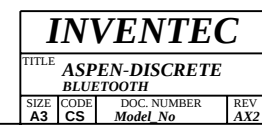


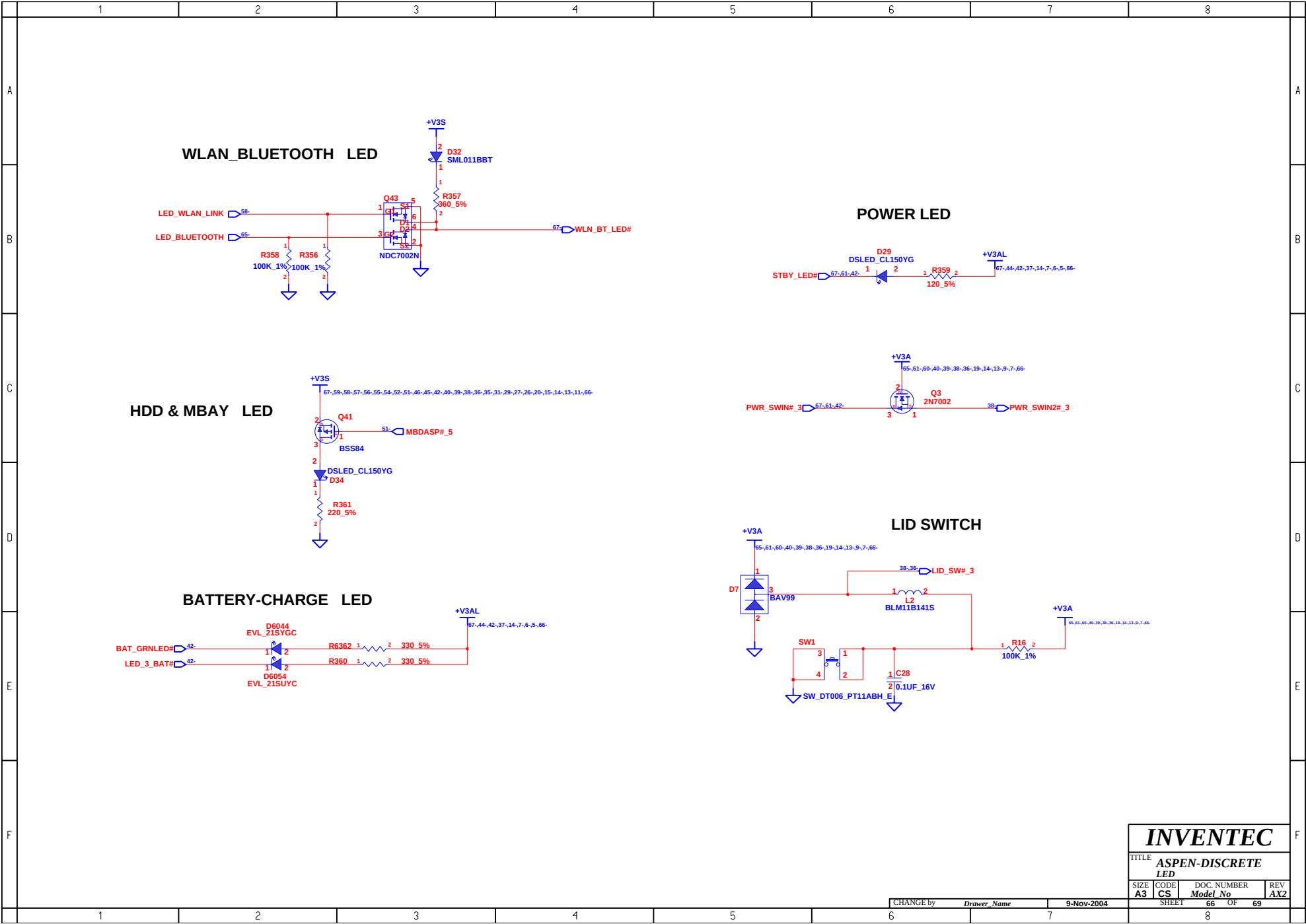
AMP 20PIN CONNECTOR
MOTHERBOARD SIDE
ACES_88028_2400

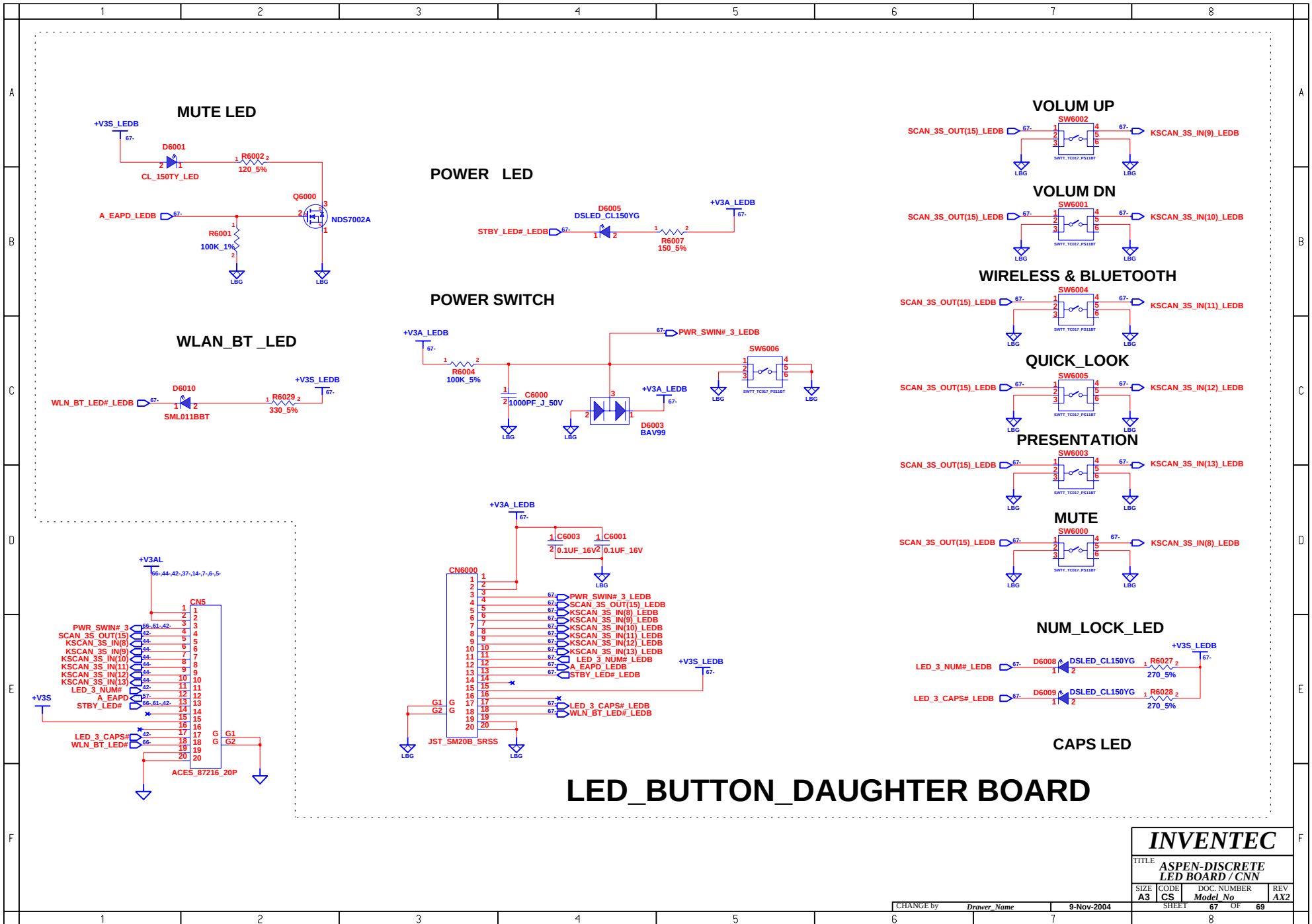
MDC , TCPA , OCP , FHW , AC97 B/B DAUGHTER BOARD

INVENTEC			
TITLE			
ASPEN-DISCRETE			
TCPA			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2
SHEET		64	OF 69

CHANGE by *Drawer Name* 31-Aug-2004







[illegible]

IEC / HP Aspen_Discrete PV Schematic Changes

November 9, 2004

HP Confidential Information

Open Items 29

Item #	Project	Component / Subsection	PG. #	Description of Requested Change	Date Open+F7 / Closed	Requested By / Owner	Status / Aspen	Aspen FBK/Date
63	Discrete	Charger	5	To fix the battery charge over 8hrs. -Change C6151 from open to 330pf to reduce noise.	2004/10/12	IEC	Open	Implement on SI2 build, sch1012 done
64	Discrete	3V/5V	7	Change the C277 voltage rating from 2.5V to 6.3V	2004/10/12	IEC	Open	Implement on SI2 build, sch1012 done
65	Discrete	3V/5V	7	3A/5A no power issue -Additional Thomas's circuit on +V3AL/+V5AL	2004/10/12	HP	Open	sch1012 done.
66	Discrete	Sleep	13	SLP_S3_5R driving clamping FET for PWR_GOOD_3 signal. This avoids systems hangs/reset due to pending wake events just prior to entry in S3 -Additional Q6056.	2004/10/12	HP	Open	sch1012 done.
67	Discrete	VGA_VCC/1.5A		The rebound of SLP_S3#_3R is caused by C6050 / D6016 discharging and need a buffer to isolate the cap-diode discharging circuit. 9- Change U13 from signal buffer to dual buffer. 13-Change R6387,R6388 from 470pf to 10K.	2004/10/12	IEC	Open	sch1012 done.
68	Discrete	audio		Need a shutdown GPIO for audio noise, same as Vail. 37-Add a A_SHUTDOWN on ICH6 pin P4. 50-Change Q4001 from signal to dual FET. 57-Change CN6001,CN6002 pin 29 from gnd to A_SHUTDOWN.	2004/10/12	HP	Open	sch1012 done.
69	Discrete	NIC		Relayout the L504 for return loss. 59-Remove R6020 and pass through BUF_PLT_RST# to U6 pinC2 for saving space. -Remove TP718~TP722 for saving space. 60-Change L504 from LG_2408S (small package) to LG_2402S (Normal package) for adjust 100M/GigaLan return loss. -Remove L5020(open pad) for saving space.	2004/10/12	IEC	Open	sch1012 done.
70	Discrete	OCP	5	Change R4064 from 909 ohm to 221 ohm for OCP is throttling the CPU on AC mode	2004/10/14	HP	Open	sch1020 done
71	Discrete	3V/5V	7	Change the U5033 from MIC5205 to MIC5235	2004/10/20	IEC	Open	sch1020 done
72	Discrete	3V/5V	7	To improve dynamic regulation. -Change R6105 from 2.7K to 1.2K. -Change C225 from 330uF_4V to 220uF_4V.	2004/10/20	IEC	Open	sch1020 done
73	Discrete	Fan controller	20	Change the U20 power rail from +V3AL to +V3S and then change KBC BIOS to drive pin1 to low	2004/10/20	HP	Open	sch1020 done
74	Discrete	M22	31	1.Change C651 from 1uF_6.3V to 22uF_4V for +V1.8S, same as Vail 2.Change L19 from ACM321611 to NFM60R30T222 for +V1.2S_PEG, same as Vail	2004/10/20	IEC	Open	sch1020 done
75	Discrete	audio	45	Audio amp shutdown changes for noise pop issue. -Change the U8 pin23 and R97 pin2 from GP40 to A_SHUTDOWN, same as Vail	2004/10/20	HP	Open	sch1020 done
76	Discrete	Super I/O	45	PCI Express Card Reset -Install R6085 with 0 ohm jumper. -Install R83 to 10K -Remove R6096.	2004/10/20	HP	Open	sch1020 done
77	Discrete	ICH6-2	38	Increase driver to advance more device and to avoid bridge: -Add NI AND gate(U5040) with 0 ohm (R6414) jumper on SLP_S3#_3R from ICH6.	2004/10/29	HP	Open	sch1029 done
78	Discrete	ICH6-2	38	Add a 100k(R6415) pull-down resistor on PM_DPRSLPVR to ensure the signal does not float from an Intel sighting report	2004/10/29	HP	Open	sch1029 done

79	Discrete	Cardbus	54	Since we do not support WAKE from S3-S5, the TI spec requires that GRST and PRST are connected together. - Add a NI resistor(R6418), 2N7002 FET(Q6057) between PCI_3S_PME# and U31 pin T3, connect a 47k (R6419) pull-up resistor to +V3S on U31 pinT3. -connect PCI_3S_RST# to U31 pin T1(GRST#) and add this selectable with 2 zero ohm jumpers (NI R6416, Put R6417).	2004/10/29	HP	Open	sch1029 done
80	Discrete	SD clk&data	55	The S_DATA trace space is 1:2, S_CLK trace space is 1:3 for TI layout recommend	2004/10/29	HP	Open	layout1029 done
81	Discrete	+V2.5L	8	Video corruption by +V2.5L no power. -Change L510 from 4.7uH to 8.2uH -Change C6159 from 4.7uF to 1uF. -Add R1 (10 ohm) between +V5A and U5007 pin28 (VCC). -Add C16 4.7U between U5007 pin28(VCC) and U5007 pin1(GND). -Change R6117 from 16.2K to 2K. -Change R6116 from 12.4K to 2K. -Change C551 from 10pF to 0.01uF. Prevent LAN noise : -Add R10 (0 ohm) between U5007 pin24 and Q6 pin2. -Add R15(0 ohm) between U5007 pin5 and Q4 pin2.	2004/11/3	HP	Open	sch1103 done
82	Discrete	SMVREF	12	Modify the DDR2 termination regulator (U3) from NS LP2996MRX to TI TPS51100	2004/11/3	HP	Open	sch1103 done
83	Discrete	SM_OCDCOMP	21	NI R651,R665 on Alviso OCDCOMP from Intel sighting report	2004/11/3	HP	Open	sch1103 done
84	Discrete	M22	31	1. Remove L19 and Change the C128 from 10uf to 47uf for +V1.2S 2.Change L523 from ACM321611 to NFM60R30T222 for +V1.8S	2004/11/3	HP	Open	sch1103 done
85	Discrete	Bios Socket	43	Remove CN4005 and Install U1000 by BOM change	2004/11/3	HP	Open	sch1103 done
86	Discrete	TI_PRST	54	NI U33,R345,C318,D31	2004/11/3	HP	Open	sch1103 done
87	Discrete	Fan controller	20	To improve PWM percentage of FAN -Change R6139 from 10k to 1k	2004/11/5	IEC	Open	sch1105 done
88	Discrete	M22	31	Leave L19	2004/11/5	HP	Open	sch1105 done
89	Discrete	RTC accuracy	37	Change C882,C883 from 10pf to 22pf for RTC accuracy	2004/11/5	IEC	Open	sch1105 done
90	Discrete	LED's brightness	66 67	1.Change R357 from 270 to 360 2.Change R359 from 150 to 120 3.Change R361 from 270 to 220 4.Change R6362,R360 from 270 to 330 5.Chnage R6002 from 270 to 120 6.Change R6029 from 270 to 330	2004/11/5	IEC	Open	sch1105 done
91	Discrete	USB connecter	53	NI R6398(0 ohm) and short the trace between R6398 pin1&2 by layout for EMI request	2004/11/9	EMI	Open	sch1109 done