

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE
DESIGN				
CHECK				
RESPONSIBLE				
SIZE = 3		VER :		
FILE NAME : XXXX-XXXXXX-XX				
P/N	XXXXXXXXXXXX			

INVENTEC	
TITLE OSAKA 30	
SIZE A3	CODE CS
DOC NUMBER TC7562AA000	REV A05
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1.COVER PAGE	33.LCD CONN
2.INDEX	34.S/B(1/3)
3.BLOCK DIAGRAM	35.S/B(2/3)
4.POWER SEQUENCE BLOCK	36.S/B(3/3)
5-13.SYSTEM POWER	37.S/B STRAPPING
14.CLOCK GENERATOR	38.K/B CONTROLLER
15.CPU(1/2)	39.SUPER I/O
16.CPU(2/2)	40.SERIAL & PARALLEL PORT
17.CPU DECOUPLING	41.K/B & D/B CONN
18.FAN & THERMAL CONTROLLER	42.AC97 CODEC
19.N/B(1/5)	43.AUDIO AMP & PHONE JACK
20.N/B(2/5)	44.MDC CONN
21.N/B(3/5)	45.HDD CONN
22.N/B(4/5)	46.ODD CONN
23.N/B(5/5)	47.USB CONN
24.N/B STRAPPING	48.CARDBUS CONTROLLER(CB712)
25.DDR DIMM0	49.PC CARD SLOT & SD CARD CNN
26.DDR CHIP TERMINATOR	50.MINIPCI CONN
27.DDR CHIP(1/2)	51.1394 CONTROLLER
28.DDR CHIP(2/2)	52.LAN CONTROLLER
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30.DDR DIMM0 DAMPING	54.SWITCH & D/B
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32.CRT& SVEDIO CONN	

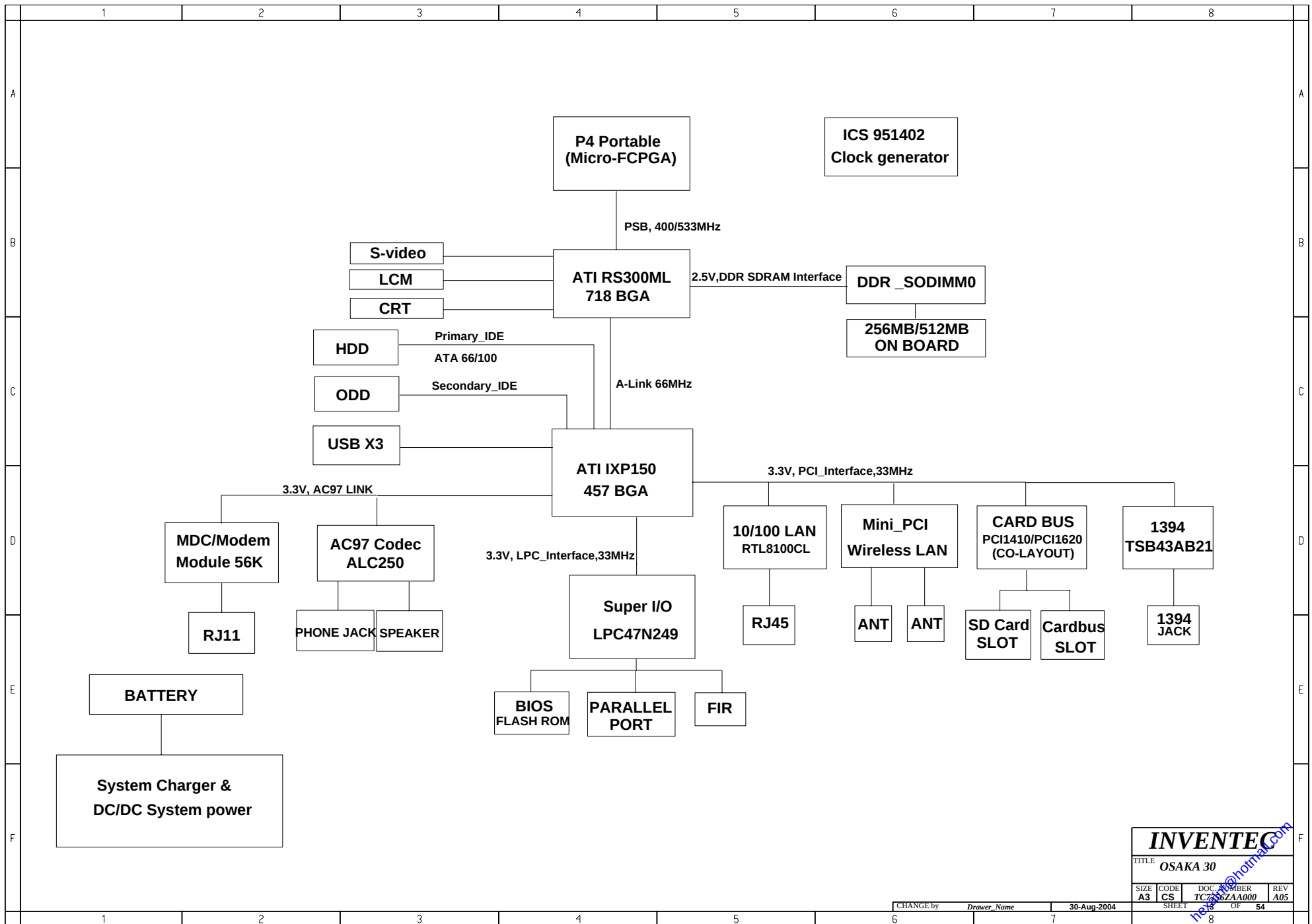
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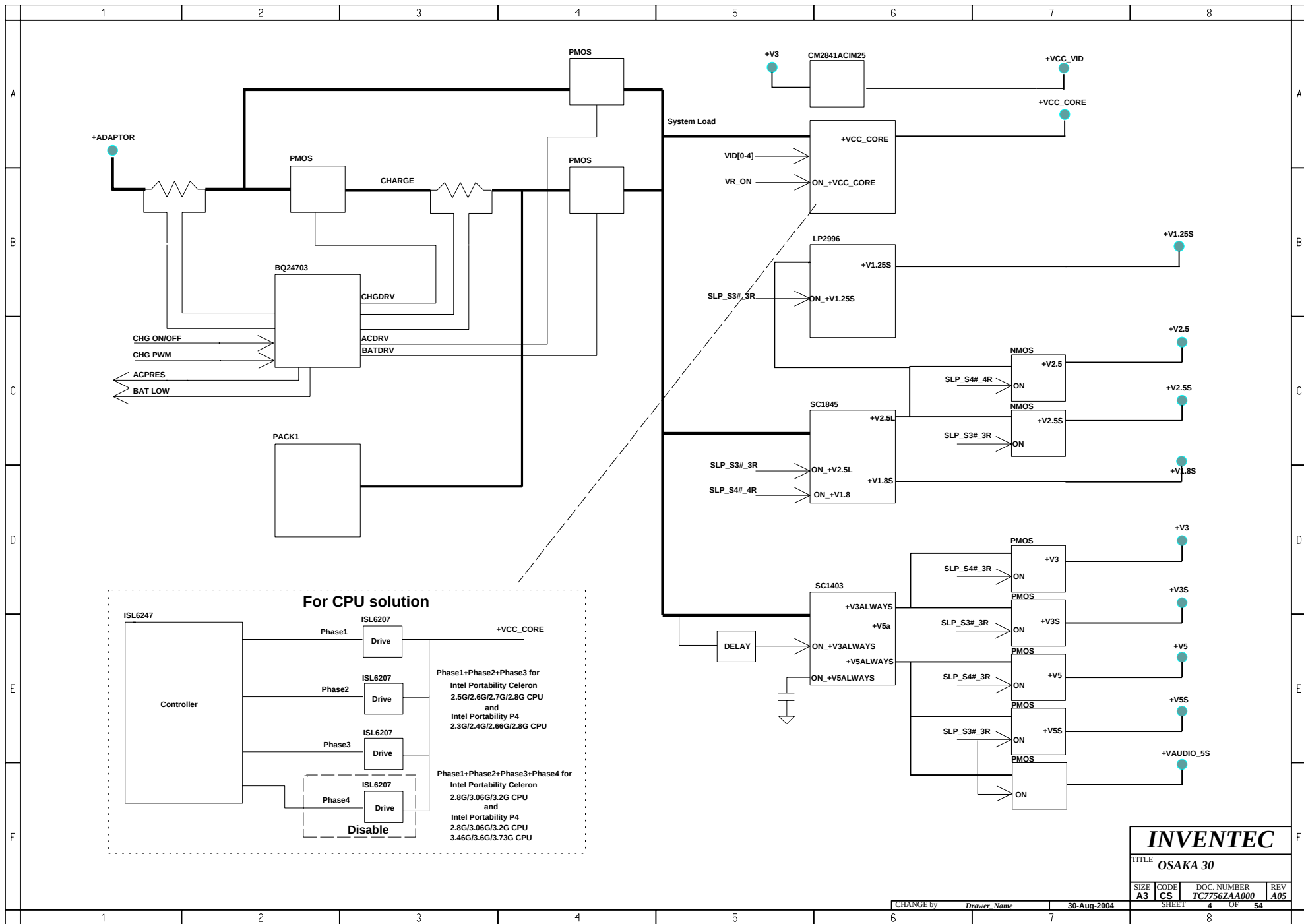
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CHANGE by *Drawn Name* 30-Aug-2004

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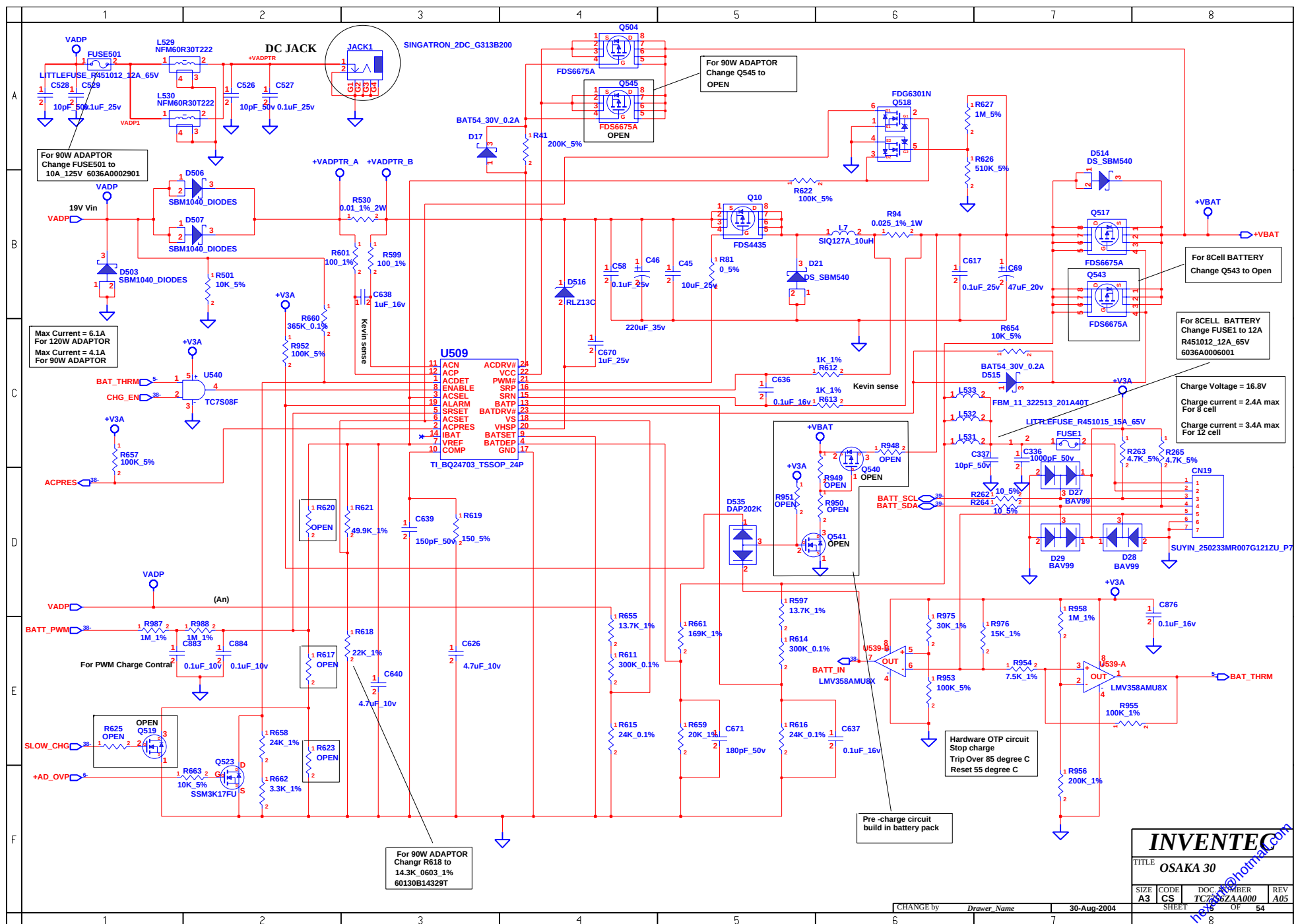
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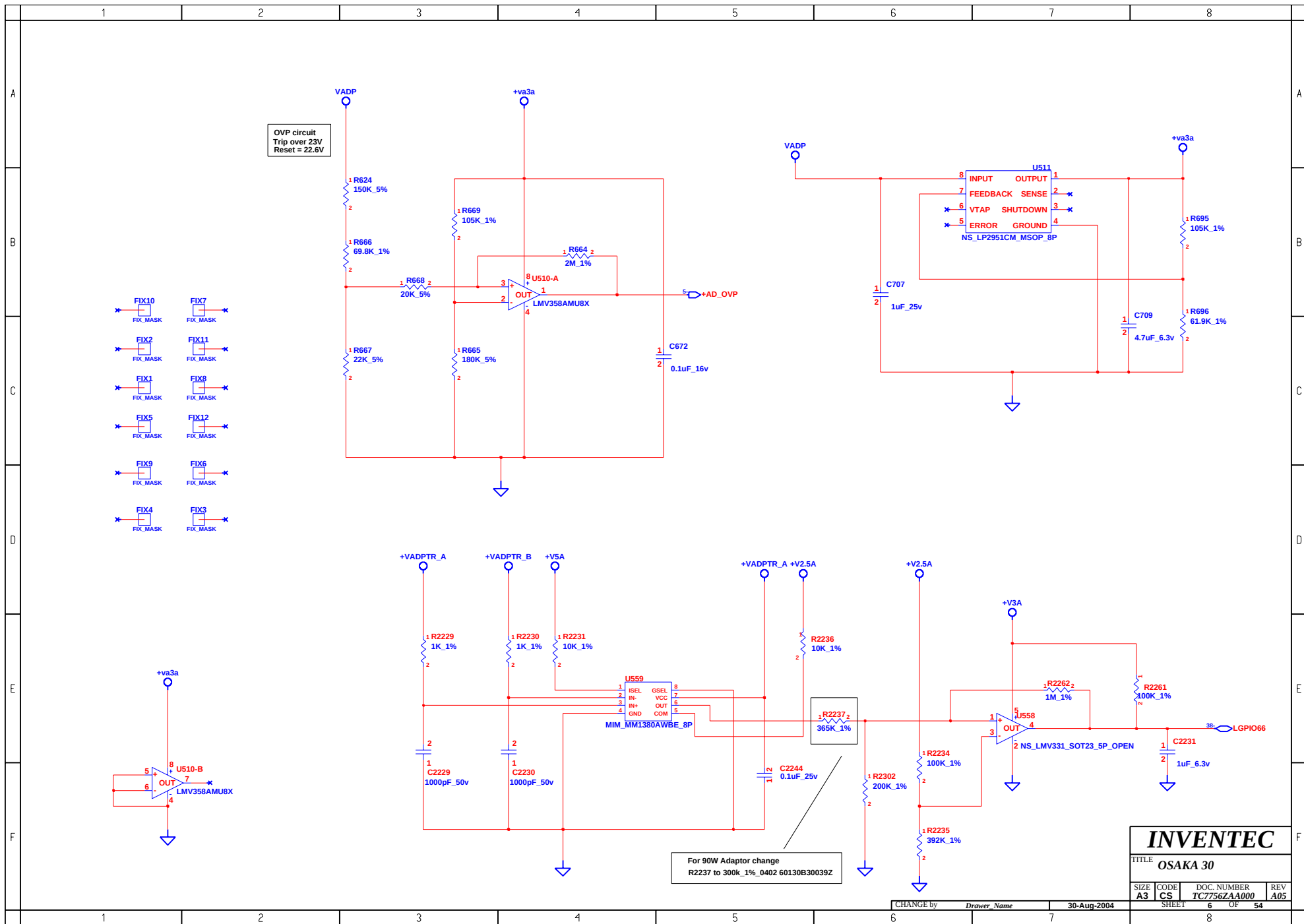
TITLE
OSAKA 30

SIZE	CODE	DOC. NUMBER	REV
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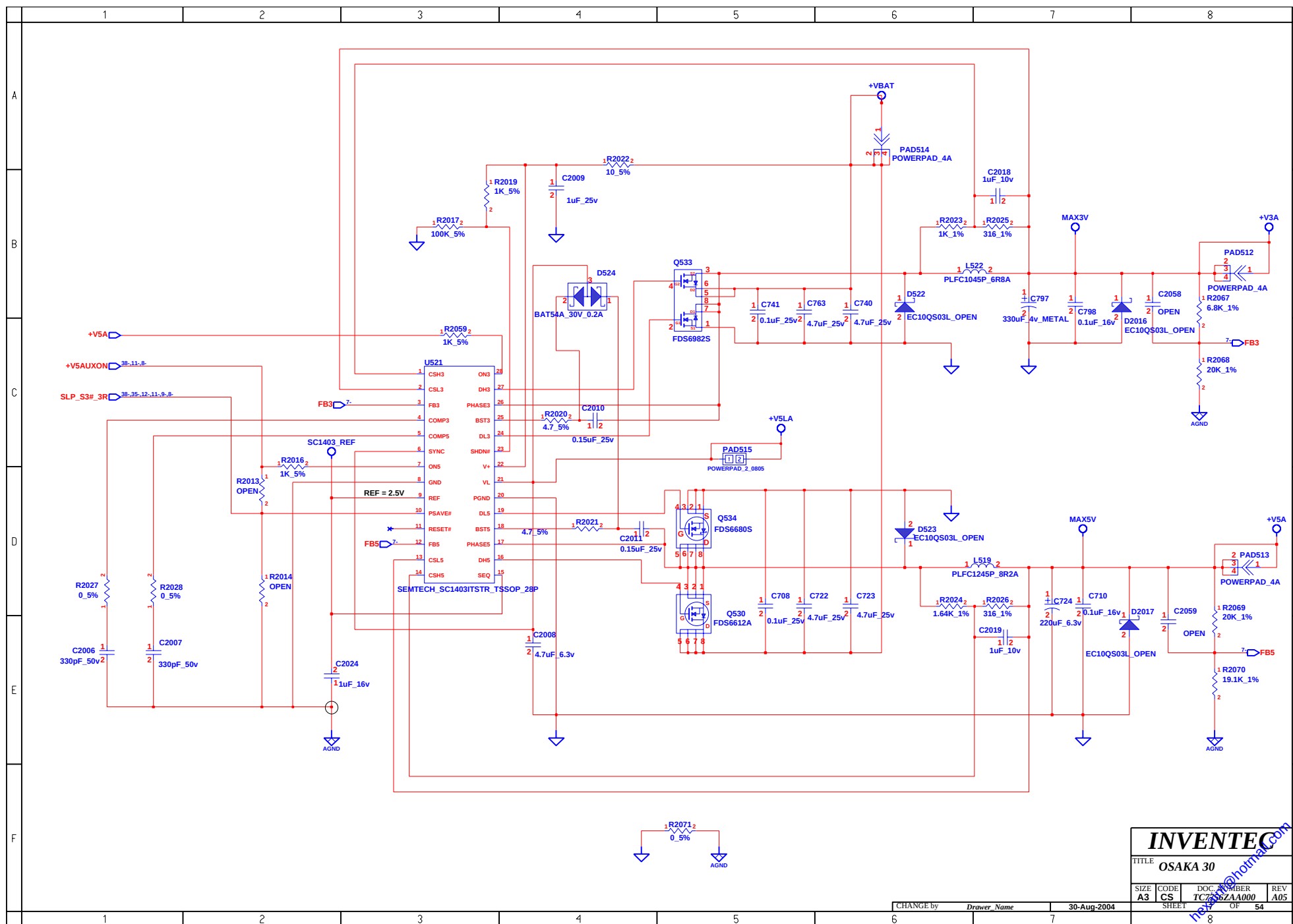
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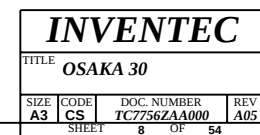
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OSAKA 30

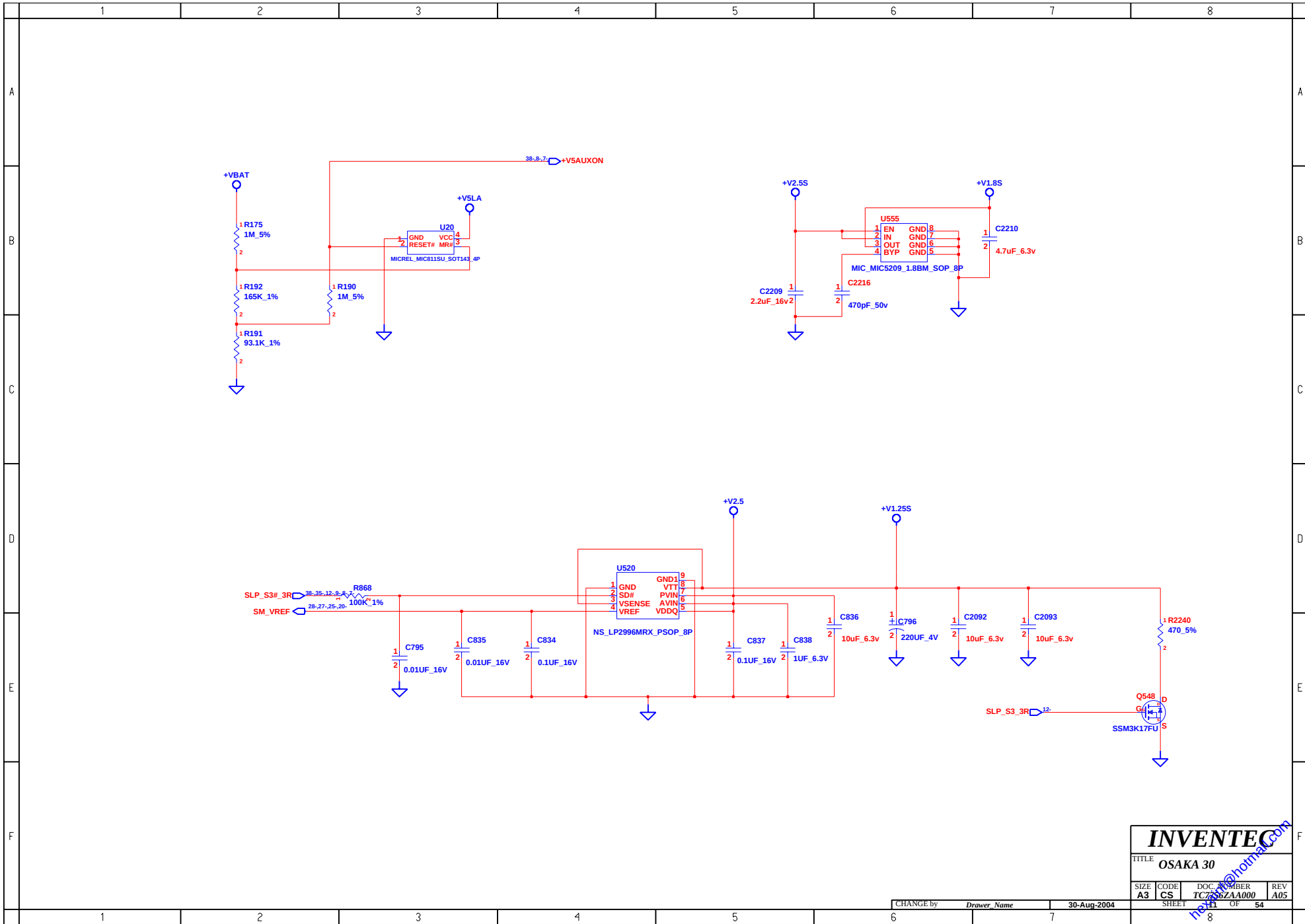
SIZE A3	CODE CS	DOC. NUMBER TC7756ZAA000	REV A05
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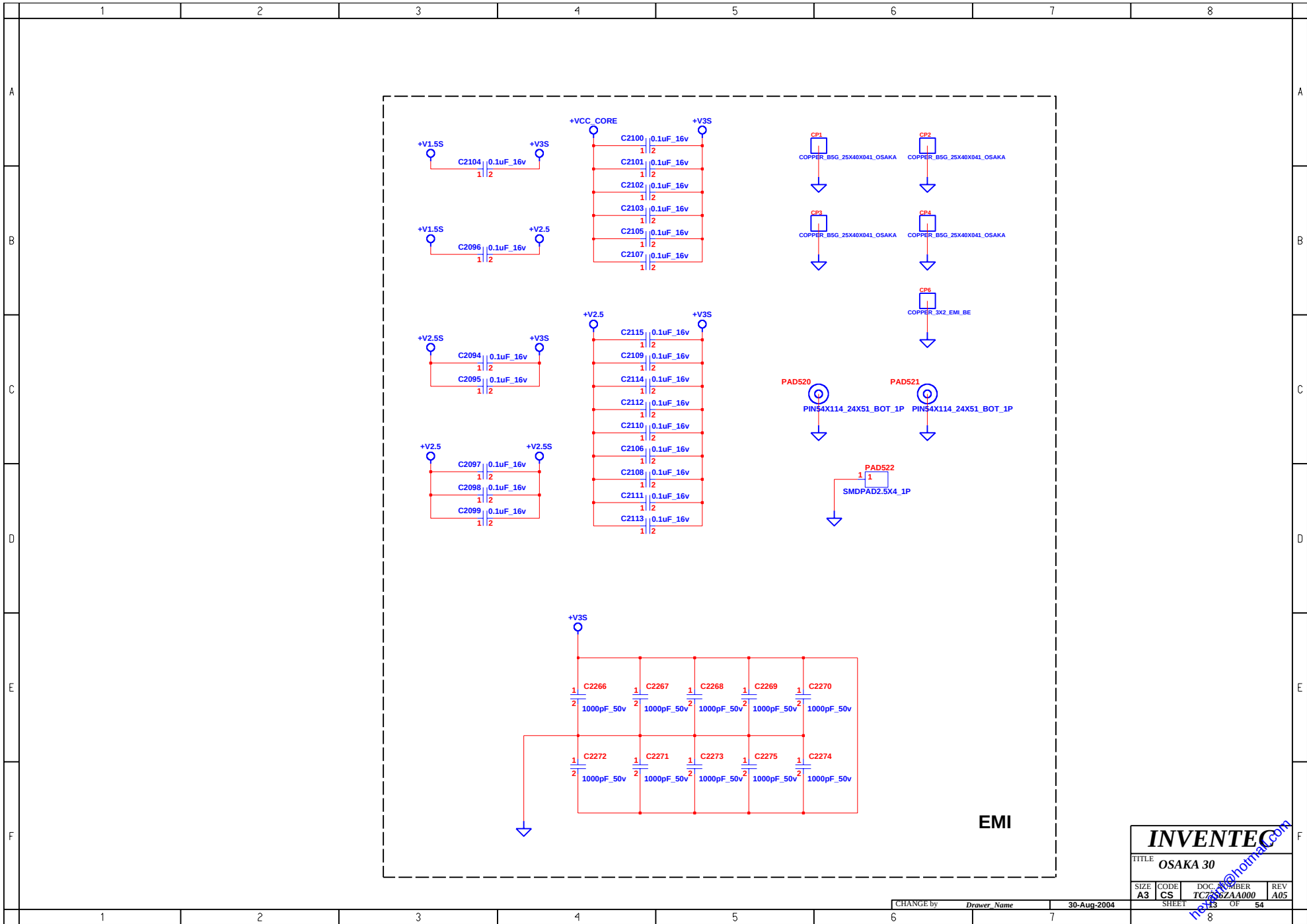
CHANGE by *Drawn Name* 30-Aug-2004

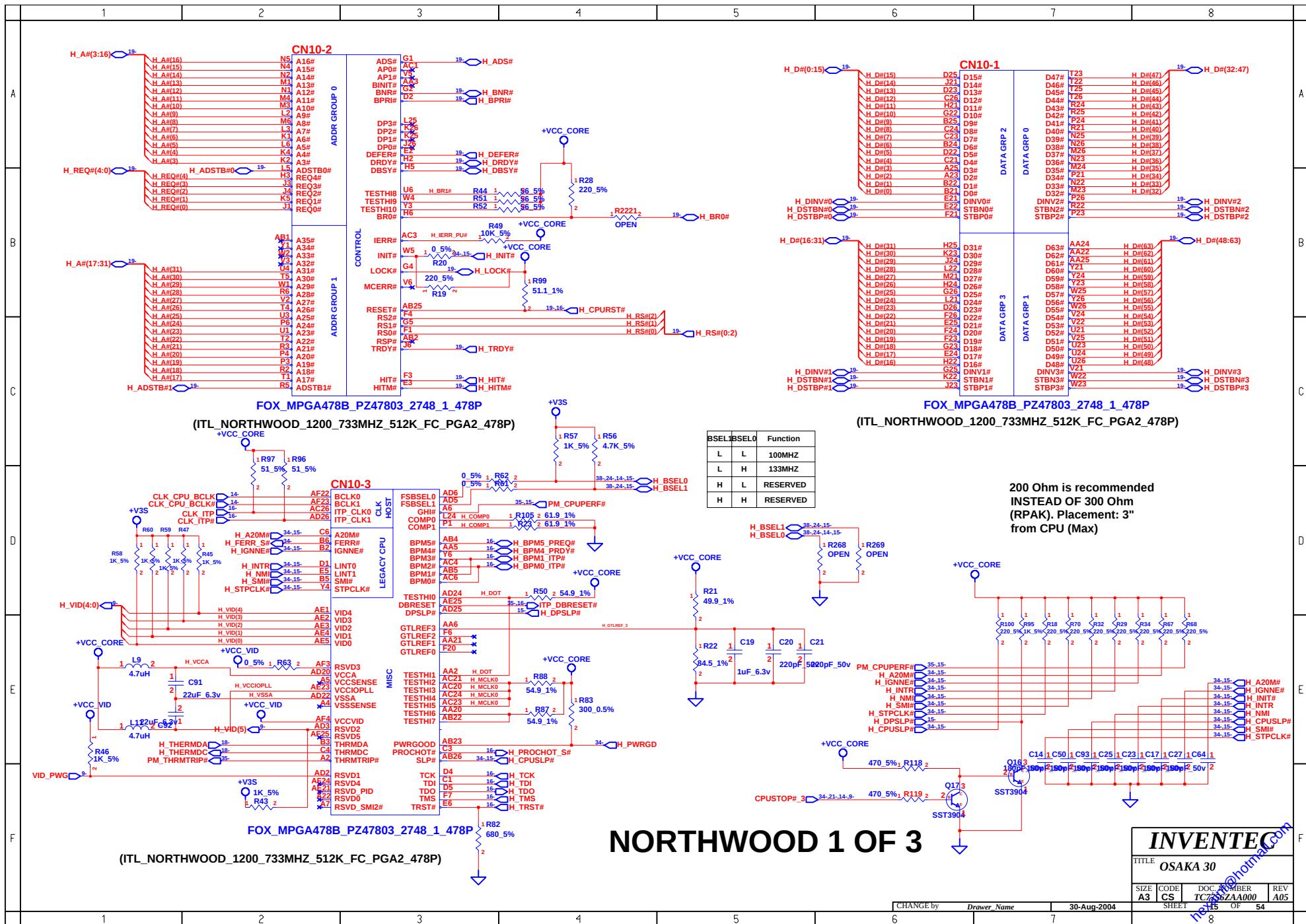
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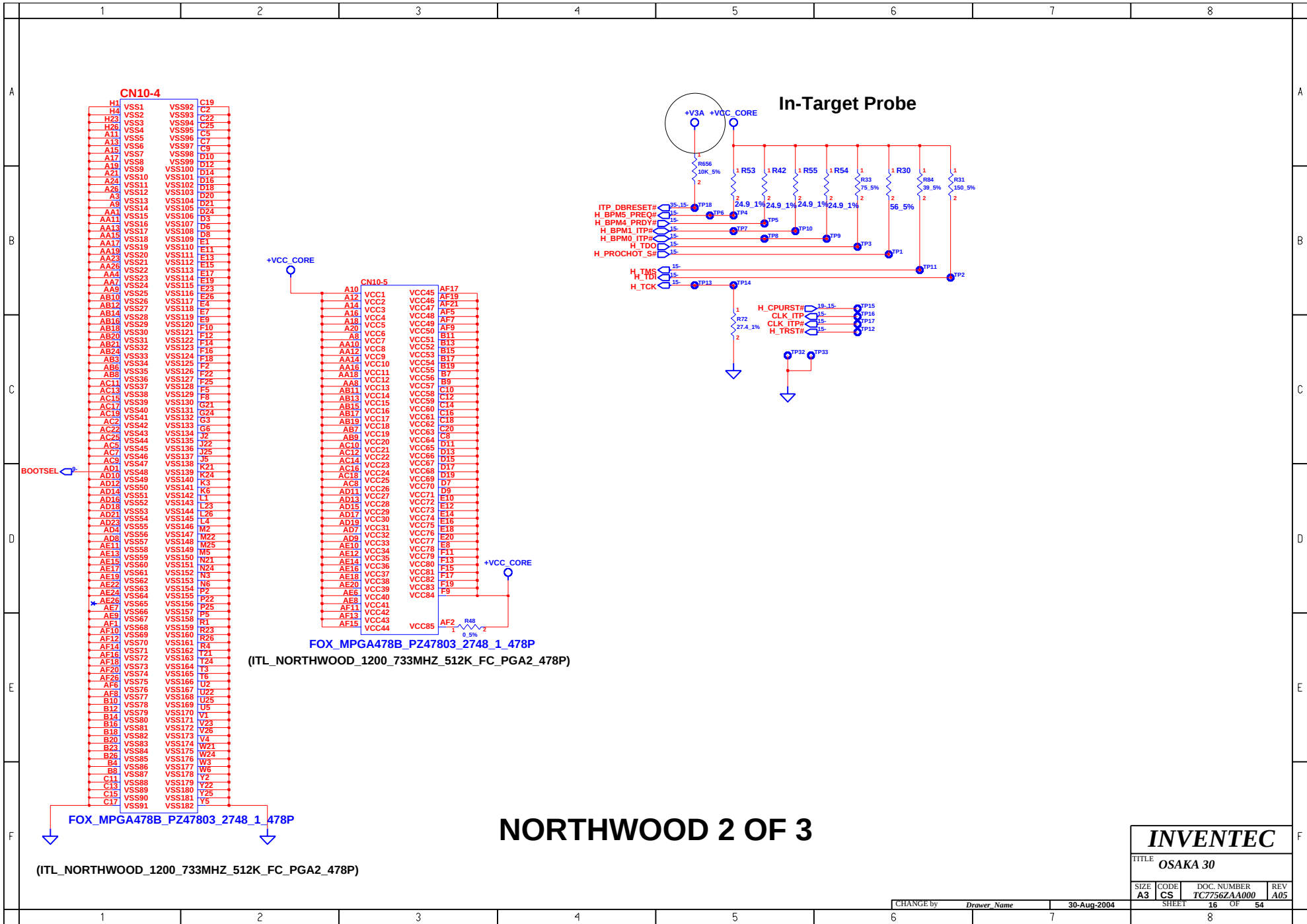


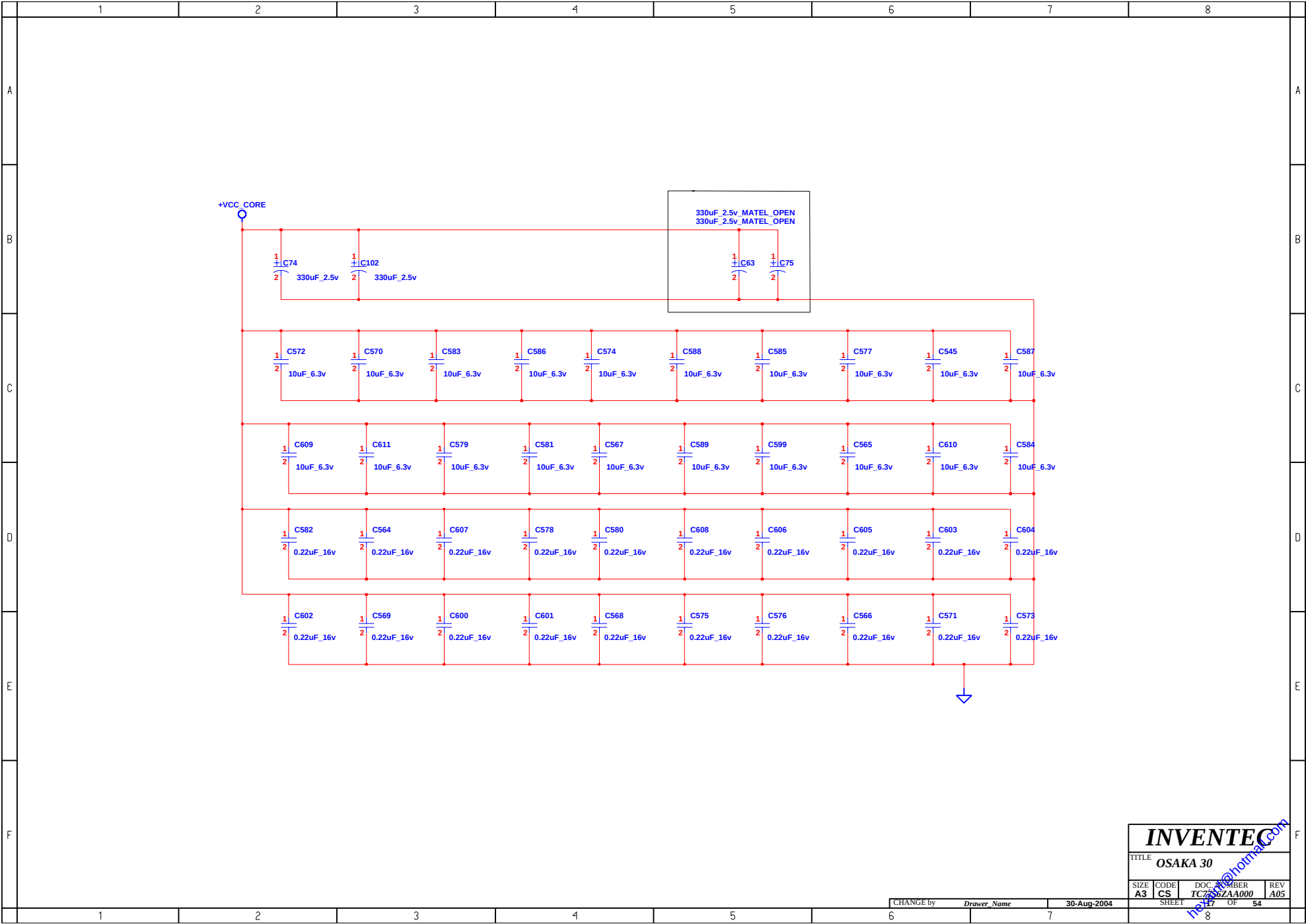


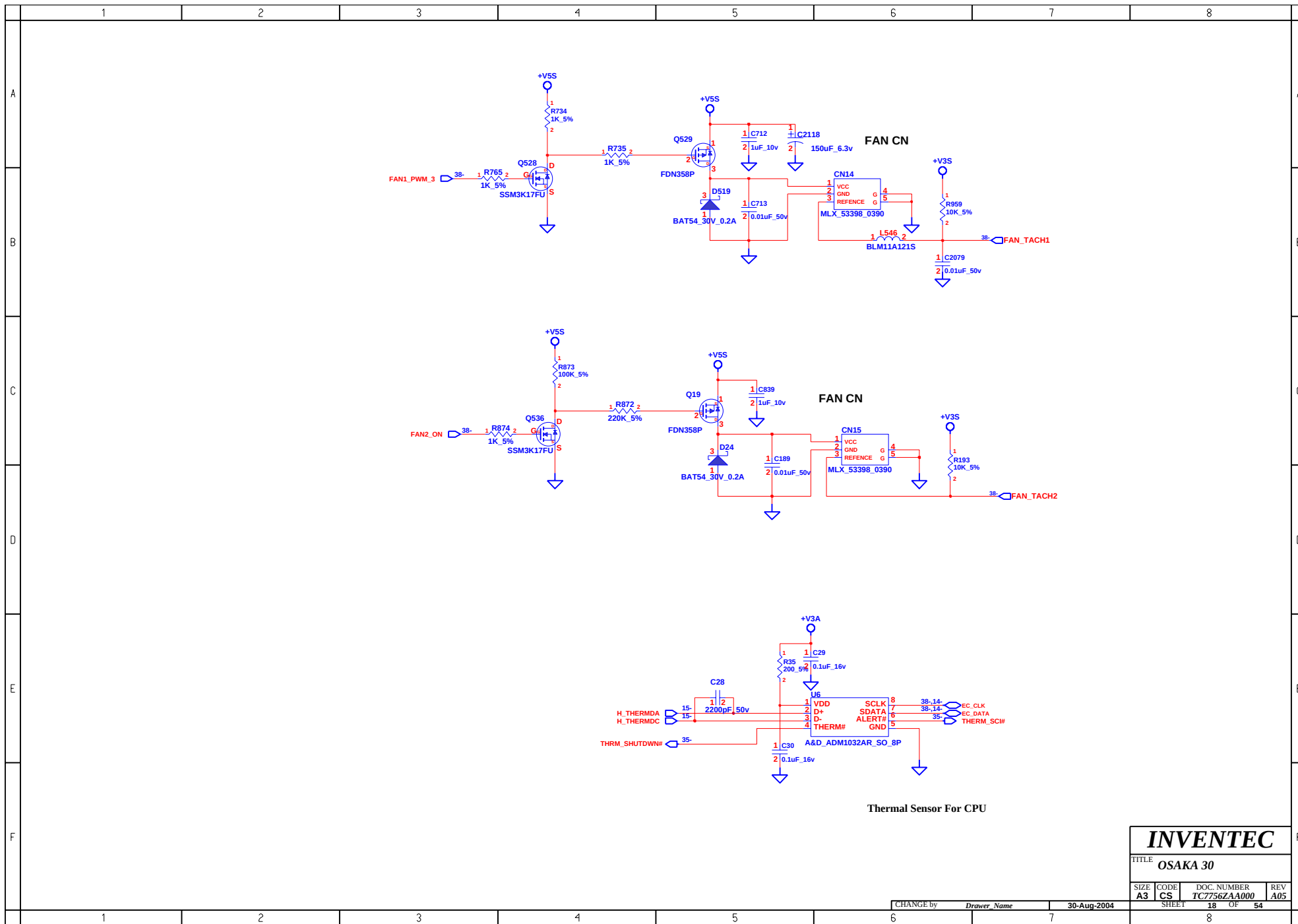


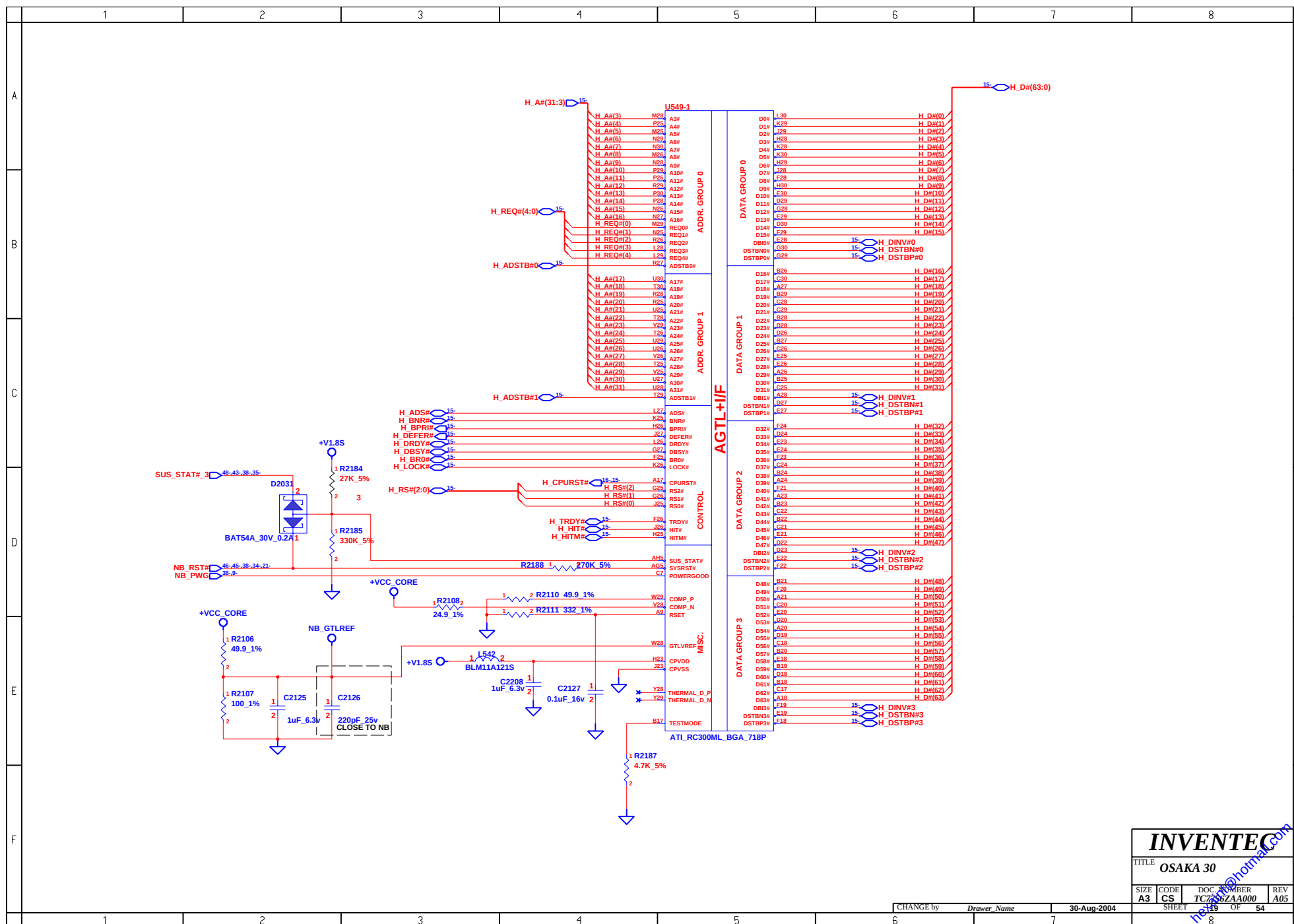












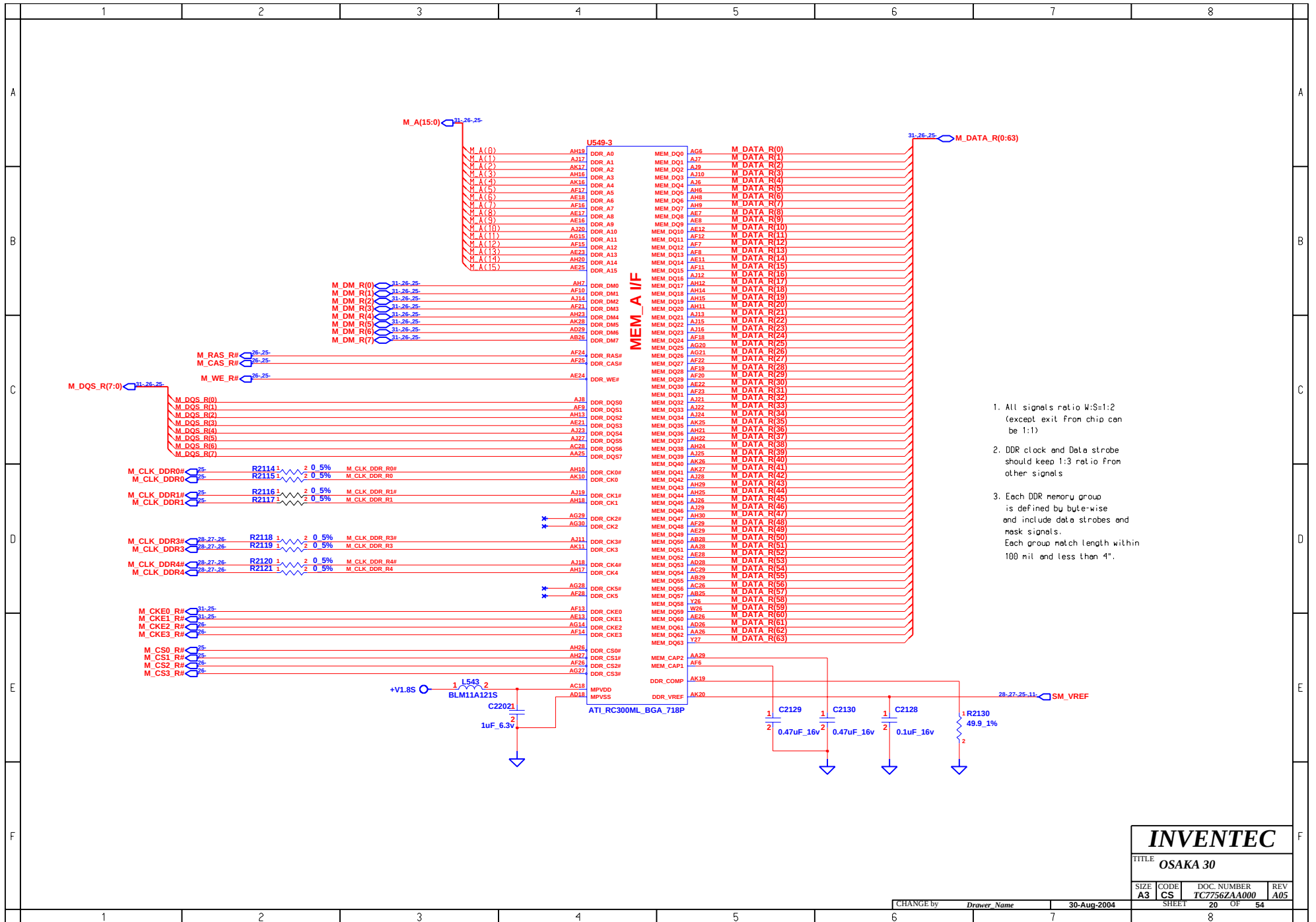
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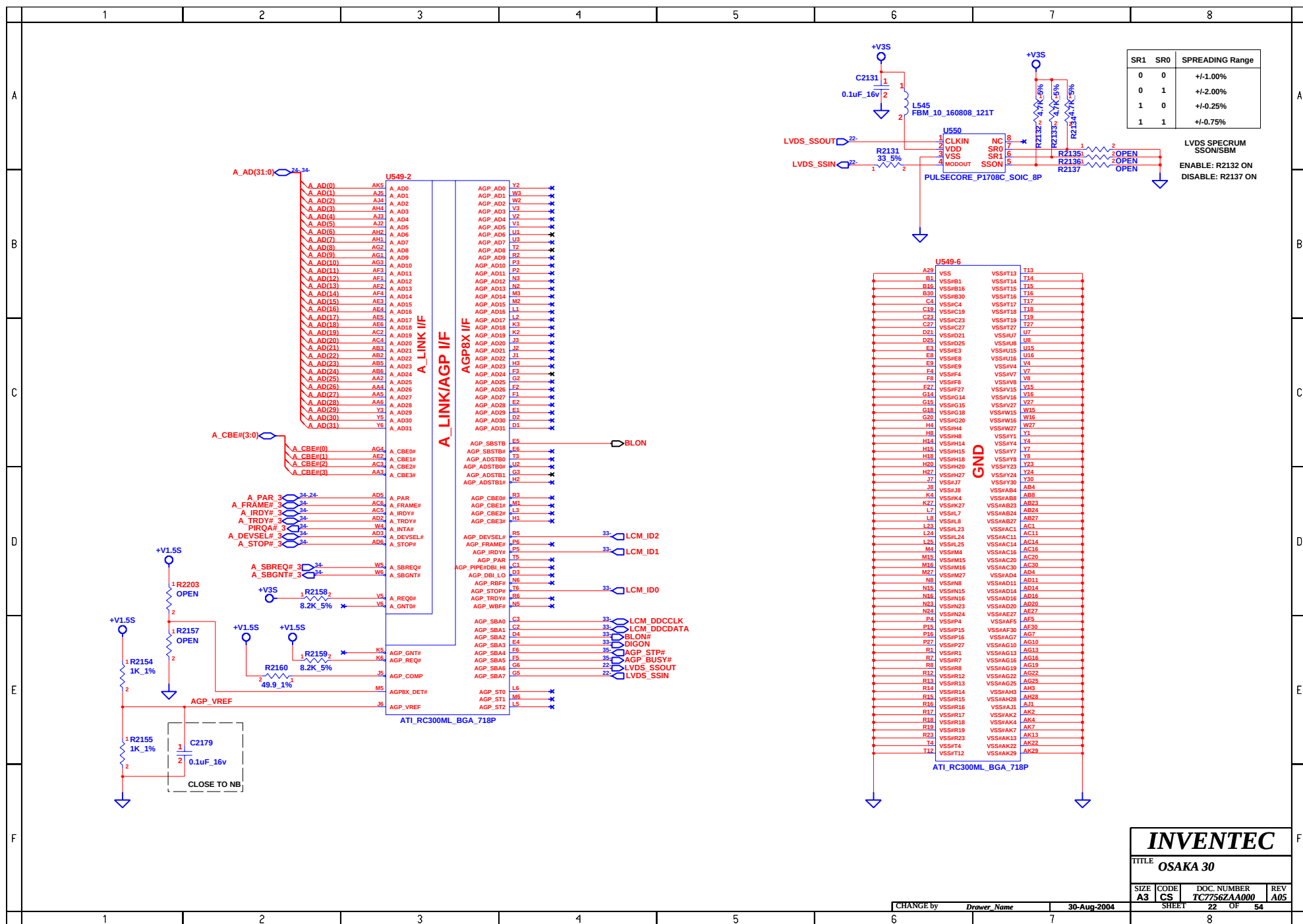
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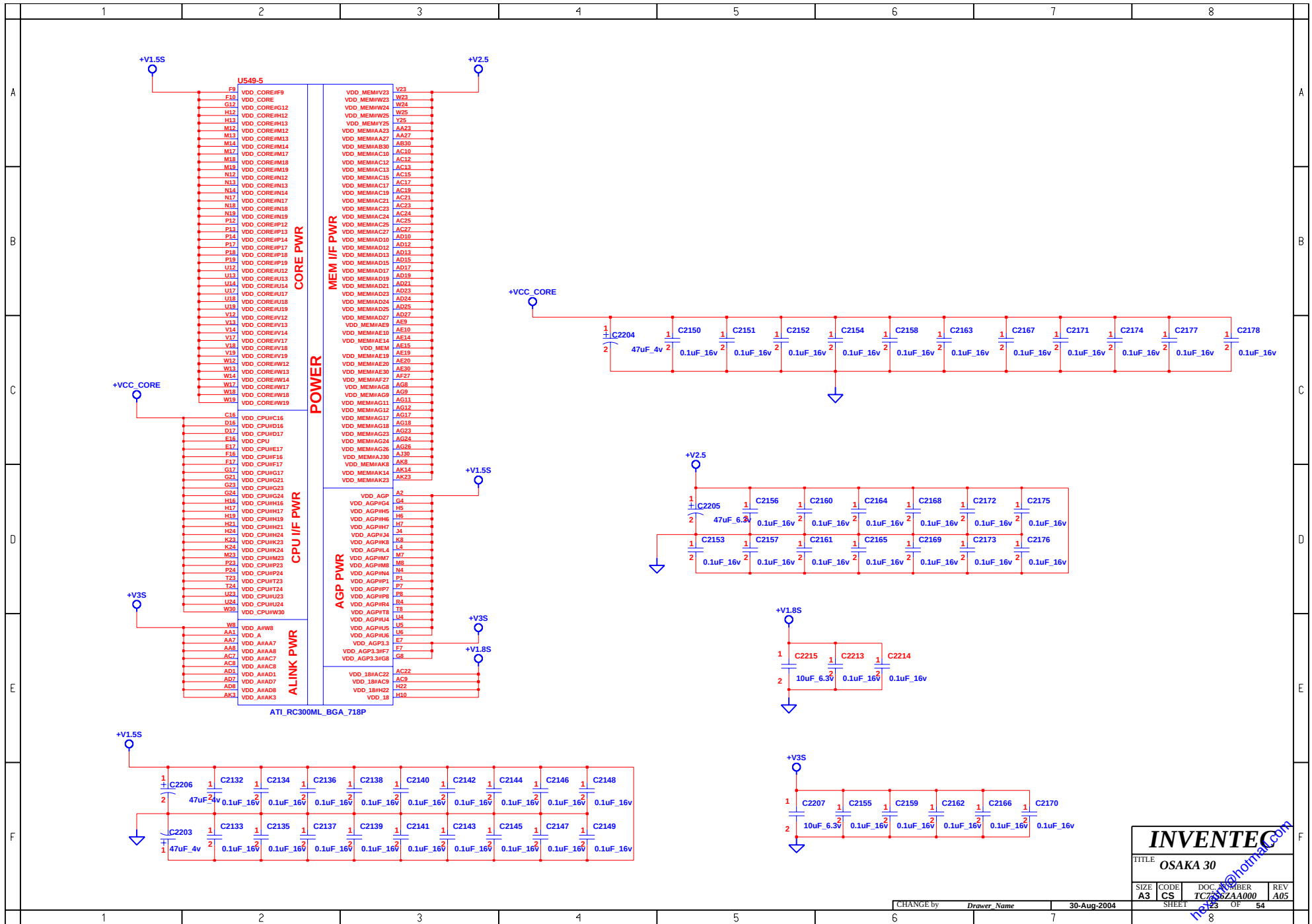
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A3	CS	TC7256ZAA000	A

CHANGE by	<i>Drawer Name</i>	30-Aug-2004
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	1	2	3	4	5	6	7	8	
A	A_AD(31) 34-22- H_BSEL1 38-15-1R220424.7K_5%	AD(31:30) : FSB CLK SPEED 0 0 : 100MHZ DEFAULT : 00 0 1 : 133MHZ 1 0 : 200MHZ 1 1 : 166MHZ	A_AD(24) 34-22- 1R217724.7K_5% 1R21782OPEN	AD24 : MOBILE CPU SELECT 0 : BANIAS CPU DEFAULT : 1 1 : OTHER CPU	A_PAR_3 34-22- 1R221524.7K_5% 1R22162OPEN	PAR:EXTENDED DEBUG MODE 0 : DEBUG MODE DEFAULT : 1 1 : NORMAL	A		
B	A_AD(30) 34-22- H_BSEL0 38-15-141R220524.7K_5%		A_AD(23) 34-22- 1R217924.7K_5%	AD23 : CLOCK BYPASS DISABLE 0 : TEST MODE DEFAULT : 1 1 : NORMAL			B		
C	A_AD(29) 34-22- 1R220724.7K_5% 1R22062OPEN	AD29 : STRAP CONFIGURATION 0 : REDUCED SET DEFAULT : 1 1 : FULL SET	A_AD(22) 34-22- 1R221324.7K_5% 1R22142OPEN	AD22 : OSC PAD OUTPUT PCICLK 0 : PCICLK OUT DEFAULT : 1 1 : OSC CLK OUT			C		
D	A_AD(28) 34-22- 1R22082OPEN 1R220924.7K_5%	AD28 : SPREAD SPECTRUM ENABLE 0 : DISABLE DEFAULT : 0 1 : ENABLE	A_AD(21) 34-22- 1R215024.7K_5% 1R21512OPEN	AD21 : AUTO_CAL ENABLE 0 : DISABLE DEFAULT : 1 1 : ENABLE			D		
E	A_AD(27) 34-22- 1R217324.7K_5%	AD27 : FrcShortRest# 0 : TEST MODE DEFAULT : 1 1 : NORMAL MODE	A_AD(20) 34-22- 1R21802OPEN 1R218124.7K_5%	AD20 : INTERNAL CLK GEN ENABLE 0 : DISABLE DEFAULT : 0 1 : ENABLE			E		
F	A_AD(26) 34-22- 1R217424.7K_5%	AD26 : ENABLE IOQ 0 : IOQ = 1 DEFAULT : 1 1 : IOQ = 12	A_AD(19) 34-22- 1R215224.7K_5% 1R21532OPEN	AD19 : MEMORY IO VOLTAGE 0 : 1.8V DEFAULT : 1 1 : 2.5V	A_AD(18) 34-22- 1R21822OPEN 1R218324.7K_5%	AD18 : ENABLE PHASE CALIBRATION 0 : DISABLE DEFAULT : 0 1 : ENABLE	F		
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CHANGE by

Drawer Name

30-Aug-2004

SIZE

CODE

DOC. NUMBER

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54

REV

A05

INVENTEC

TITLE

OSAKA 30

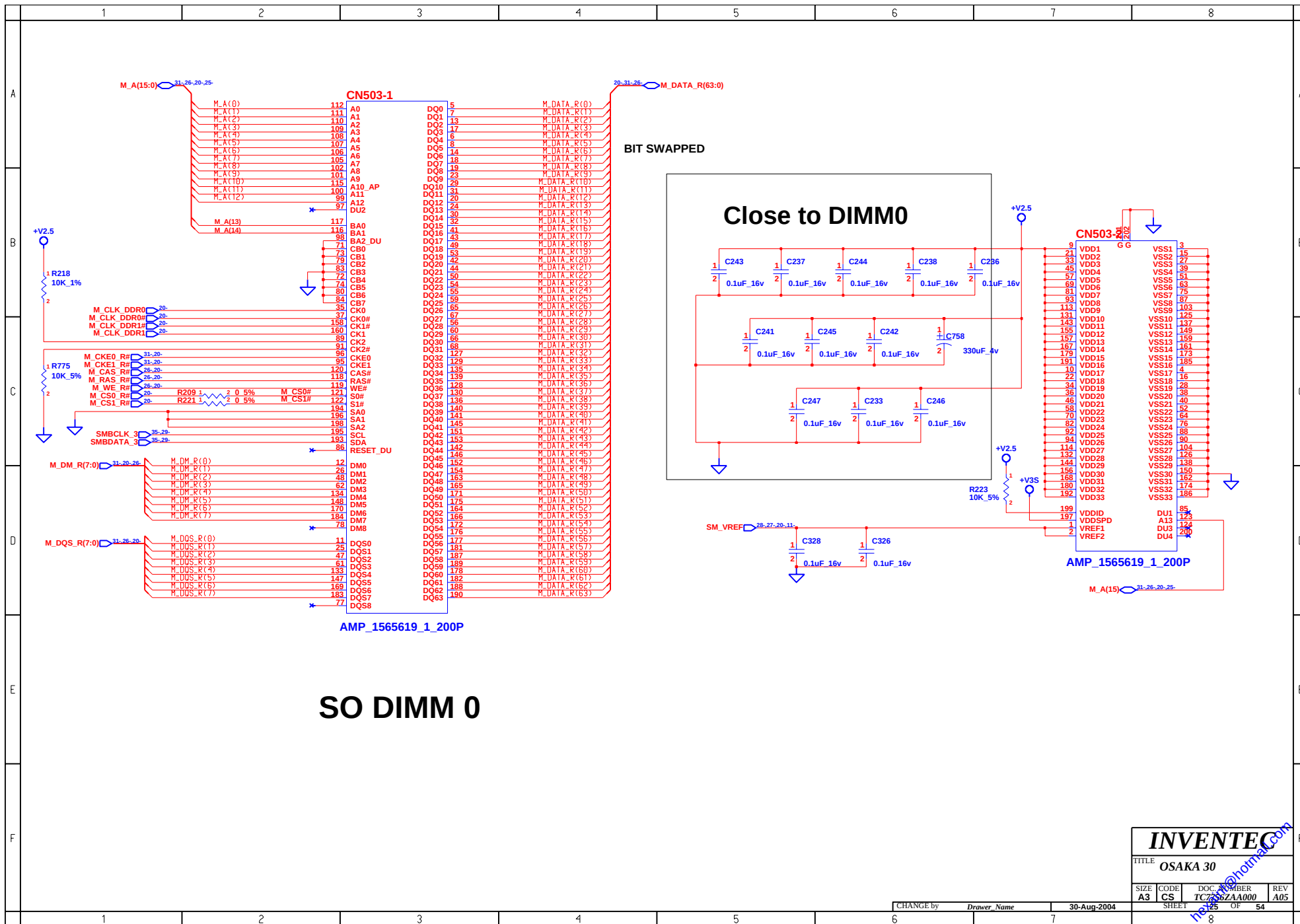
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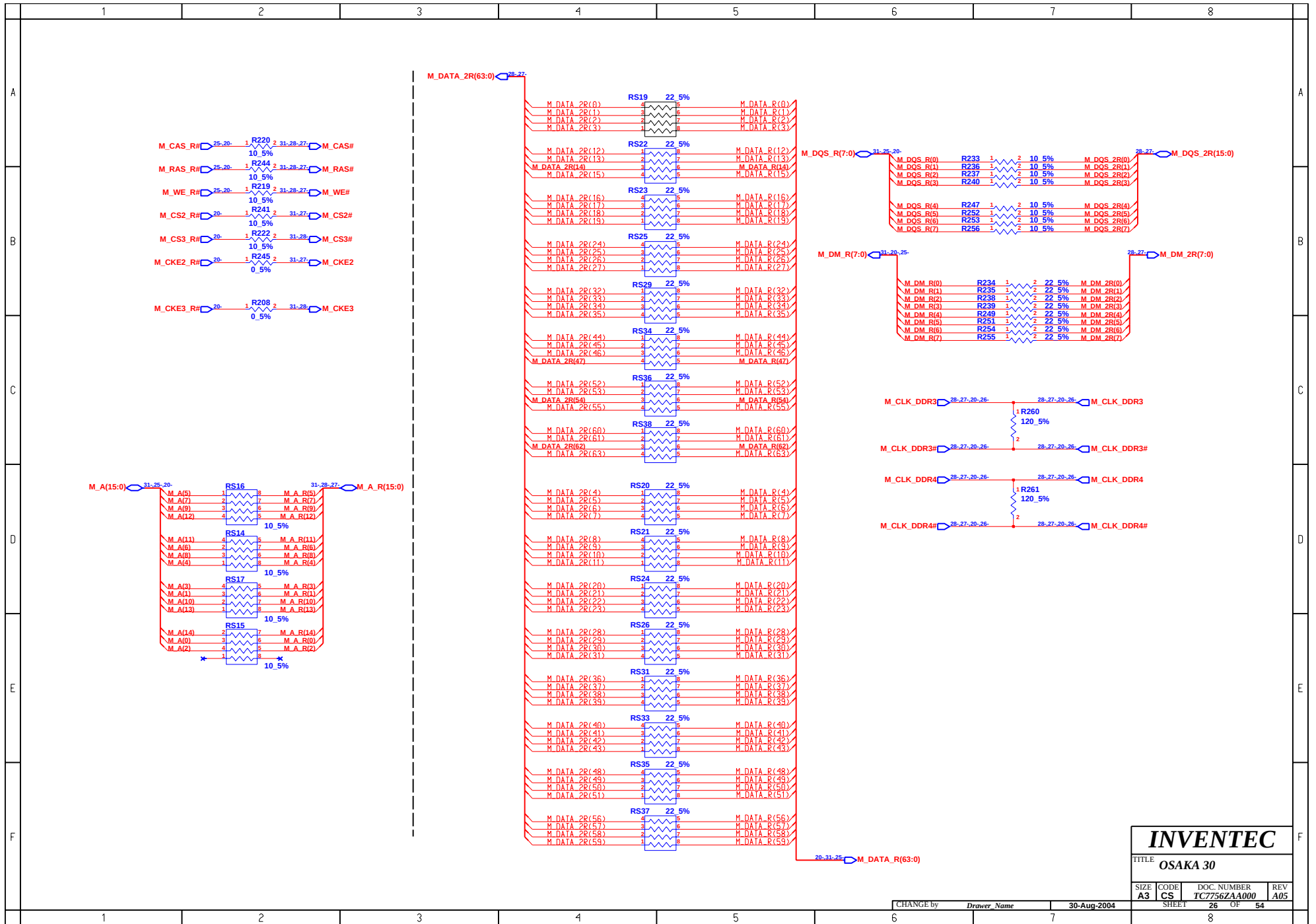
TITLE
OSAKA 30

SIZE	CODE	DOC. NUMBER	REV
A3	CS	TC7756ZAA000	A05

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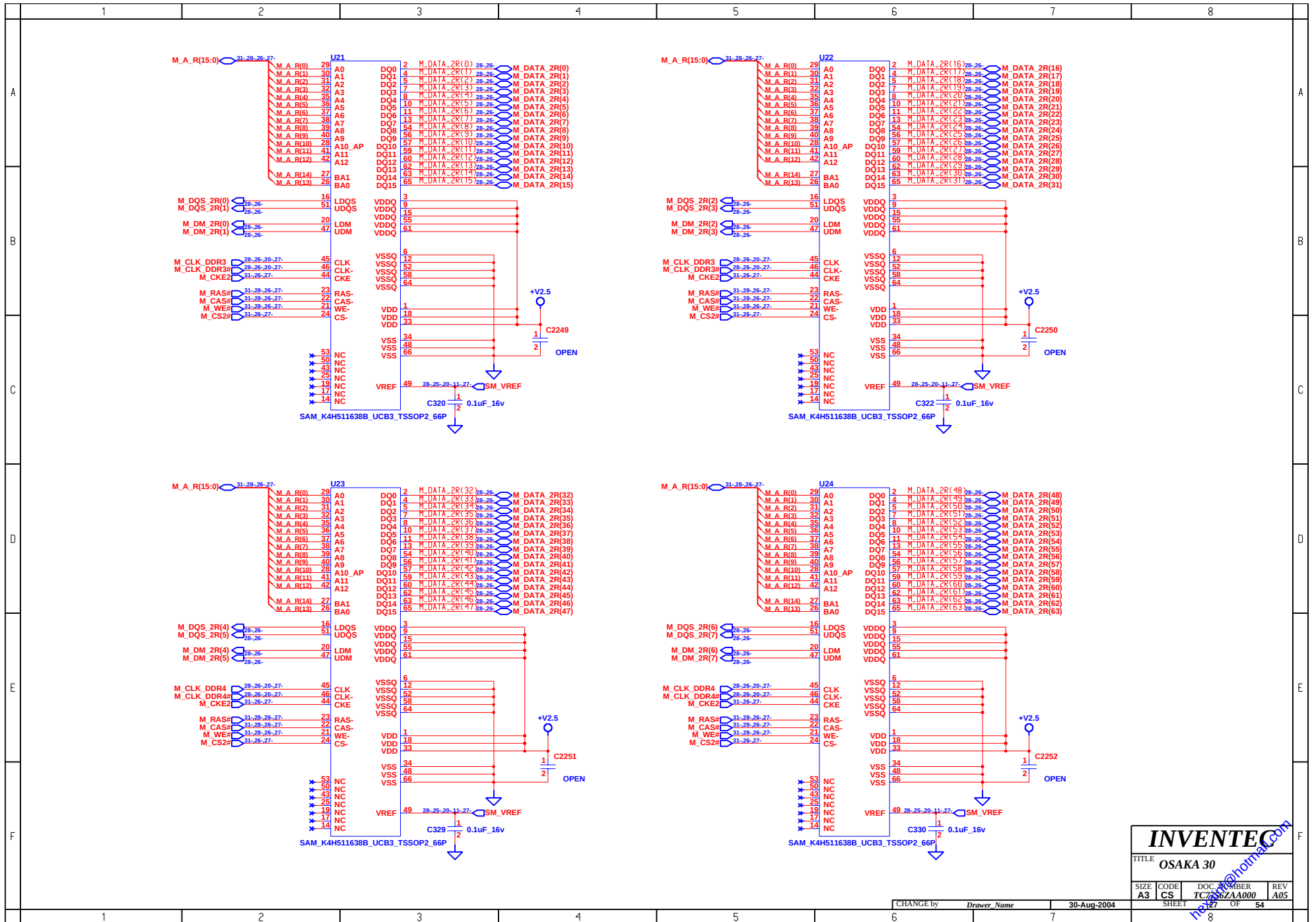
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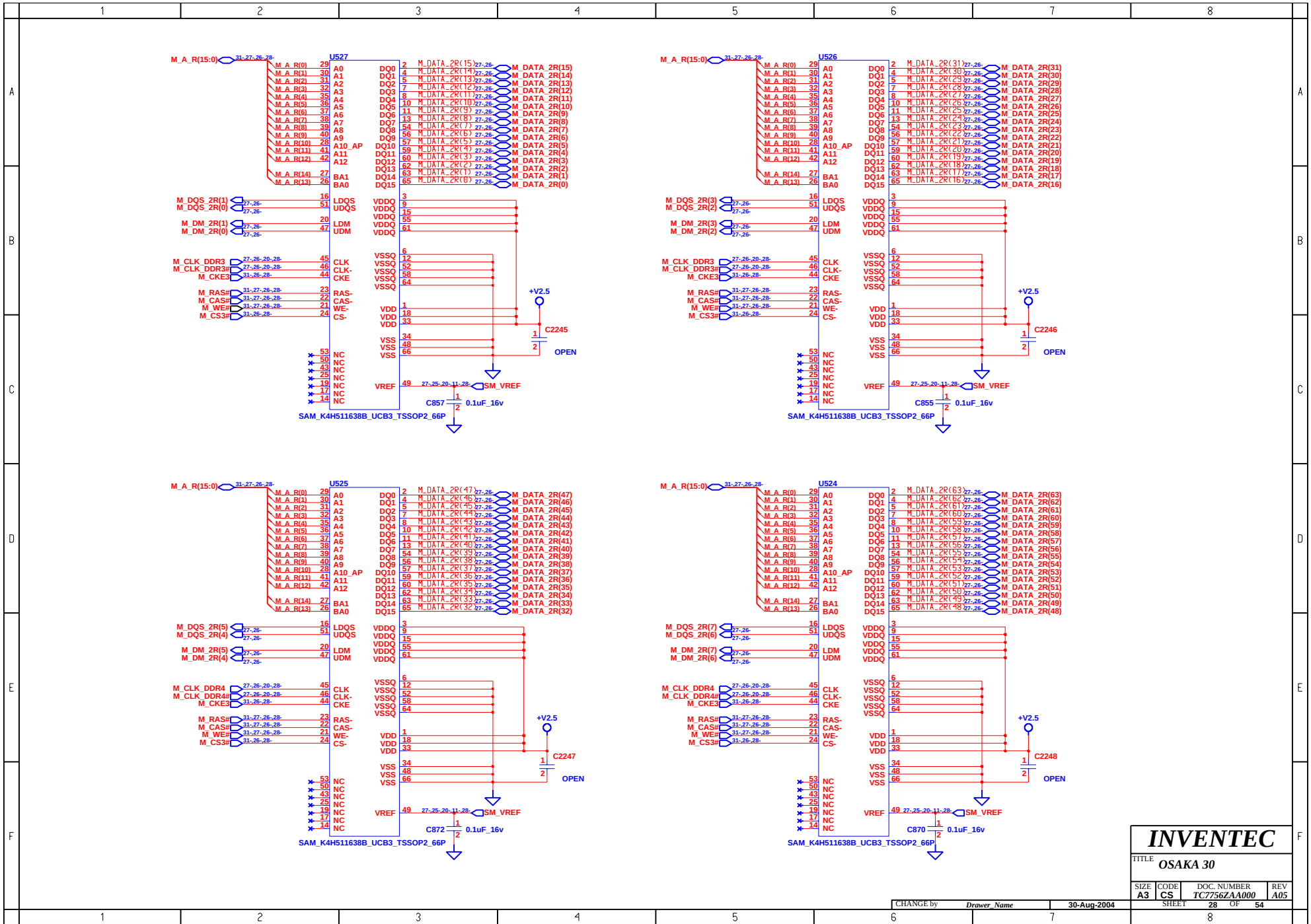
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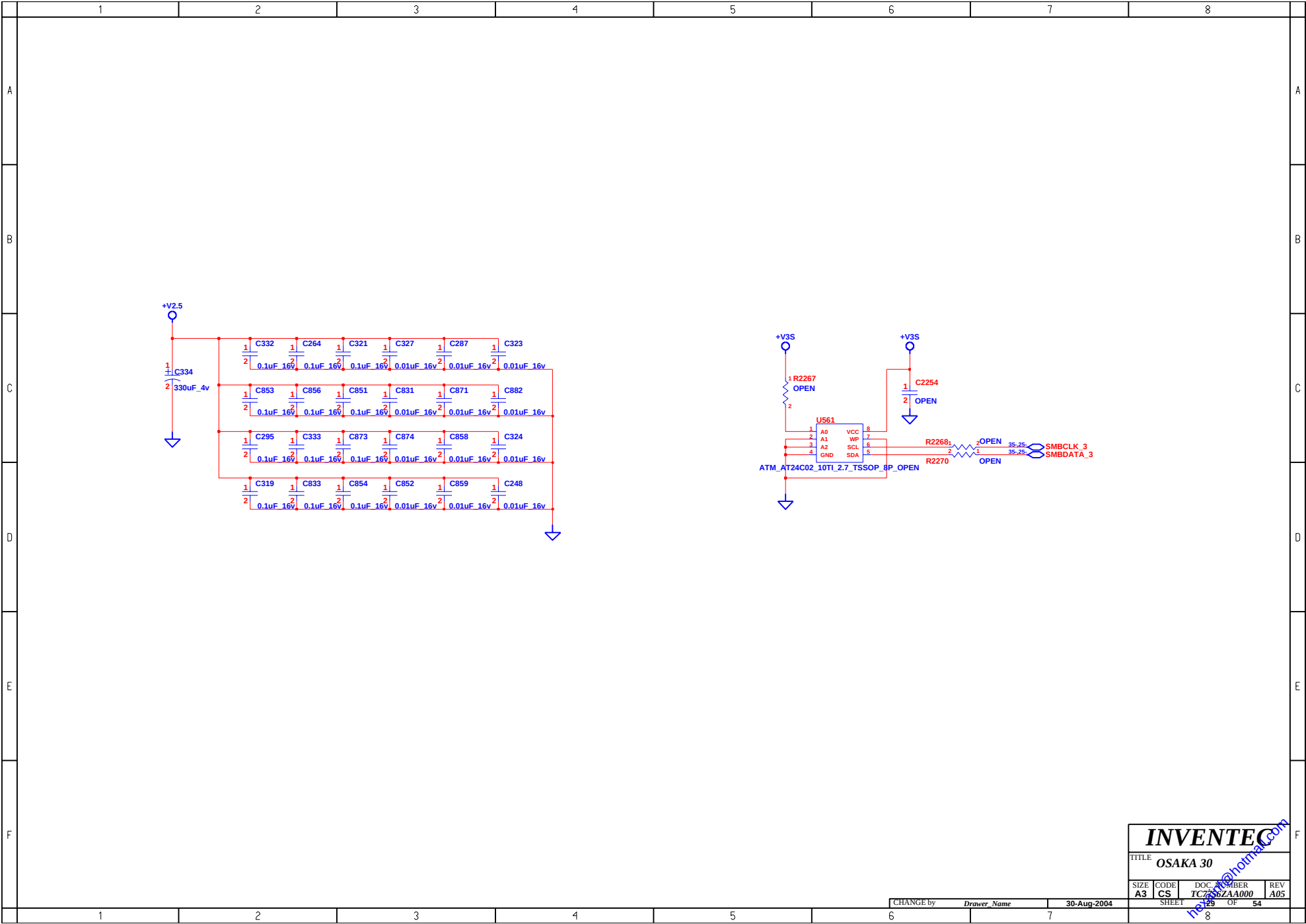
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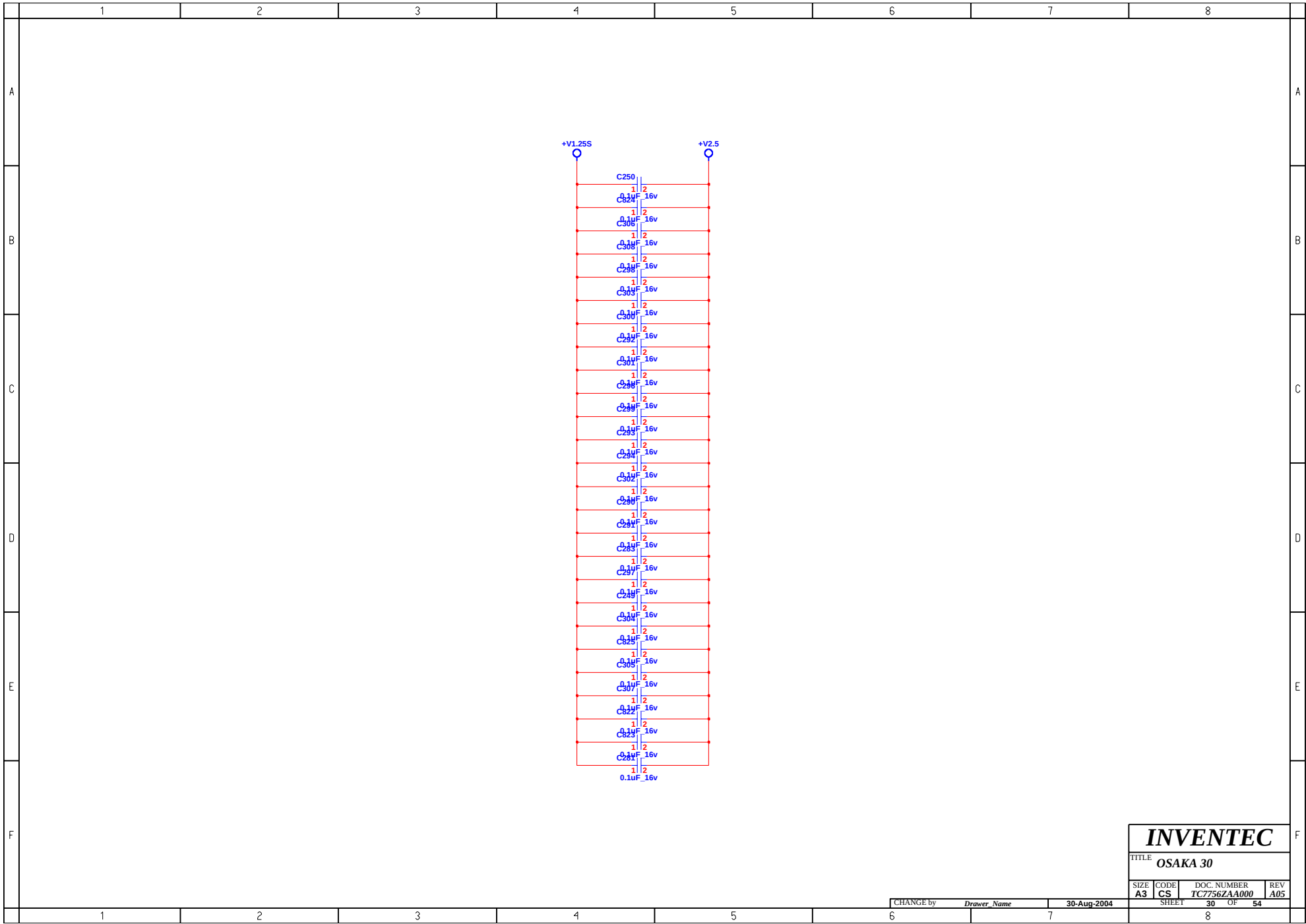
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INVENTEC			
TITLE OSAKA 30			
SIZE A3	CODE CS	DOC NUMBER TC7562AA000	REV A05
CHANGE by Drawer Name		30-Aug-2004	SHEET 29 OF 54



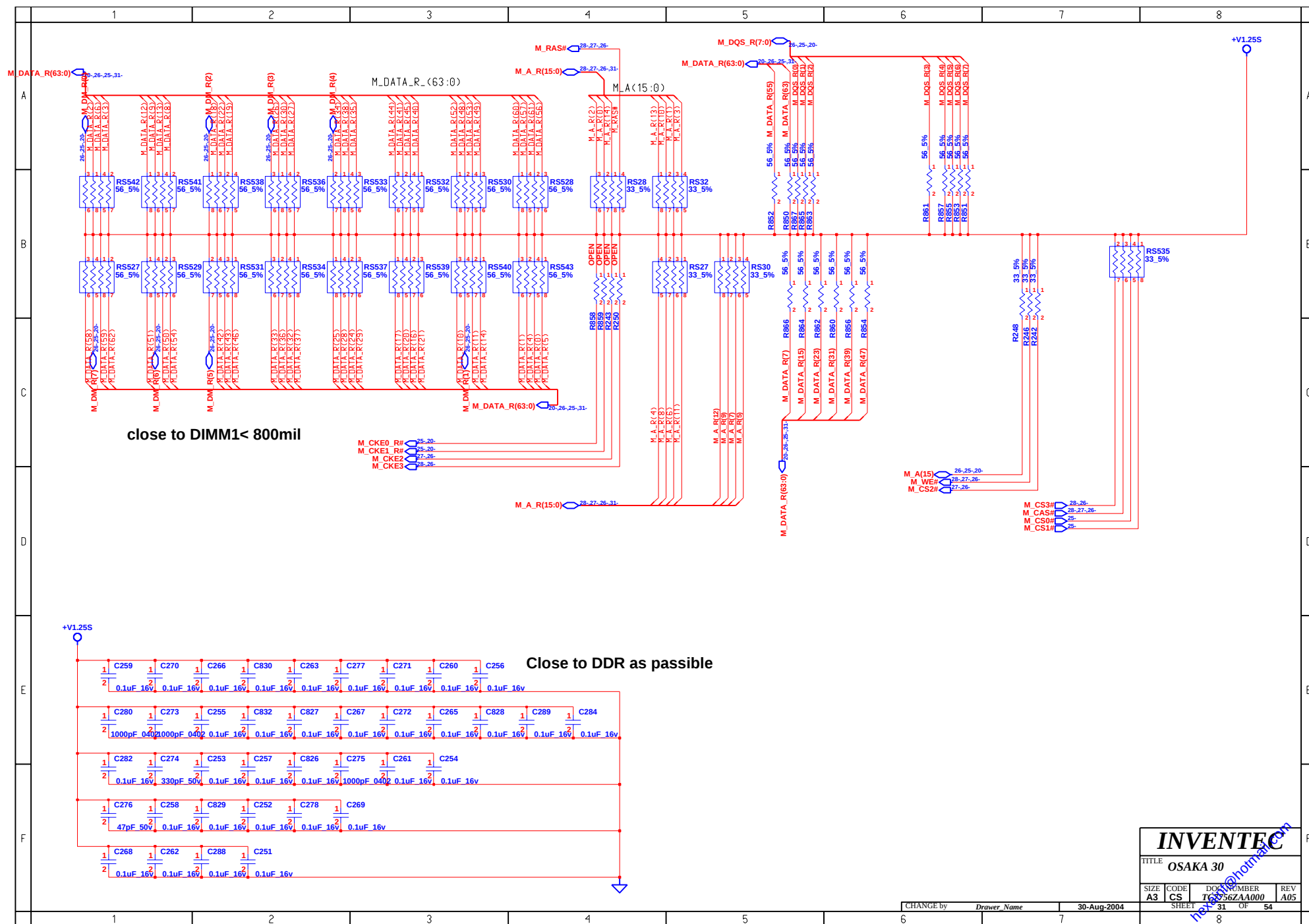
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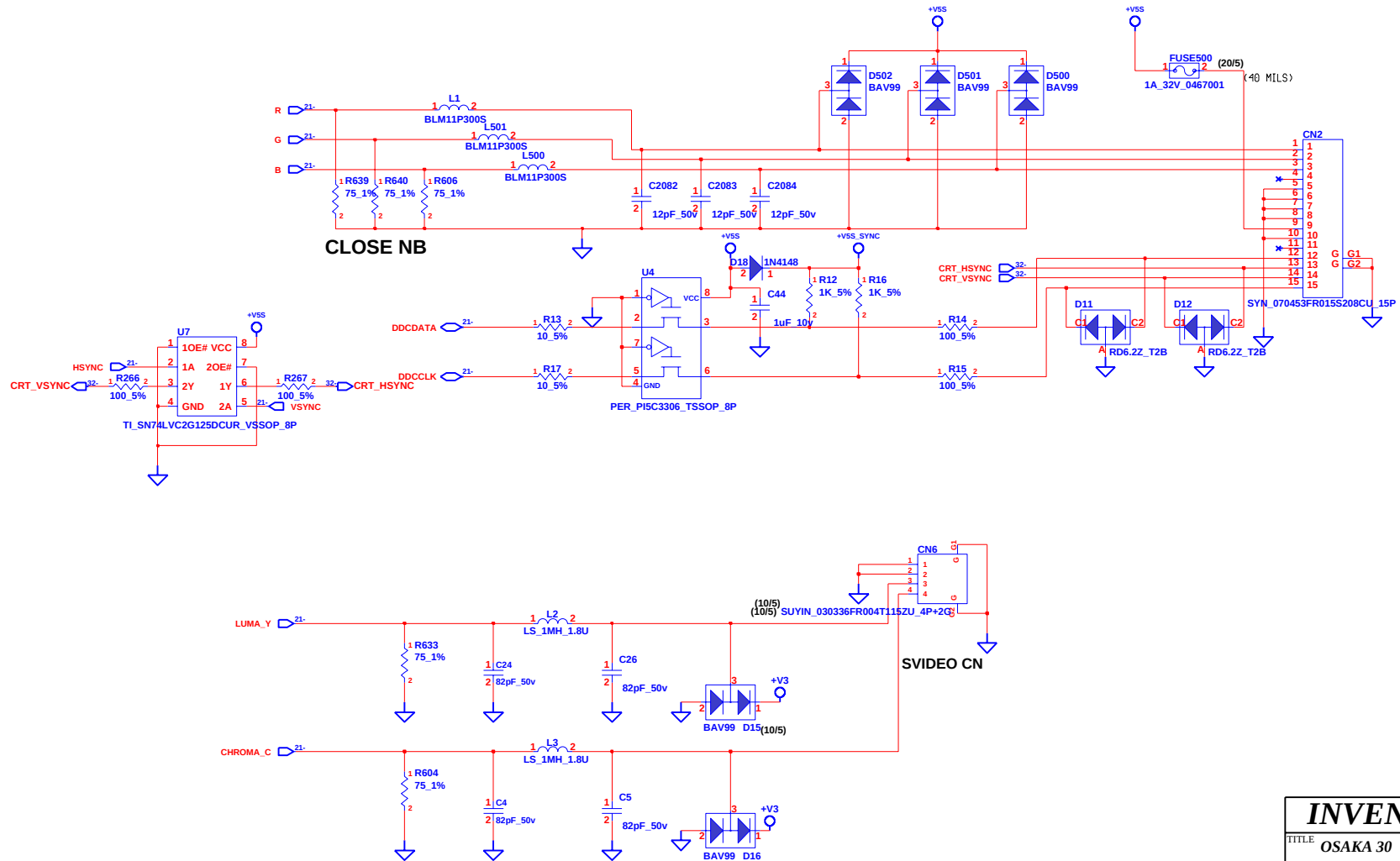
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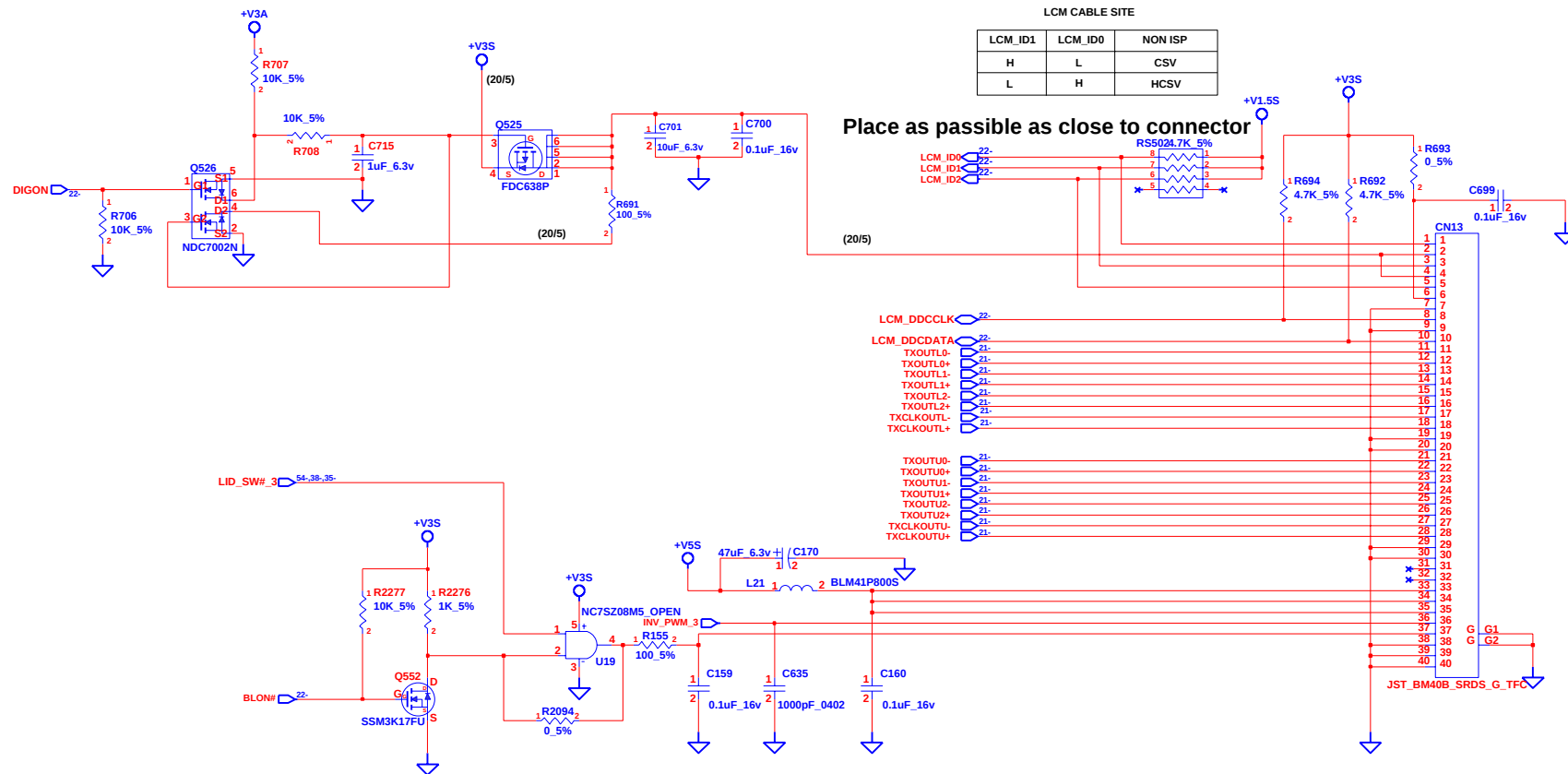
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INVENTEC			
TITLE OSAKA 30			
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LCM CABLE SITE

LCM_ID1	LCM_ID0	NON ISP
H	L	CSV
L	H	HCSV

Place as possible as close to connector

INVENTEC

TITLE
OSAKA 30

SIZE	CODE	DOC. NUMBER	REV
A3	CS	TC7552AA000	A05

CHANGE by *Drawer Name* 30-Aug-2004

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mailto:na@normal.com

Change IXP150 from version A44(6019A0219401)
to A45(6019A0219402) from CS1 BOM
to A46(PIN is the same as A45) from VP BOM

U522-1

A-LINK PULL UPS RESERVAL

ATI_IXP150_BGA_457P

PCI PULL UPS

INVENTEC

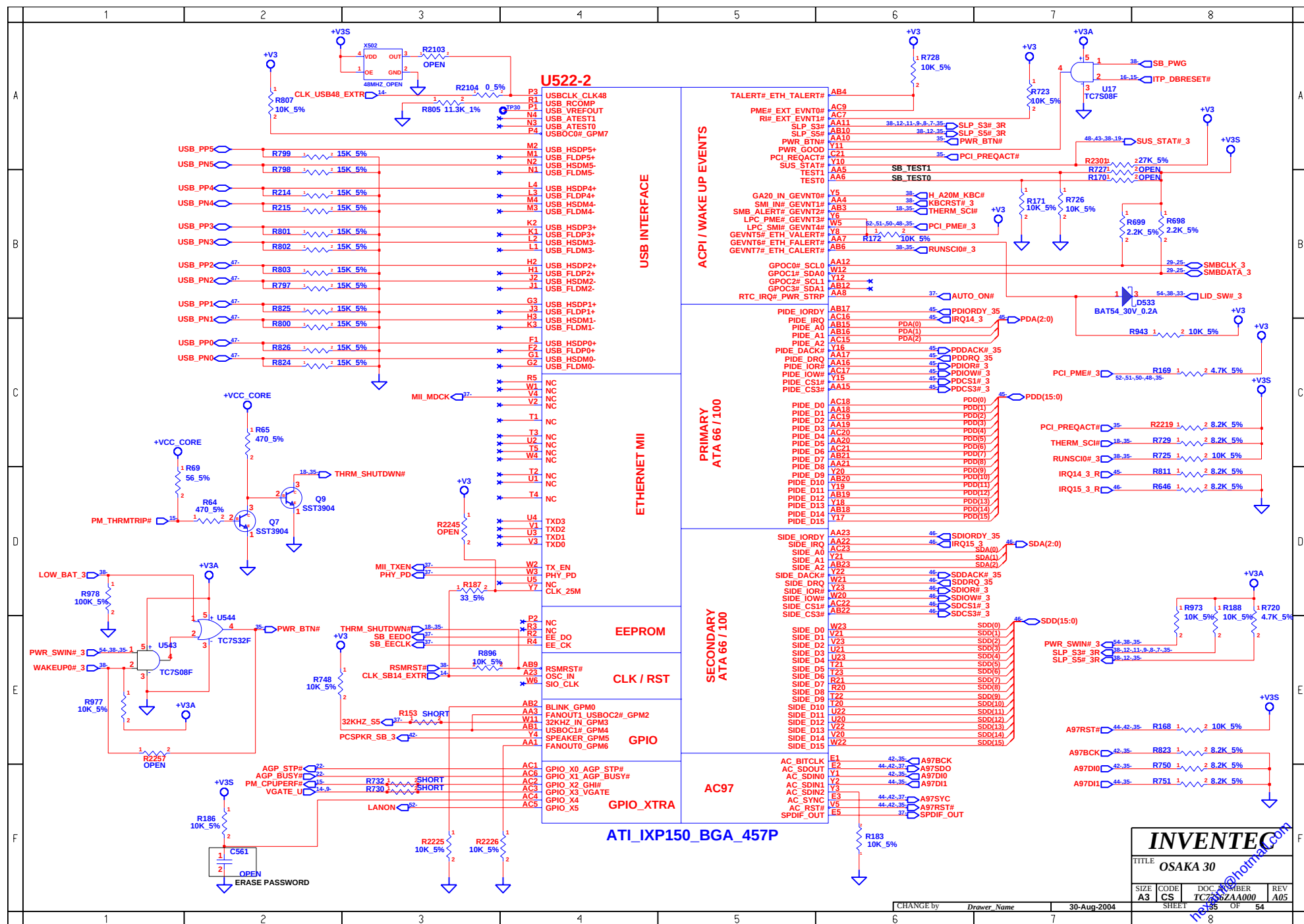
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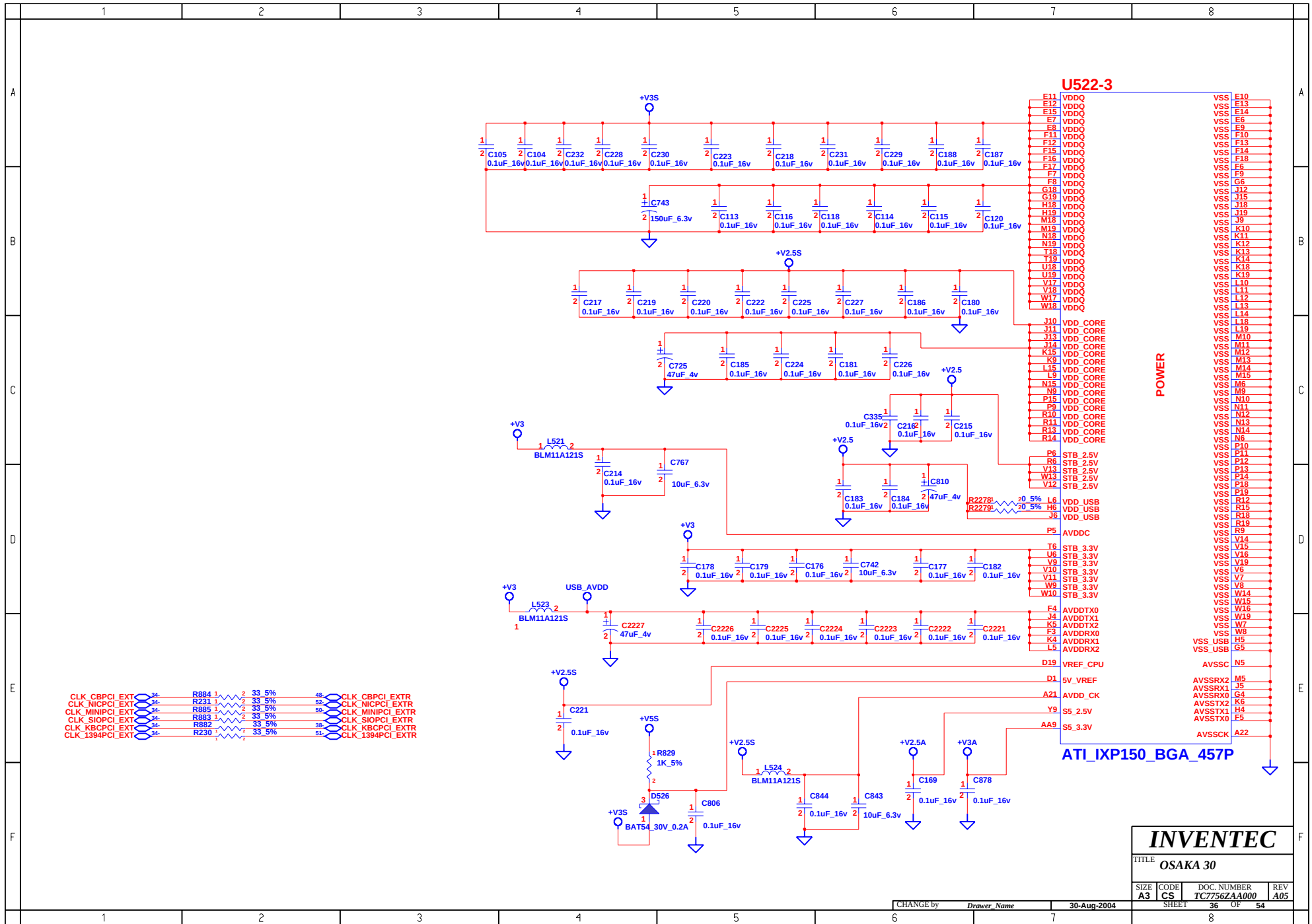
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A3 CS TC7756ZAA000 A05

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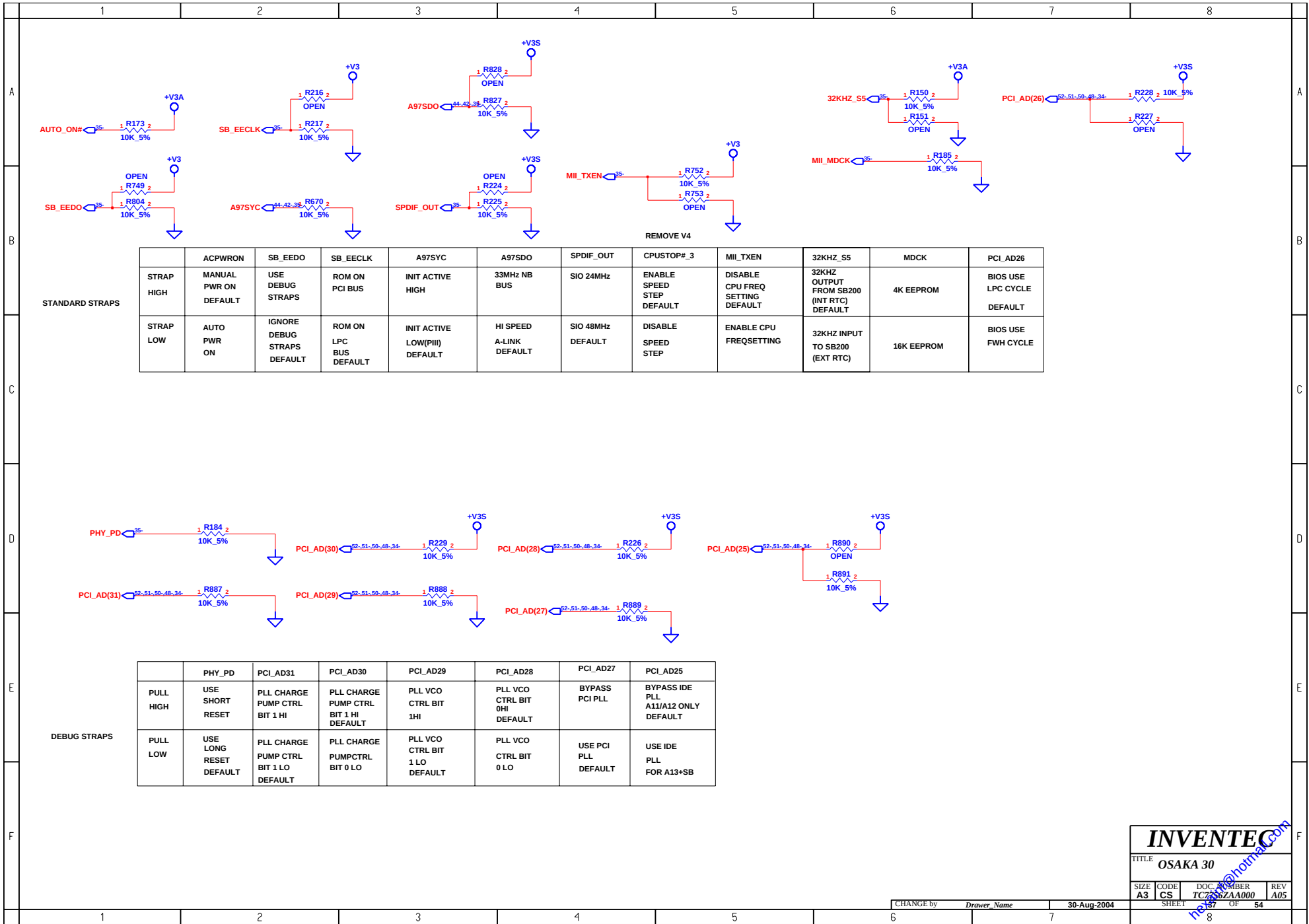
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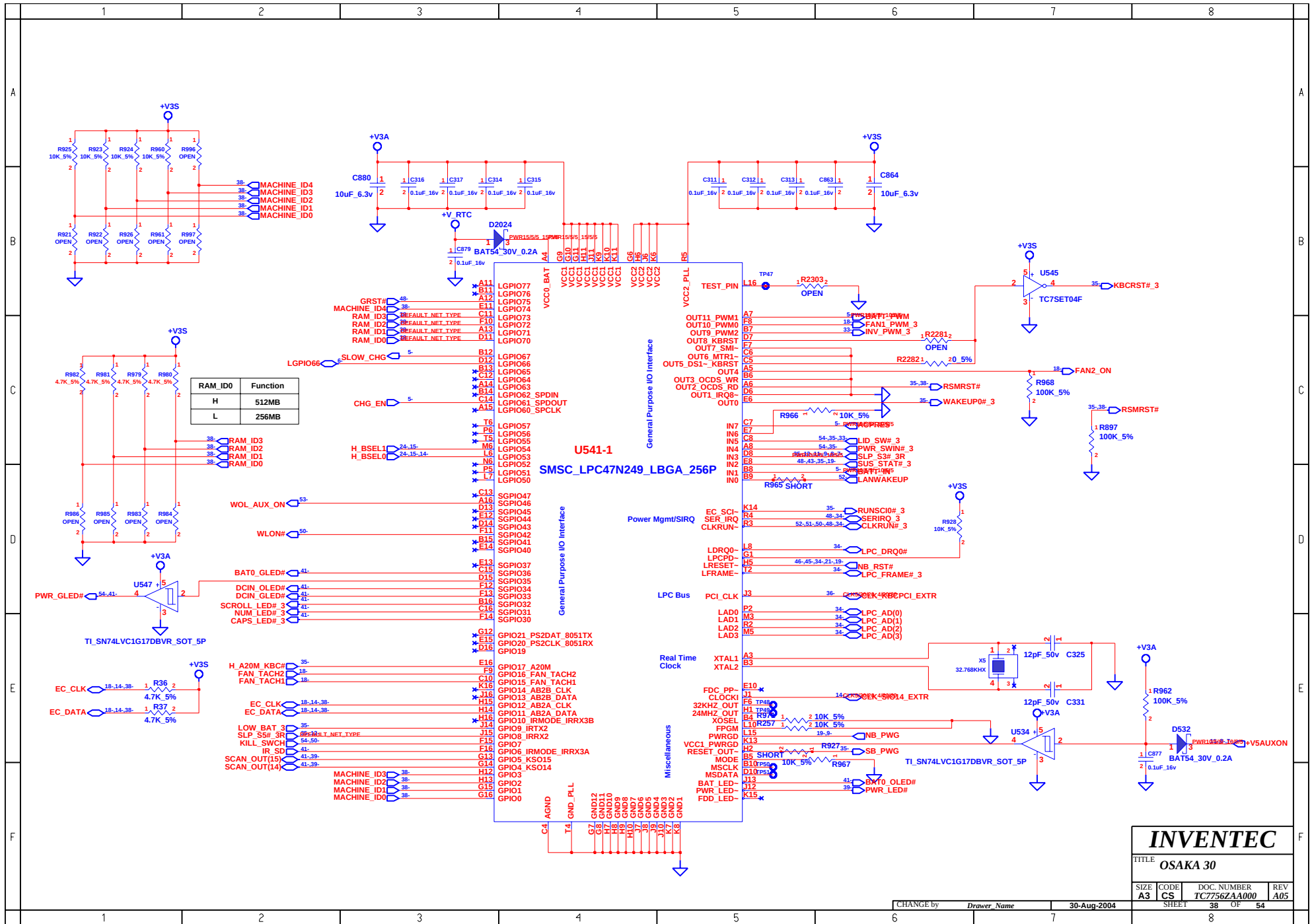
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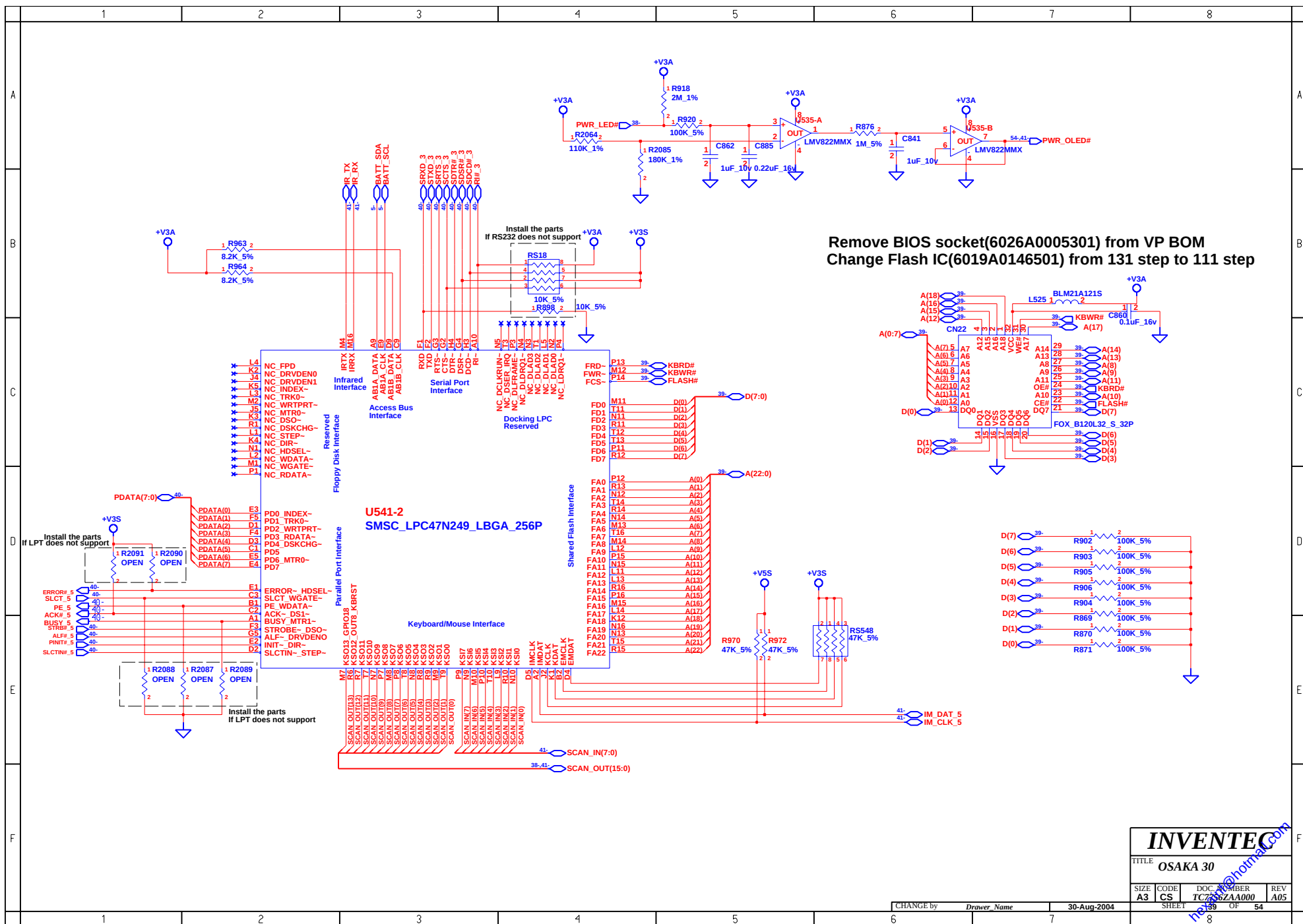
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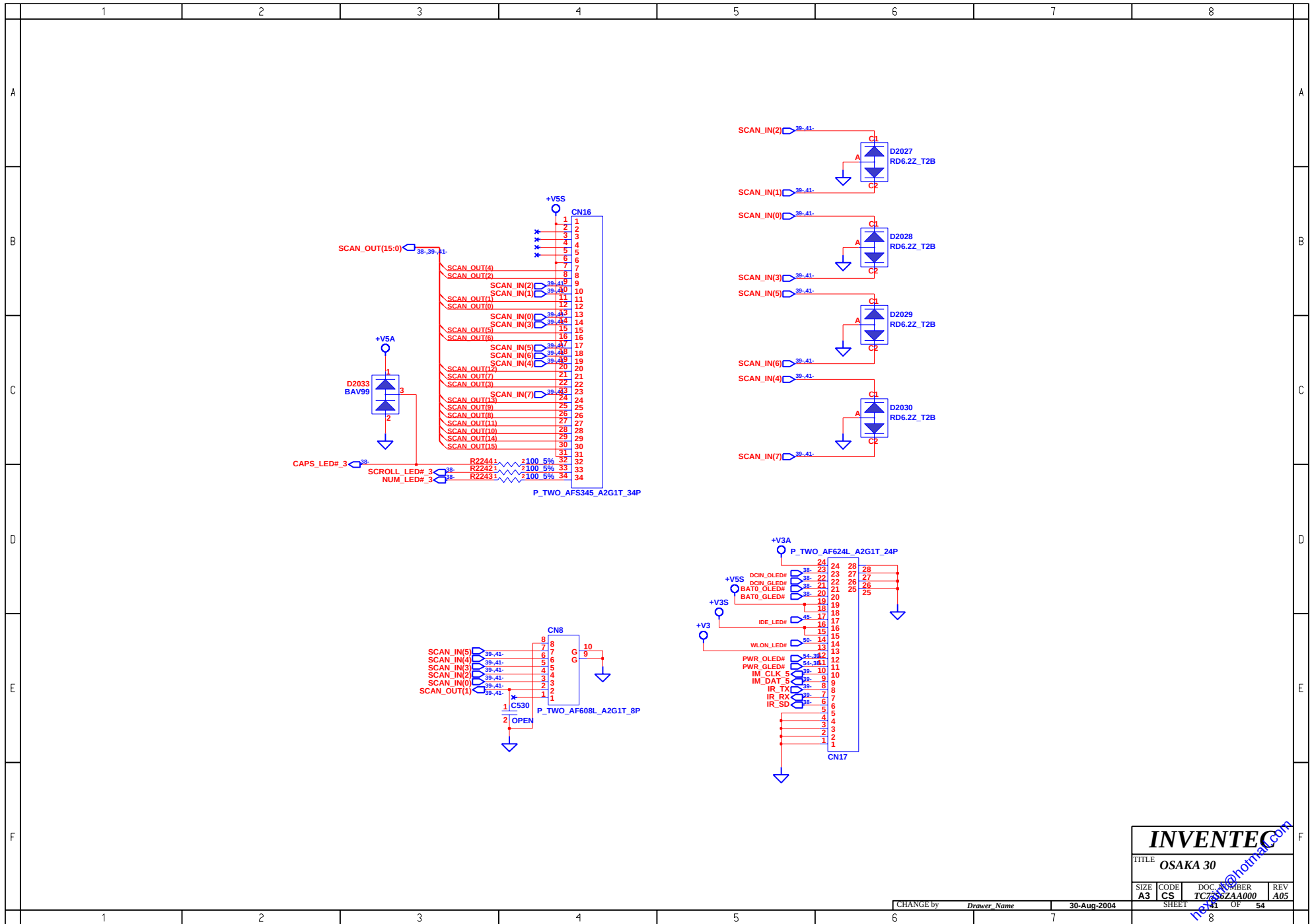
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A



C |

D

E

F

A

B

C

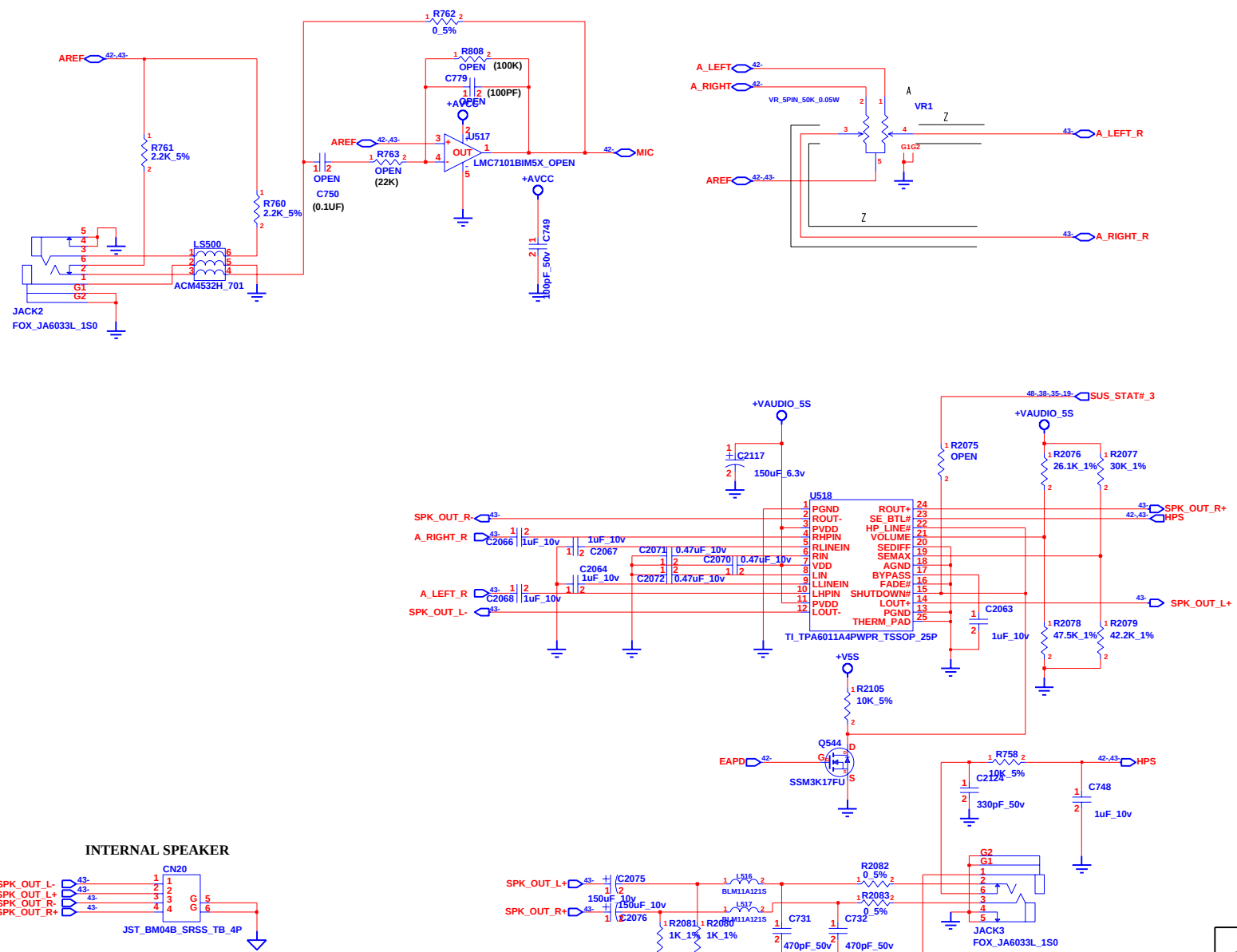
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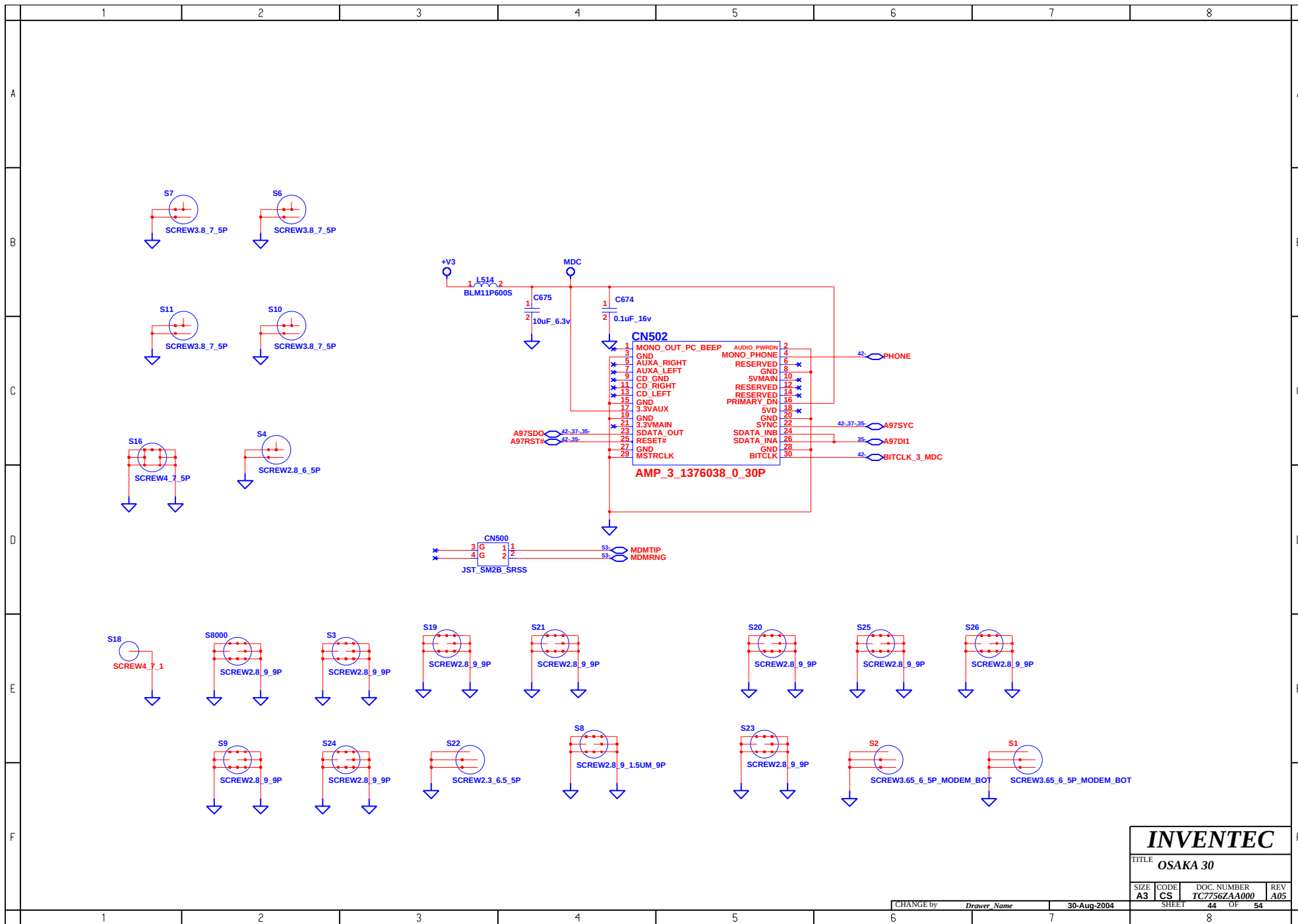
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INVENTEC			
TITLE OSAKA 30			
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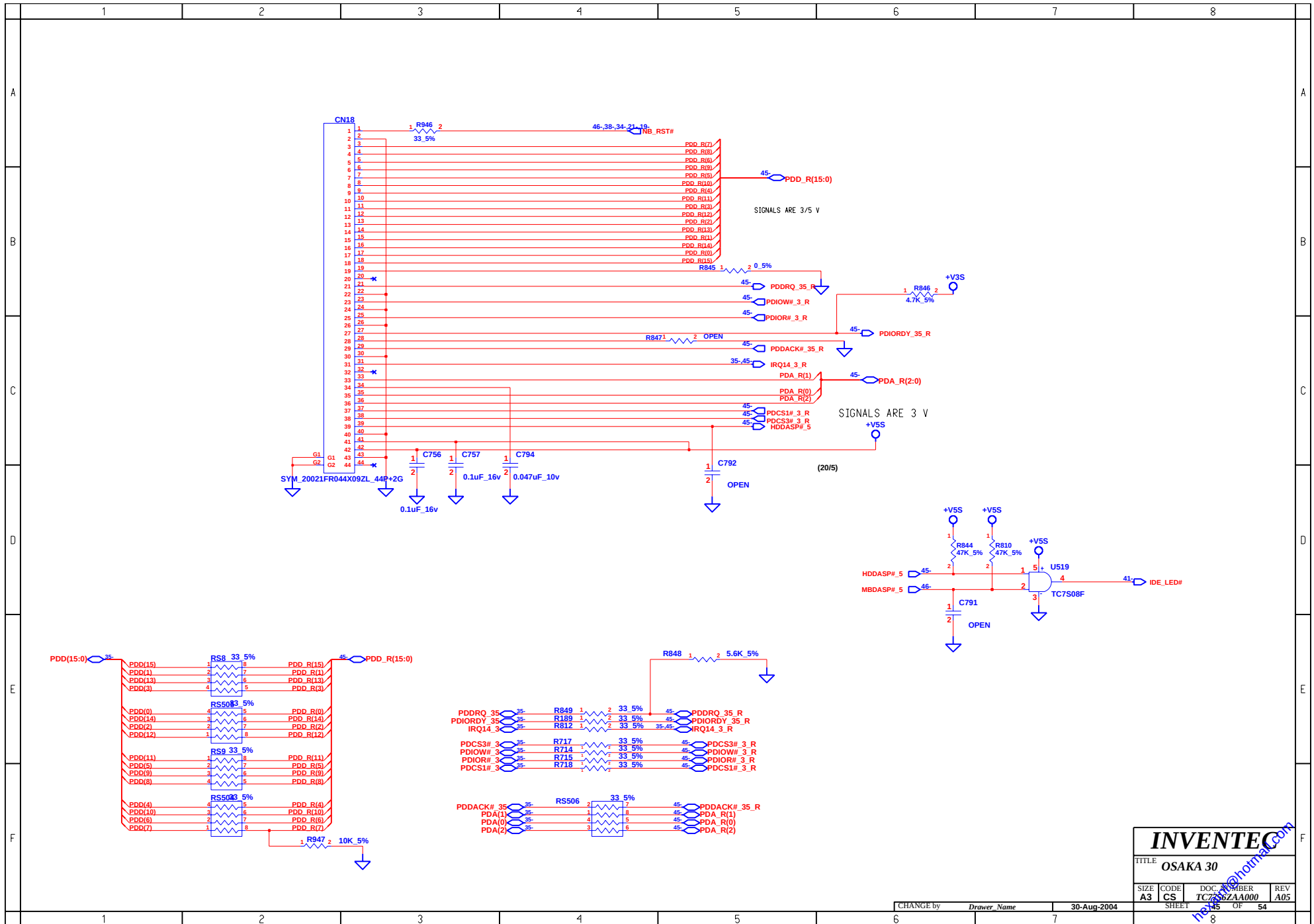
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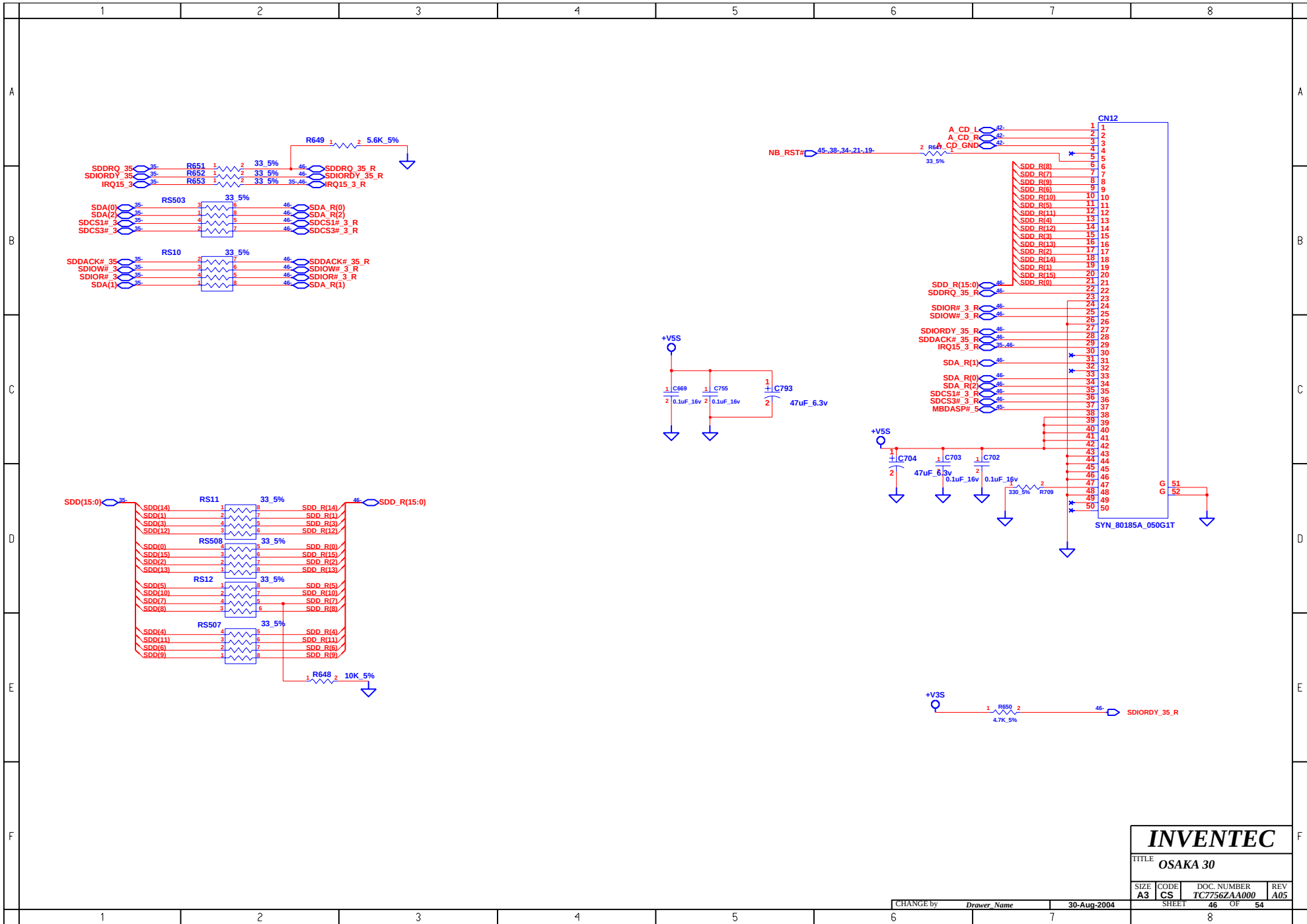
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OSAKA 30

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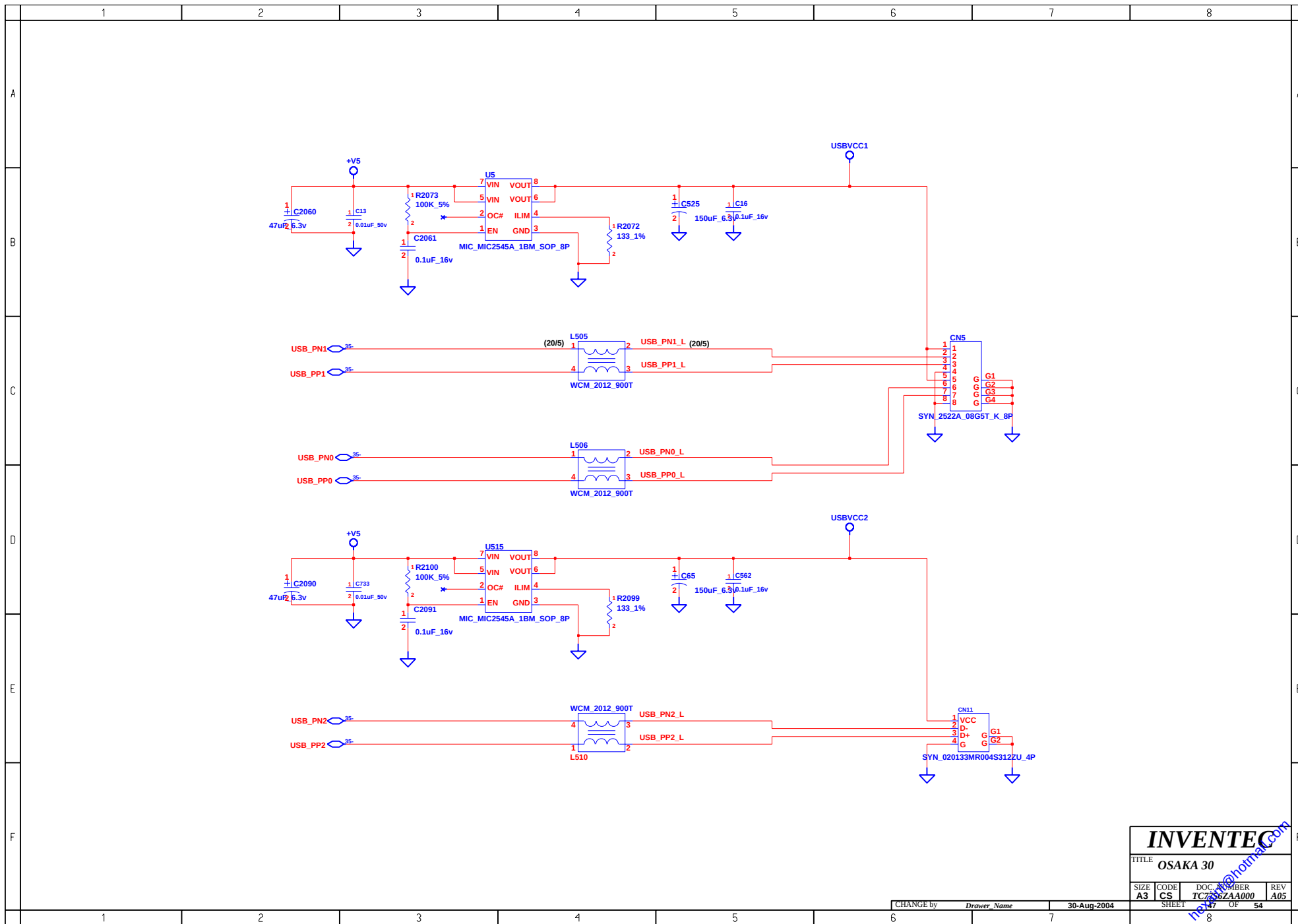
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OSAKA 30

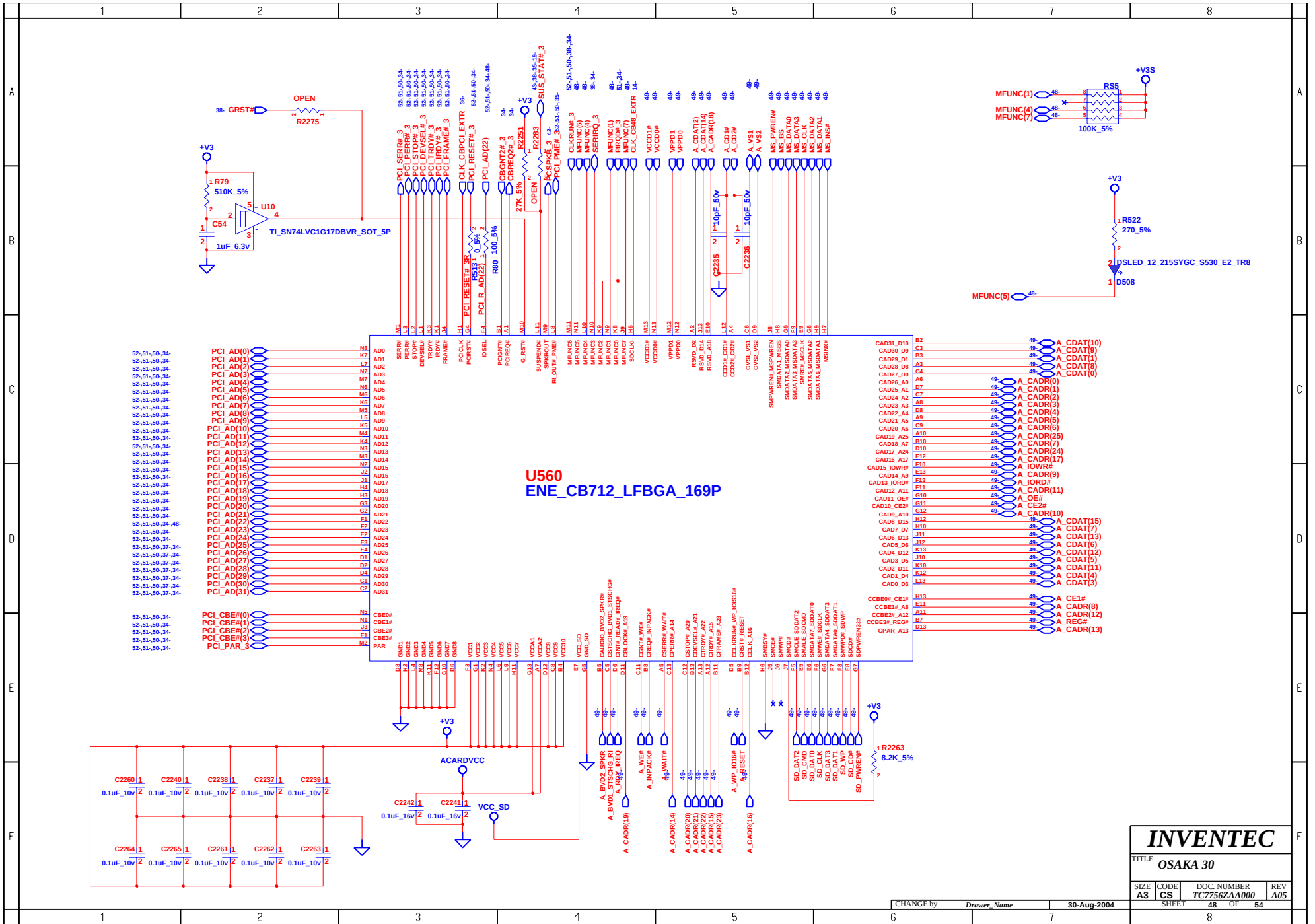
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A3	CS	TC7756ZAA000	A05

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TITLE OSAKA 30			
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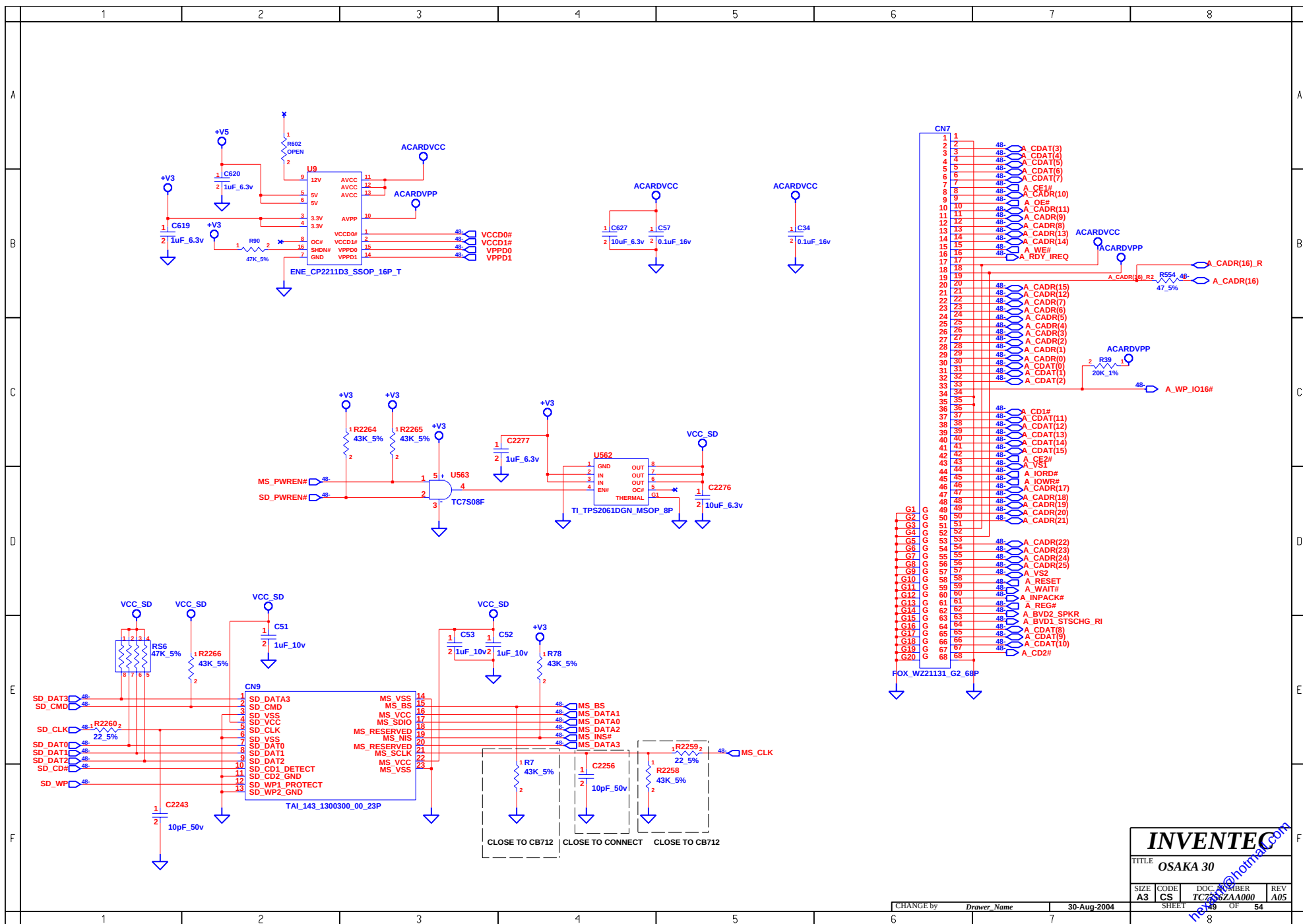
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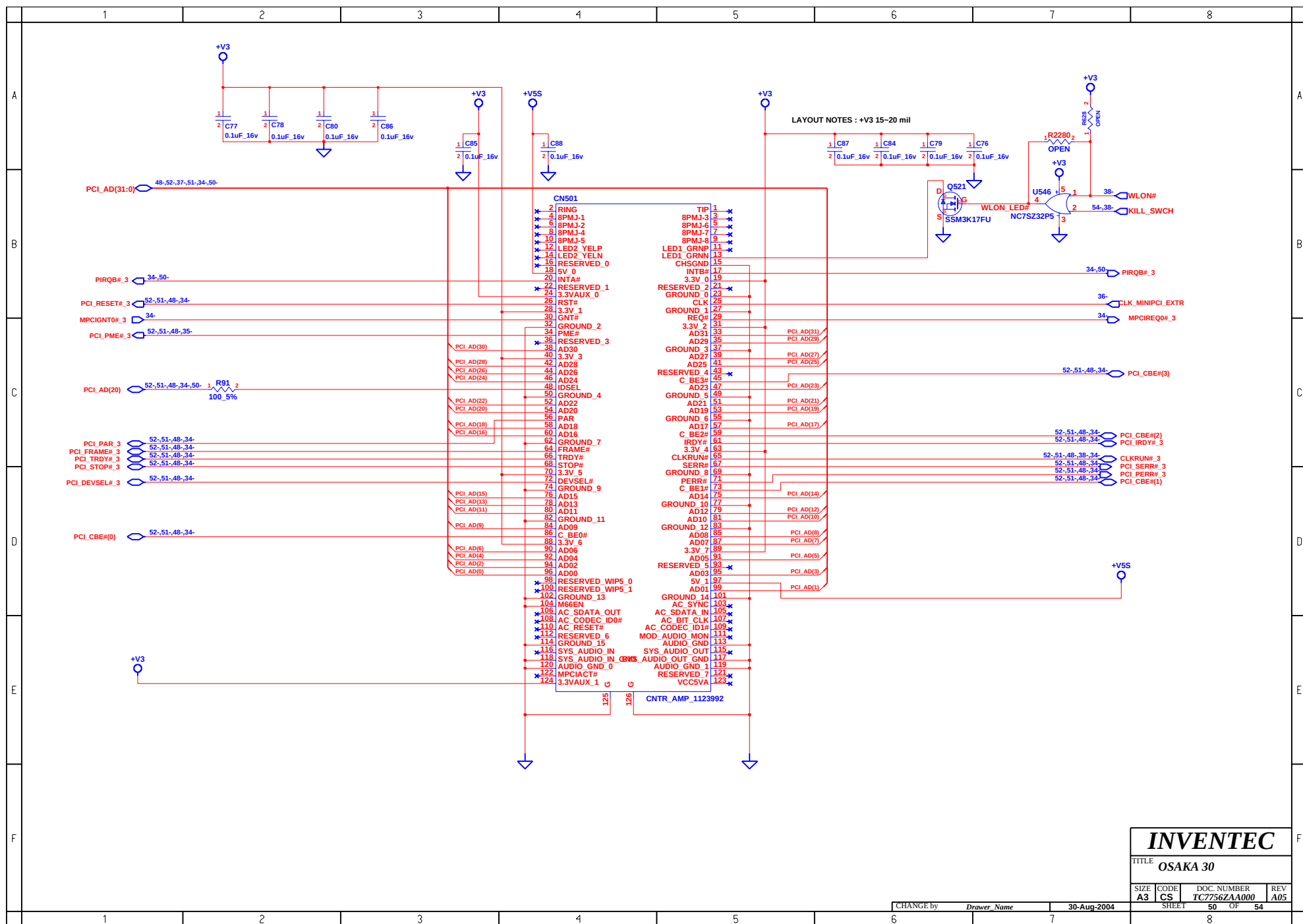
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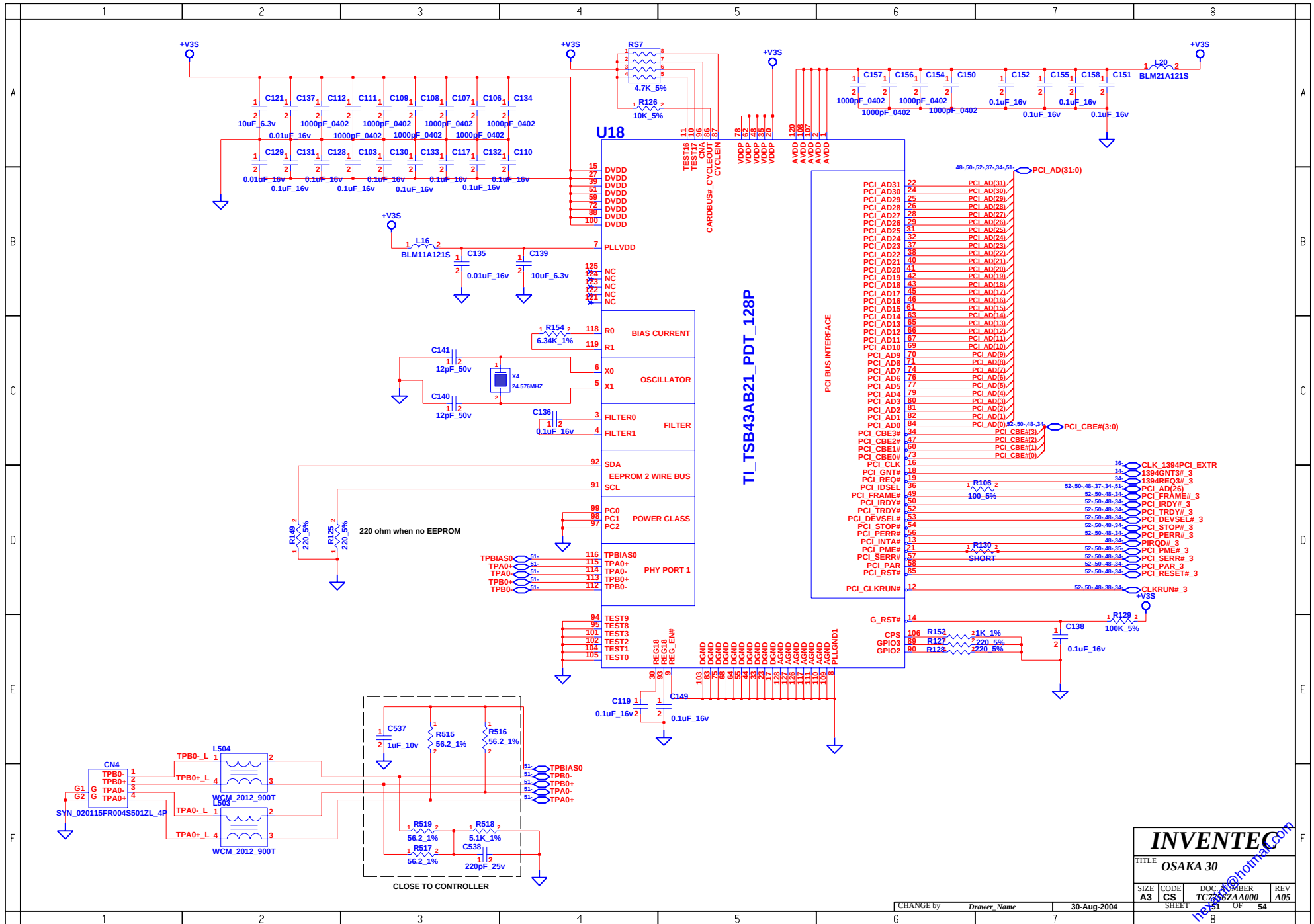
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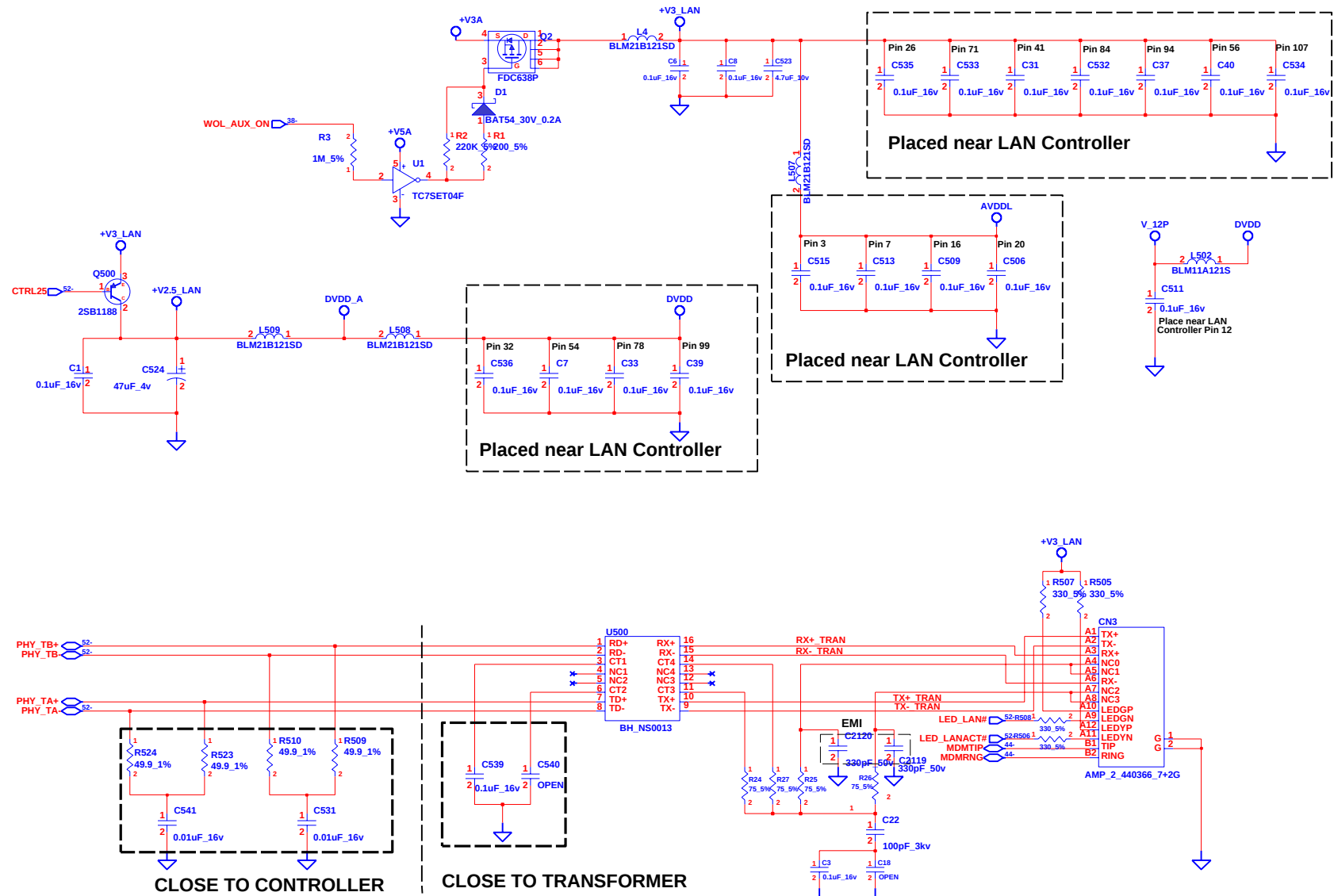
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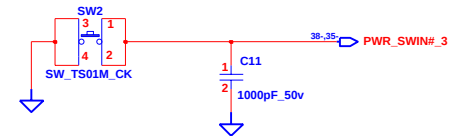




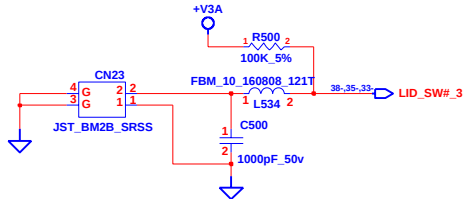




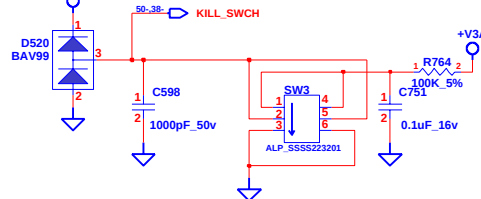
POWER SWITCH



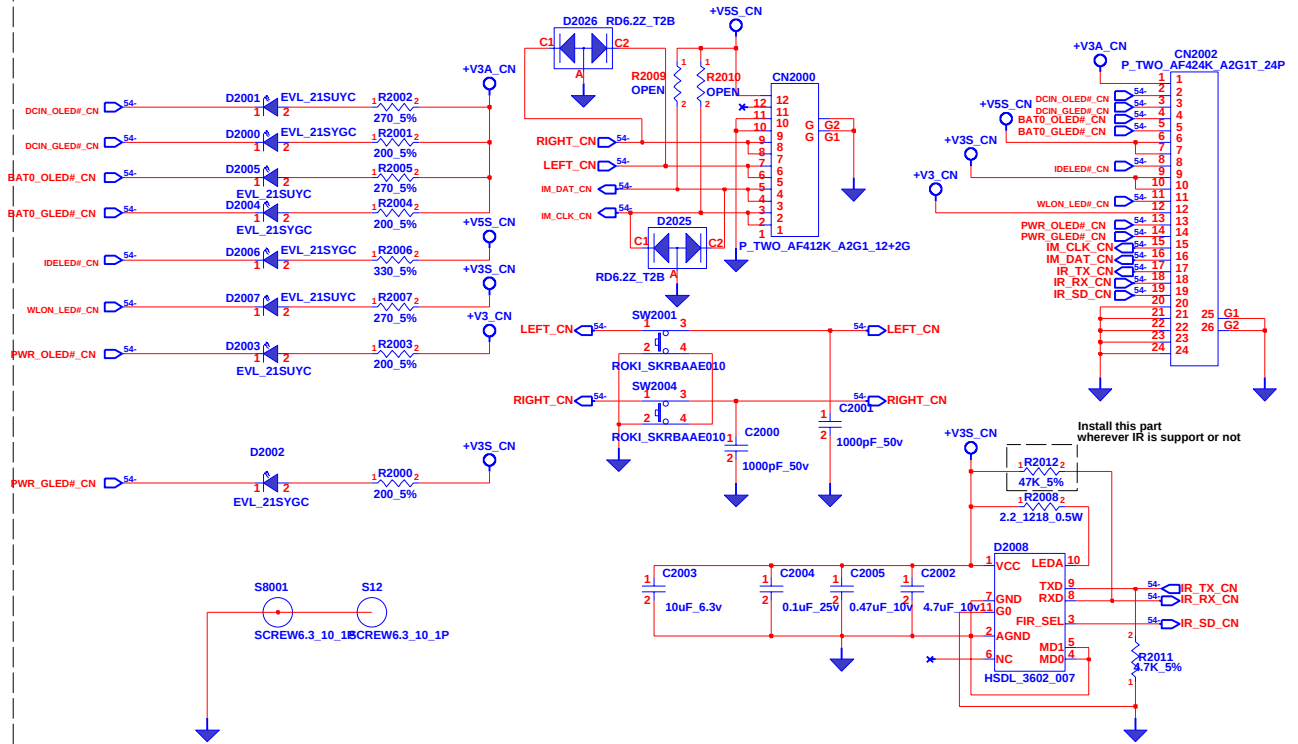
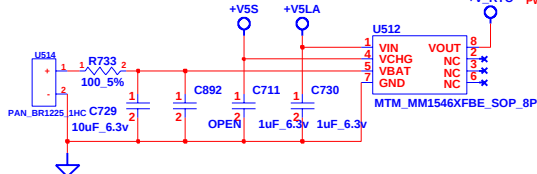
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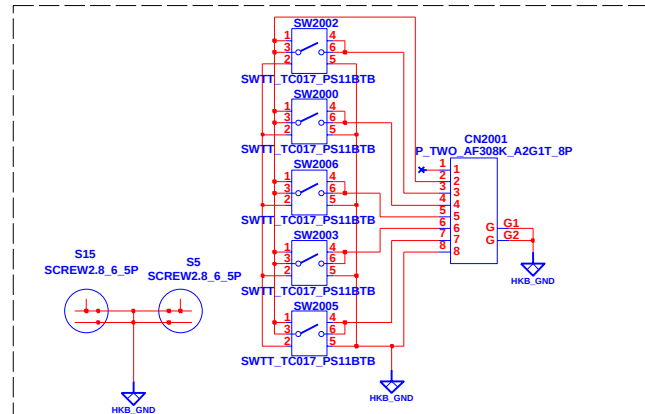
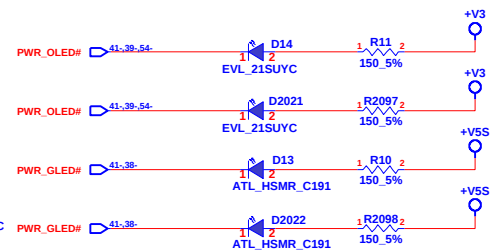
KILL SWITCH



RTC BATTERY



LED & TOUCHPAD BUTTON BOARD



HOTKEY BOARD

INVENTEC

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