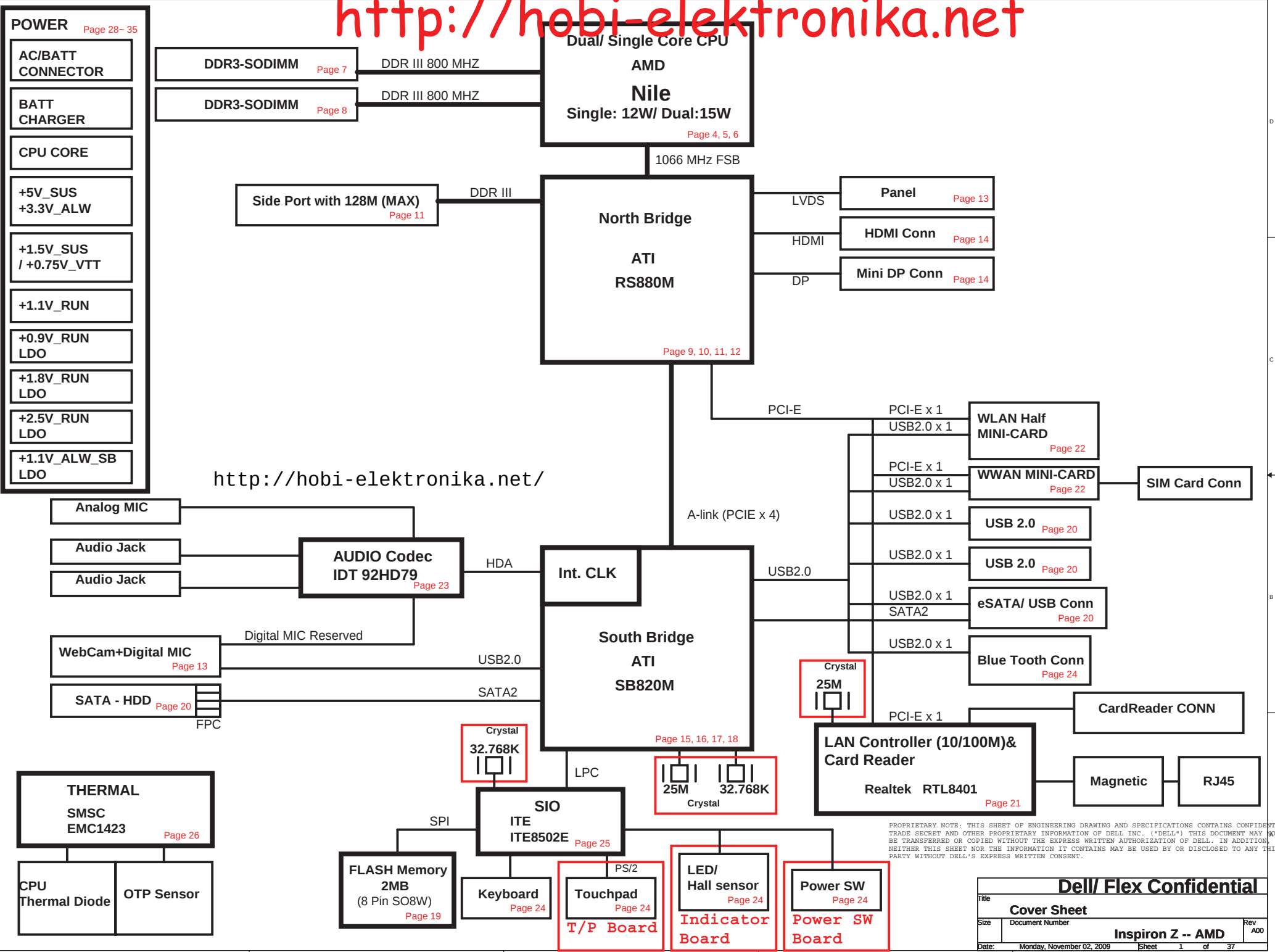
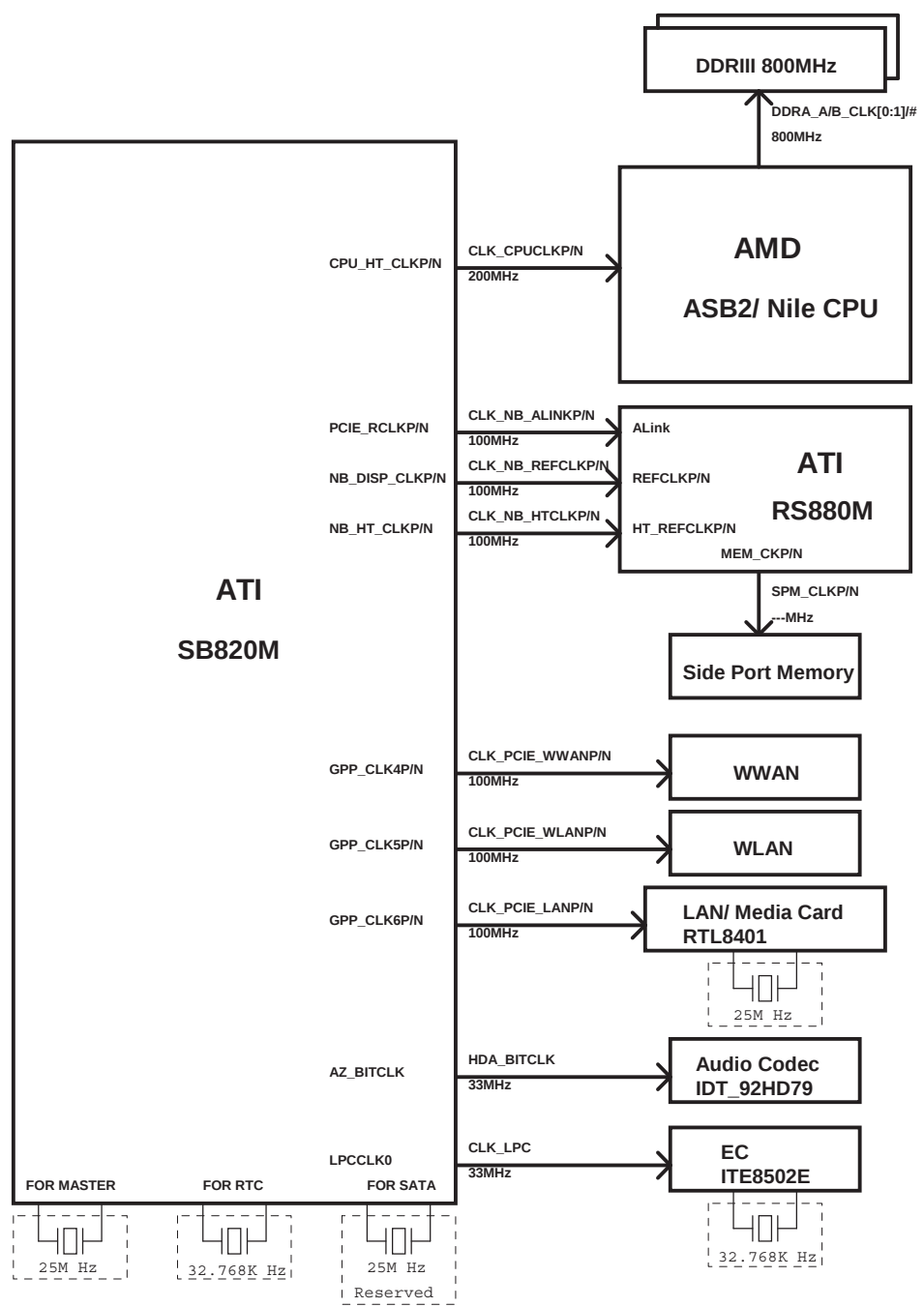




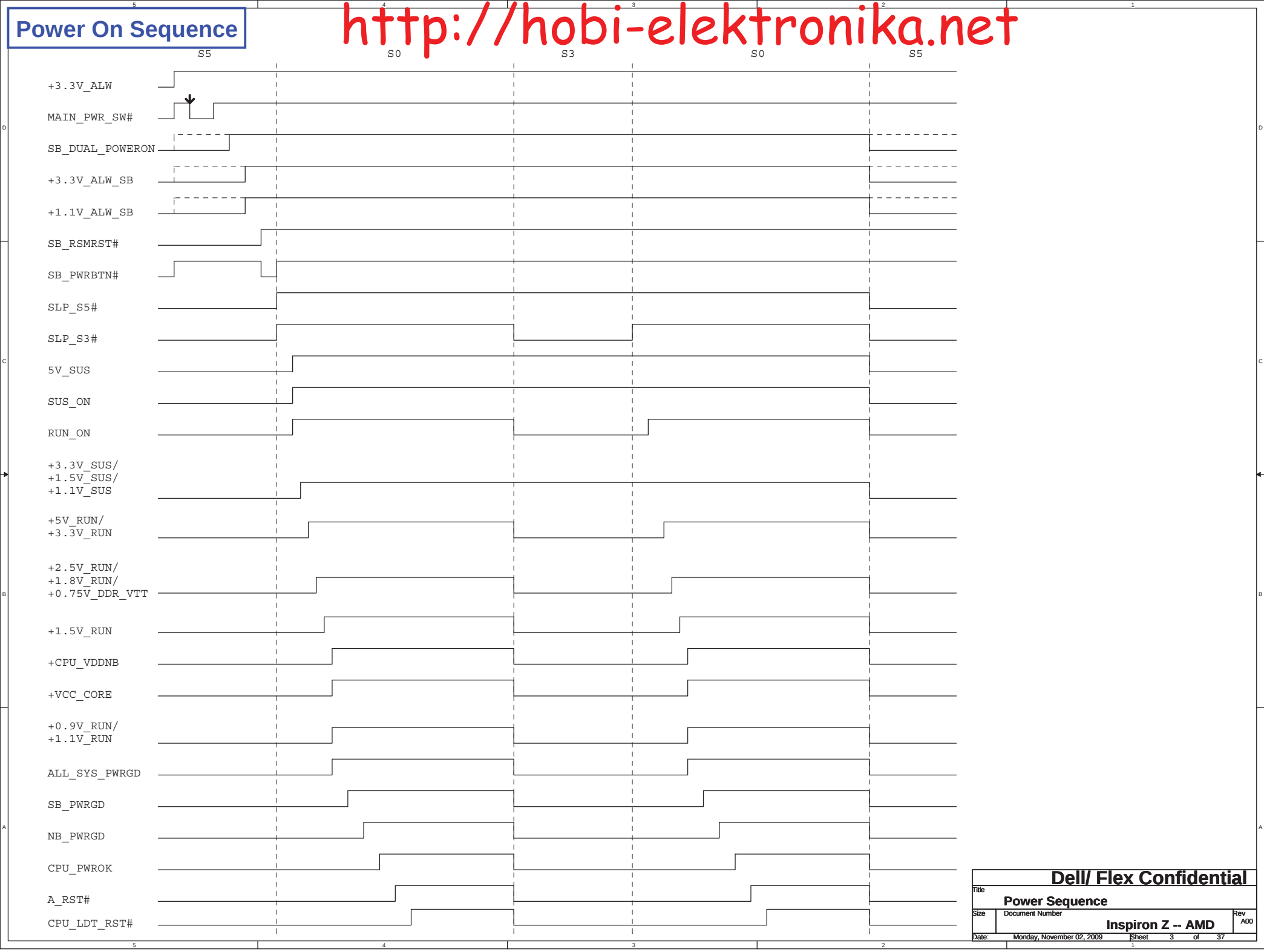
PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION, NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.

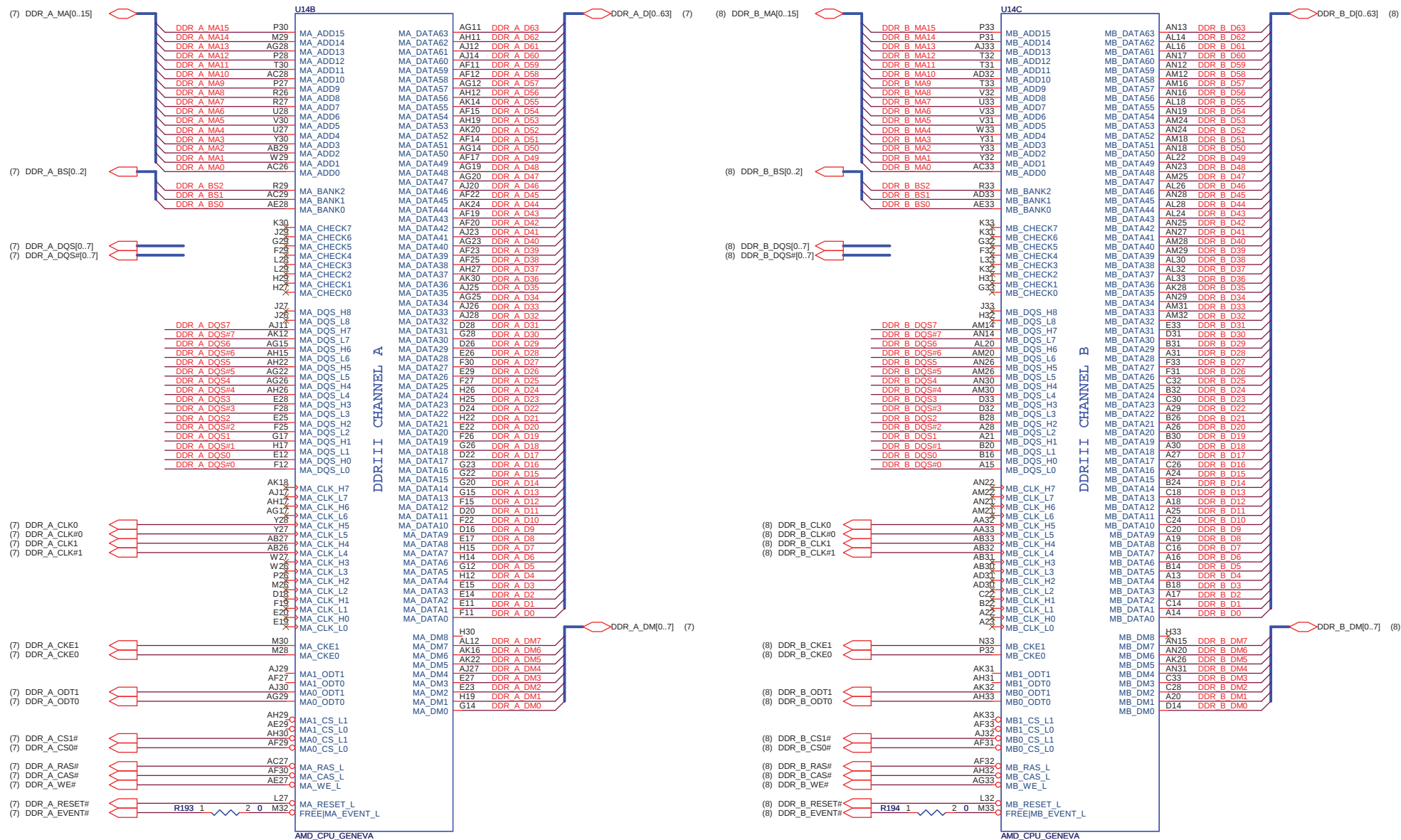


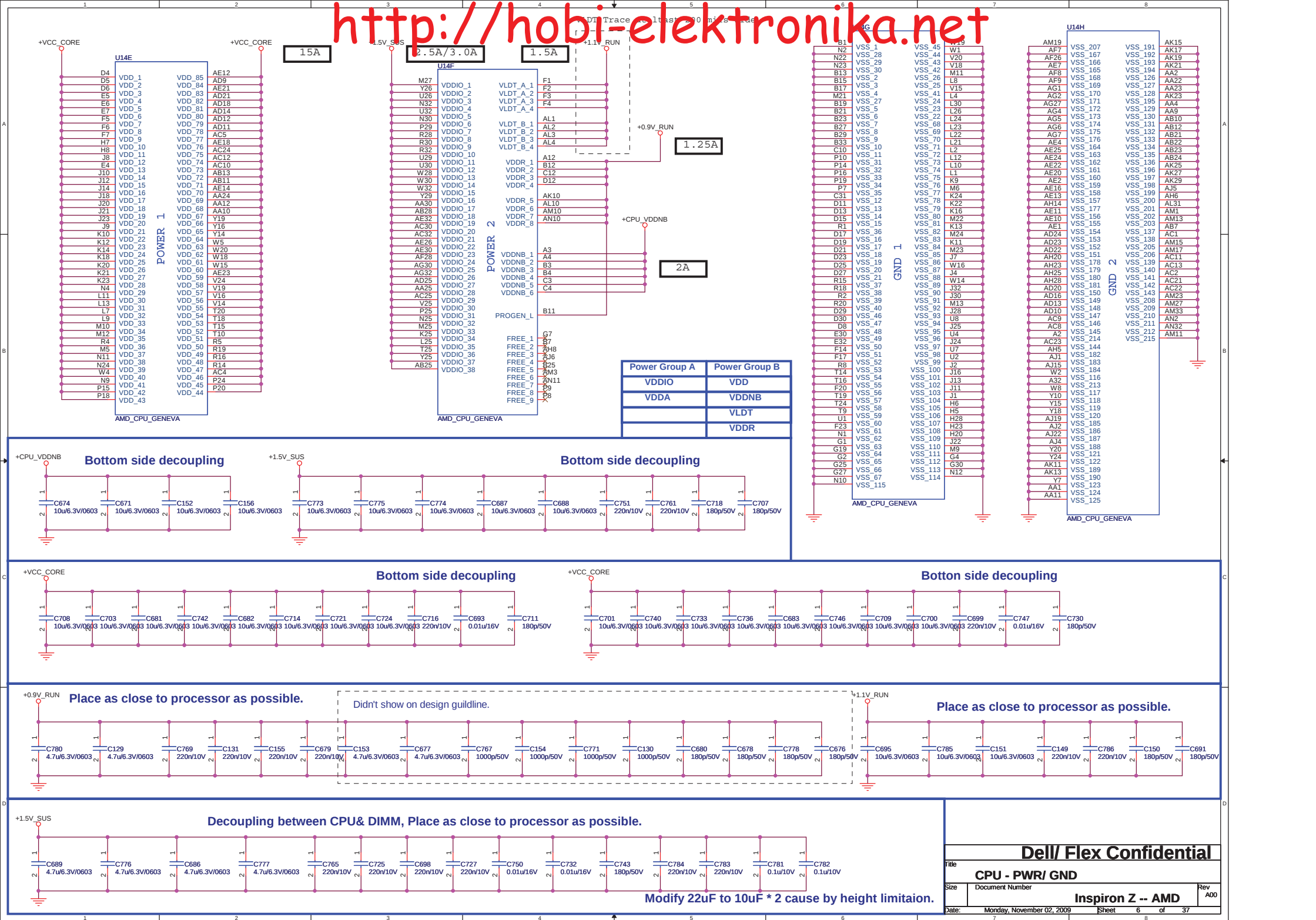
Power States							
Power Rail	Control Signal	S0	S3	S4	S5	G3/ AC-in	G3/ Battery Only
+PWR_SRC	N/A	V	V	V	V		
+0.75V_DDR_VTT	RUN_ON	V					
+0.9V_RUN	RUN_ON	V					
+1.1V_ALW_SB	SB_DUAL_PWRON	V	V	V	V		
+1.1V_SUS	SUS_ON	V	V				
+1.1V_RUN	RUN_ON	V					
+1.5V_SUS	SUS_ON	V	V				
+1.5V_RUN	RUN_ON	V					
+1.8V_RUN	RUN_ON	V					
+2.5V_RUN	RUN_ON	V					
+3.3V_ALW	+3.3V_EN2	V	V	V	V		
+3.3V_ALW_SB	SB_DUAL_PWRON	V	V	V			
+3.3V_SUS	SUS_ON	V	V				
+3.3V_RUN	RUN_ON	V					
+3.3V_KBVCC	N/A	V	V	V	V		
+5V_LDO	N/A	V	V	V	V		
+5V_SUS	5V_SUS_ON	V	V				
+5V_RUN	RUN_ON	V					
+GFX_PWR_SRC	N/A	V					
+LCDVCC	LCD_DIGON	V					
+CPU_VDDNB	2.5V_PWRGD	V					
+VCC_CORE	2.5V_PWRGD	V					
+RTC_CELL	N/A	V	V	V	V	V	V
+USB_RIGHT_PWR	USB_EN#	V	V				
+USB_LEFT_PWR	USB_EN#	V	V				

Power On Sequence

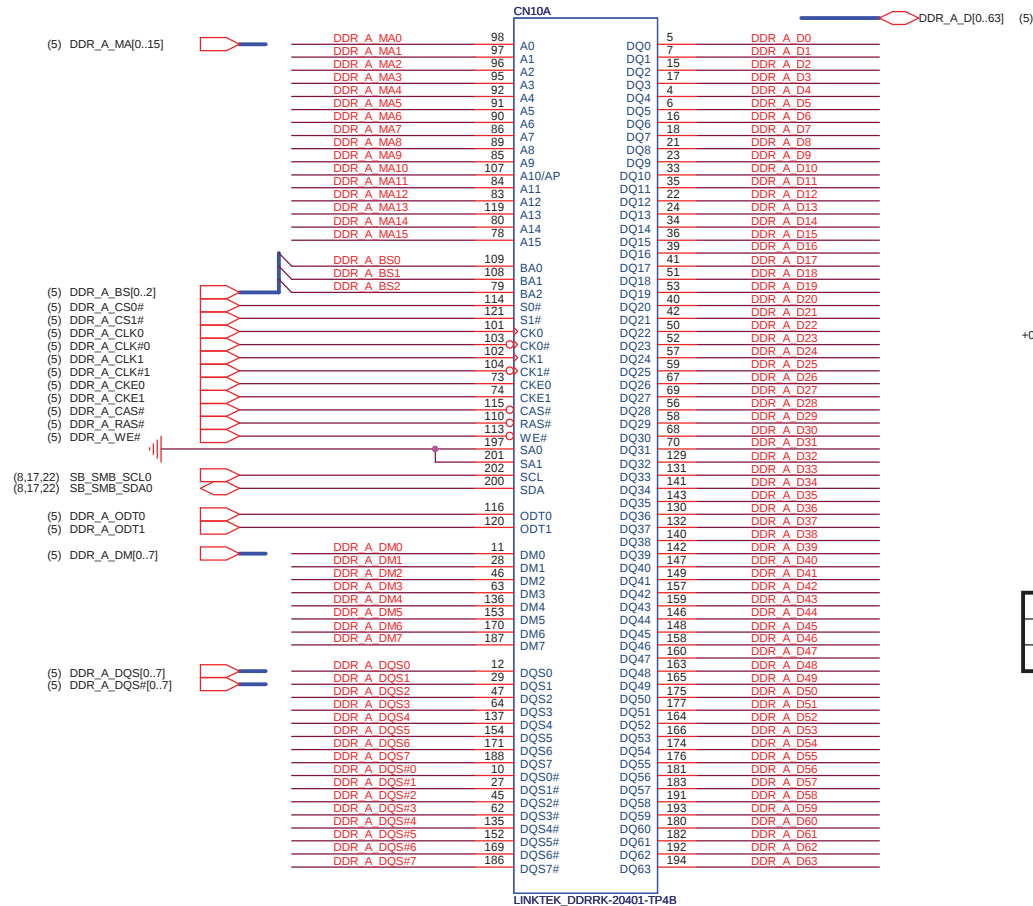
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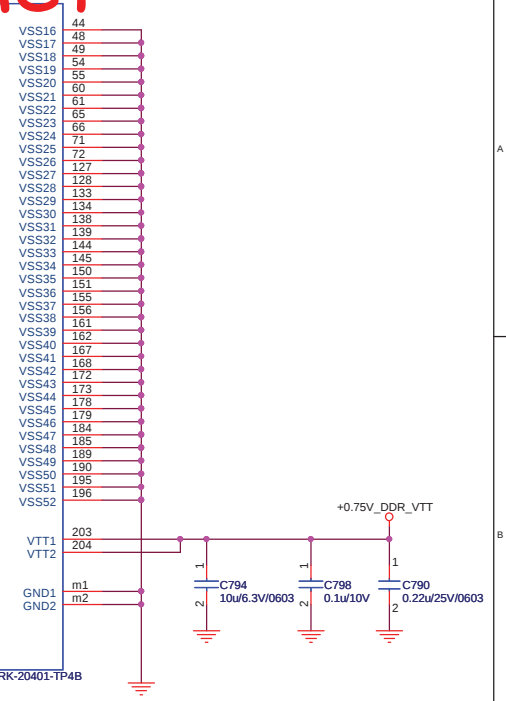
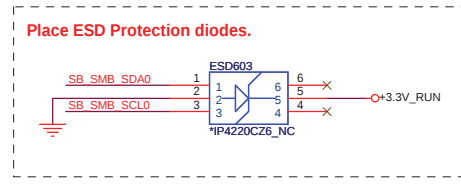
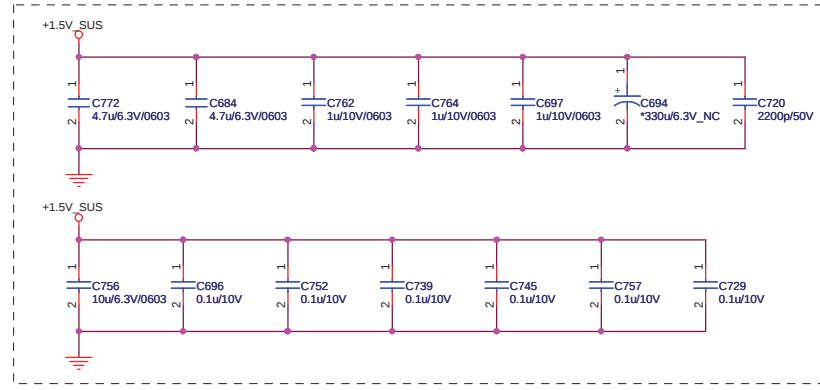
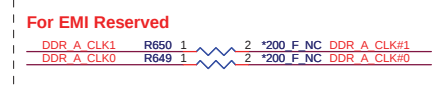




DDR3 DIMM A

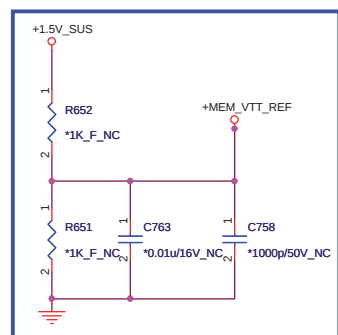
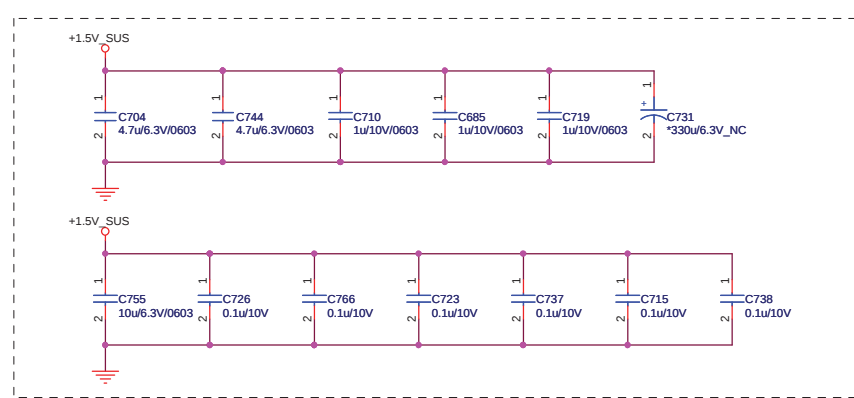
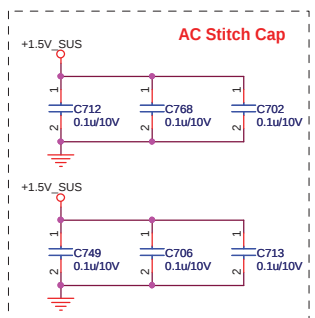
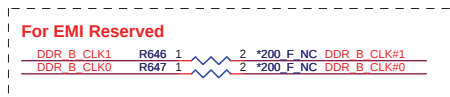
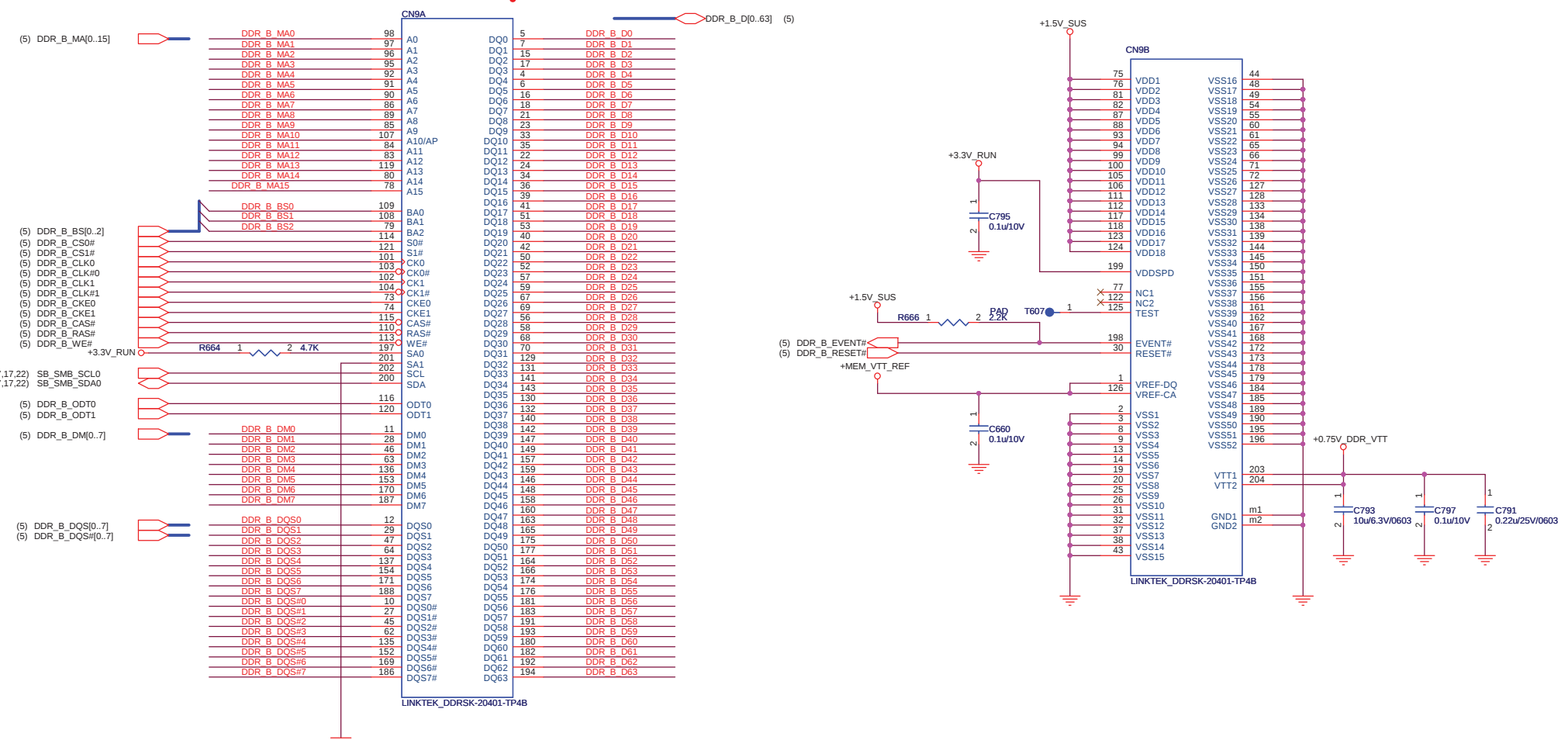


SM_MEM BUS ADDRESS	
SO-DIMM0	1010 000
SO-DIMM1	1010 001



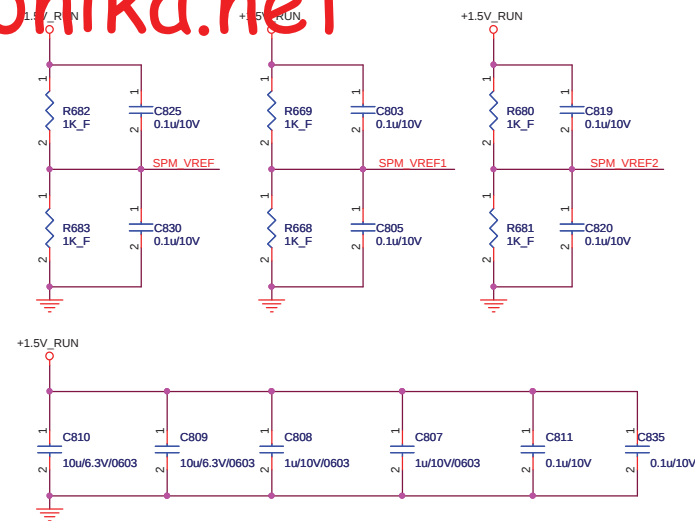
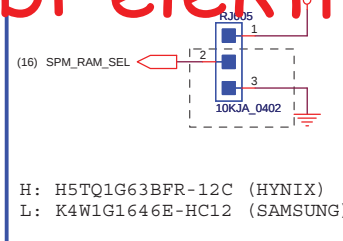
DDRIII DIMM B

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RS880M H/W STRAPS

Enables the Test Debug Bus using GPIO.

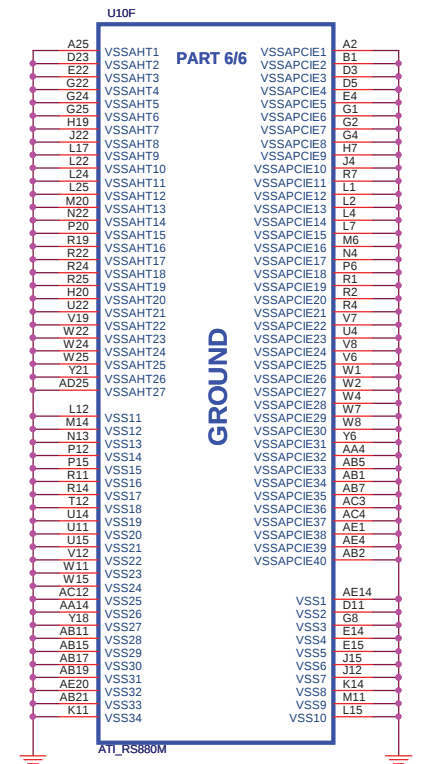
(10) CRT_VSYNC 

Selects Loading of STRAPS from EPROM.

(10) SUS_STAT#_R 

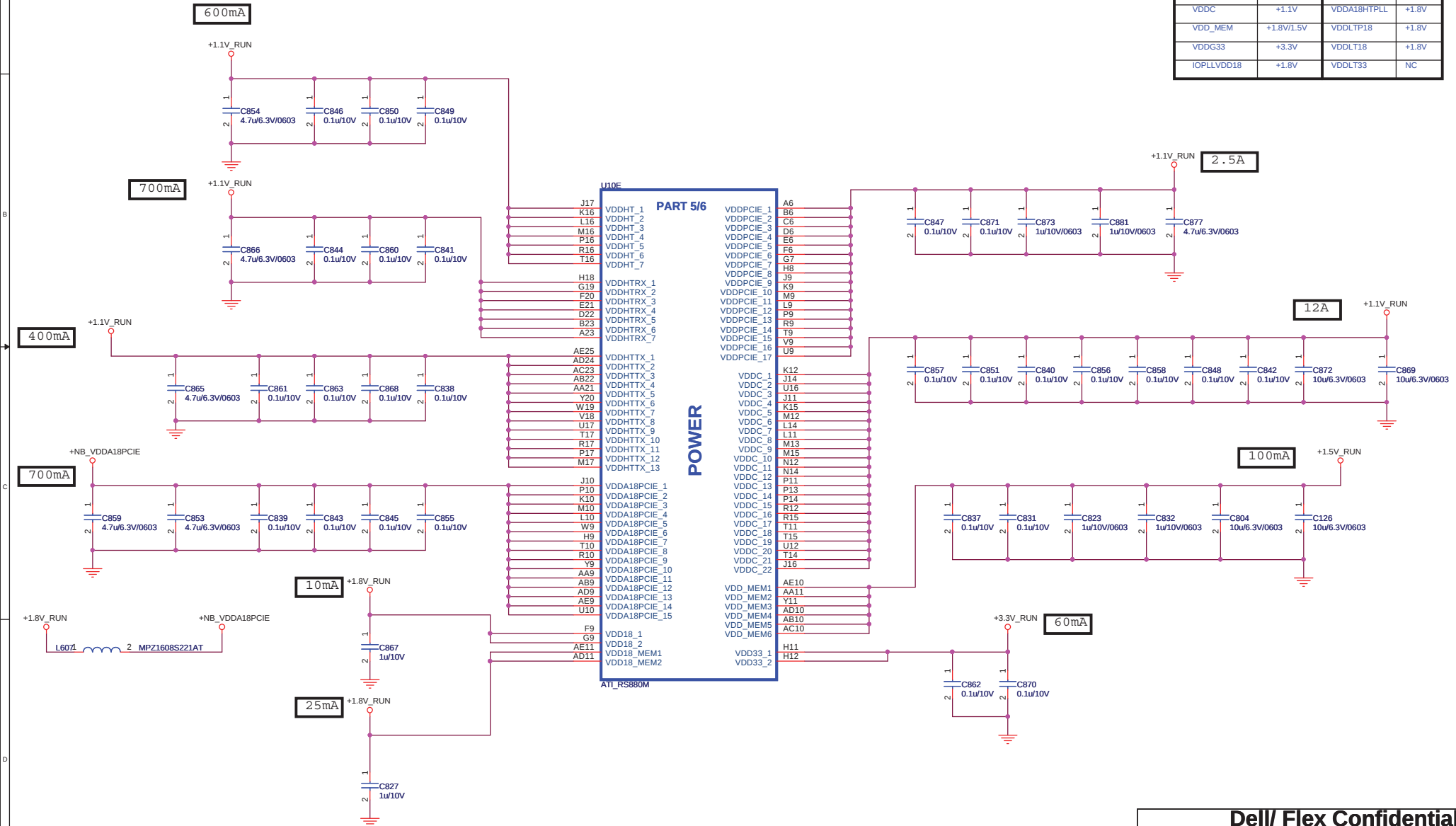
Selects if Memory SIDE PORT is available or not

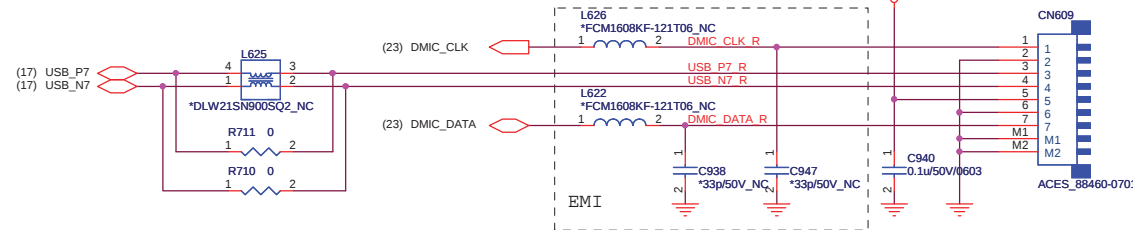
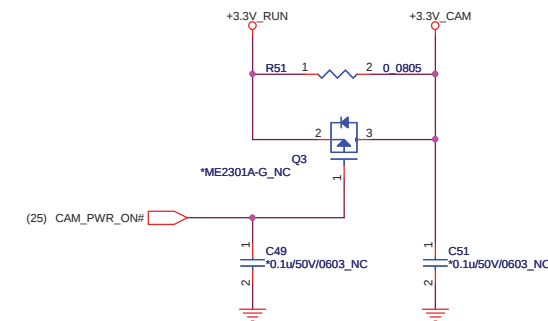
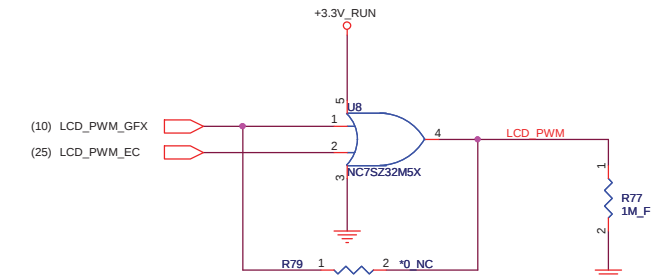
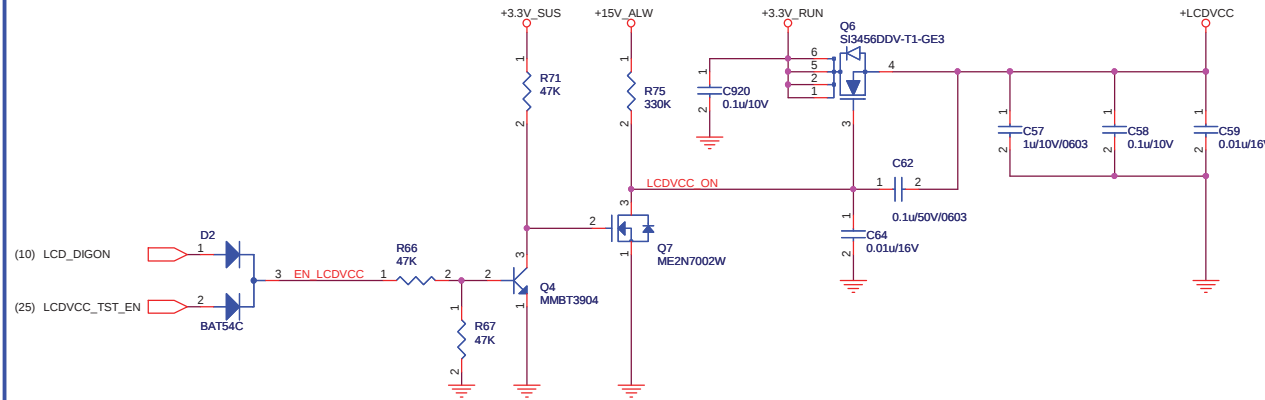
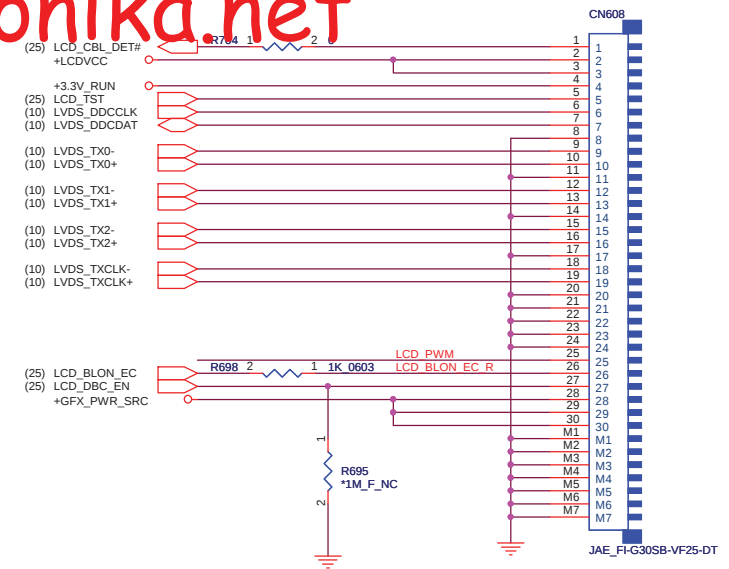
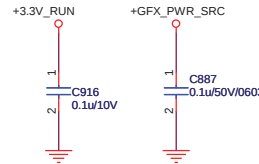
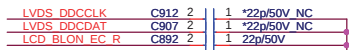
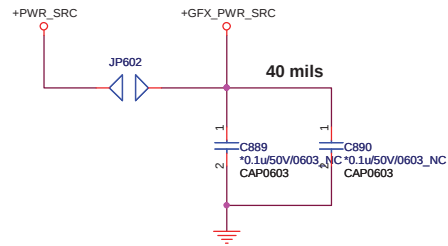
(10) CRT_HSYNC 

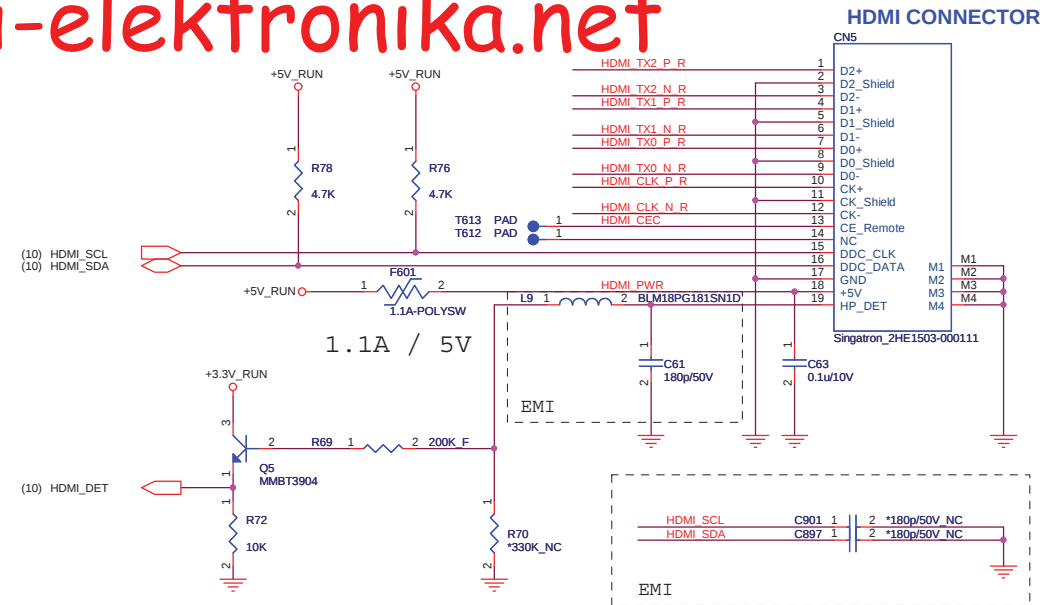


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NB - STRAP/ SIDE PORT			
Size	Document Number	Rev	
	Inspiron Z -- AMD	A00	
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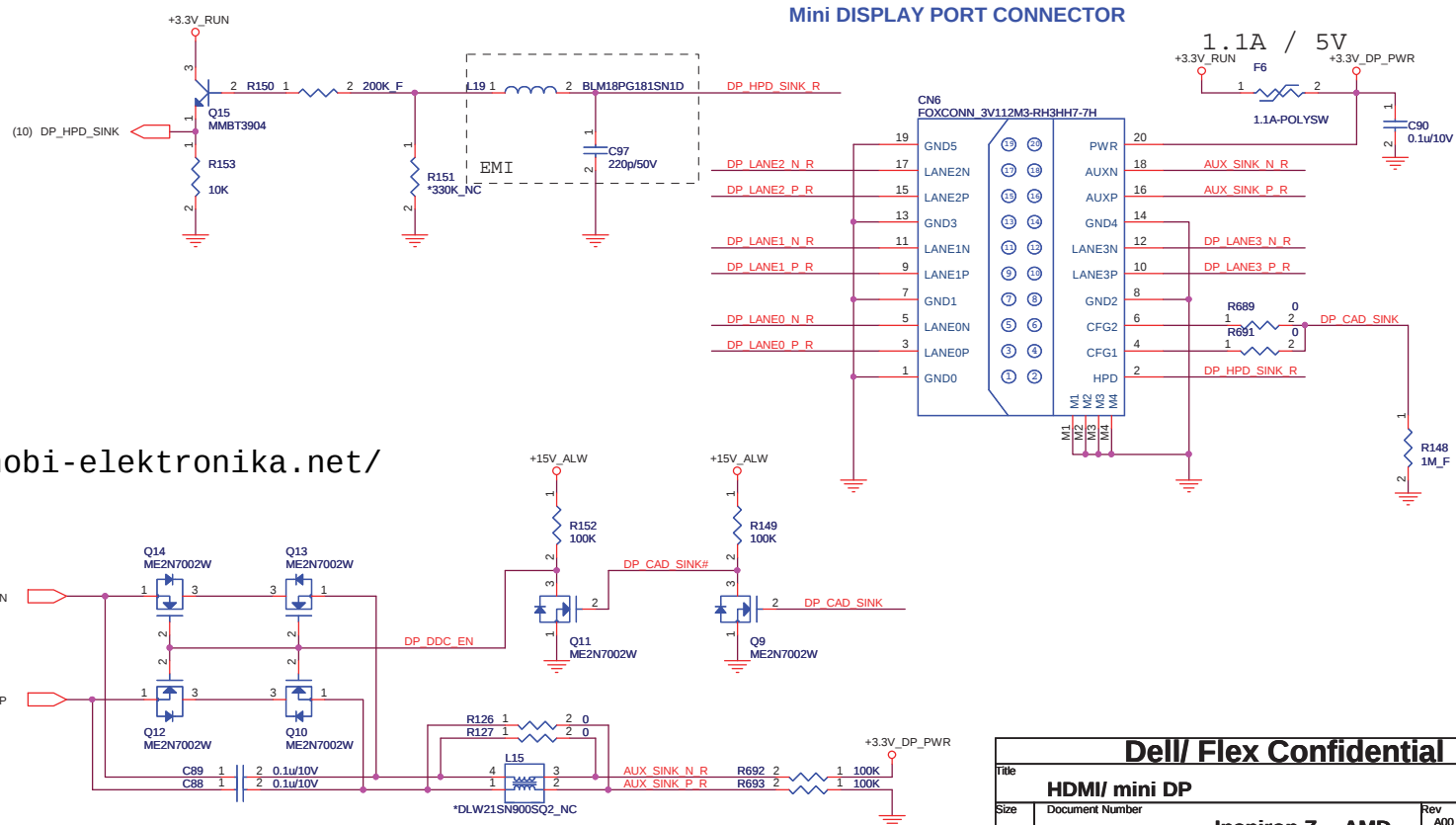
Del// Flex Confidential			
Title NB - PWR			
Size	Document Number		Rev A00
Inspiron Z -- AMD			
Date:	Monday, November 02, 2009	Sheet	12 of 37



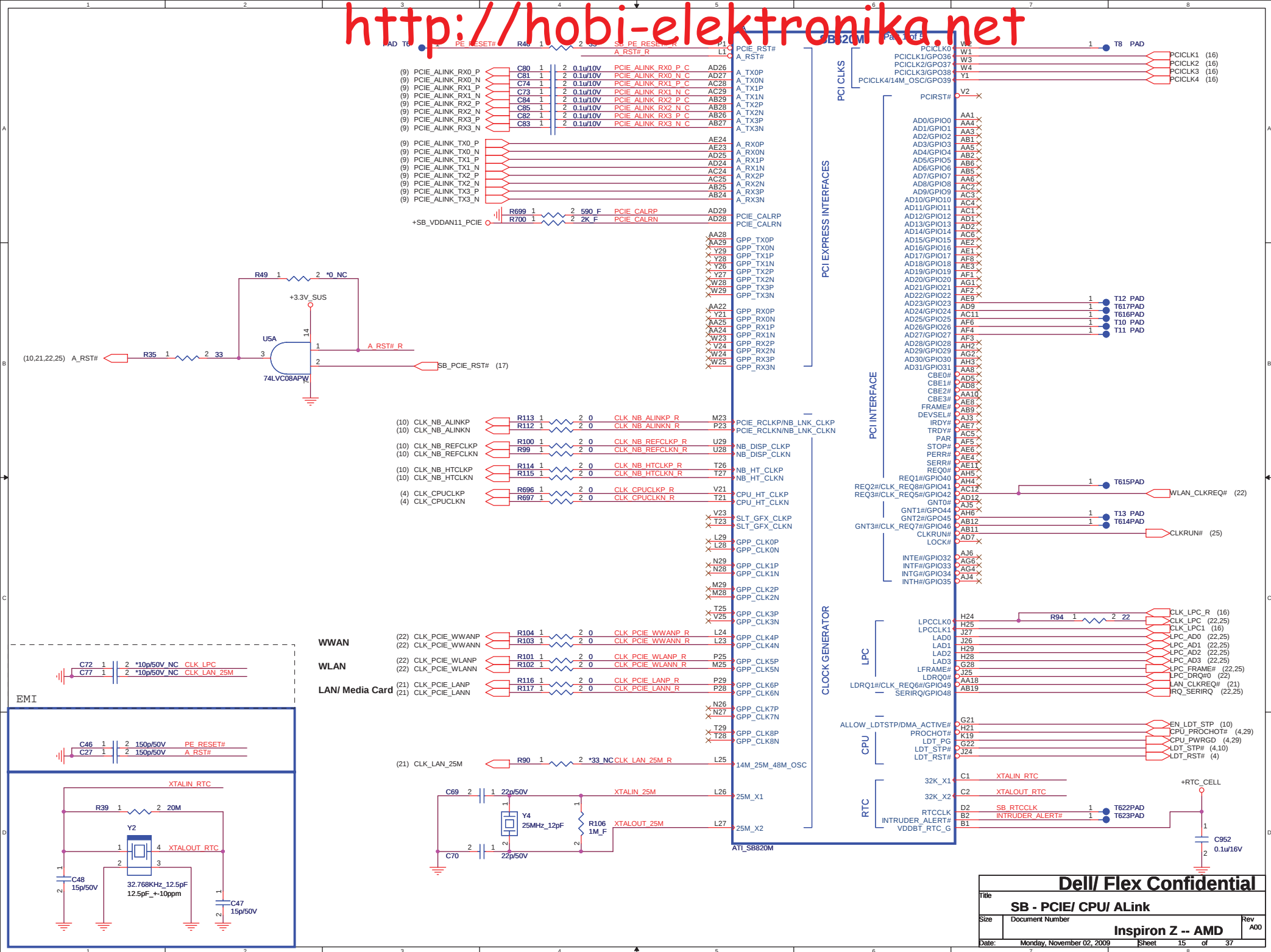


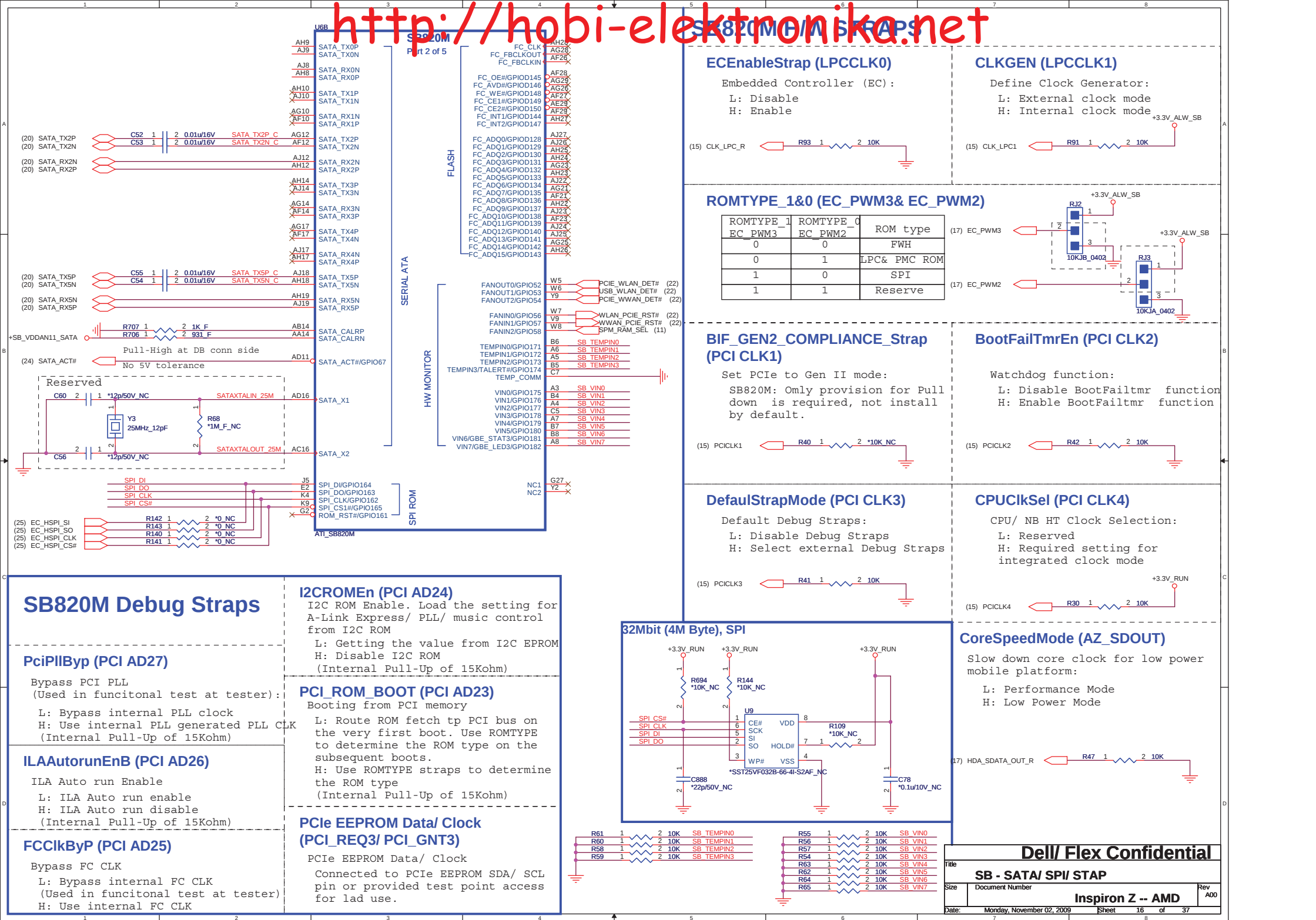


Reserve For EMI



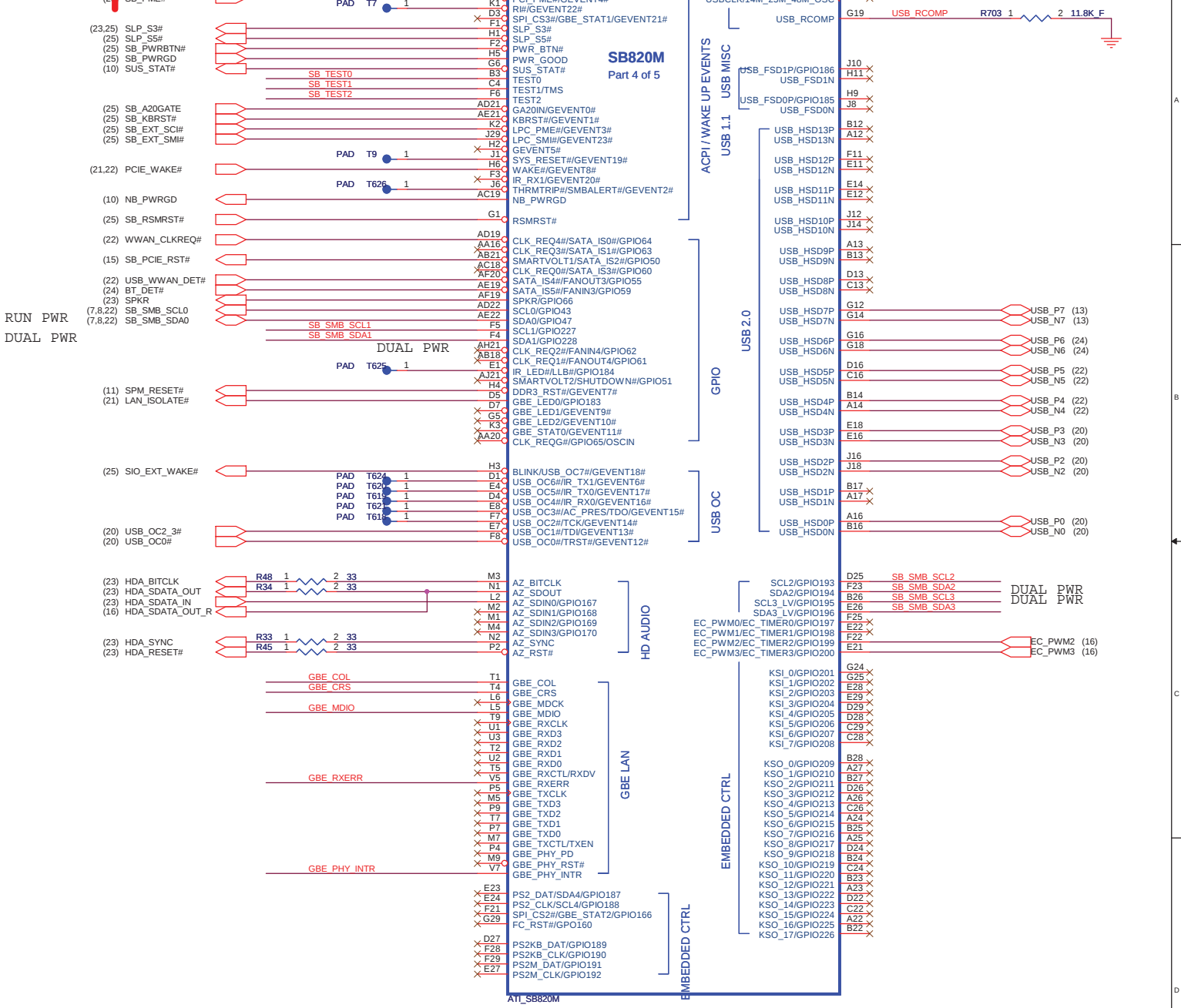
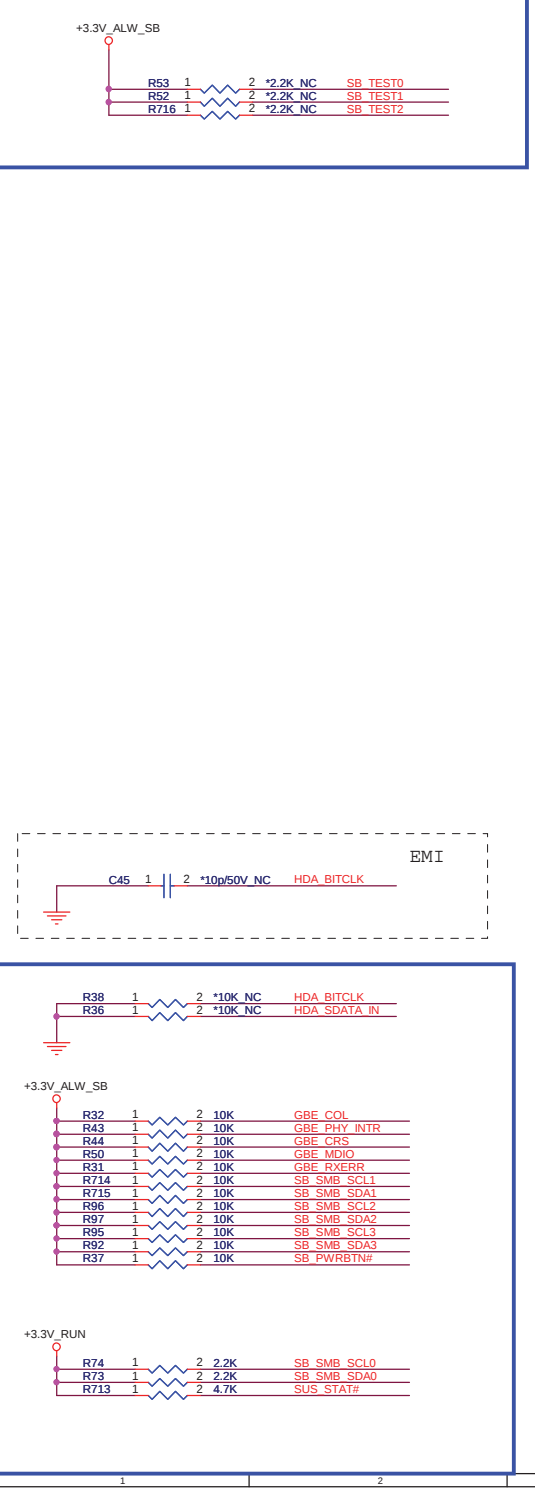
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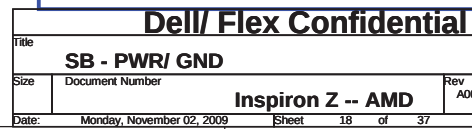




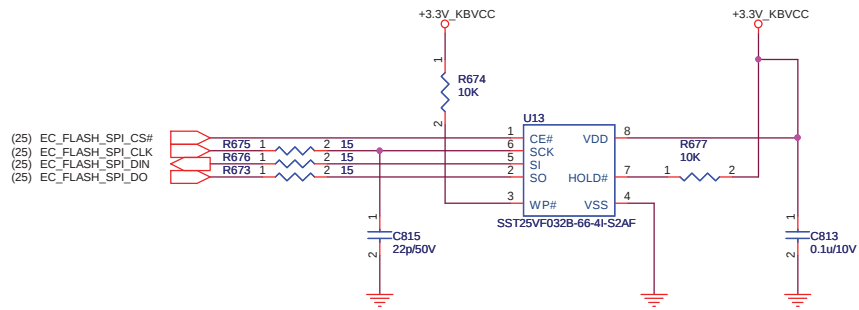
SB820M Debug Straps

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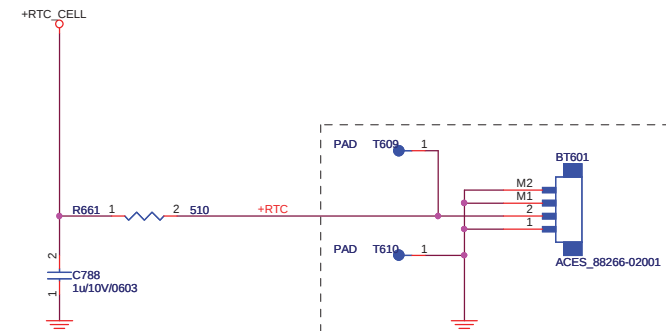




32Mbit (4M Byte), SPI



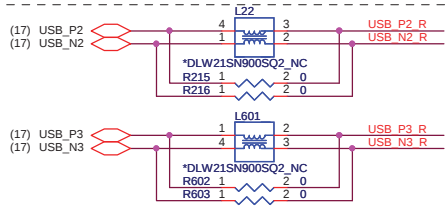
RTC BATTERY



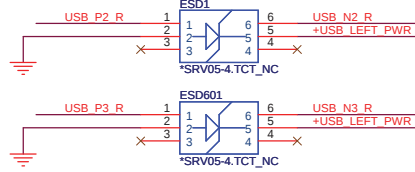
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Title	RTC/ SPI		
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USB Jack x 2

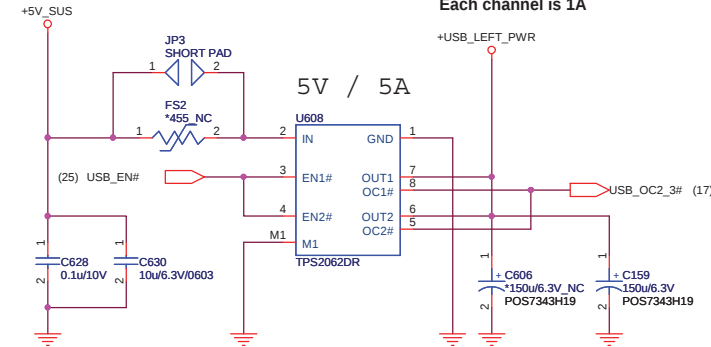


Platforms should put in PADS for the USB chokes if they have the room.
Chokes should be NOPOP.



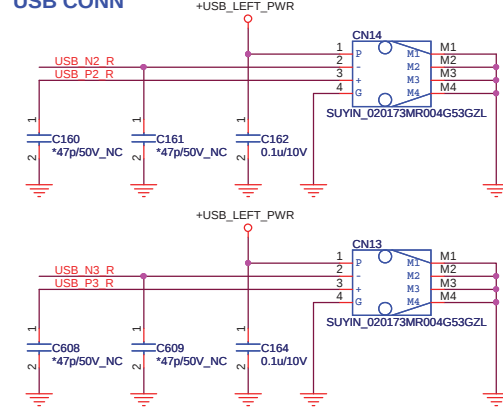
Place ESD diodes as close as USB connector.

USB POWER SW



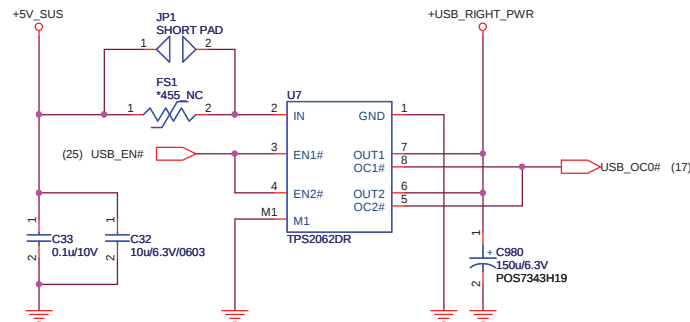
Place one 150uF cap by each USB connector.

USB CONN



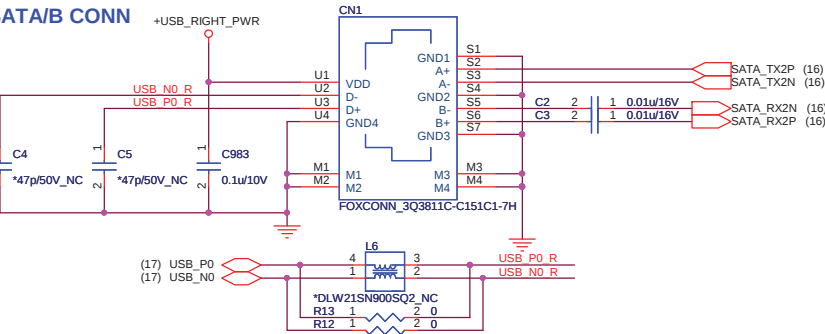
eSATA Connector

USB POWER SW



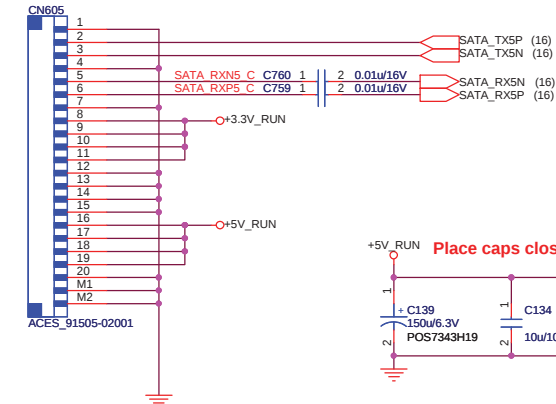
Place one 150uF cap by each USB connector.

ESATA/B CONN



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SATA HDD Connector

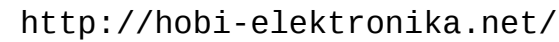


Place caps close to connector.

Place caps close to connector.

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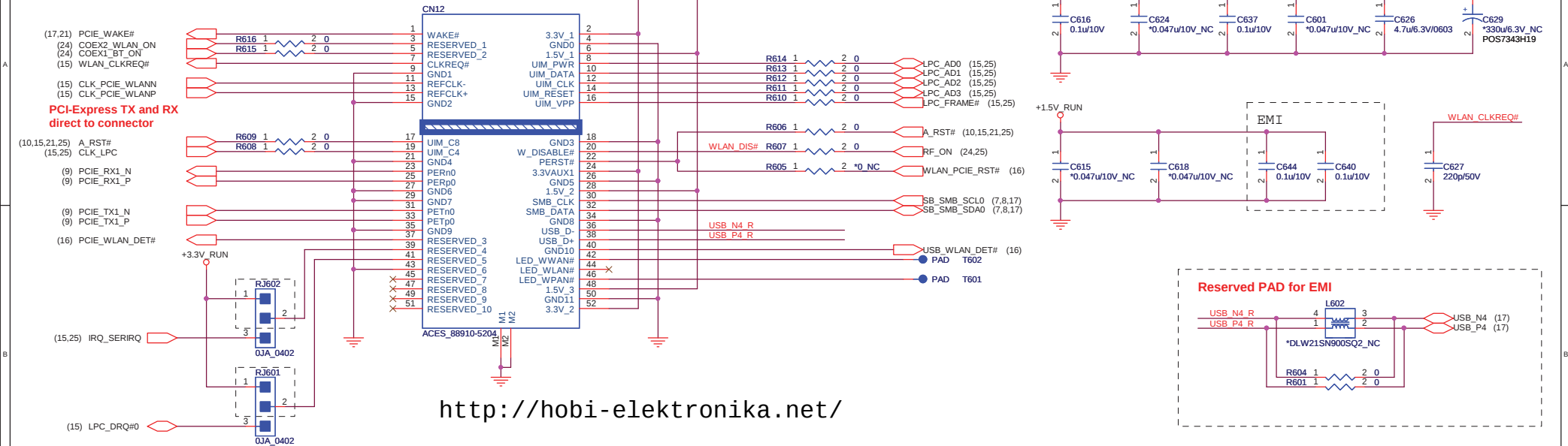
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USB/ eSATA/ HDD			
Size	Document Number		Rev
	Inspiron Z -- AMD		A00
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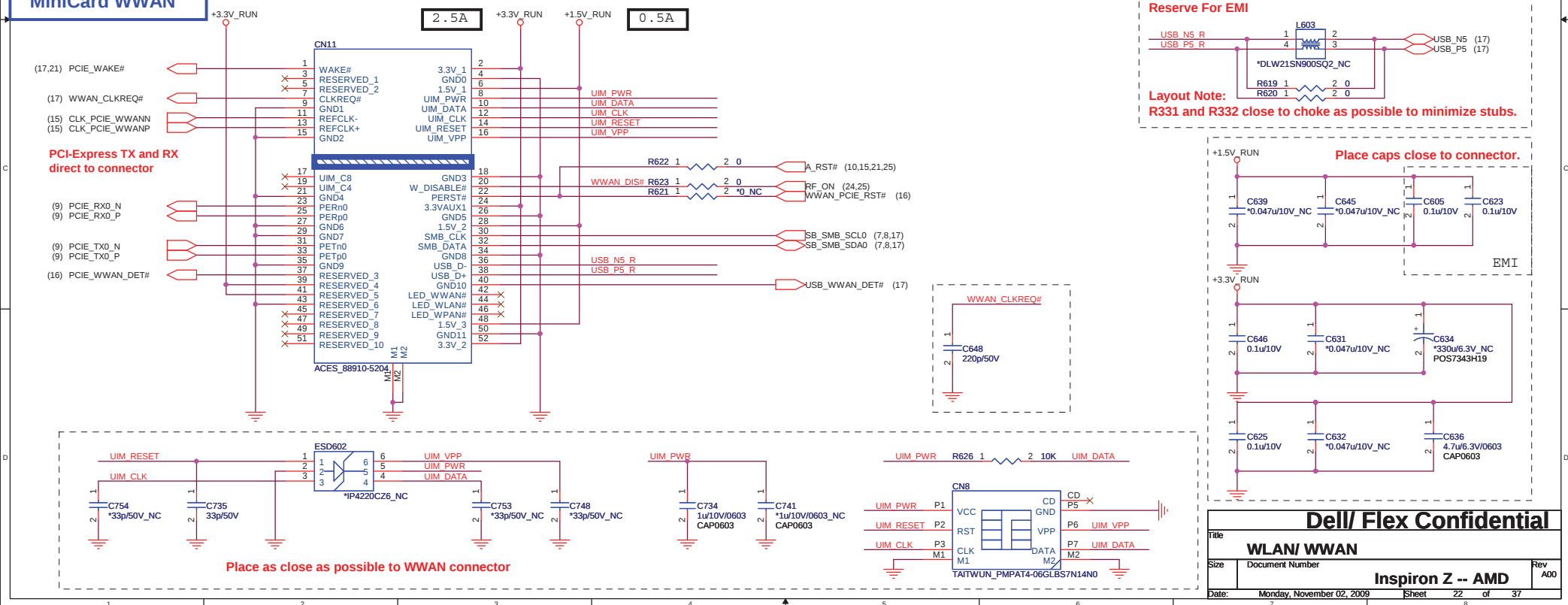
EMI

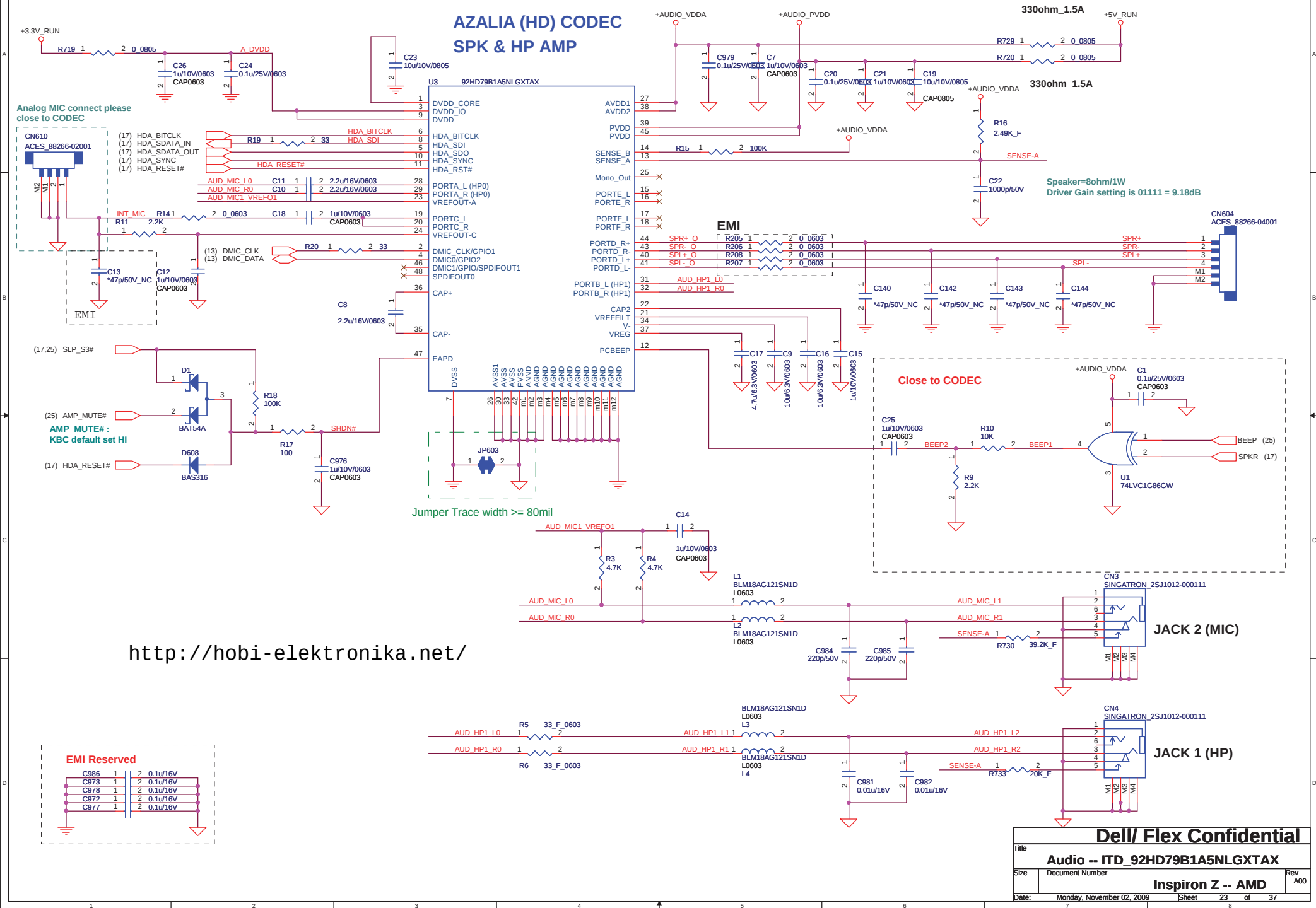


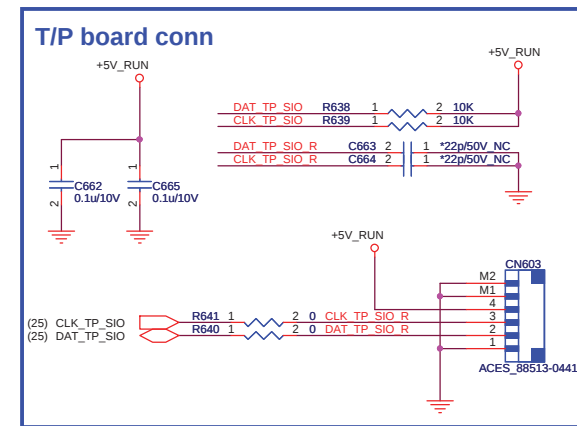
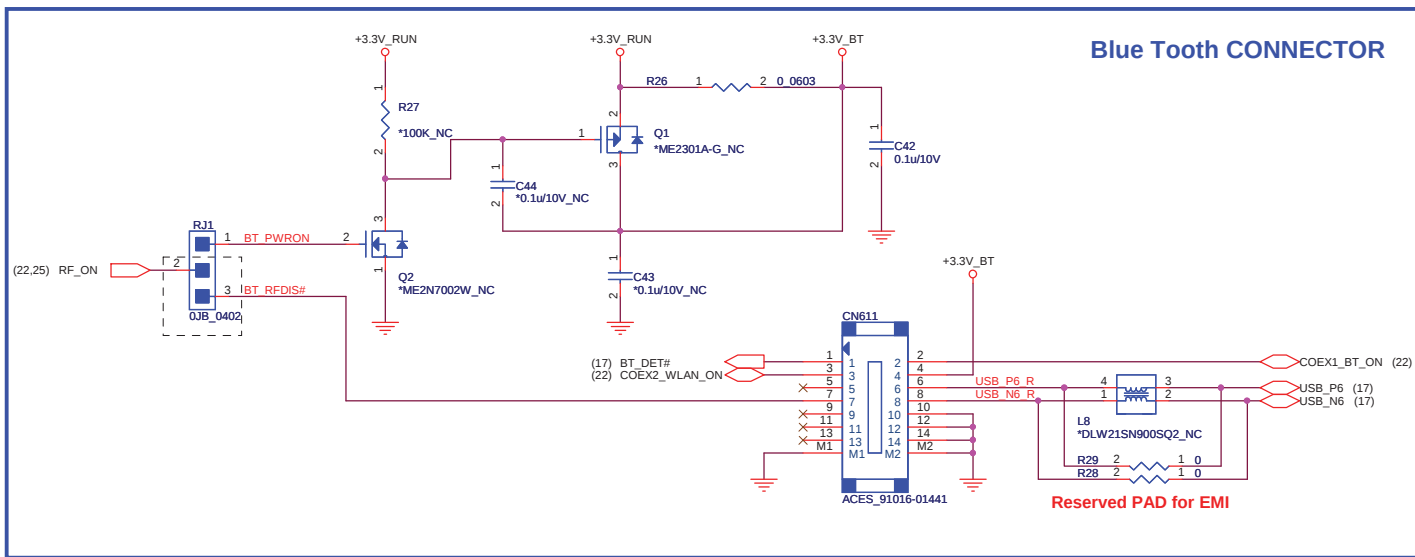
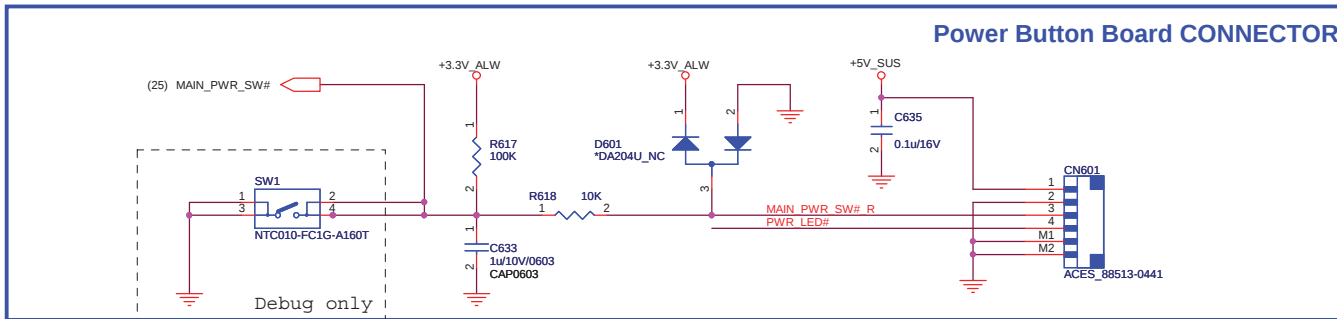
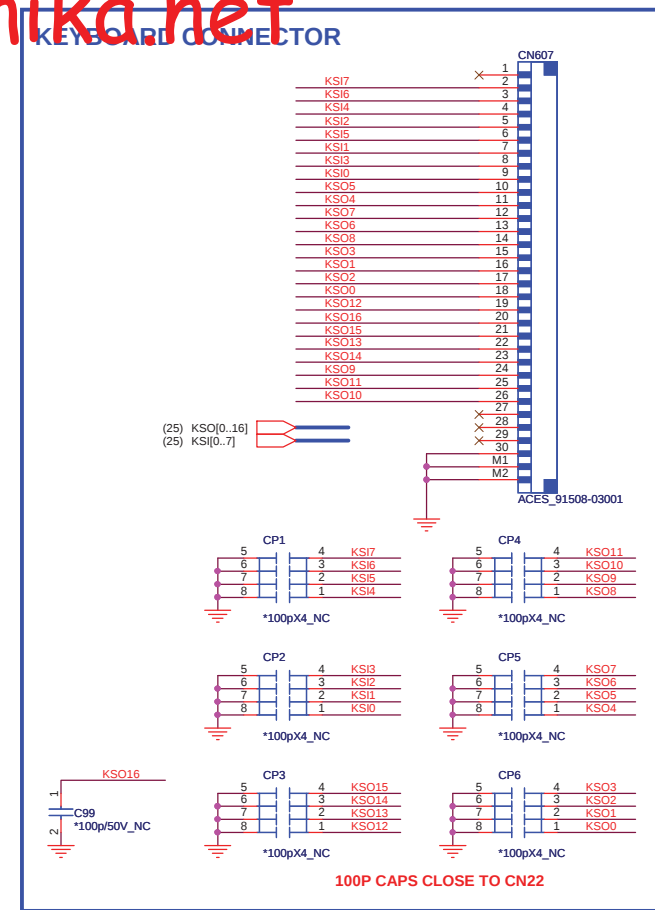
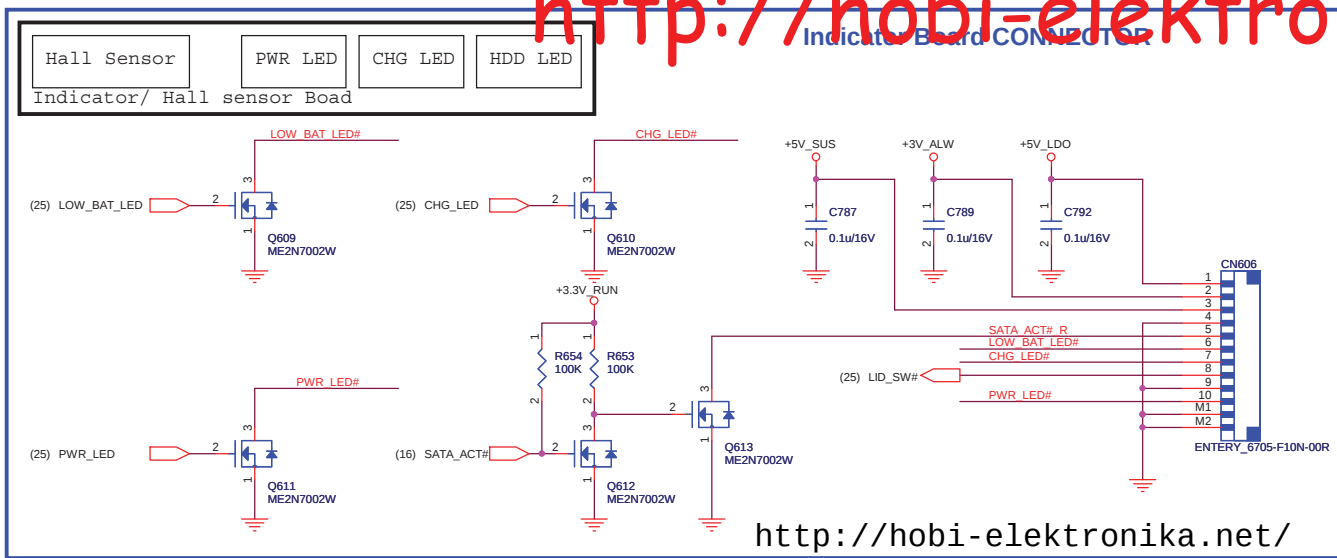
Date:	Monday, November 02, 2009	Sheet	21	of	37
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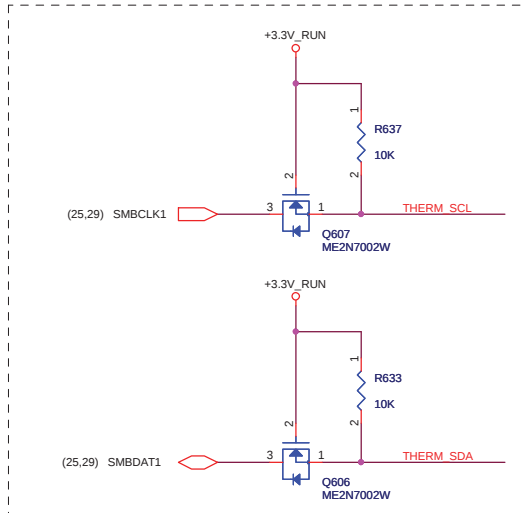
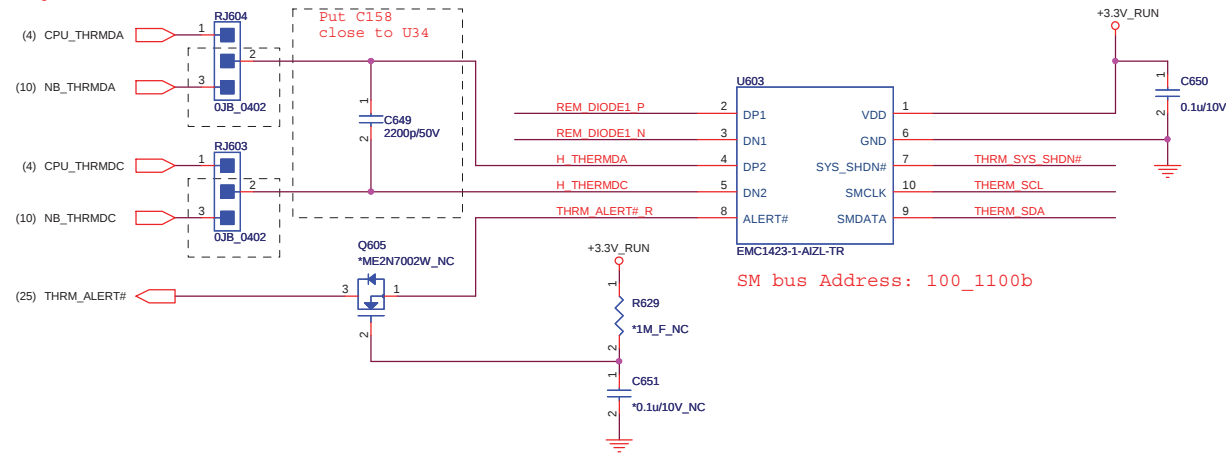
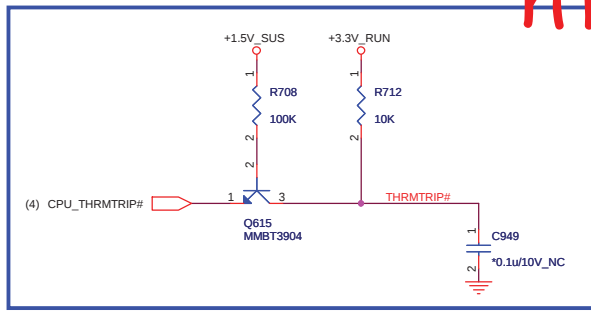


MiniCard WWAN





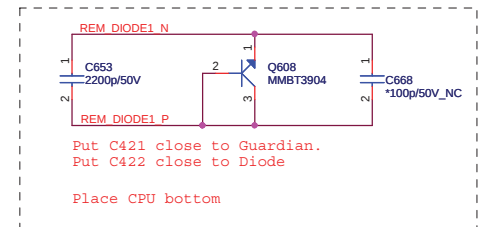
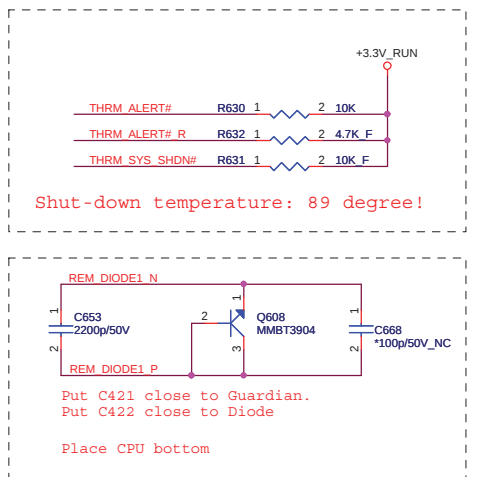
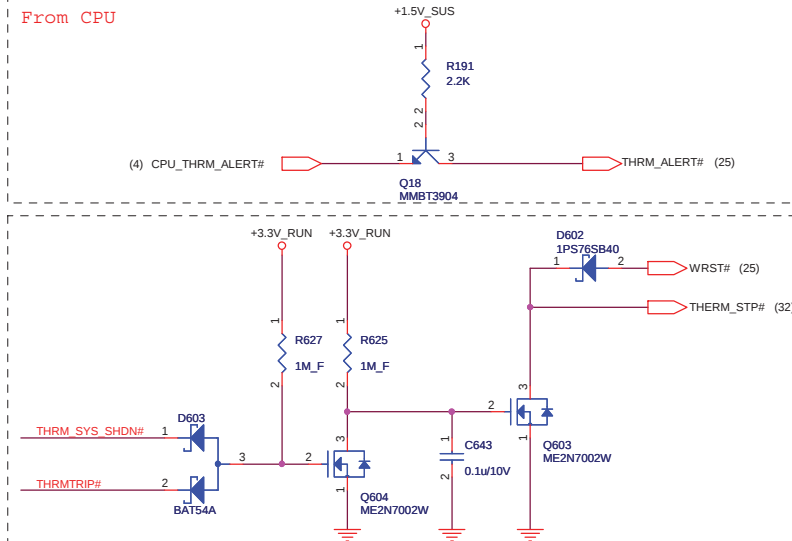
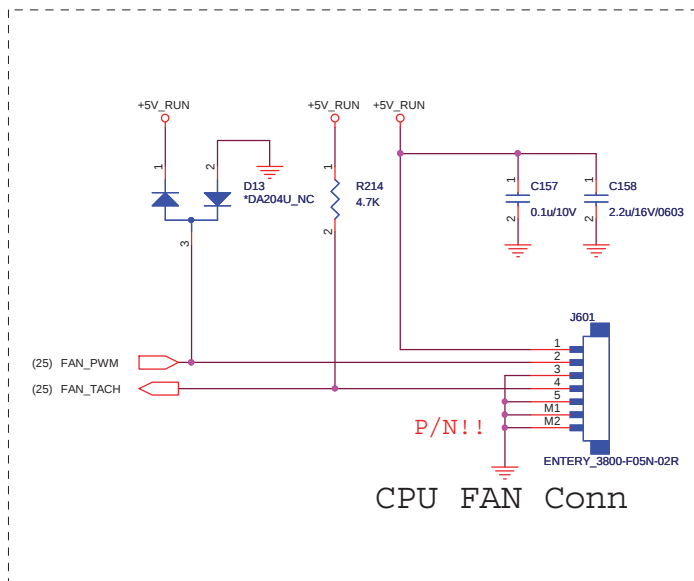


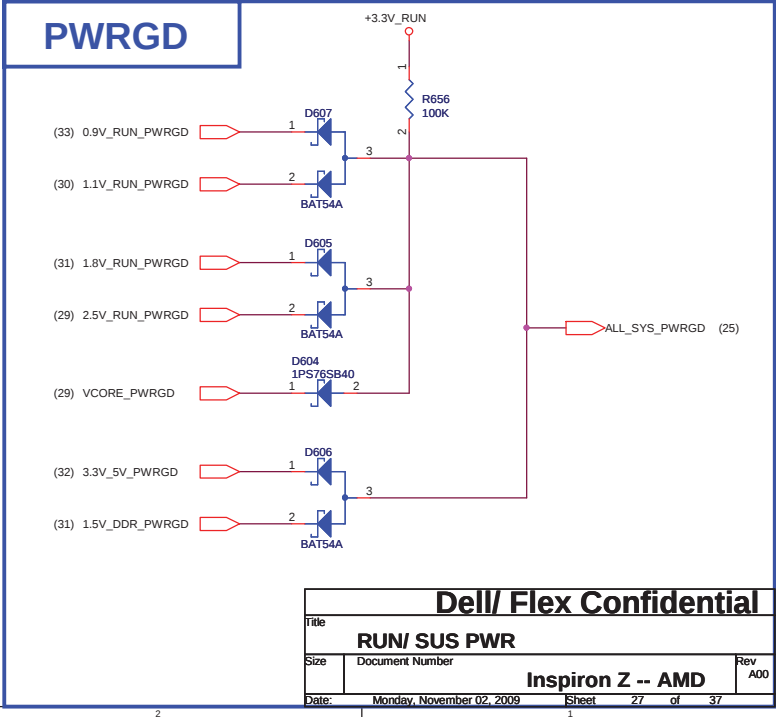


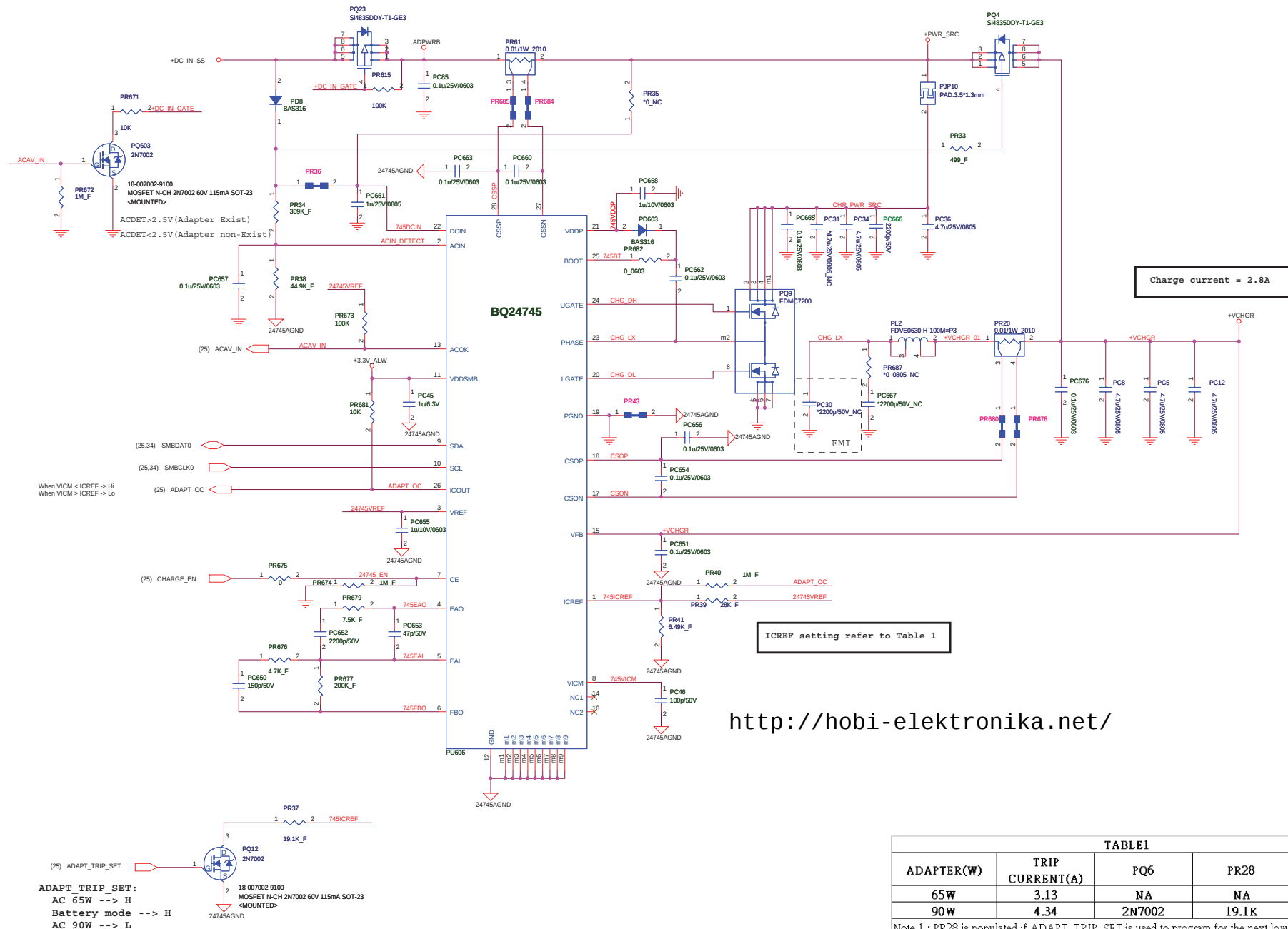
http://hobi-elektronika.net/

Table 5.2 SYS_SHDN Threshold Temperature

SYS_SHD PULL-UP ALERT PULL-UP	4.7K OHM ±10%	6.8K OHM ±10%	10K OHM ±10%	15K OHM ±10%	22K OHM ±10%	33K OHM ±10%
4.7K OHM ±10%	77°C	83°C	89°C	95°C	101°C	107°C
6.8K OHM ±10%	78°C	84°C	90°C	96°C	102°C	108°C
10K OHM ±10%	79°C	85°C	91°C	97°C	103°C	109°C
15K OHM ±10%	80°C	86°C	92°C	98°C	104°C	110°C
22K OHM ±10%	81°C	87°C	93°C	99°C	105°C	111°C
33K OHM ±10%	82°C	88°C	94°C	100°C	106°C	112°C







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TABLE1				
ADAPTER(W)	TRIP CURRENT(A)	PQ6	PR28	PR25
65W	3.13	NA	NA	6.49K
90W	4.34	2N7002	19.1K	10K

Note 1 : PR28 is populated if ADAPT_TRIP_SET is used to program for the next lower adapter.
ADAPT_TRIP_SET is floating for the higher adapter , grounded for the lower adapter.

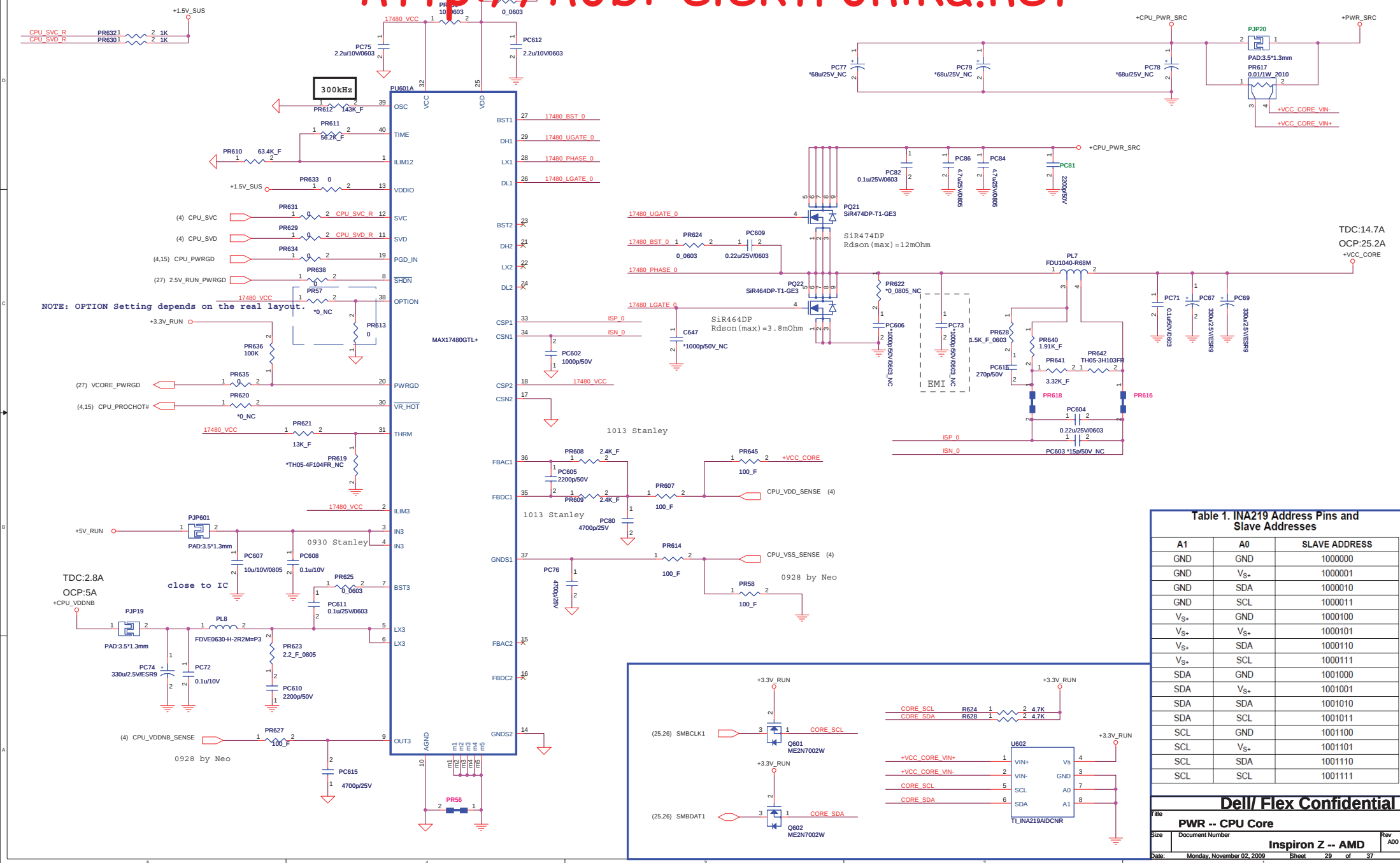


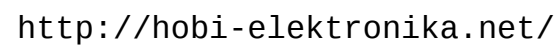
Table 1. INA219 Address Pins and Slave Addresses

A1	A0	SLAVE ADDRESS
GND	GND	1000000
GND	V _{S+}	1000001
GND	SDA	1000010
GND	SCL	1000011
V _{S+}	GND	1000100
V _{S+}	V _{S+}	1000101
V _{S+}	SDA	1000110
V _{S+}	SCL	1000111
SDA	GND	1001000
SDA	V _{S+}	1001001
SDA	SDA	1001010
SDA	SCL	1001011
SCL	GND	1001100
SCL	V _{S+}	1001101
SCL	SDA	1001110
SCL	SCL	1001111

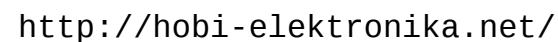
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Title			
PWR -- CPU Core			
Size	Document Number		Rev
	Inspiron Z -- AMD		A00
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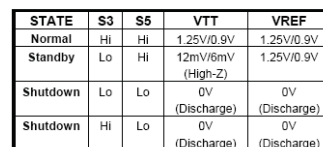
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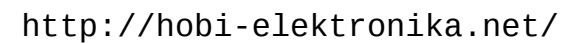


Title			
PWR -- 1.1V_RUN			
Size	Document Number		Rev
	Inspiron Z -- AMD		A00
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1.8VS

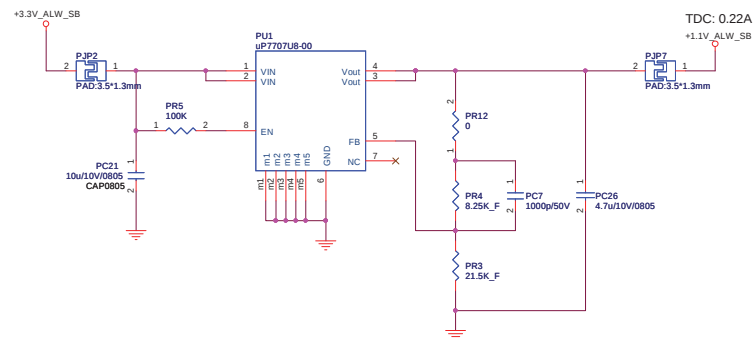




TON	Frequency
GND	OUT1@400K , OUT2@500K
VCC	OUT1@200K , OUT2@300K
OPEN	OUT1@400K , OUT2@300K

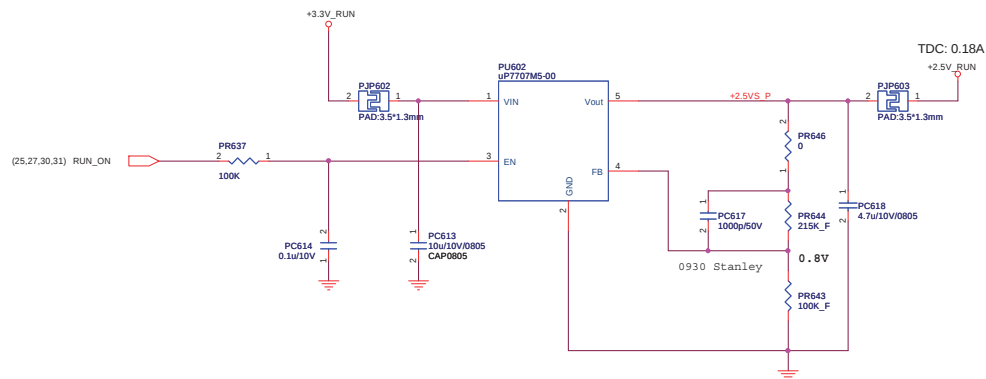
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Title PWR -- SYS 5VI 3V			
Size	Document Number		Rev A00
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+1.1V_ALW_SB

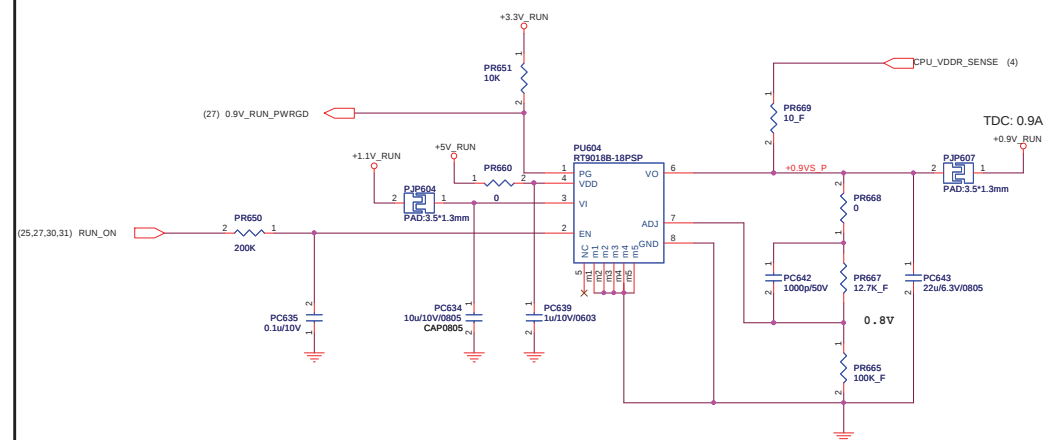


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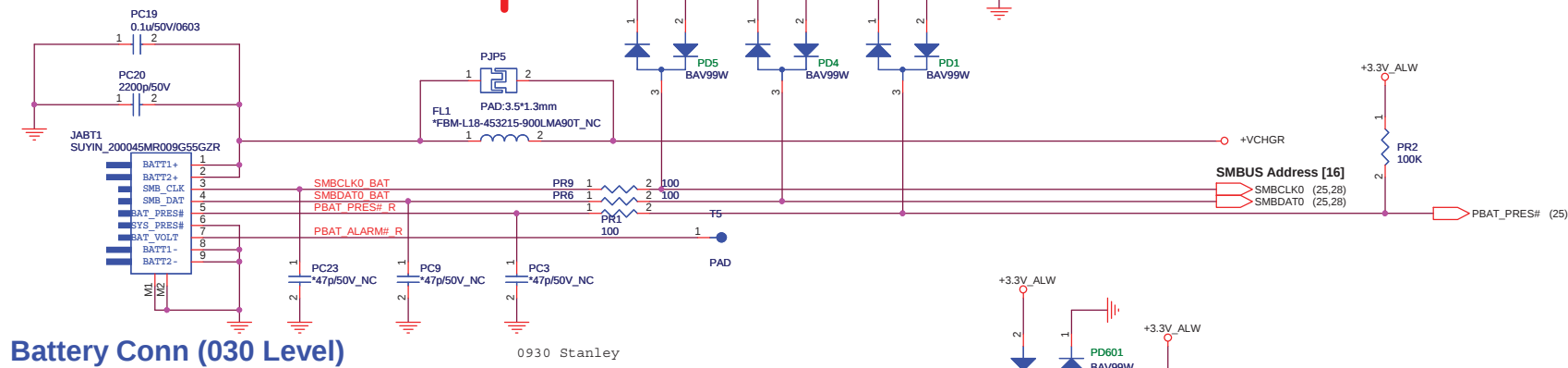
+2.5V_RUN



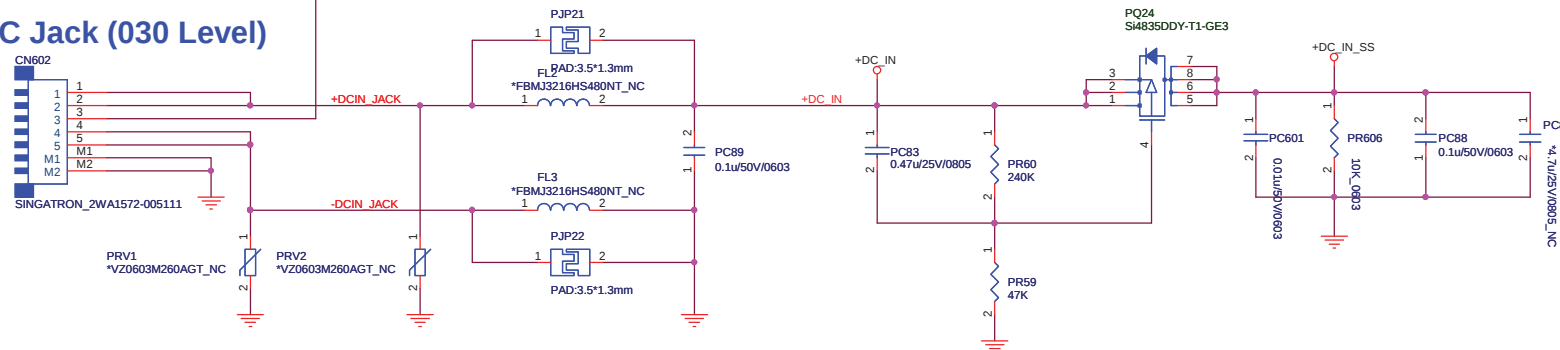
+0.9V_RUN



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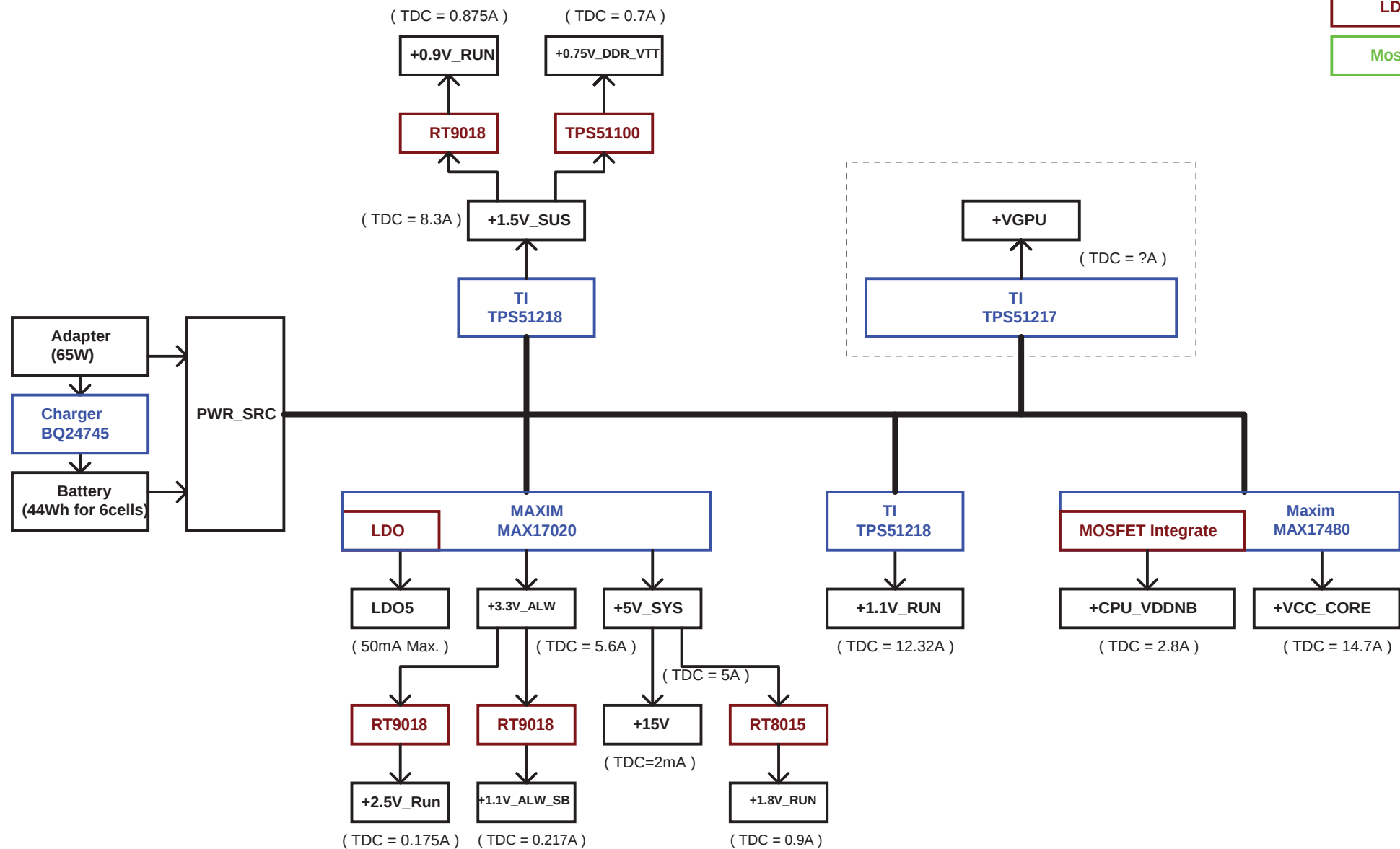
DC Jack (030 Level)



PWM Switching

LDO

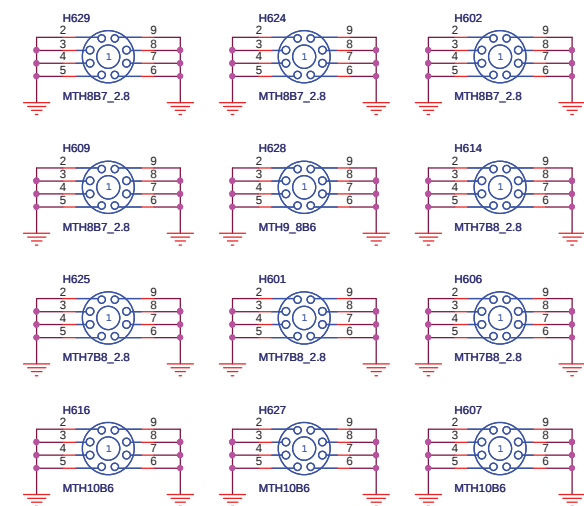
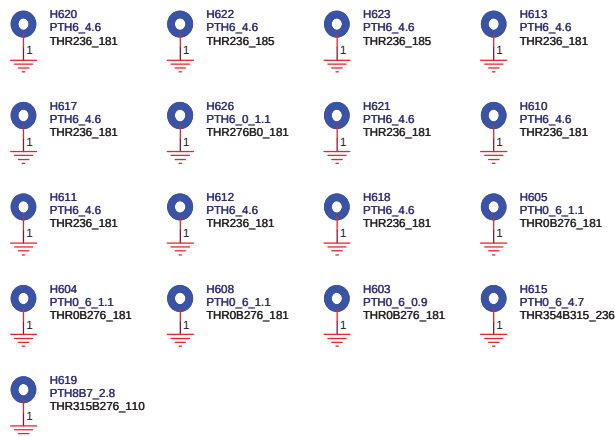
Mosfet



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Cover Sheet			
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	M11z		A00
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Screw Hole

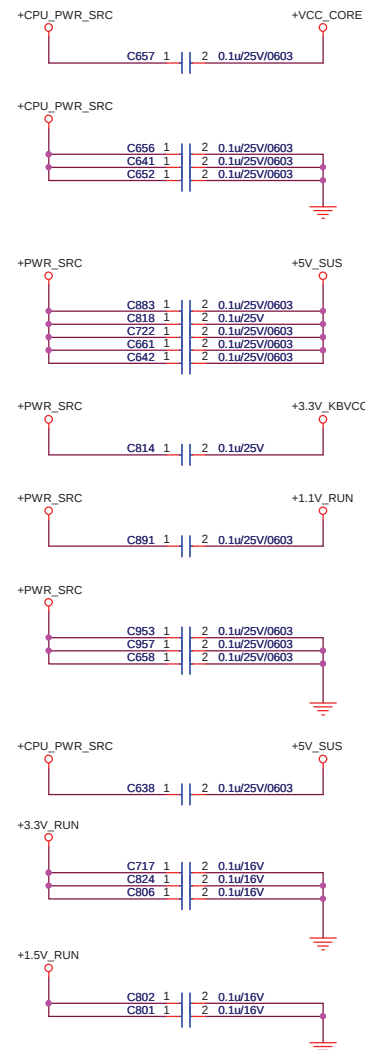
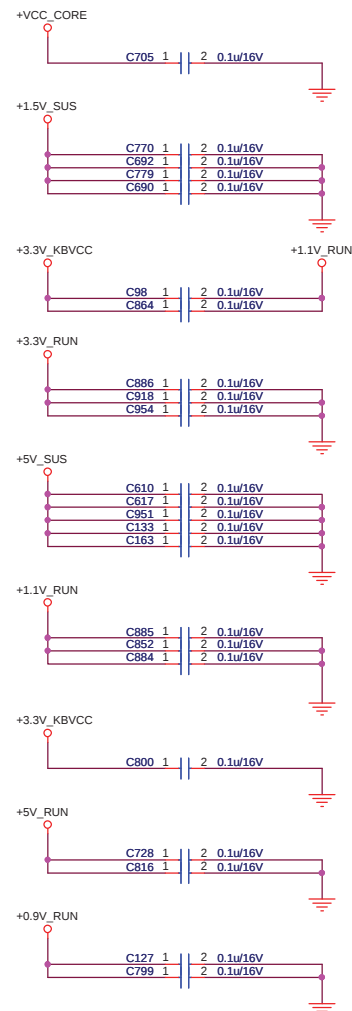
<http://hobi-elektronika.net>



FID



EMI Solution



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Screw Hole/ EMI/ Unused Gates		
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Item	Fixed Issue	Reason for Change	Rev	PG#	Modify List	B Ver#	Phase
1			X00		X00 Schematic Official Release	X00	SSI

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	Inspiron Z -- AMD	A00
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