

COMPAL CONFIDENTIAL

Carson Schematics Document

MODEL NAME : JAU00

PCB NO : LA-3741P

COMPAL P/N : DA800008W0L

uFCBGA Mobile Merom-ULV
Intel Calistoga-GMS + ICH7M

2007-01-24

REV : 0.1 (DELL: X00)

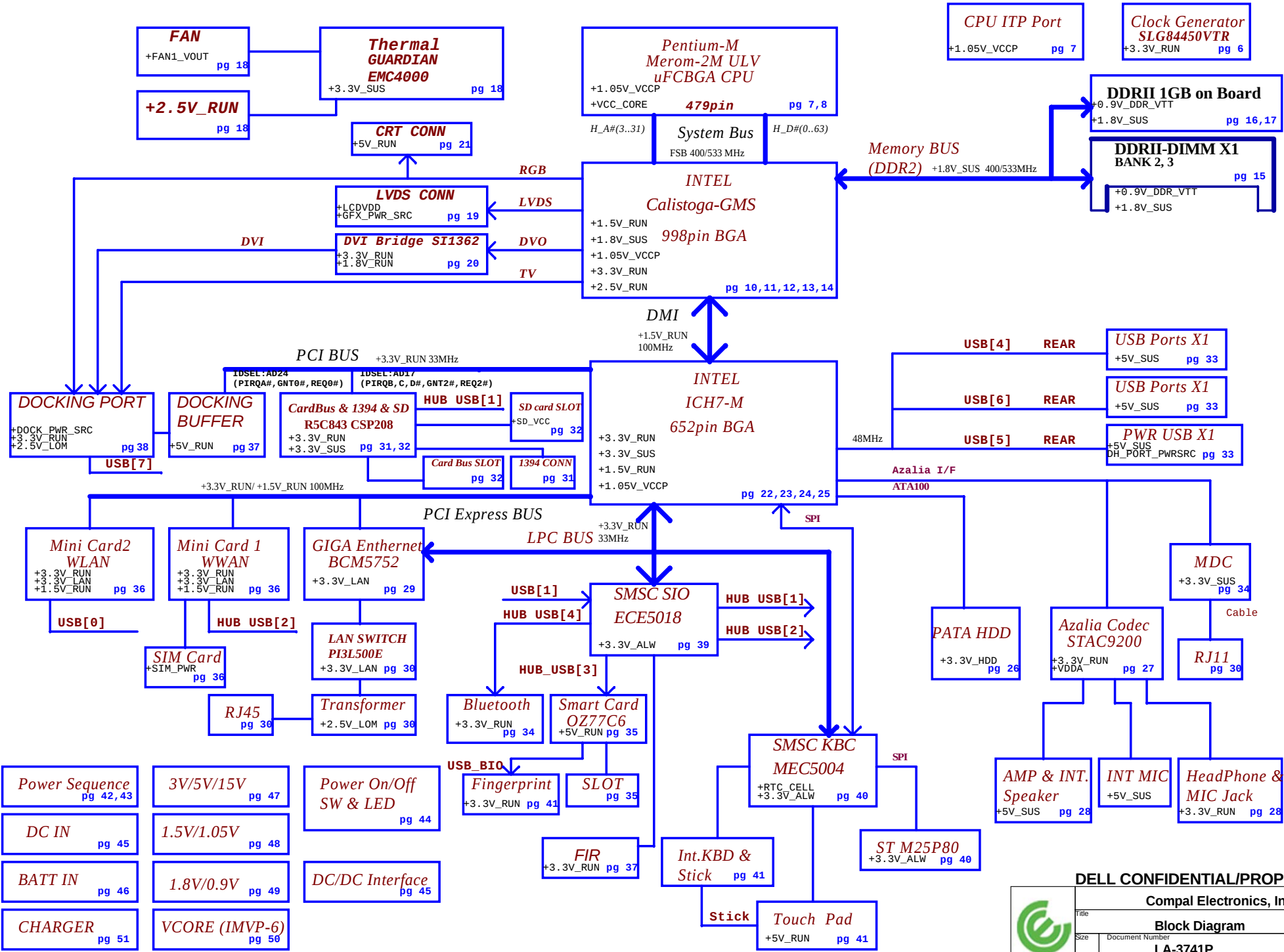
HW POP option control table											
	01 BOM	02 BOM	03 BOM	04 BOM							
BOM No.	45148731L01	45148731L02	45148731L03	45148731L04							
CPU P/N	SA00001ND0L	SA00001MA1L	SA00001ND0L	SA00001MA1L							Location: U2
CPU Type	Dual core	Dual core	Dual core	Dual core							
Speed/power	1.2G ,9W	1.06G , 9W	1.2G ,9W	1.06G , 9W							
C41,C44	220 uf	220 uf	220 uf	220 uf							
C42,C43	Pop	Pop	Pop	Pop							POP 2@ ; 220 uf for Dual core 220 uf Poly Cap ; (P/N : SGA00002D0L)
C21,C26,C29,C31 C34,C36,C28,C23	10 uf	10 uf	10 uf	10 uf							10 uf MLCC Cap ; P/N (SE00000110L) 22 uf MLCC Cap ; P/N (SE160106M8L)
C694,C697,C37,C39,C22 C698,C693,C25,C40,C35 C699,C700,C30,C33,C27 C703,C704,C38	Pop	Pop	Pop	Pop							POP 2@ ; 10 uf for Dual core 10 uf MLCC Cap ; P/N (SE160106M8L)
JP3	Depop	Depop	Pop	Pop							POP 4@ ; For with TAA Board
U50	Pop	Pop	Depop	Depop							POP 1@ ; For without TAA Board

MB PCB	
Part Number	Description
DA800008W0L	PCB 02F LA-3741P REV0 N/B

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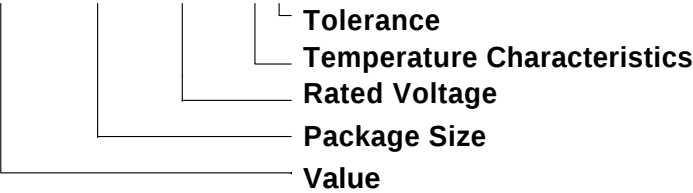
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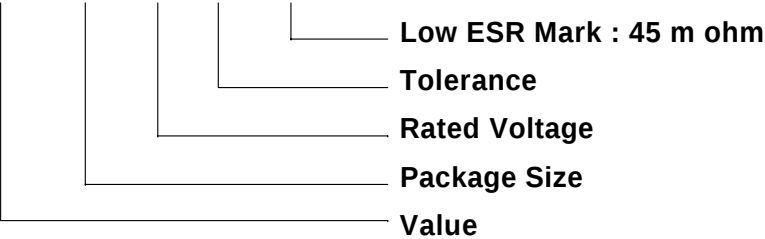
Ceramic Capacitors :

0.1U_0402_6.3VXX



Tantalum or Polymer Capacitors :

10U_D2_10VX_R45



Capacitor Spec Guide:

Temperature Characteristics:

Symbol	0	1	2	3	4	5	6	7
CODE	Z5U	Z5V	Z5P	Y5U	Y5V	Y5P	X5R	X7R

8	9	A	B	C	D	E	F	G
NPO	C0G	X6S	BJ	CH	CJ	CK	SH	SJ

H	I	J	K
UJ	UK	SL	X5S

Tolerance:

Symbol	A	B	C	D	F	G	H	J
CODE	+0.05PF	+0.1PF	+0.25PF	+0.5PF	+1PF	+2%	+3%	+5%

K	M	N	P	Q	V	X	Z	
+10%	+20%	+30%	+100,-0%	+30,-10%	+20,-10%	+40,-20%	+80,-20%	

NOTE1:

@XX : Depop component

PCI TABLE

PCI DEVICE	IDSEL	REQ#/GNT#	PIRQ
CARD BUS	AD17	2	B, C, D
DOCKING	AD24	0	A

PM TABLE

State	power plane	+3.3V_ALW +5V_ALW	+3.3V_SRC +3.3V_SUS +5V_SUS +1.8V_SUS +15V_SUS	+5V_RUN +3.3V_RUN +1.8V_RUN +0.9V_DDR_VTT +1.5V_RUN +VCC_CORE +1.05V_VCCP +2.5V_RUN
S0		ON	ON	ON
S1		ON	ON	ON
S3		ON	ON	OFF
S5 S4/AC		ON	OFF	OFF
S5 S4/AC don't exist		OFF	OFF	OFF

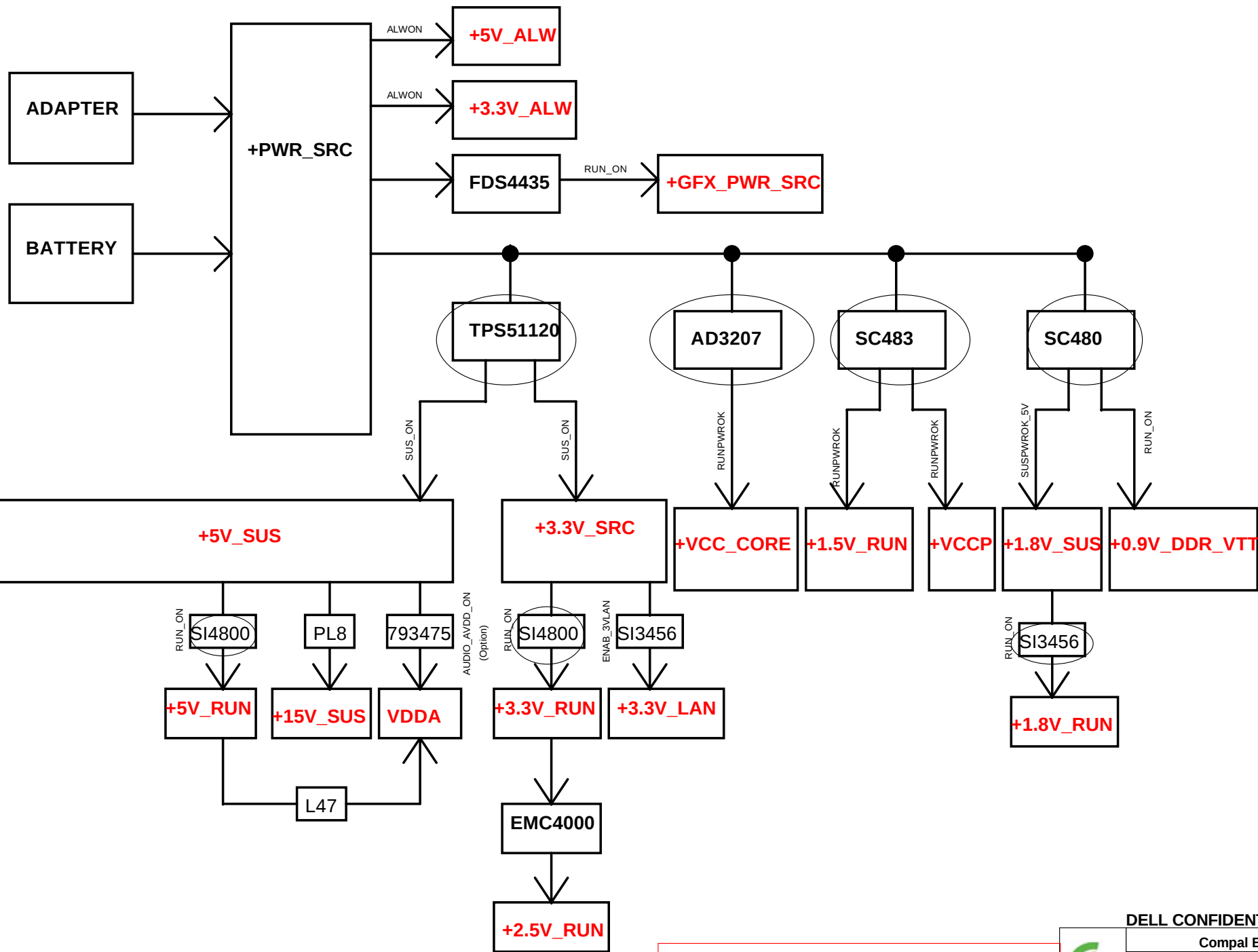
USB TABLE

USB PORT#	DESTINATION	USB HUB	DESTINATION
0	Mini 2(WLAN)	1	PC Card Bay
1	USB Hub (5018)	2	Mini 1(WWAN)
2	N/A	3	SMART CARD
3	N/A	4	Blue tooth
4,6	REAR		
5	PWR USB	USB HUB on OZ77C6LN	DESTINATION
7	Docking	DP_HUB	Fingerprint

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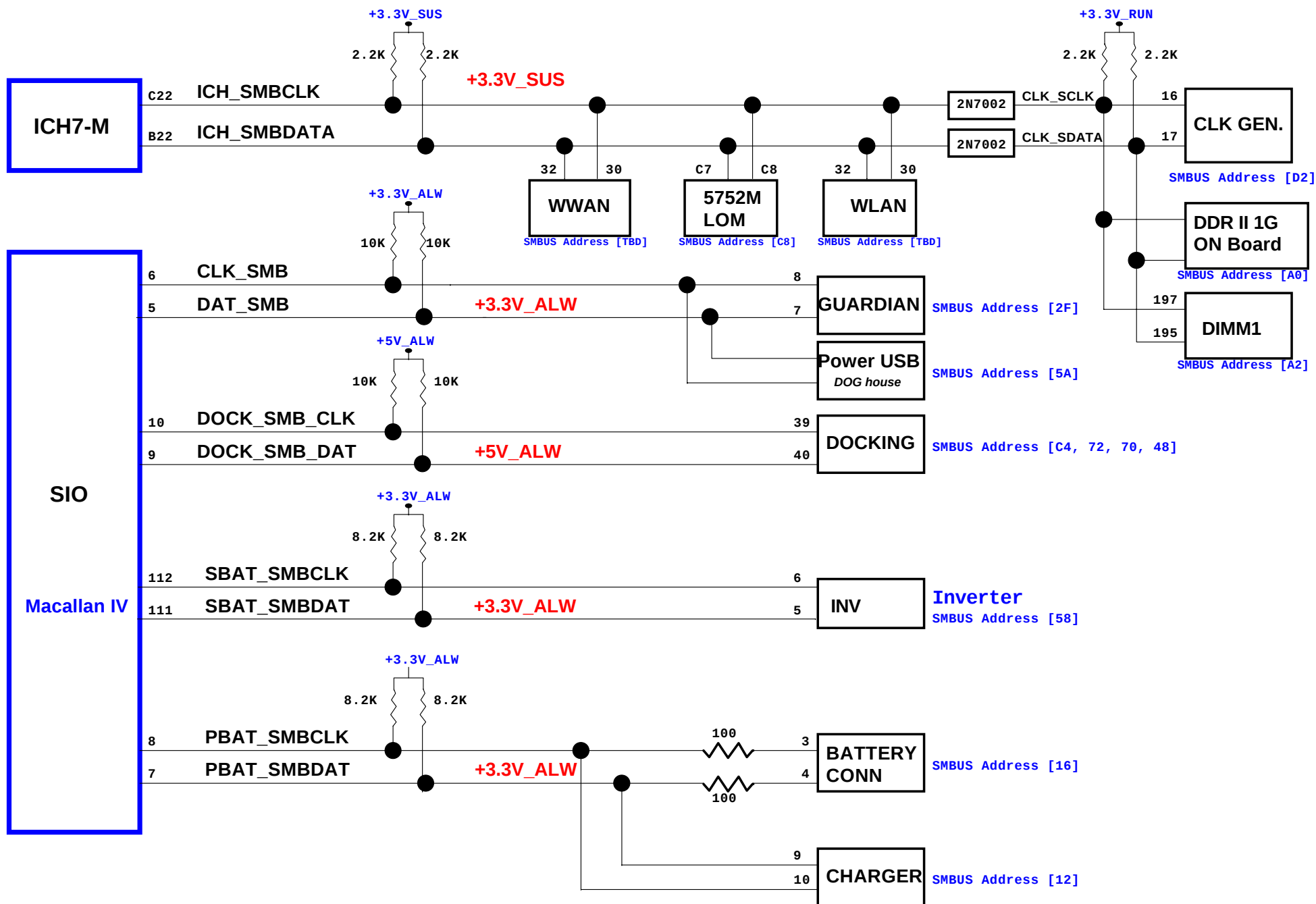
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Power Rail

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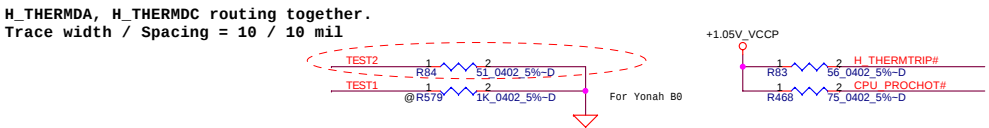
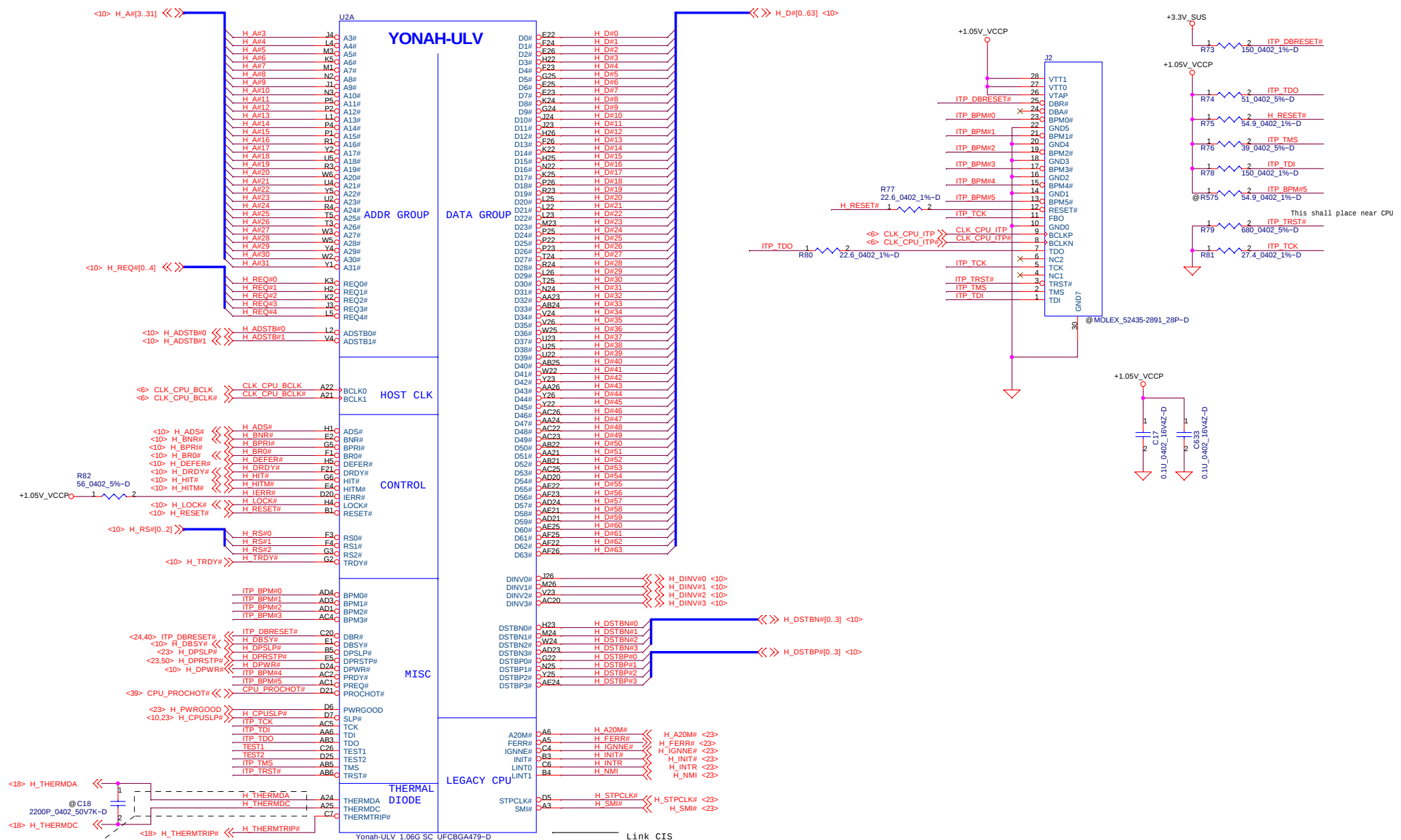


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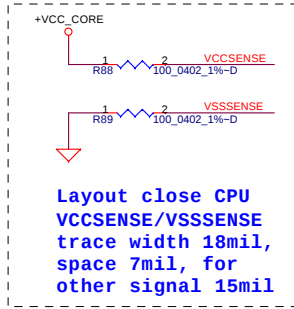
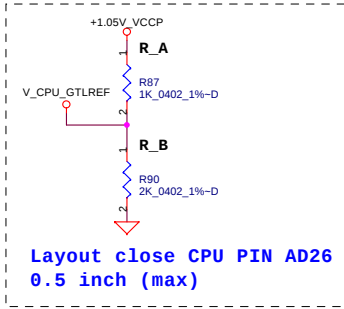


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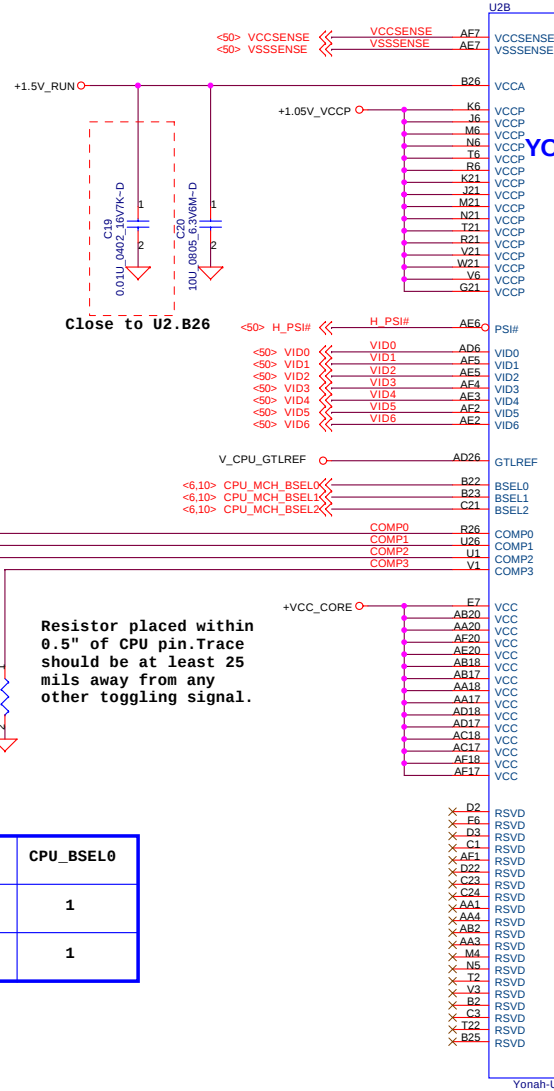
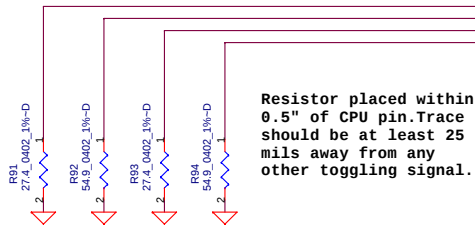


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Yonah-ULV in mFCPGA479			
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Length match within 25 mils

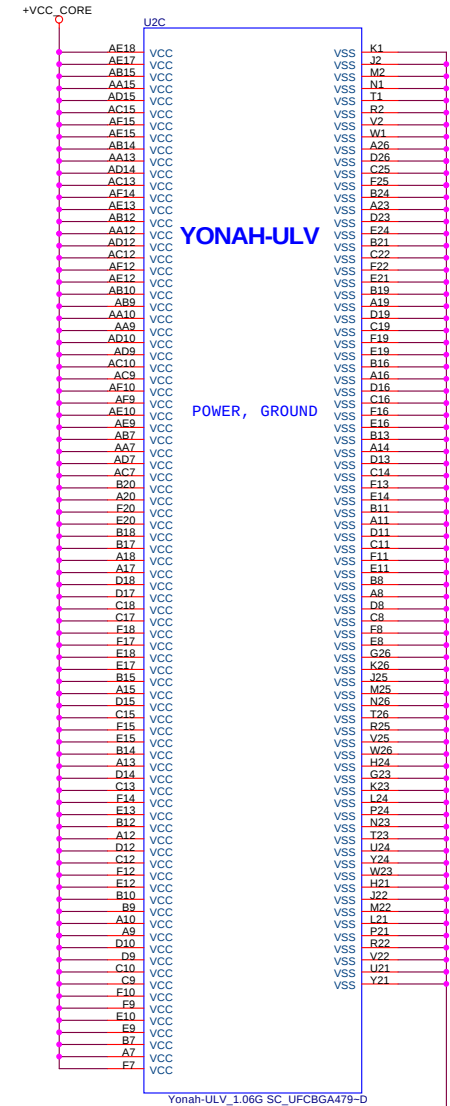


CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
133	0	0	1
166	0	1	1



POWER, GROUND, RESERVED SIGNALS AND NC

Link CIS



Link CTS

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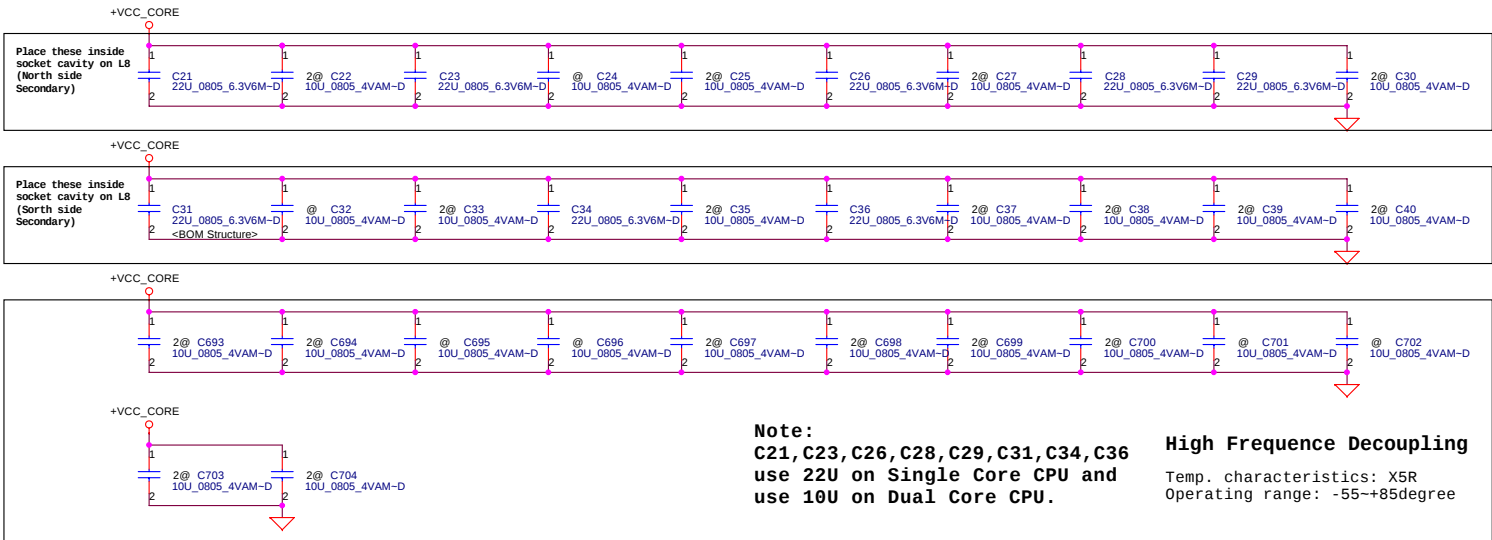
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Yonah-ULV in mFCBGA479

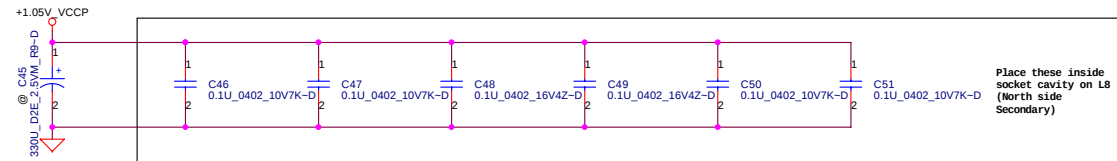
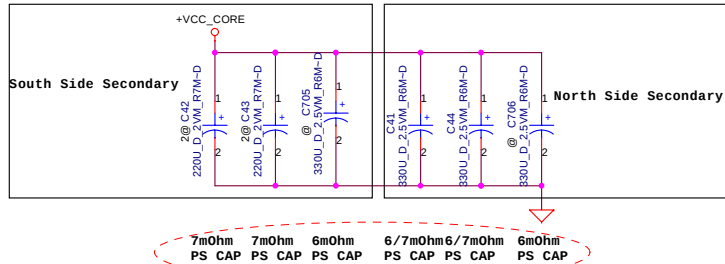
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Near VCORE regulator.



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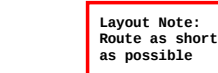
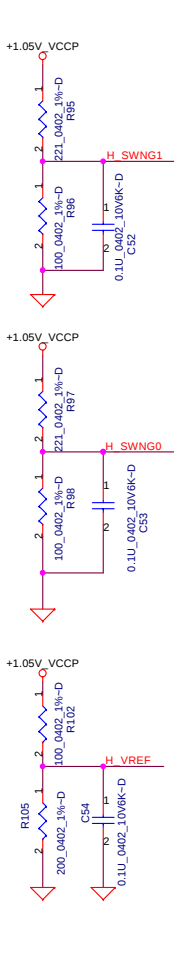
CPU Bypass

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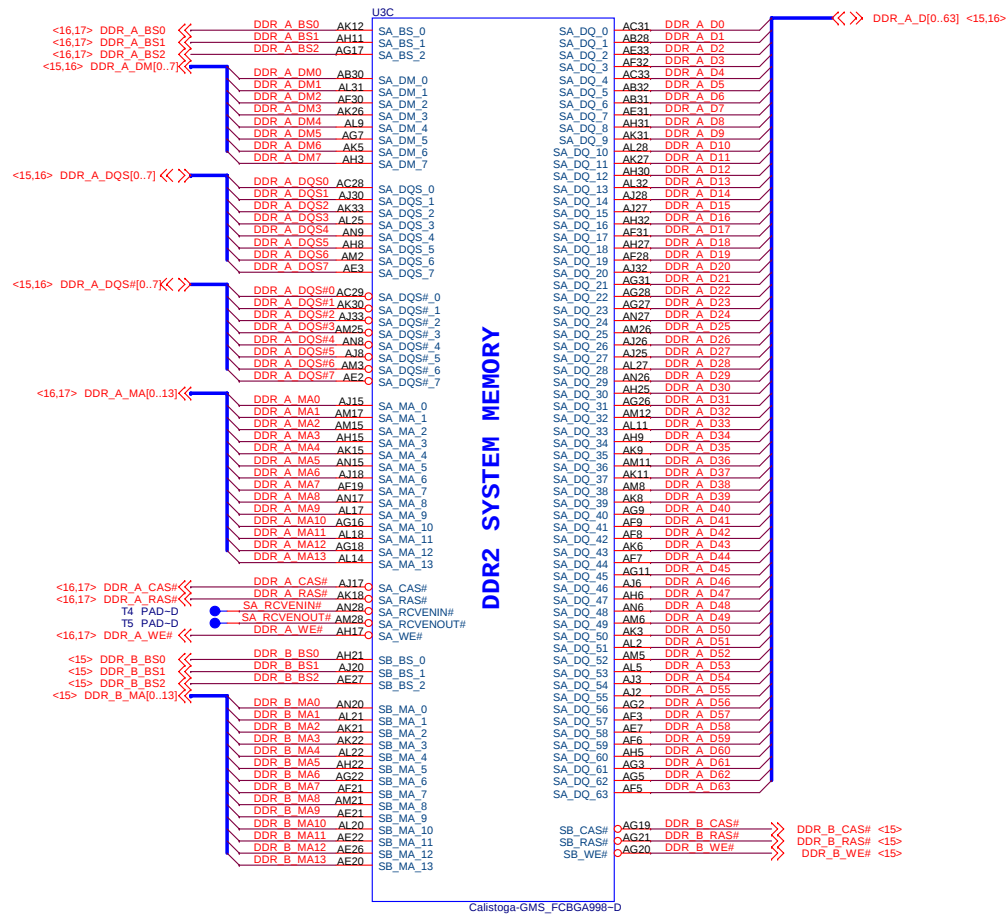
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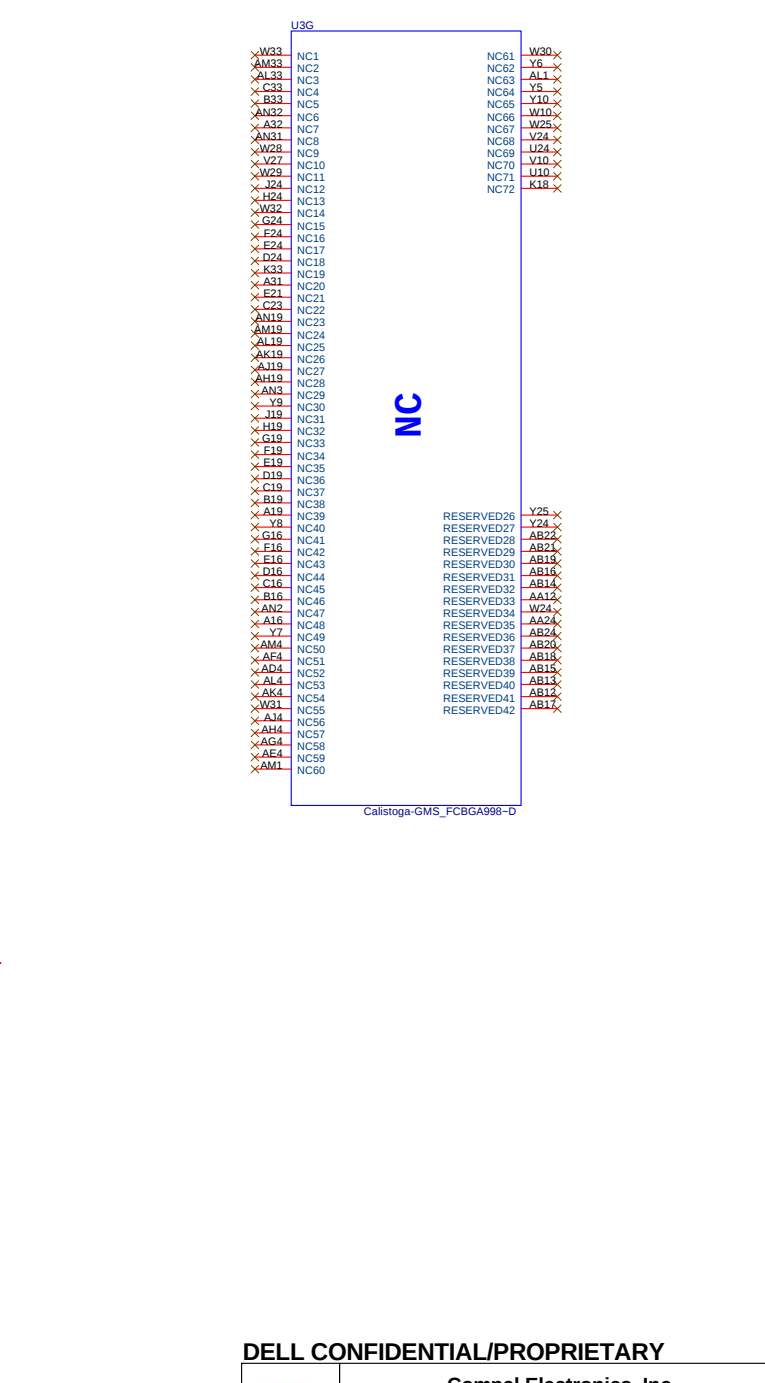
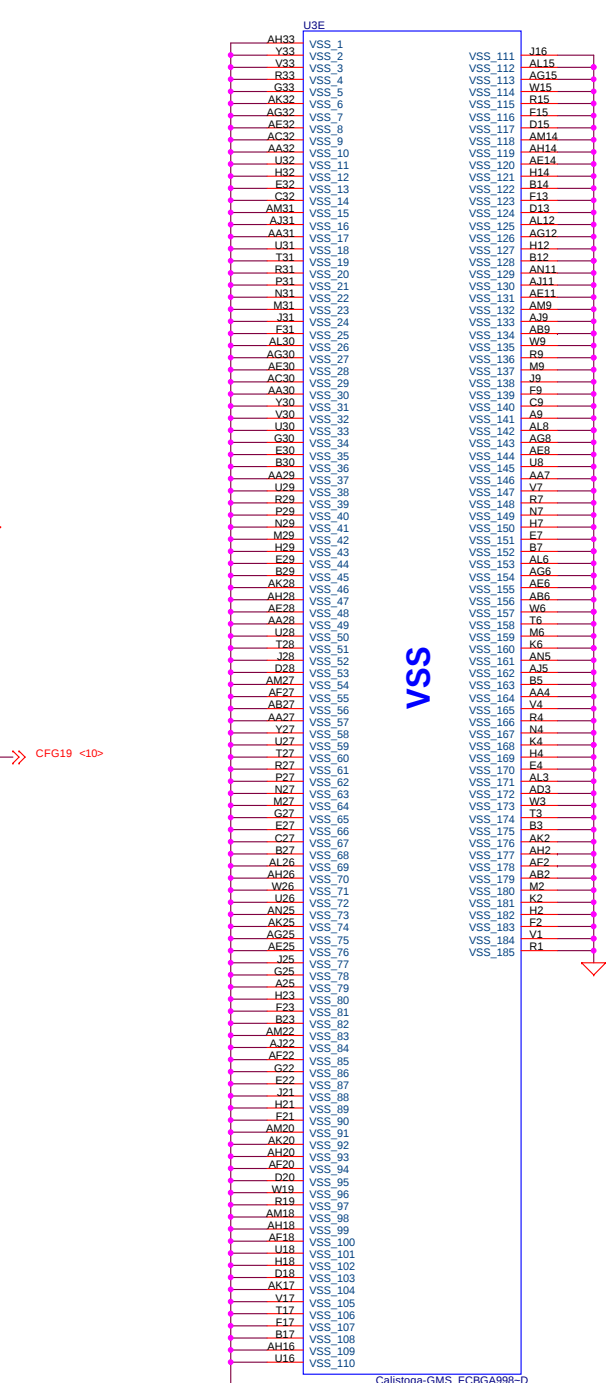
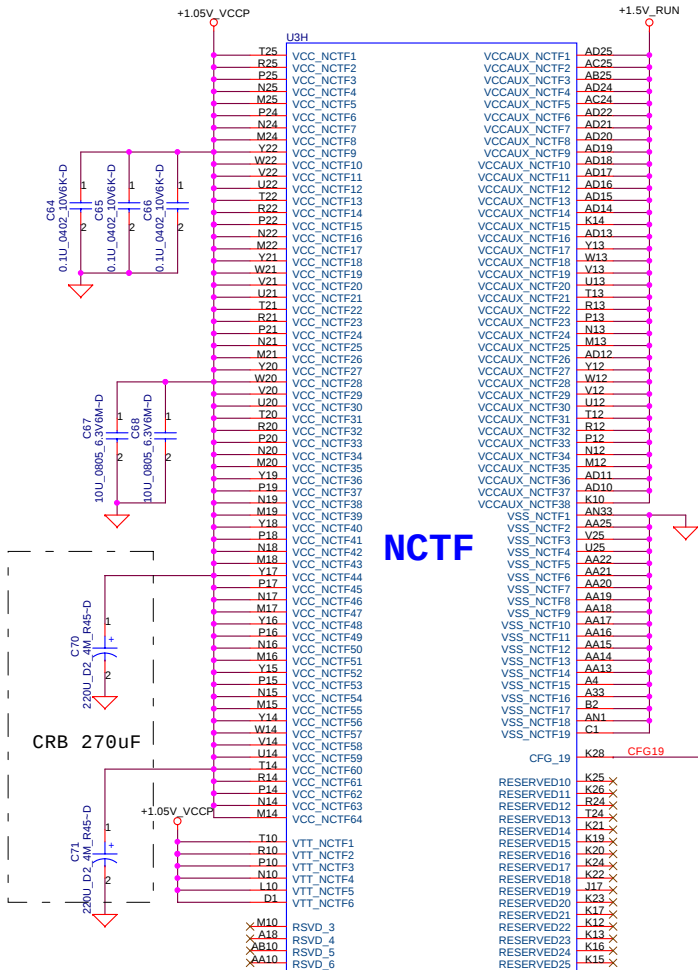
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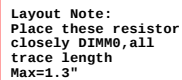
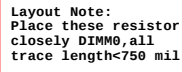
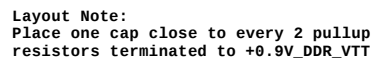
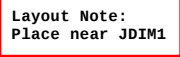
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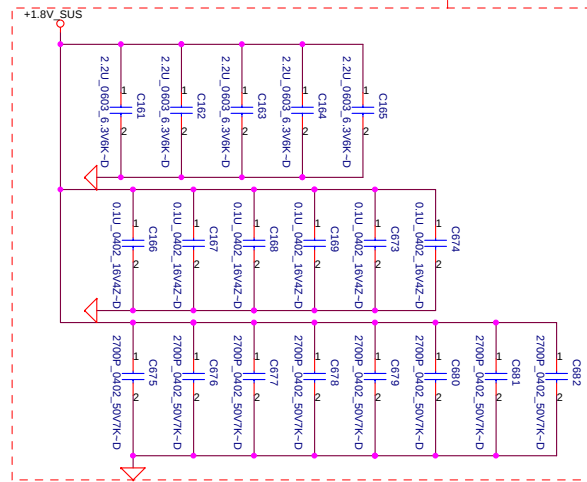


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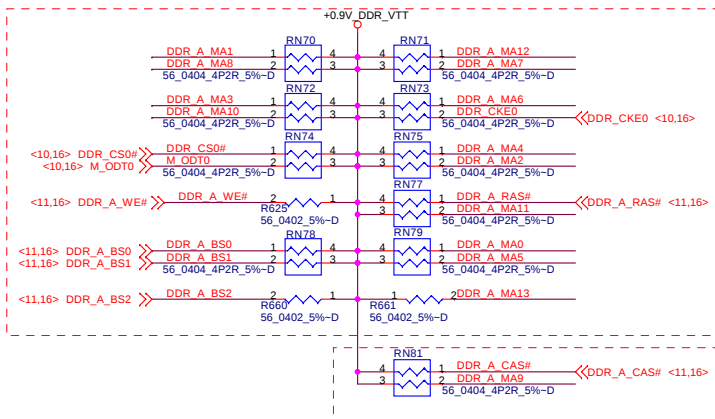
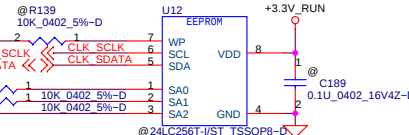
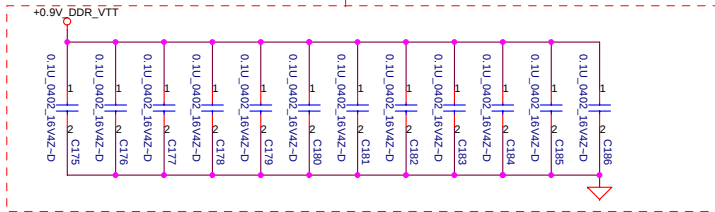
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Title			
DDRII-SODIMM SLOT1			
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Layout Note:
Place near U8,U9,U10,U11

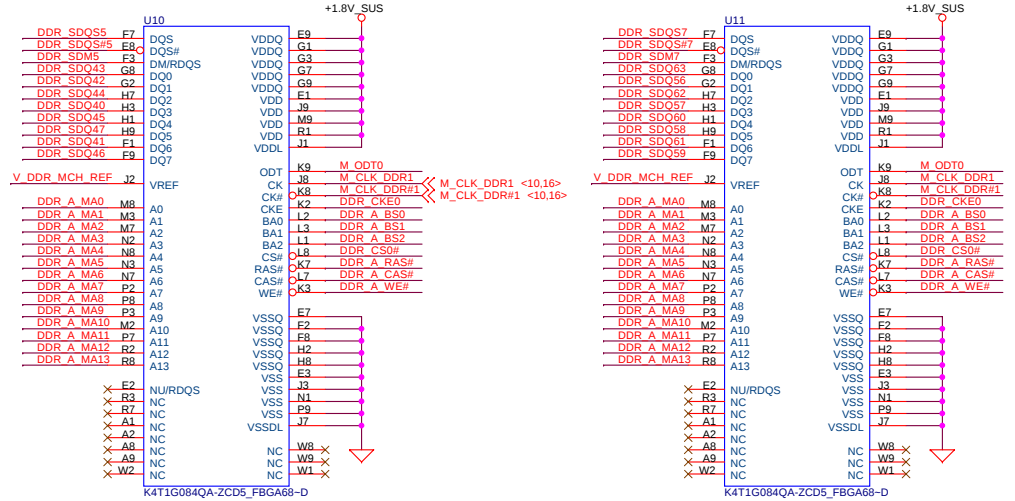
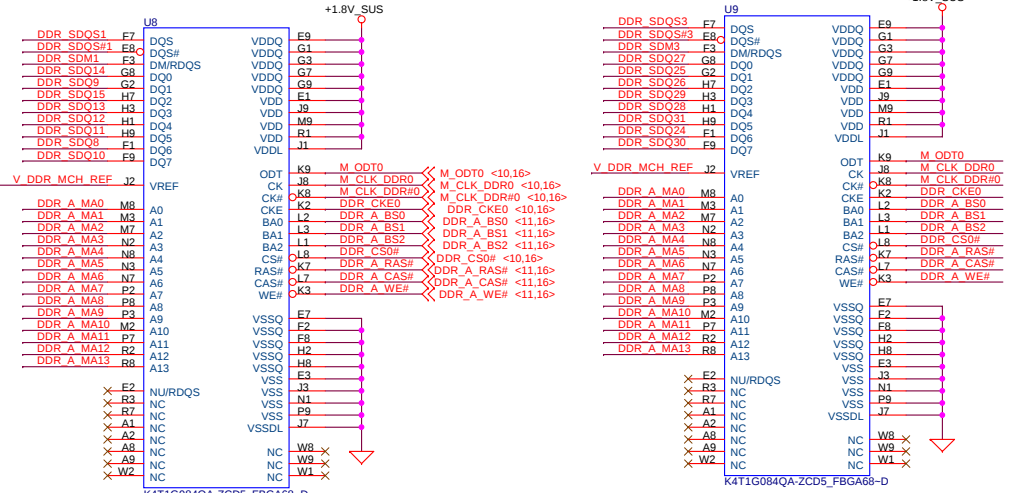


Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V_DDR_VTT

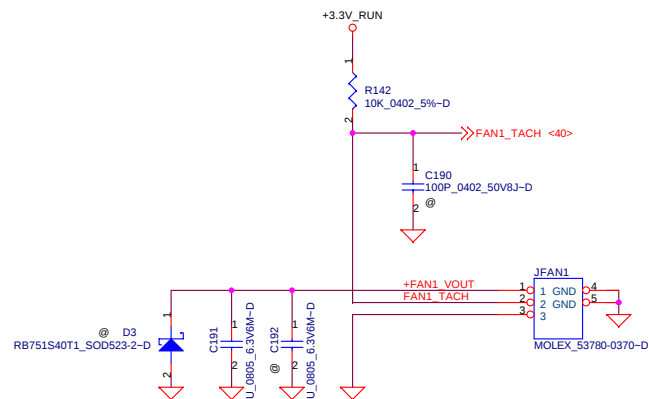


Layout Note:
Place these resistor closely DIMM0, all trace length<750 mil

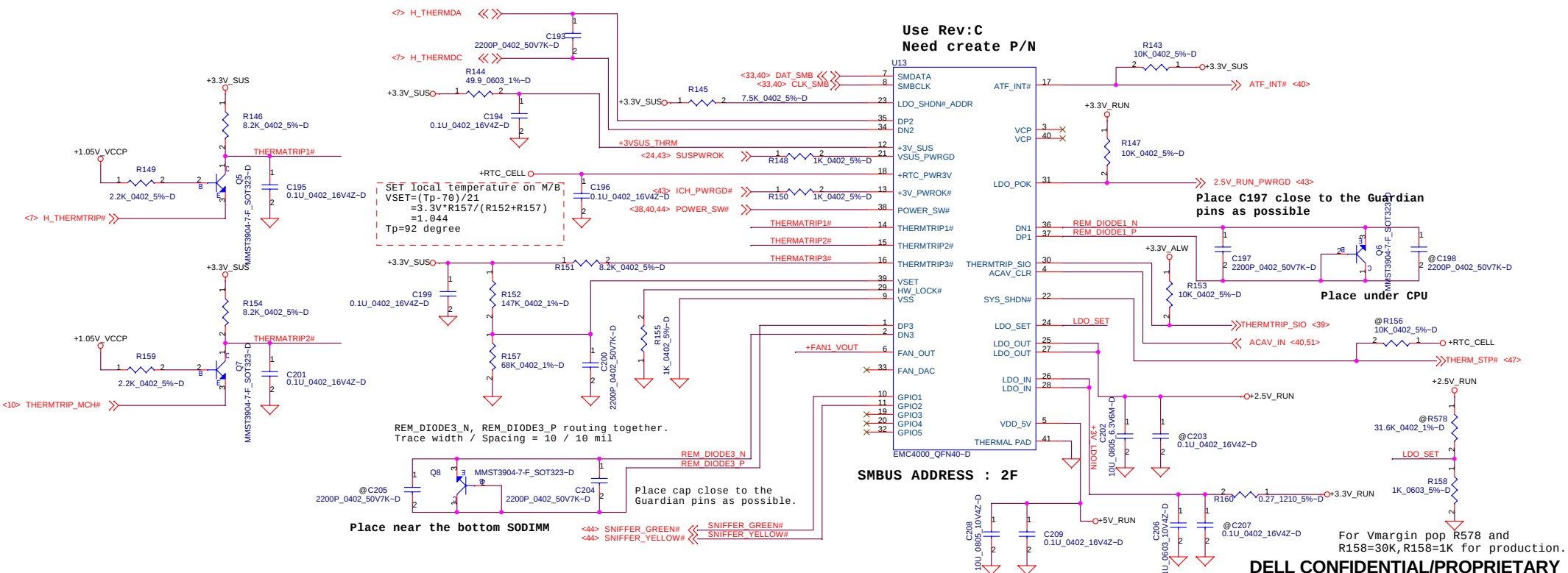
Layout Note:
Place these resistor closely DIMM0, all trace length Max=1.3"



FAN1 Control and Tachometer



Place C341 close to the Guardian pins as possible



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FAN & Thermal Sensor

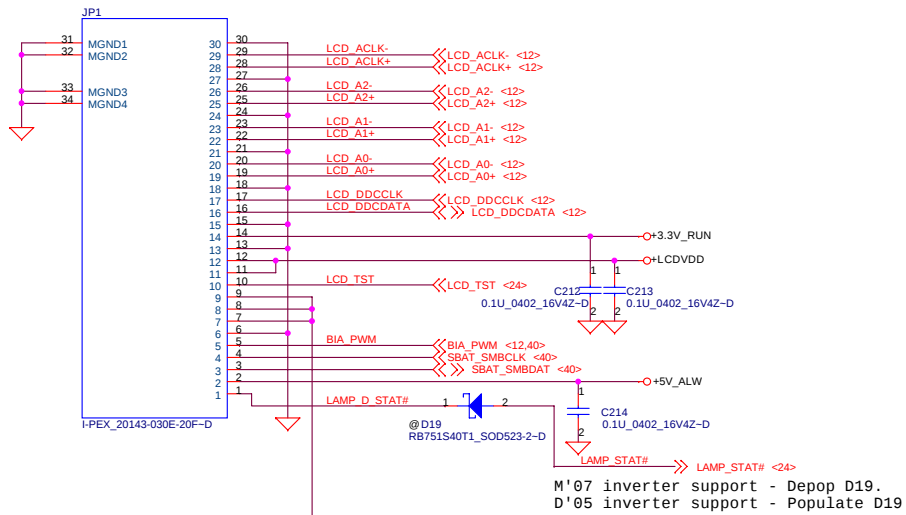
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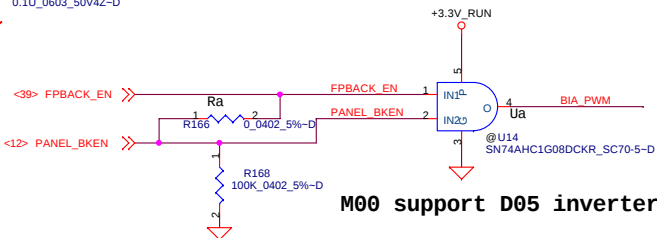
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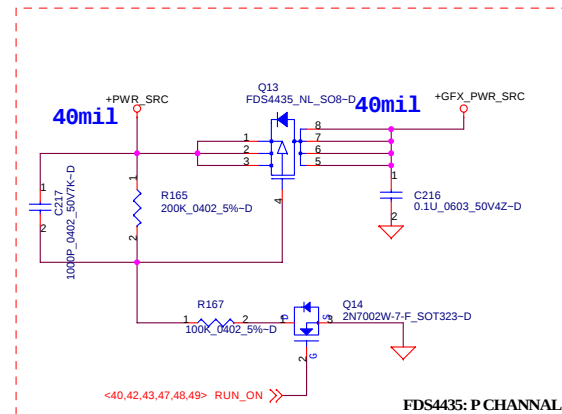
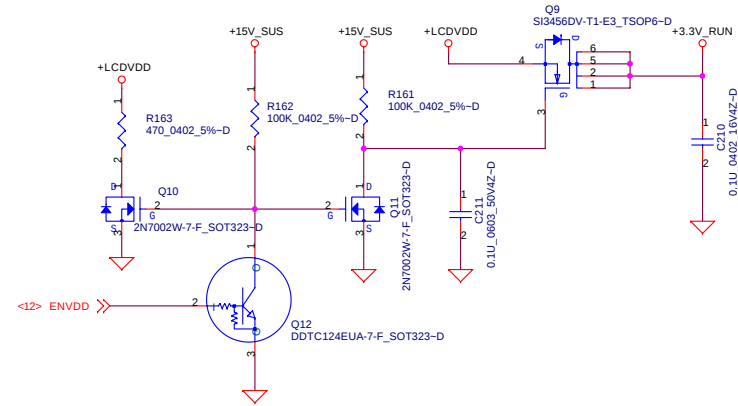


M'07 inverter support - Depop D19.
D'05 inverter support - Populate D19



M00 support D05 inverter

M'07 inverter support - Populate Ra,U54 Depop Ua.
D'05 inverter support - Populate Ua, Depop Ra,U54

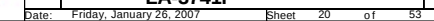
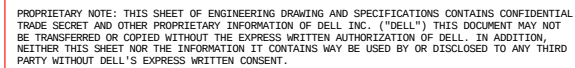


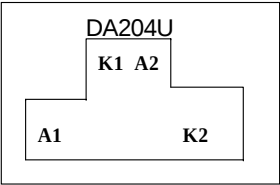
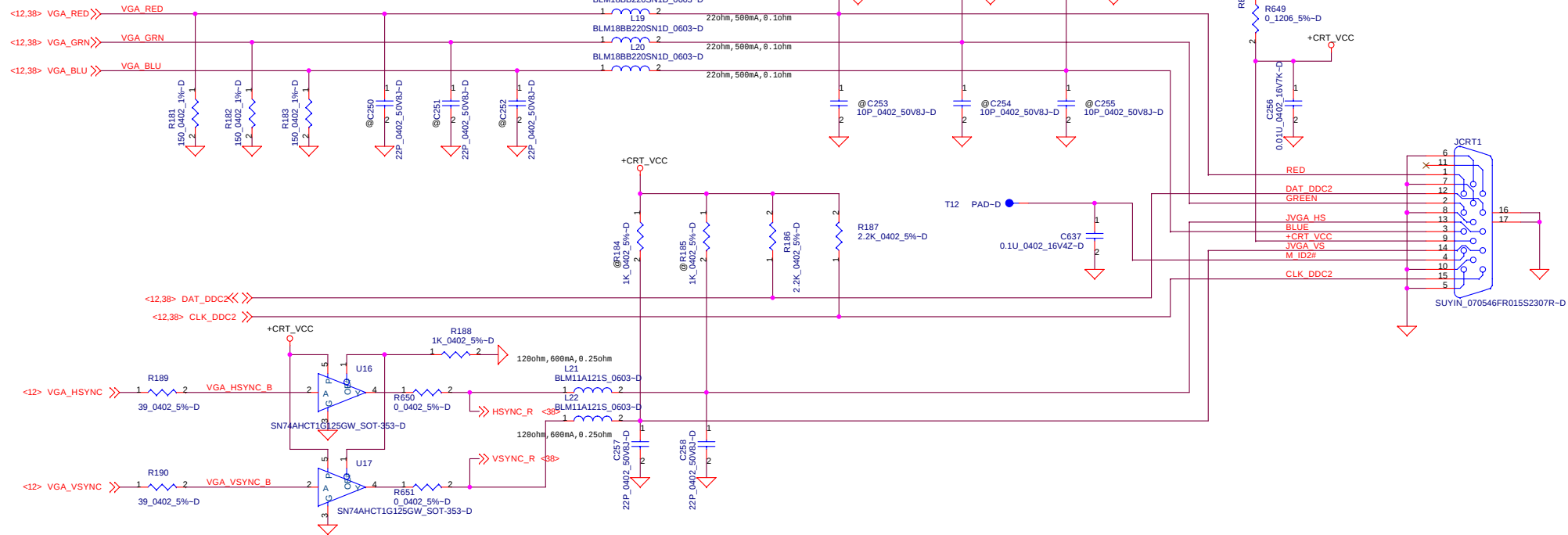
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Internal LVDS				
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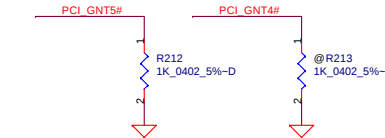
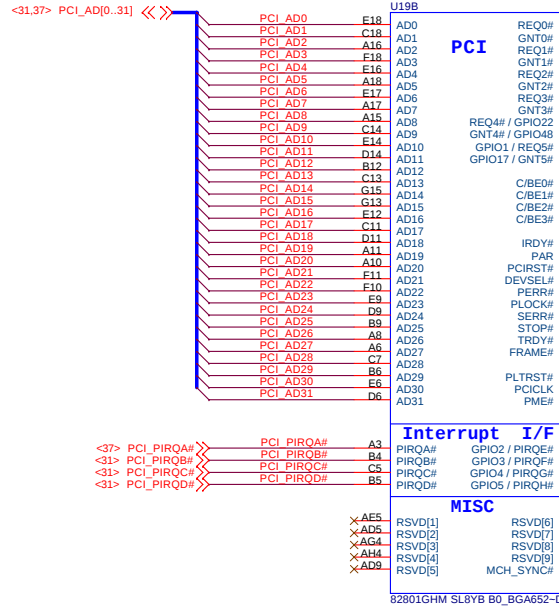
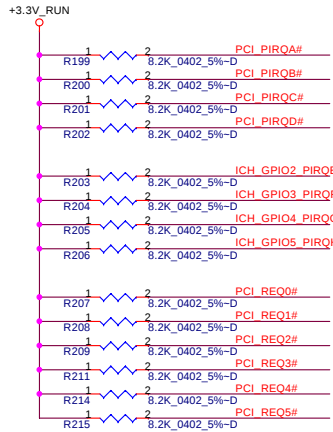
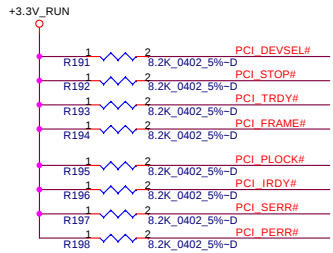




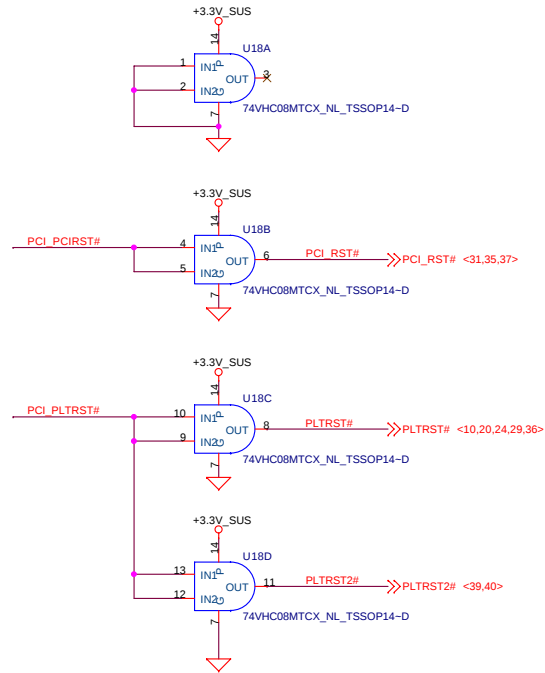
PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION, NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS WAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.



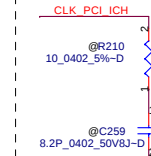
DELL CONFIDENTIAL/PROPRIETARY		
Compal Electronics, Inc.		
Title		
CRT Connector		
Size	Document Number	Rev
	LA-3741P	0.1
Date:	Friday, January 26, 2007	Sheet 21 of 53



	GNT5# R214	GNT4# R213
LPC (11)	unstuff	unstuff
PCI (10)	unstuff	stuff
SPI (01)	stuff	unstuff



Place closely pin U45.A9



DELL CONFIDENTIAL/PROPRIETARY

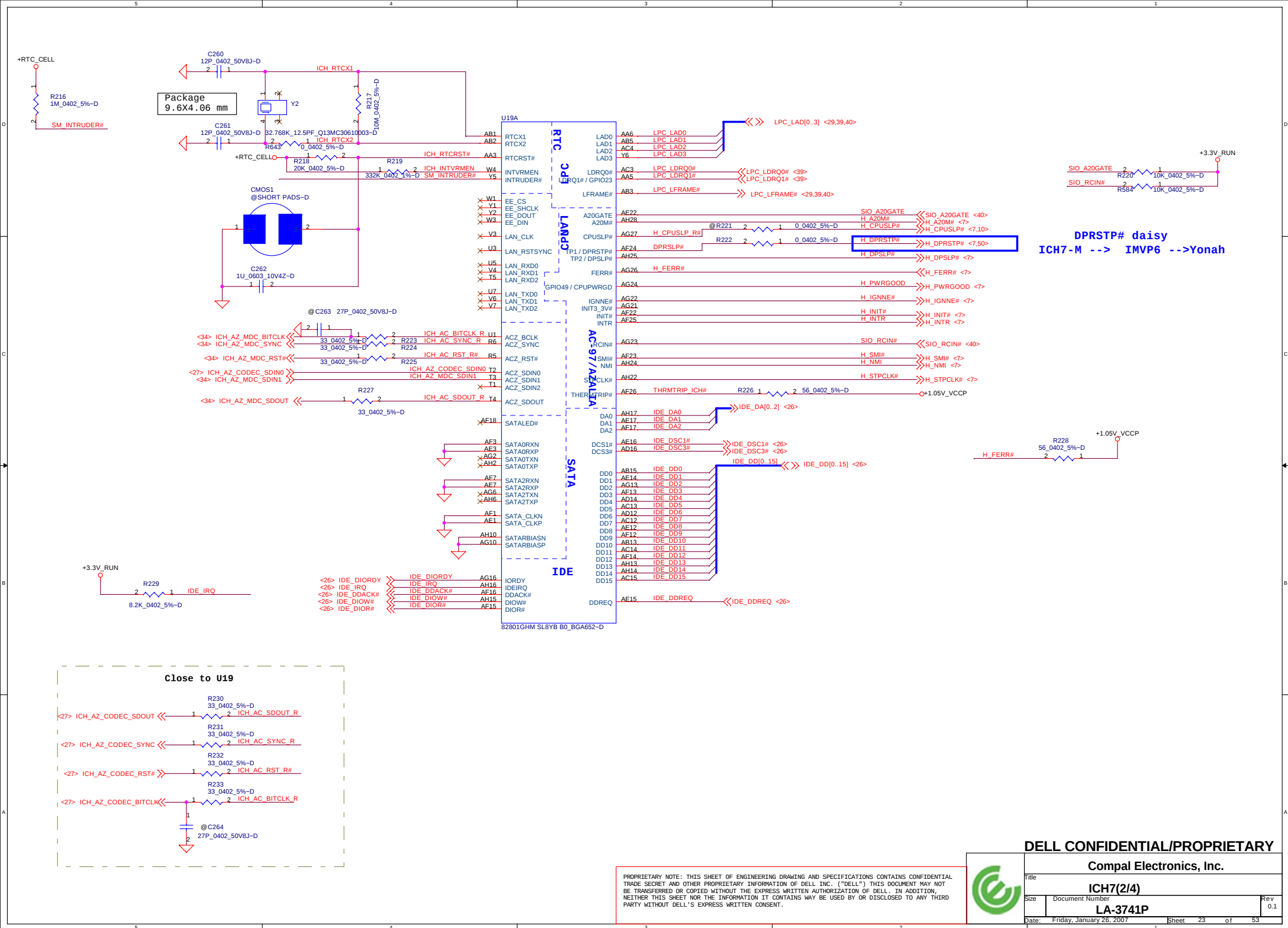


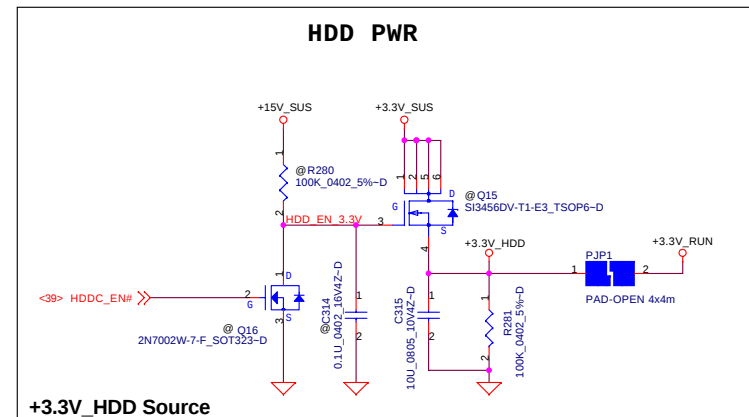
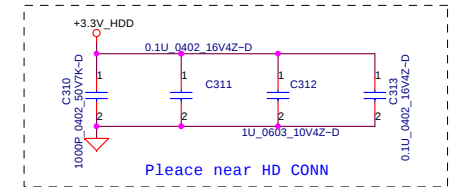
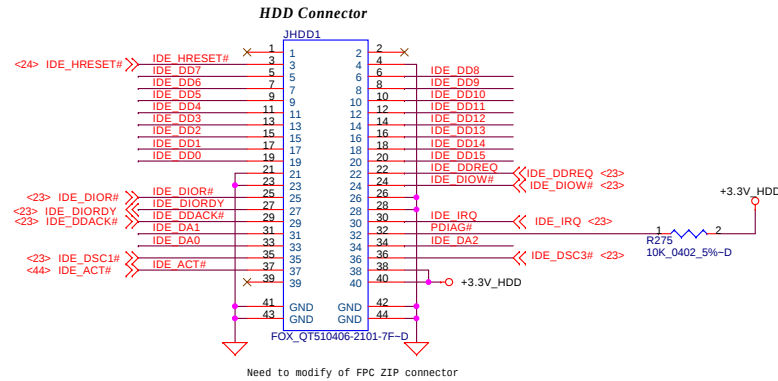
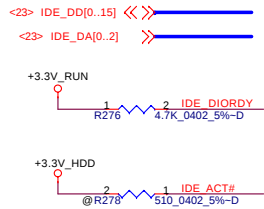
Compal Electronics, Inc.

ICH7(1/4)

LA-3741P

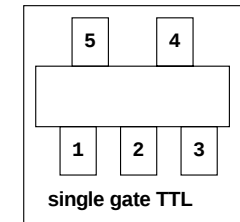
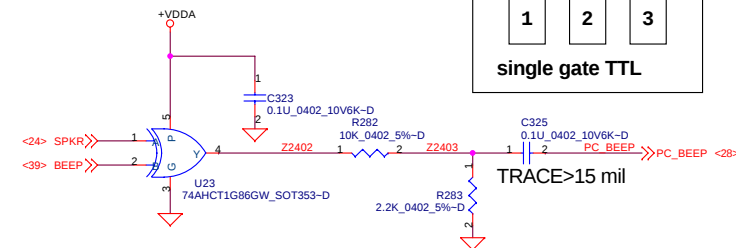
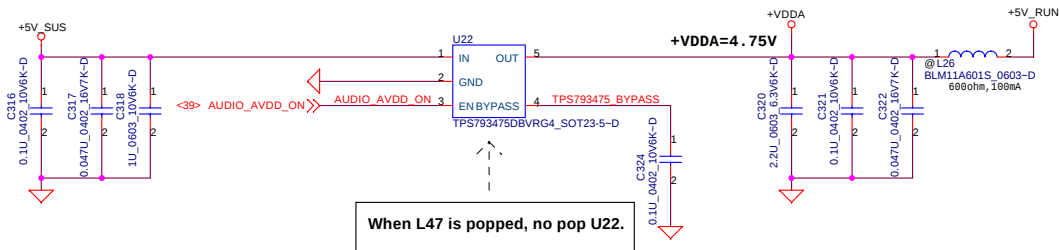
PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION, NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.



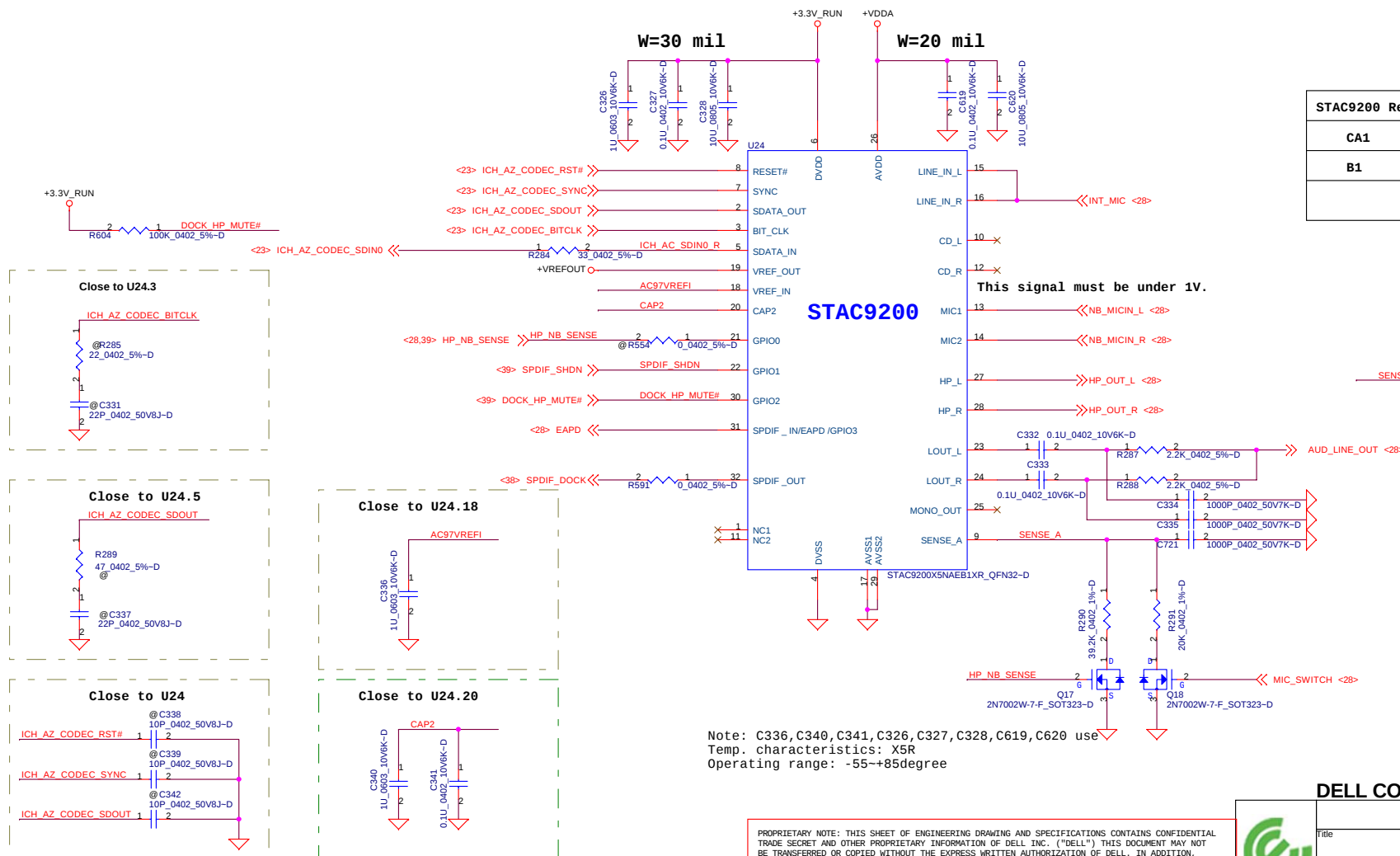


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DELL CONFIDENTIAL/PROPRIETARY			
Title		IDE HDD Connector	
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STAC9200 Rev.	R22	R109
CA1	5.11K	10K
B1	39.2K	20K



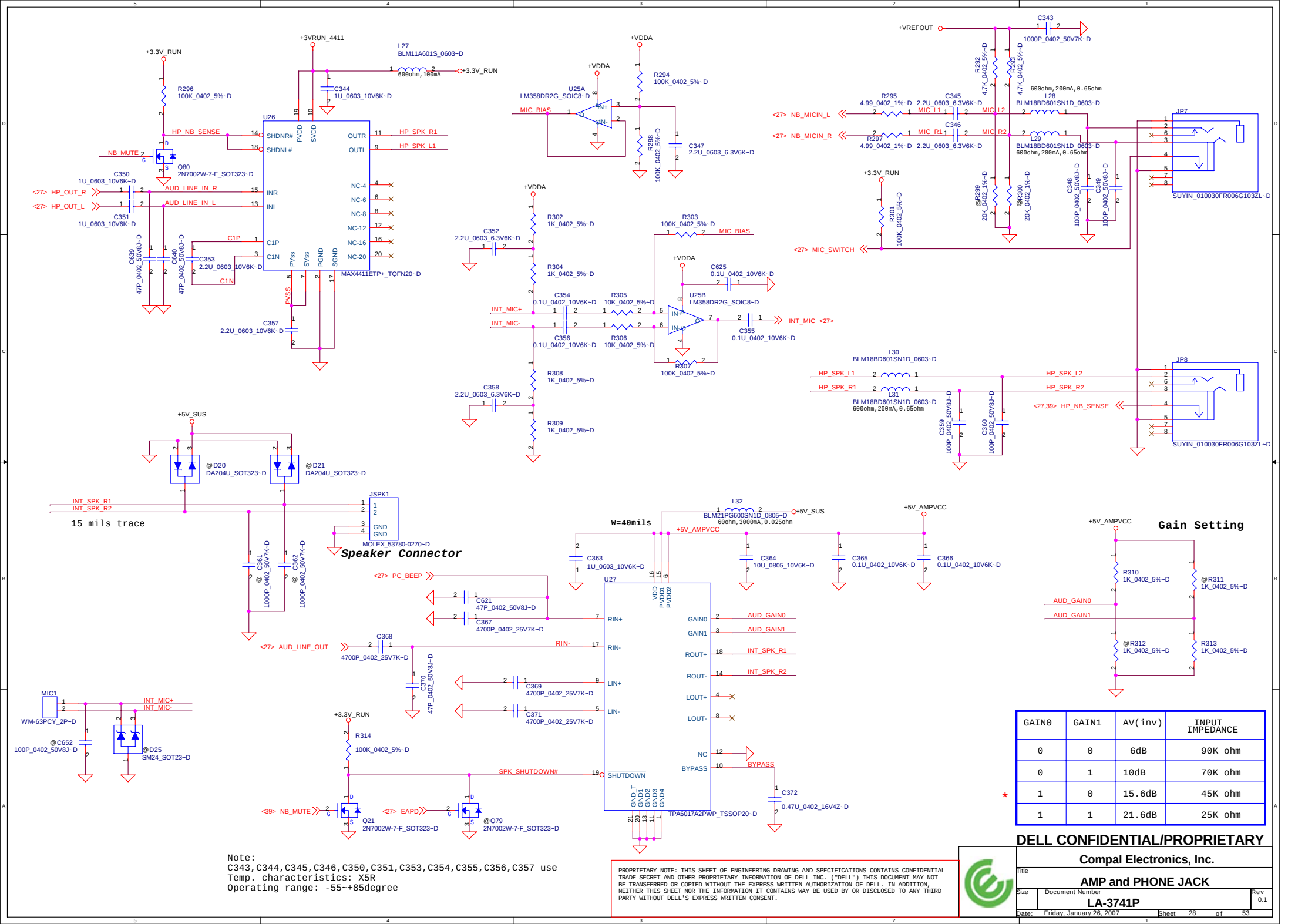
Note: C336, C340, C341, C326, C327, C328, C619, C620 use Temp. characteristics: X5R
Operating range: -55~+85degree

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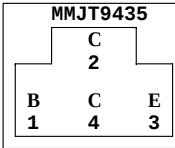


Compal Electronics, Inc.		
Azalia (HD) Codec		
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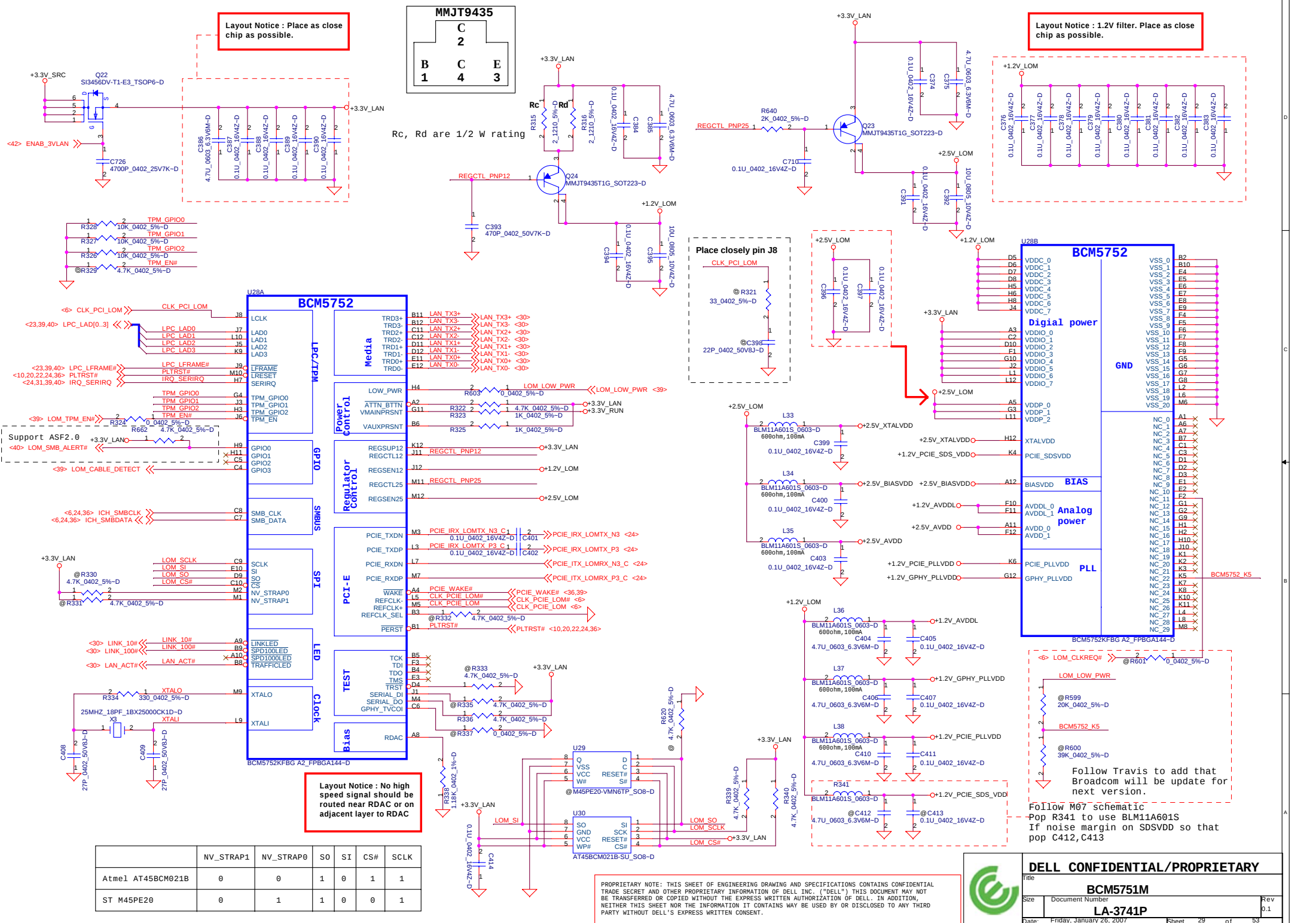


Layout Notice : Place as close chip as possible.



Rc, Rd are 1/2 W rating

Layout Notice : 1.2V filter. Place as close chip as possible.



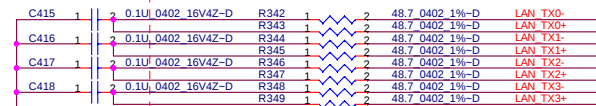
	NV_STRAP1	NV_STRAP0	S0	SI	CS#	SCLK
Atmel AT45BCM021B	0	0	1	0	1	1
ST M45PE20	0	1	1	0	0	1

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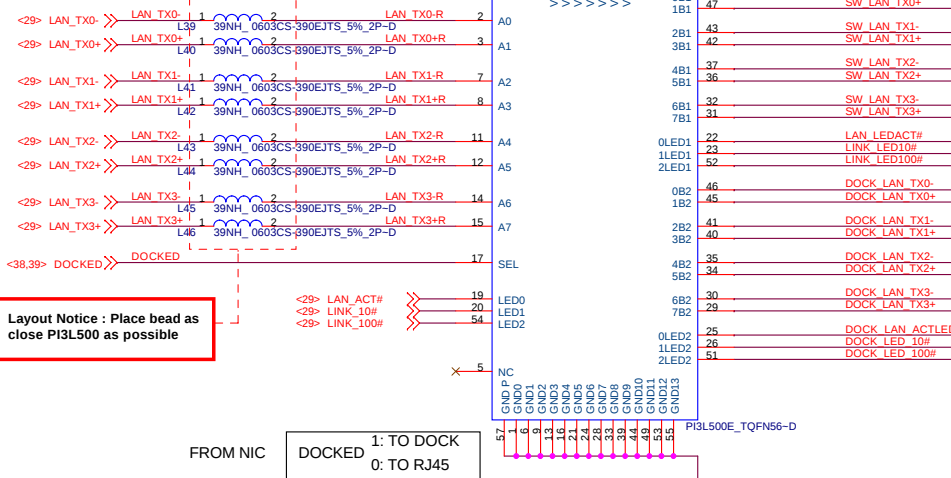
DELL CONFIDENTIAL/PROPRIETARY

Title			
BCM5751M			
Size	Document Number	Rev	
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Date	Friday, January 26, 2007	Sheet	29 of 33

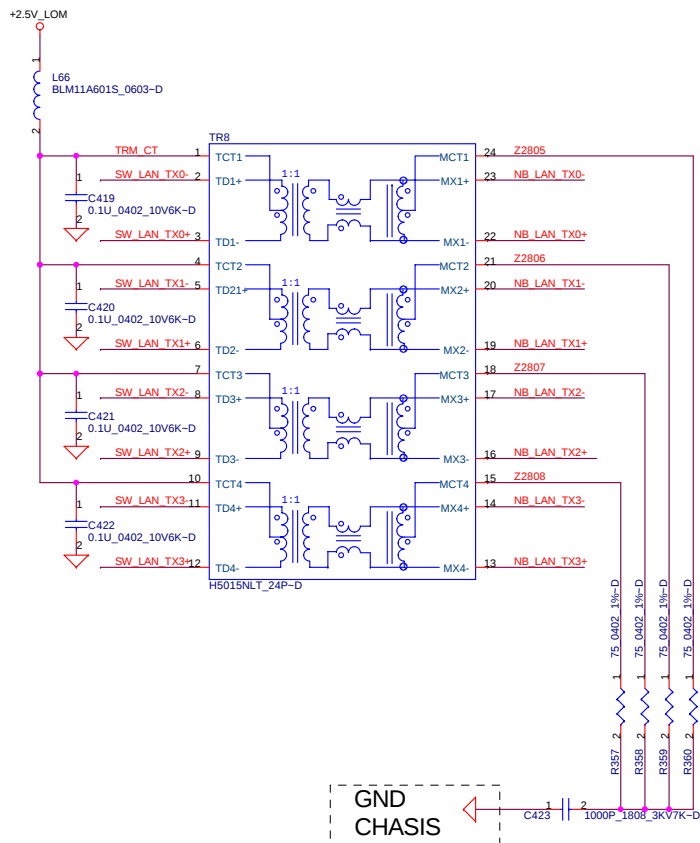


Layout Notice : Place termination as close as ASIC as possible

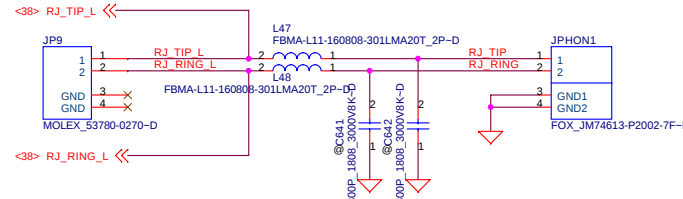
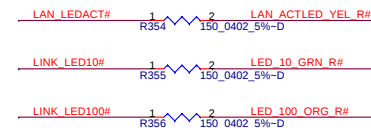
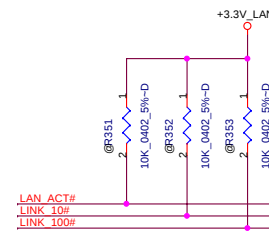
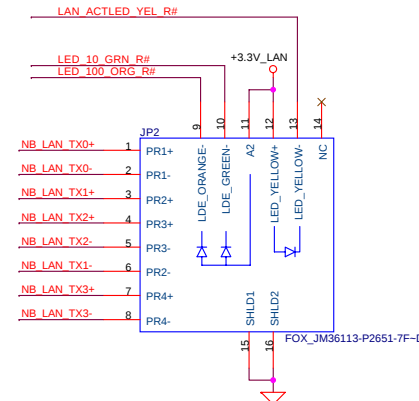
The resistors need at least 1/16W



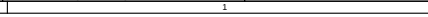
Layout Notice : Place bead as close PI3L500 as possible

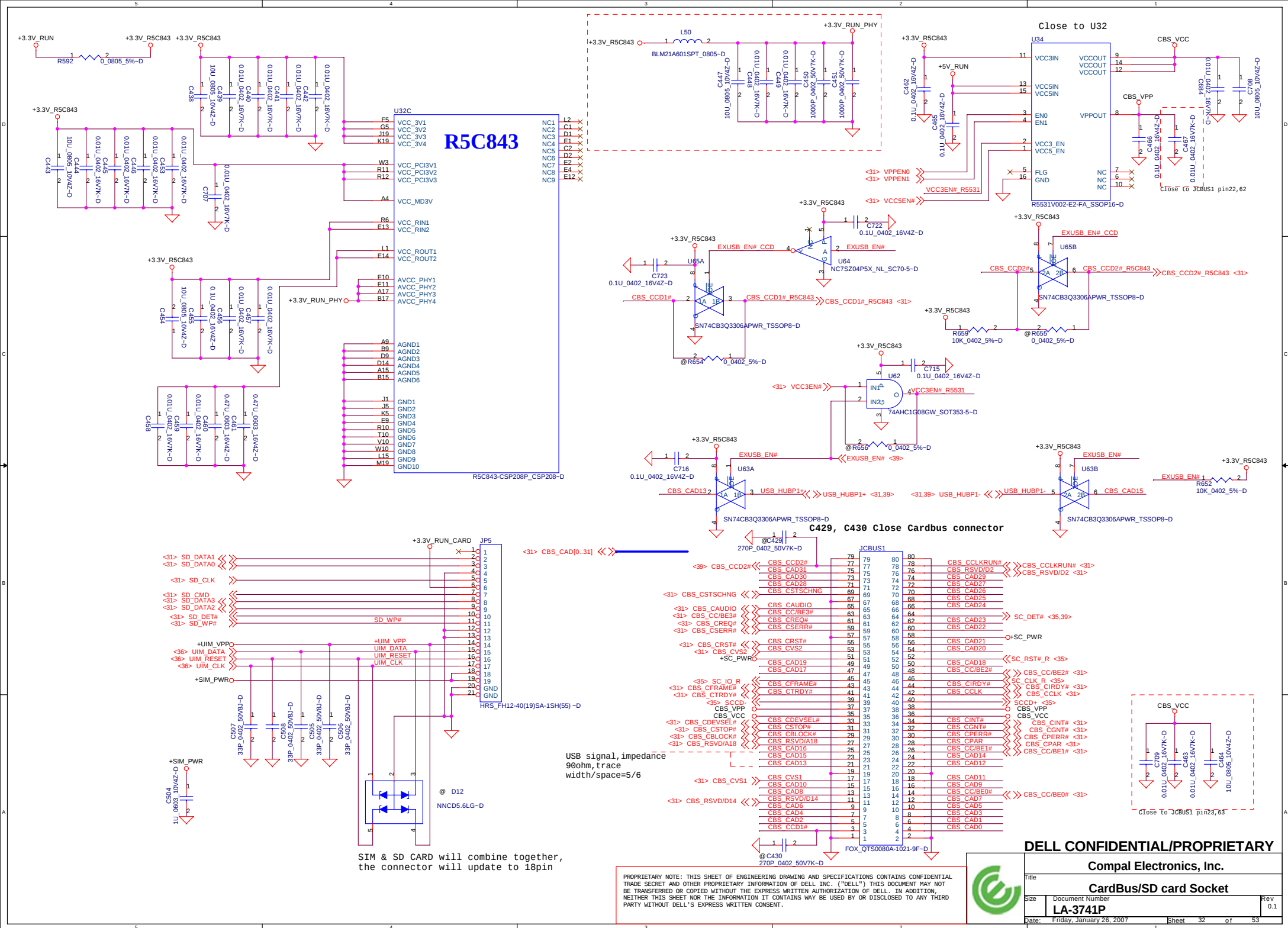


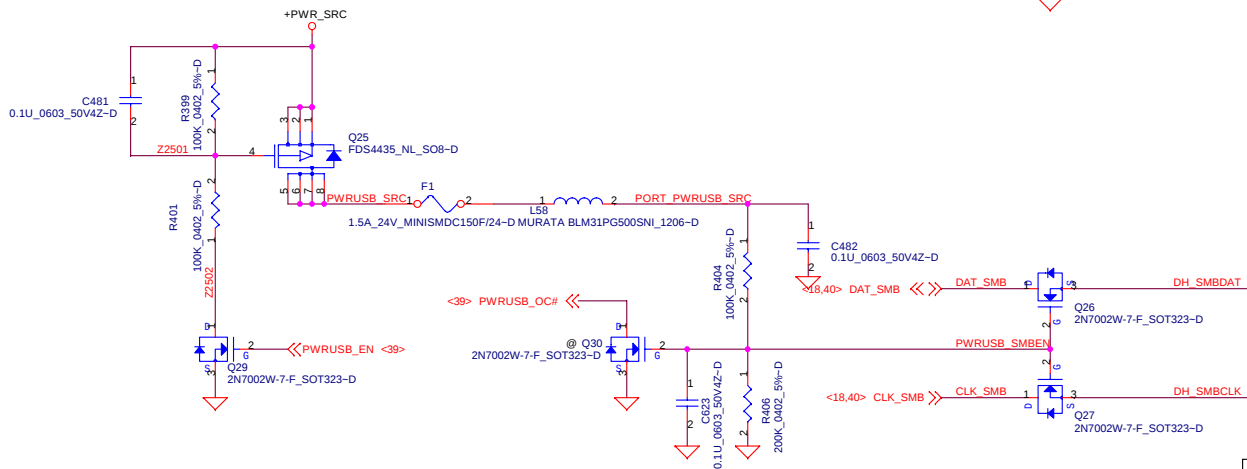
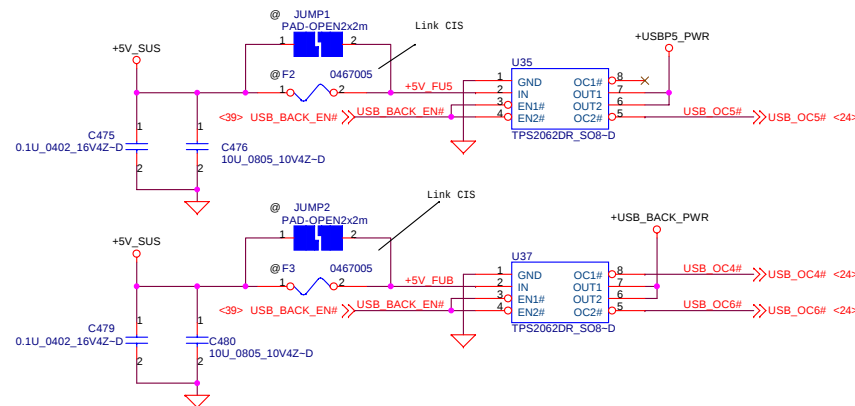
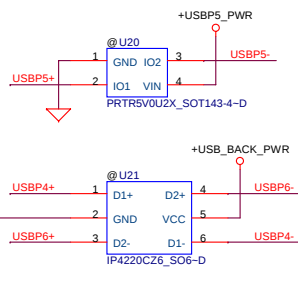
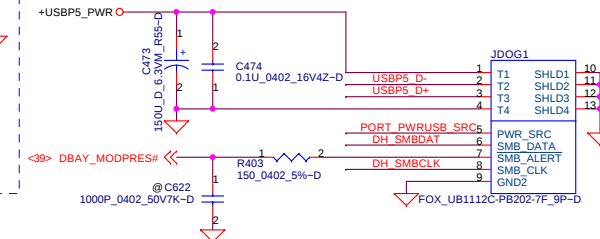
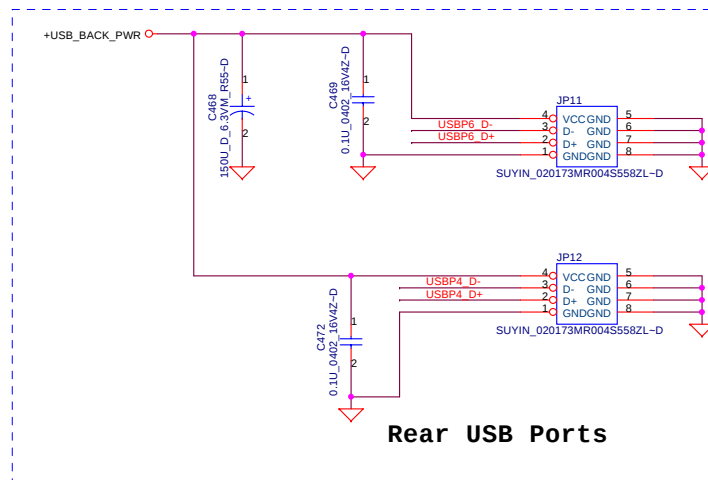
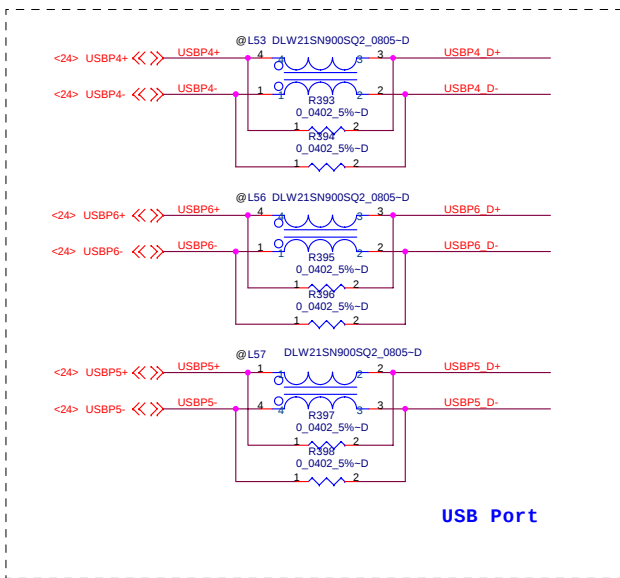
GND CHASIS



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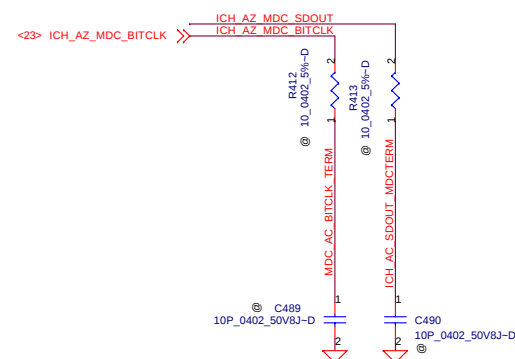
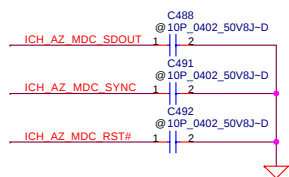
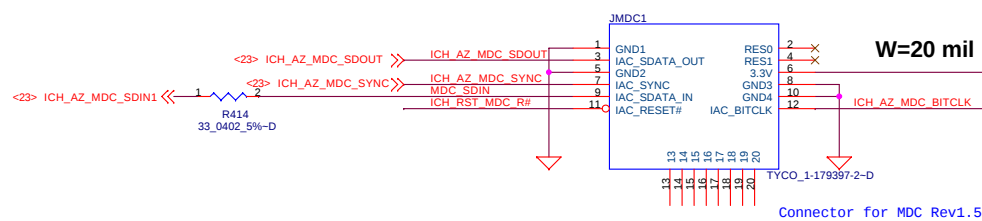
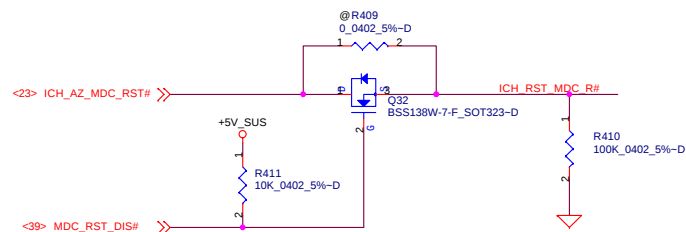
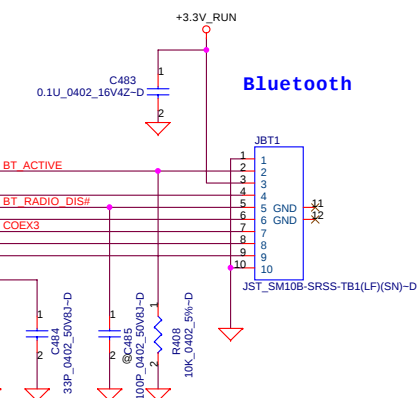
USB 2.0 Port

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1	GND	RES	2
3	IAC_SDATA0	RES	4
5	GND	3.3V	6
7	IAC_SYNC	GND	8
9	IAC_SDATAIN	GND	10
11	IAC_RESET#	IAC_BITCLK	12

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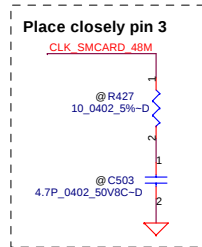
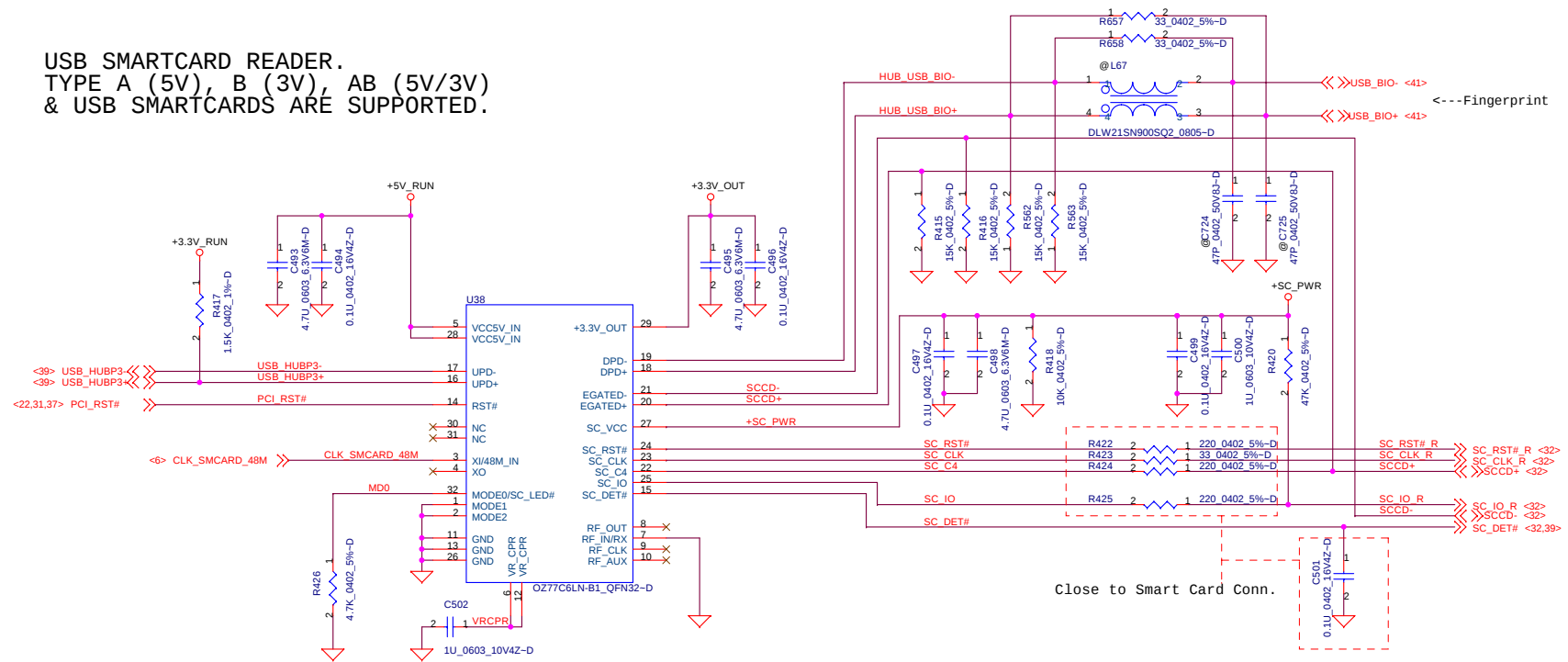
BT PORT & MDC

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USB SMARTCARD READER.
TYPE A (5V), B (3V), AB (5V/3V)
& USB SMARTCARDS ARE SUPPORTED.



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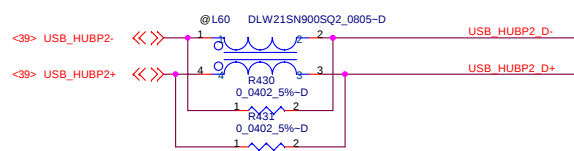
Smart Card OZ77C6

LA-3741P

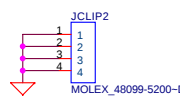
Rev 0.1

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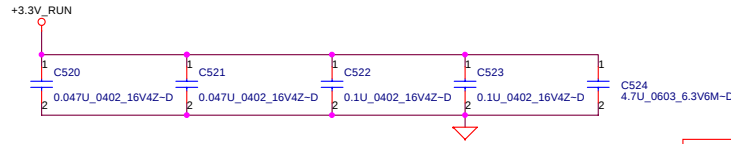
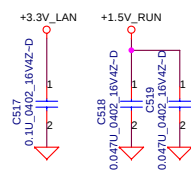
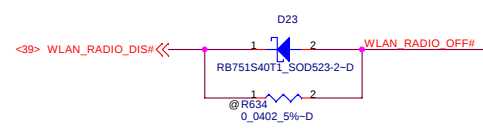
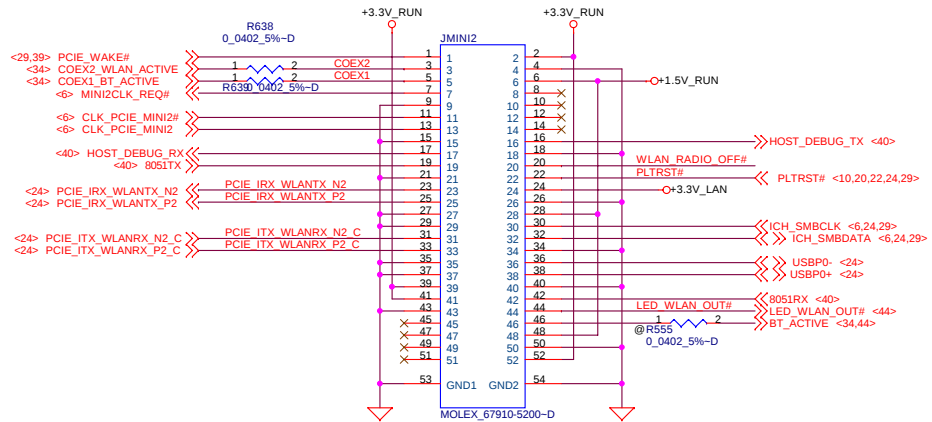
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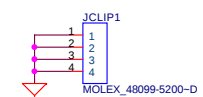
Mini Card Wire less LAN



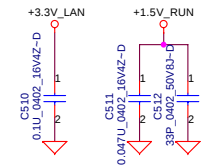
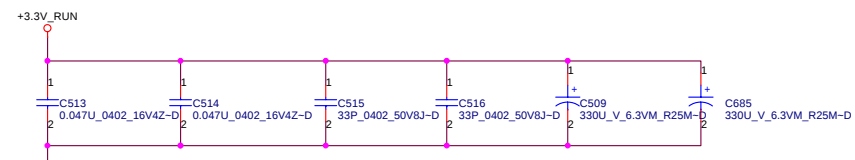
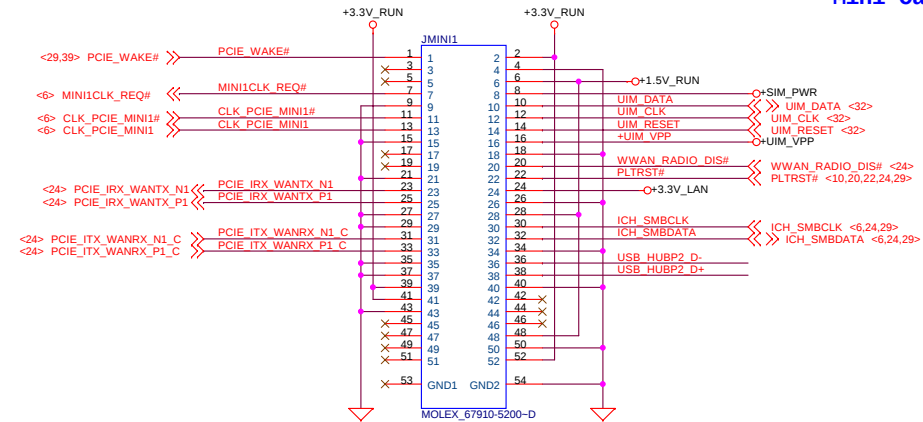
Mini-Card Latch



Mini Card Wire less WAN



Mini-Card Latch



PWR Rail	Voltage Tolerance	Primary Power		Aux Power
		Peak	Normal	Normal
+3.3V	+ -9%	1000	750	
+3.3Vaux	+ -9%	330	250	250 (wake enable) 5 (Not wake enable)
+1.5V	+ -5%	500	375	NA

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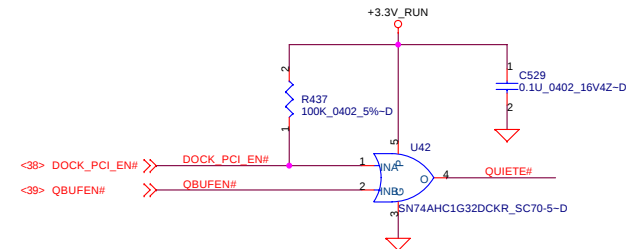
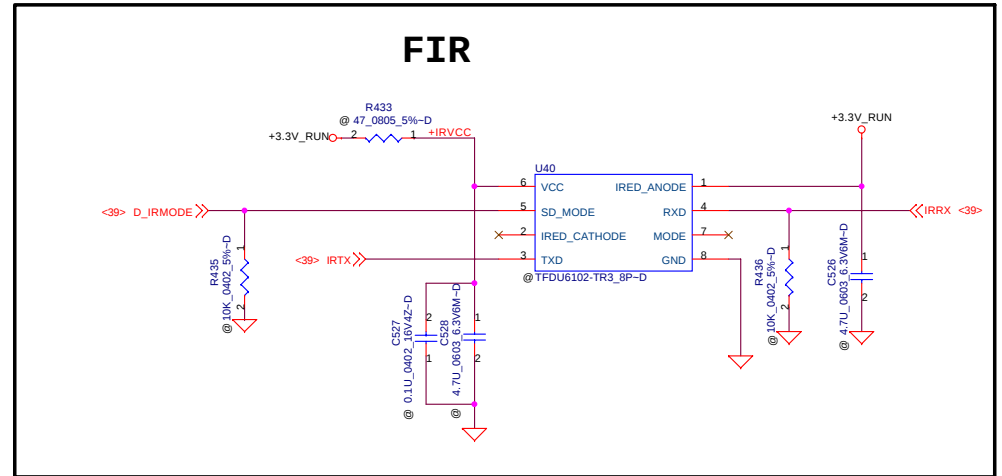
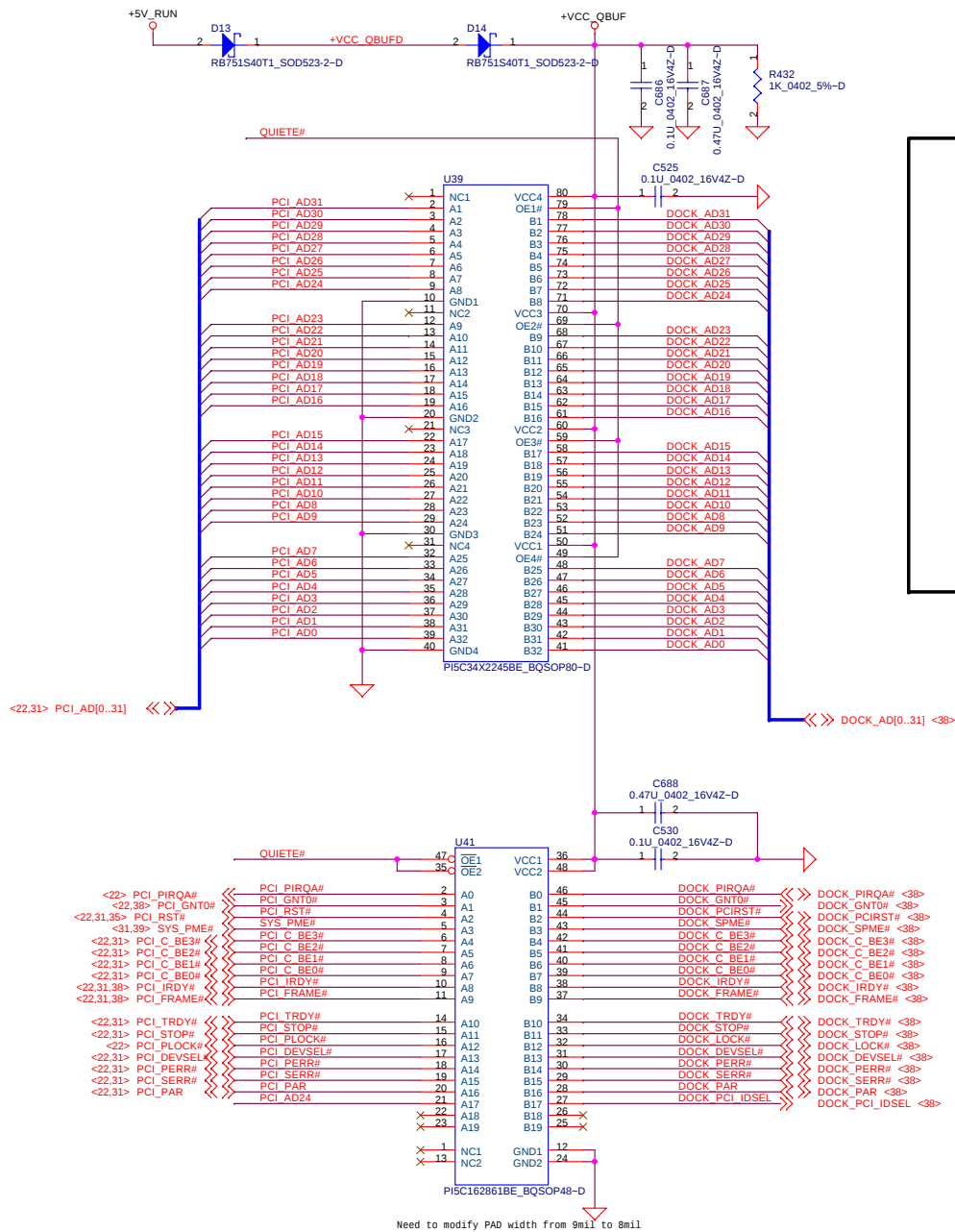
Compal Electronics, Inc.

Mini Card

LA-3741P

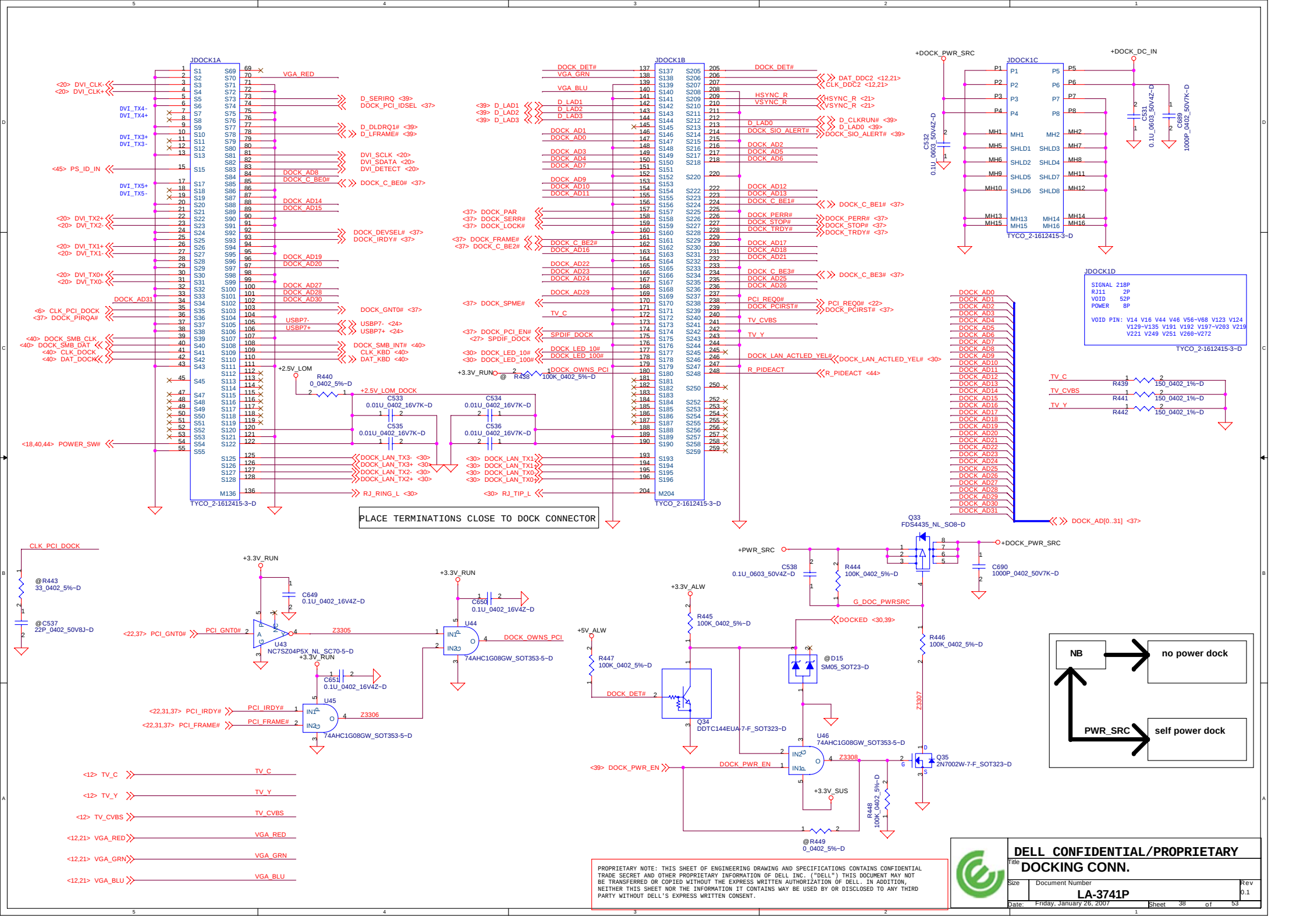
Friday, January 26, 2007

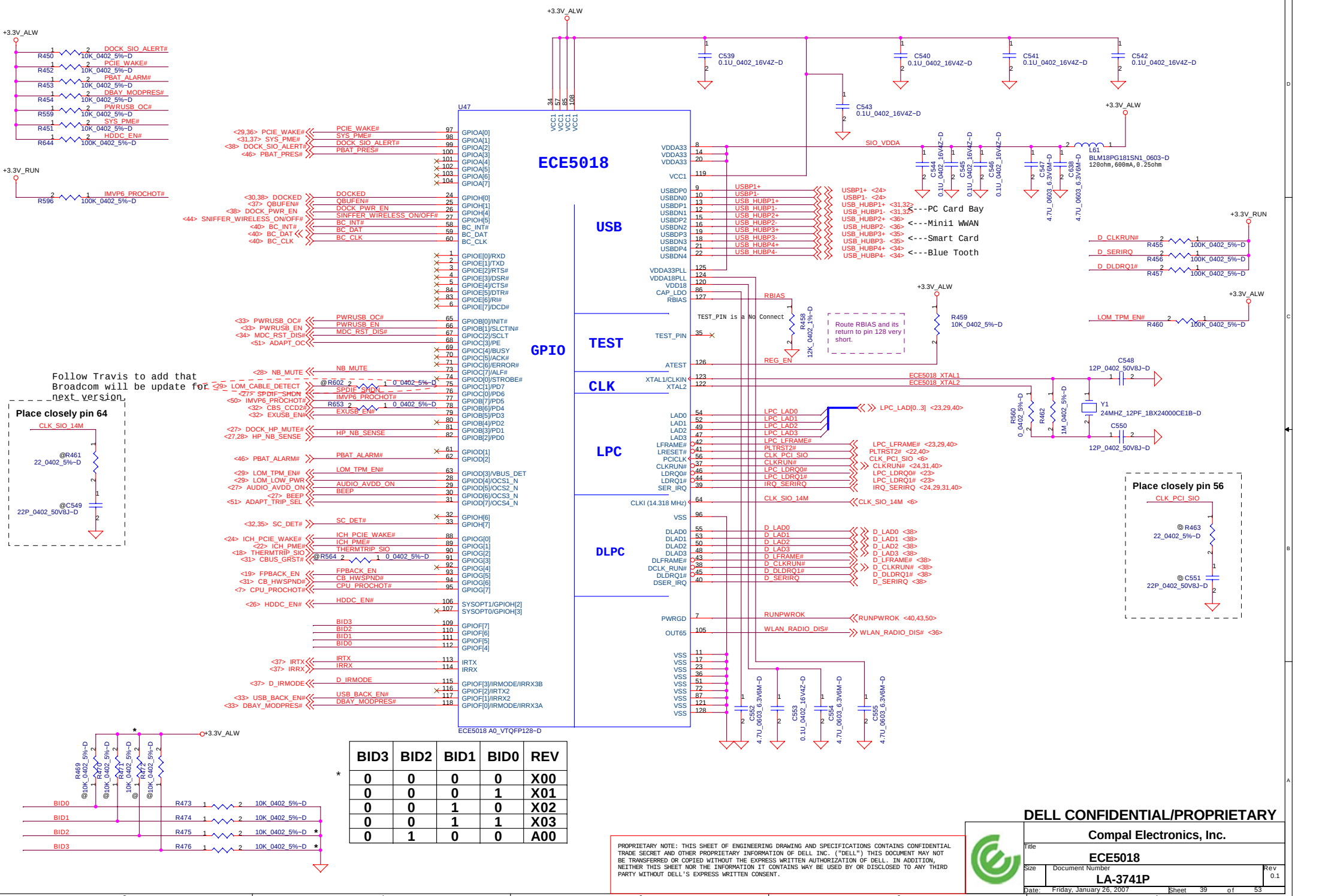
Sheet 35 of 53



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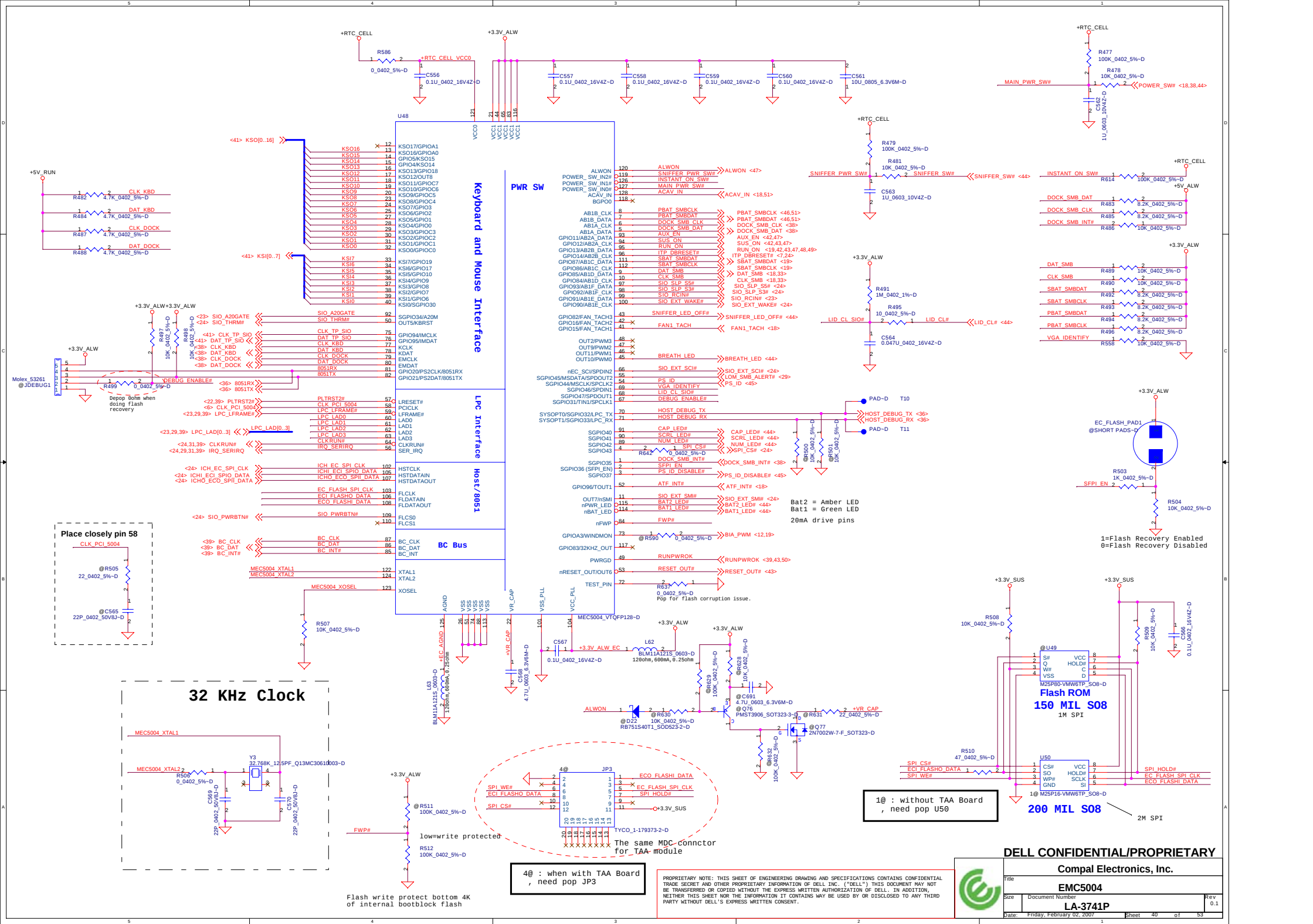
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		Title	
		DOCKING BUFFER & FIR	
Size	Document Number	Rev	
		LA-3741P	
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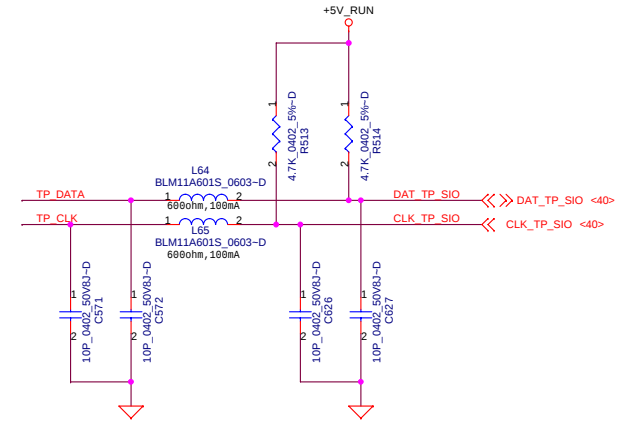
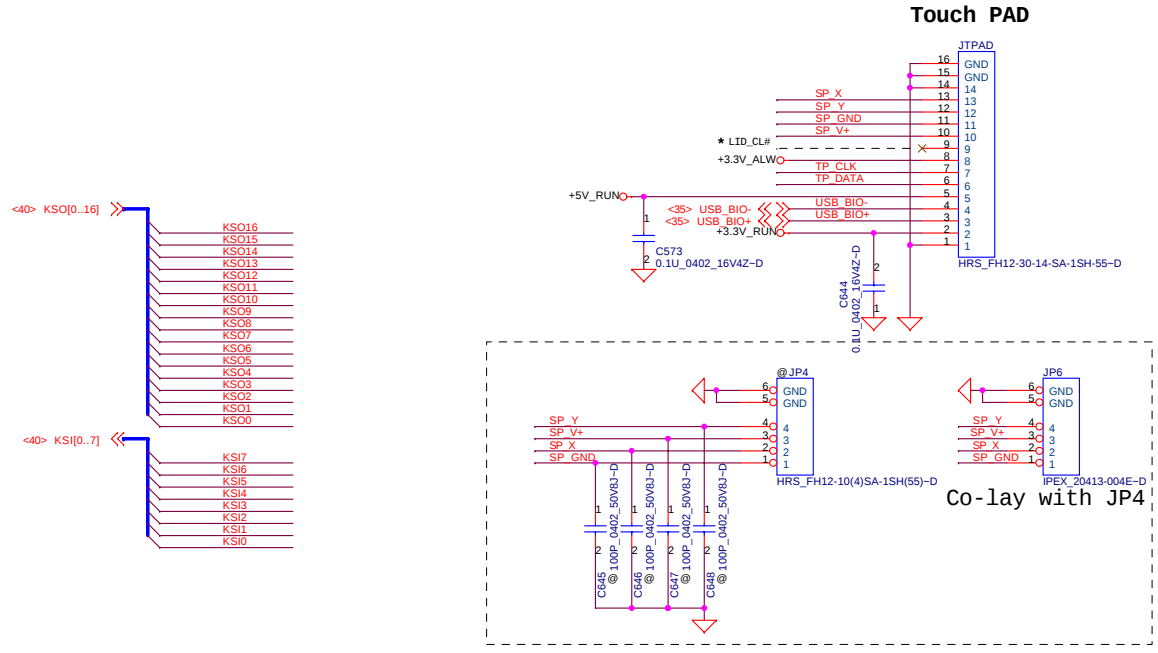




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Part Number	Description
PK230005C8L	SPK PACK 00B 1W 80HM

Part Number	Description
DC000002T0L	PCMCIA FOXCONN ICA06501-CR-4F

Part Number	Description
DC020009980L	H-CONN SET 00B M/B-B/T

Part Number	Description
DC020009960L	H-CONN SET 00B M/B-MDC

Part Number	Description
DA30000101L	FPC 00B LF-3072P REV1 1/P FPC WITH 810

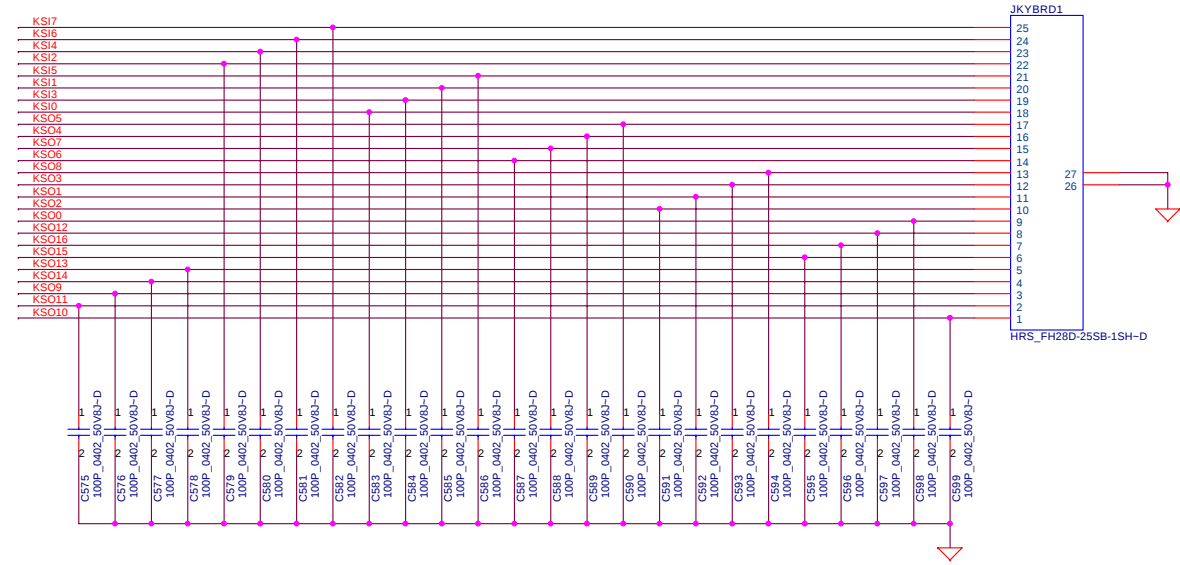
Part Number	Description
DC020008080L	H-CONN SET 00B MB-LCD 12 WXGA

Part Number	Description
DA300001N1L	FPC 00B LF-3071P REV1 HITACHI

Part Number	Description
GC020008R00	BATT CR2025 W/CABLE 170MAH MB 00B 0FD

Part Number	Description
PK000003M0L	TRACK PAD ALPS KEDDEN10A BIOSENSOR

Part Number	Description
DA300001S1L	FPC 00B LF-3073P REV1 LED FPC



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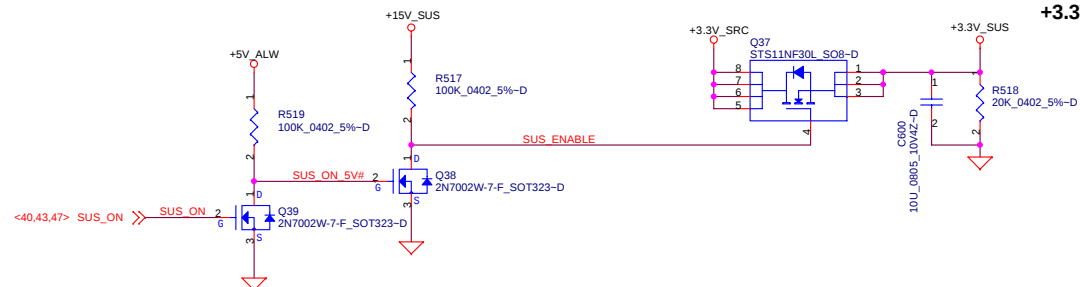
Compal Electronics, Inc.

INT KB & FT & LID & TOUCH PAD

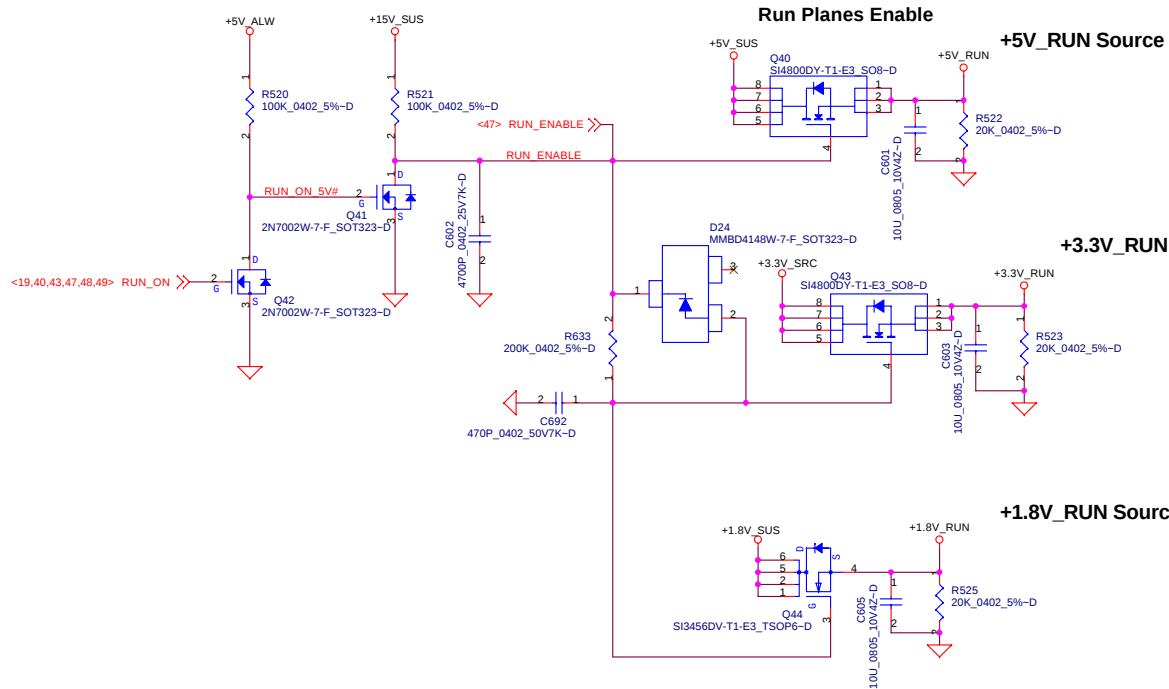
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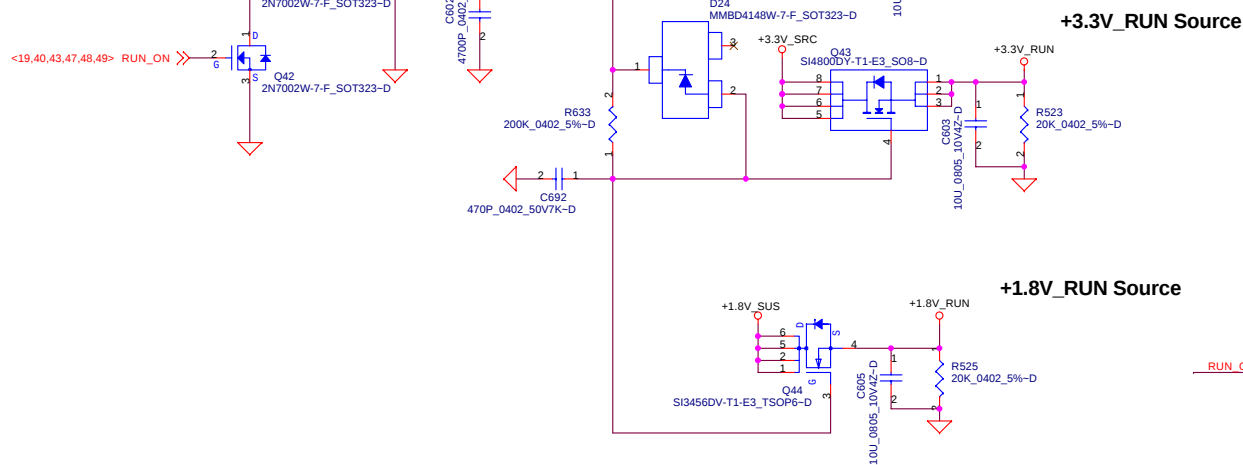
DC/DC Interface



+3.3V_SUS Source



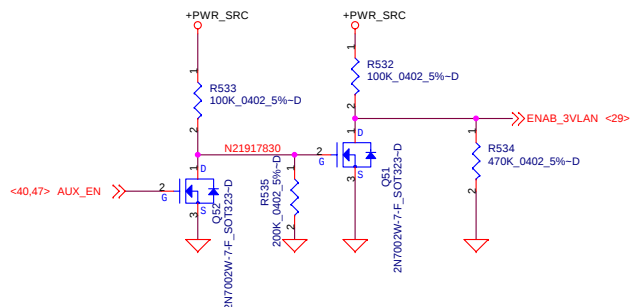
+5V_RUN Source



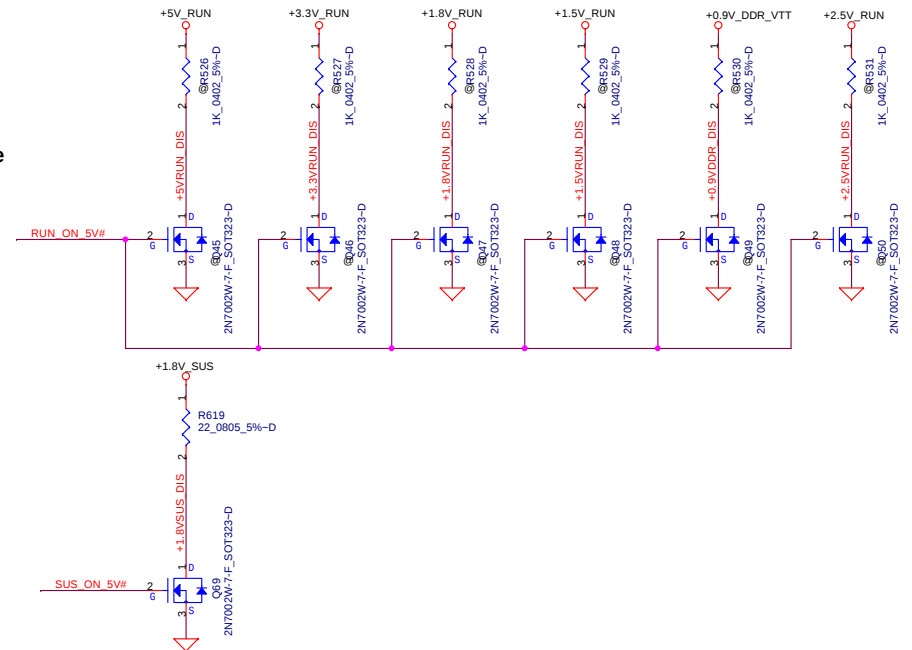
+3.3V_RUN Source



+1.8V_RUN Source



Discharge Circuit



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POWER CONTROL

LA-3741P

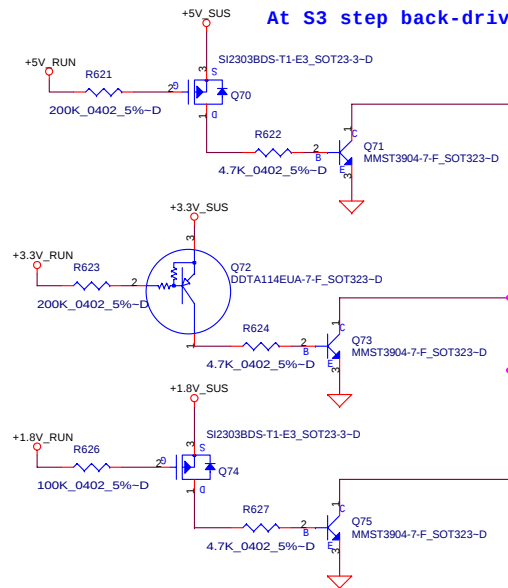
Date: Friday, January 26, 2007

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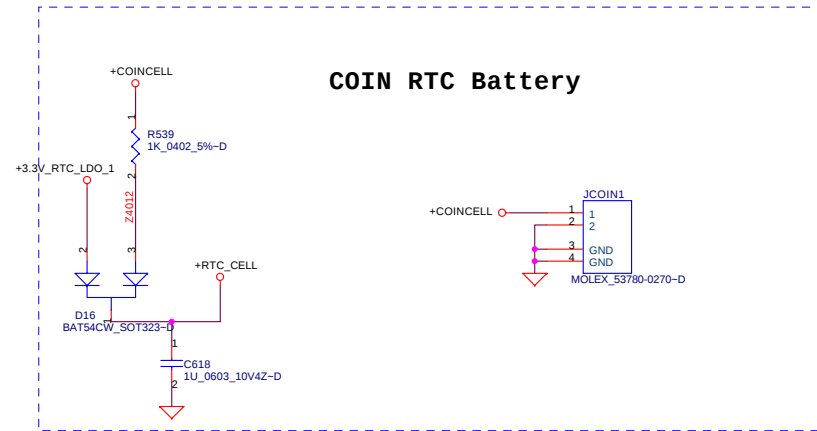
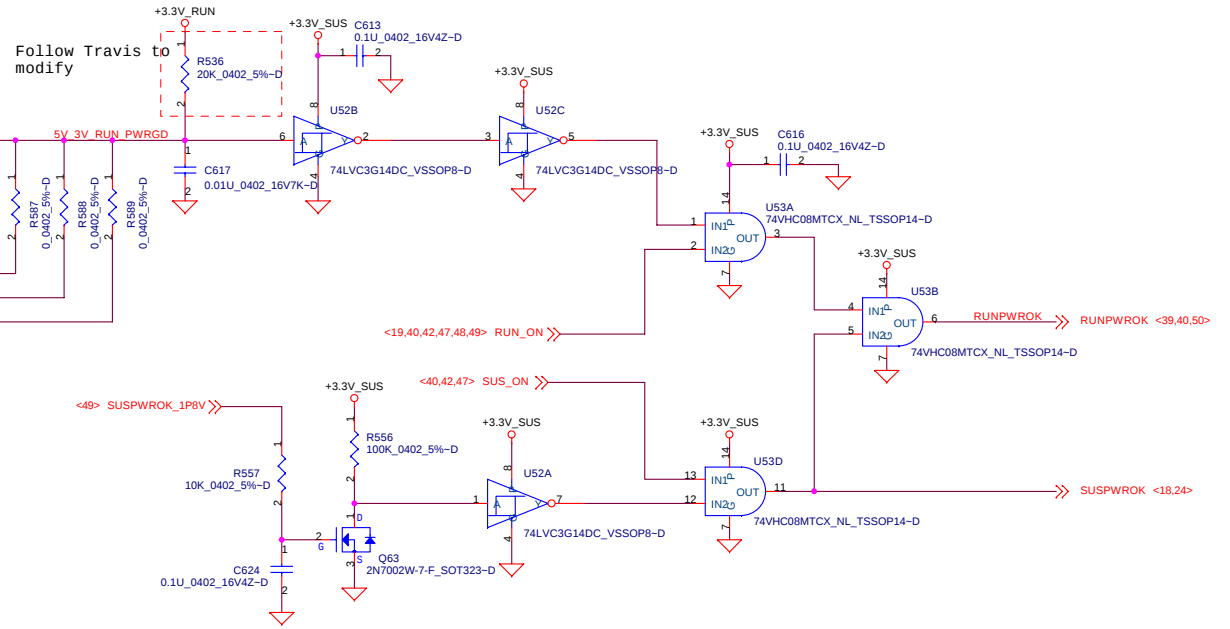
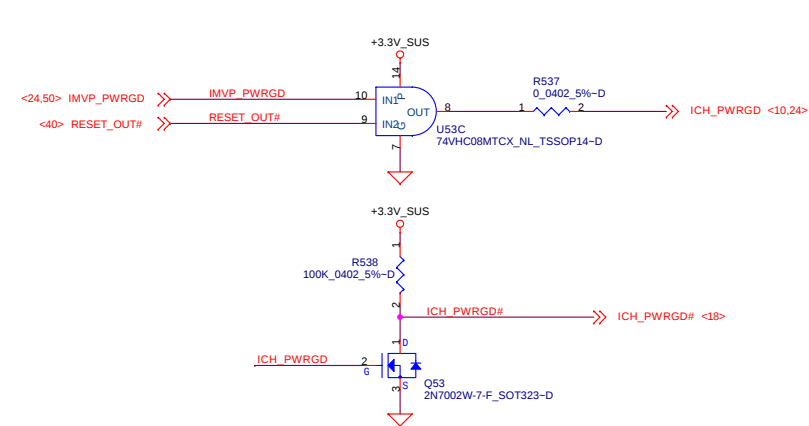
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At S3 step back-drive: 78mV, change to SI2303



At S3 step back-drive:198mV, change to SI2303



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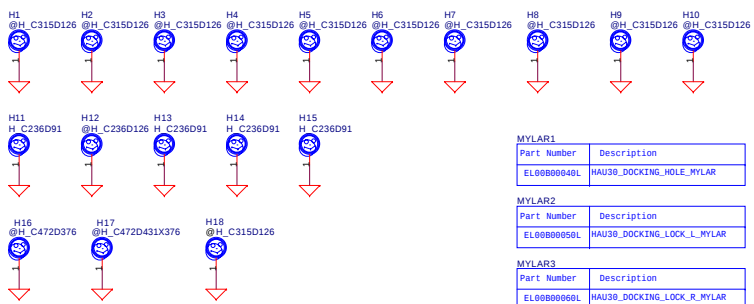
Power Good

LA-3741P

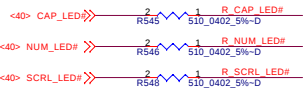
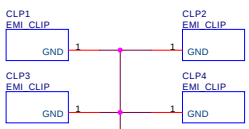
Date: Friday, January 26, 2007 Sheet 43 of 53

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MDC Cable latch

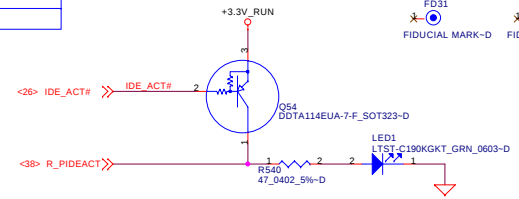


MYLAR1	Part Number	Description	MYLAR5	Part Number	Description
EL00800040L	HAU30_DOCKING_HOLE_MYLAR		EL008000000	HAU30_MYLAR_WLAN_M0AN	

MYLAR2	Part Number	Description	MYLAR6	Part Number	Description
EL008000950L	HAU30_DOCKING_LOCK_L_MYLAR		EL0080000X00	HAU30_MYLAR_HDD	

MYLAR3	Part Number	Description	MYLAR7	Part Number	Description
EL008000660L	HAU30_DOCKING_LOCK_R_MYLAR		EL0080000A0L	HAU30_MYLAR_FAN	

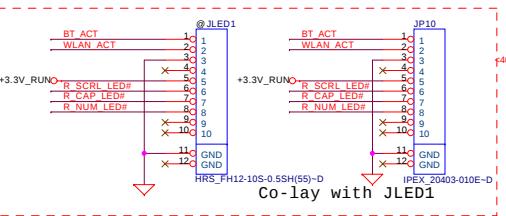
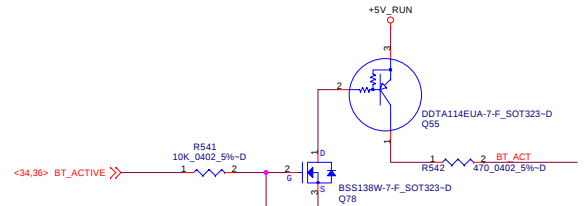
MYLAR4	Part Number	Description
EL00800070L	HAU30_MYLAR_D0R2	



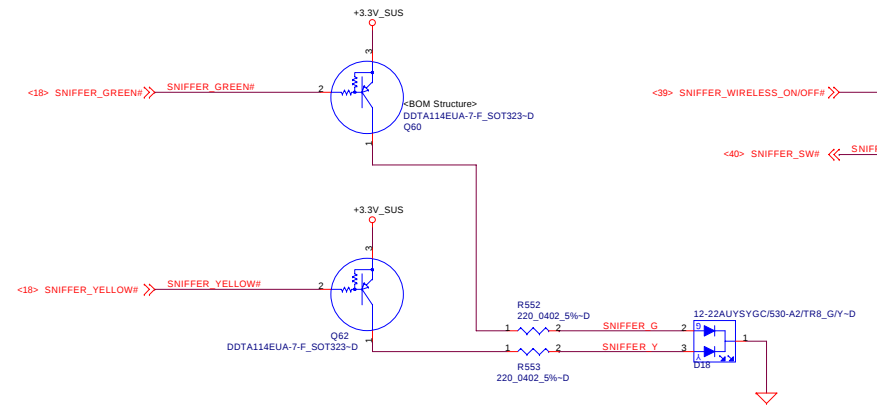
RUBBER1	Part Number	Description
FH00800060L	HAU30_MTC_RUBBER	

RUBBER2	Part Number	Description
EL008000000	HAU30_RUBBER_MDC	

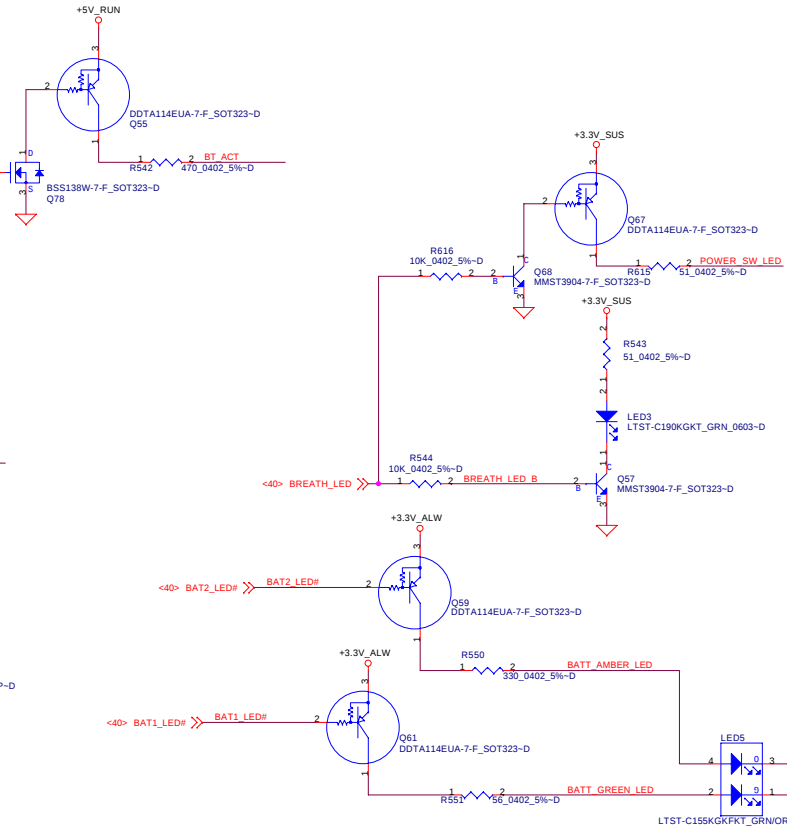
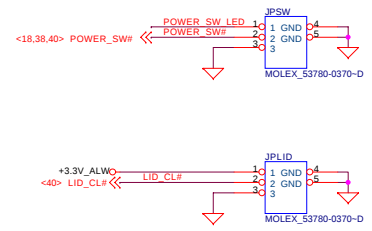
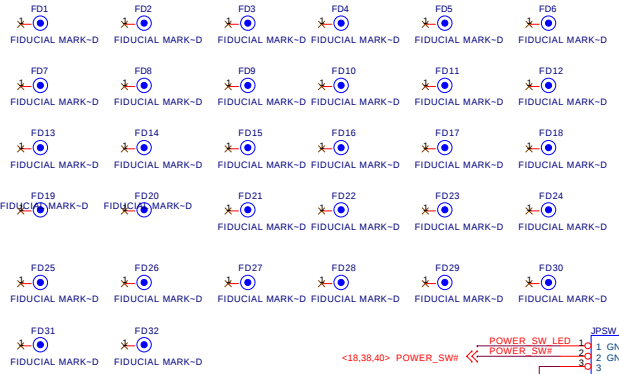
SCREW1	Part Number	Description
MAA00015300	SCREW M M 2.00 3.0L K 4.60 2K NL + CR3+	



Co-lay with JLED1



Fiducial Mark



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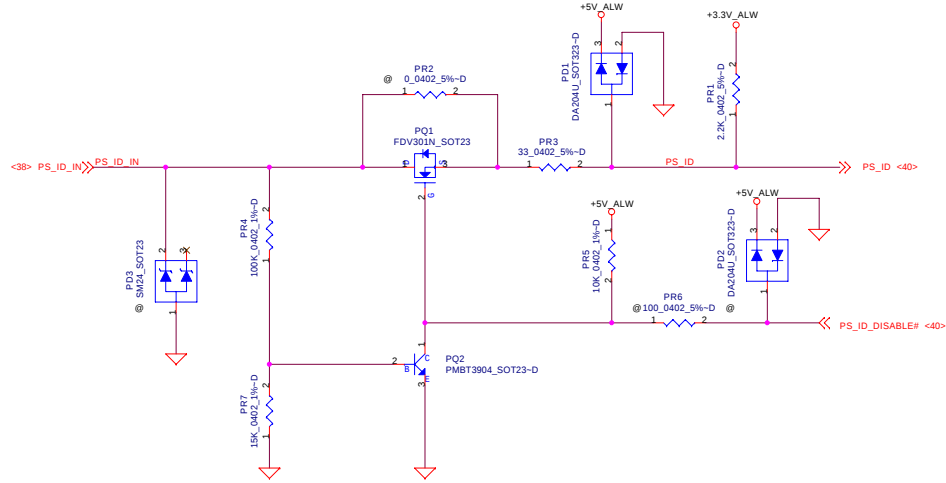
PAD and Standoff

LA-3741P

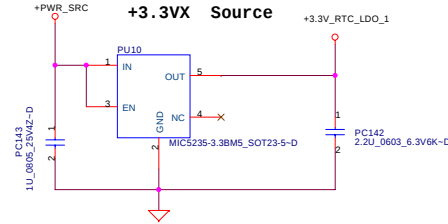


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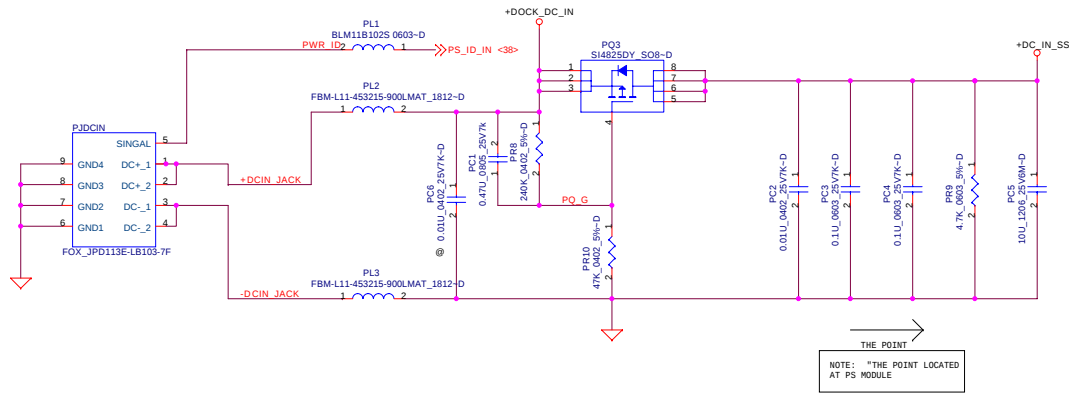
PS_ID Detector



+3.3VX Source



+DC_IN Source



THE POINT
NOTE: "THE POINT LOCATED
AT PS MODULE

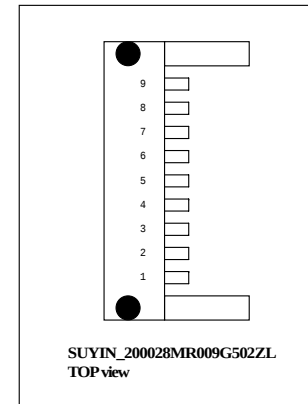
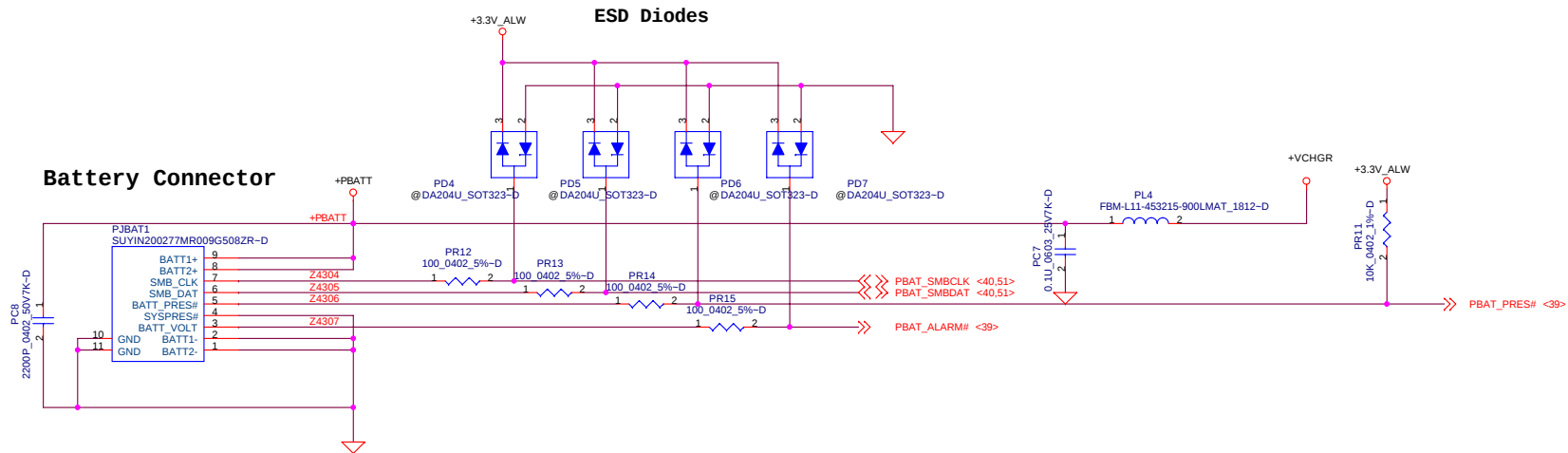
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+DCIN

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DC/DC +3V/ +5V/ +15V

15-Volt
Maximum Current: 10mA

3.3 Volt +/- 5%
Design Current: 6.5 A
Maximum current: 9.1A
OCP: 10.95A

5 Volt +/- 5%
Design Current: 3.63A
Maximum current: 5.191 A
OCP: 6.35A

5V OCP
Fsw=290 KHZ
Rds_on_MAX=20m; Itrip_MIN=8.5uA; L=4.7uH
 $\Delta I = V_{out}/L * 1/Fsw * (1-V_{out}/V_{in})$
 $= 5/4.7u * 1/290K * (1-5/19) = 2.7A$
Ivalley_MIN= Itrip*Rtrip/Rds_on=8.5u*10K/20m=4.25A
Ivalley= Itrip*Rtrip/Rds_on=10u*10K/20m=5A
Iocp_MIN=4.25+2.7/2=5.6A
Iocp=5+2.7/2=6.35A

3.3V OCP
Fsw=440 KHZ
Rds_on_MAX=15m; Itrip_MIN=8.5uA; L=2.7uH
 $\Delta I = V_{out}/L * 1/Fsw * (1-V_{out}/V_{in})$
 $= 3.3/2.7u * 1/440K * (1-3.3/19) = 2.3A$
Ivalley_MIN= Itrip*Rtrip/Rds_on=8.5u*14.7K/15m=8.33A
Ivalley= Itrip*Rtrip/Rds_on=10u*14.7K/15m=9.8A
Iocp_MIN=8.33+2.3/2=9.48A
Iocp=9.8+2.3/2=10.95A

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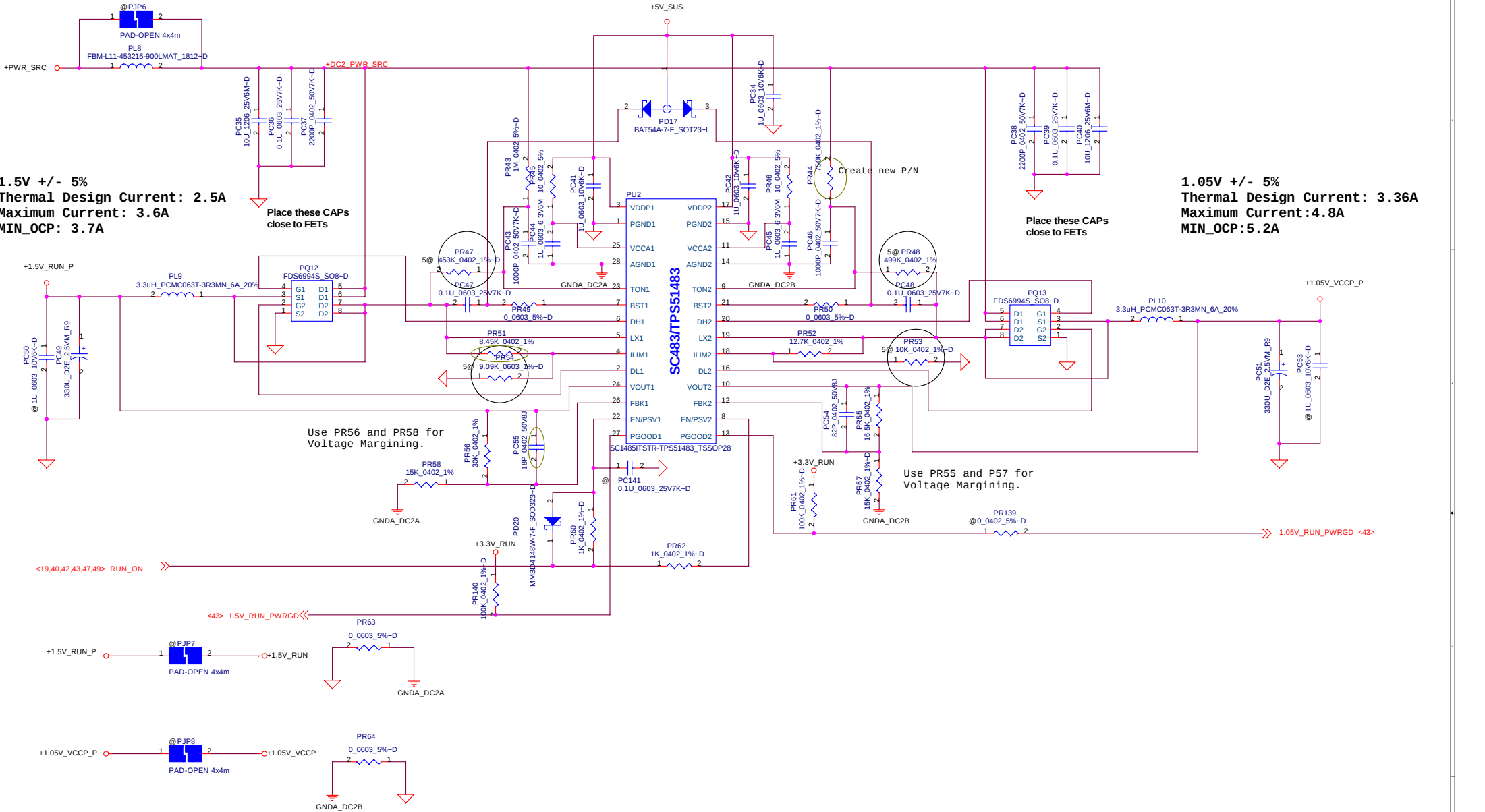
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+3.3V/+5V/+15V

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1.5V +/- 5%
Thermal Design Current: 2.5A
Maximum Current: 3.6A
MIN_OCP: 3.7A

1.05V +/- 5%
Thermal Design Current: 3.36A
Maximum Current: 4.8A
MIN_OCP: 5.2A

BOM Structure	Description
@	Do Not Populate
4@	Populate for Semtech - SC483 Only
5@	Populate for Ti - TPS51483 Only

Ref Des	SC483	TPS52483
PR56	30.0K	15.0K
PR58	15.0K	15.0K
PR55	16.5K	11.8K
PR57	15.0K	29.4K

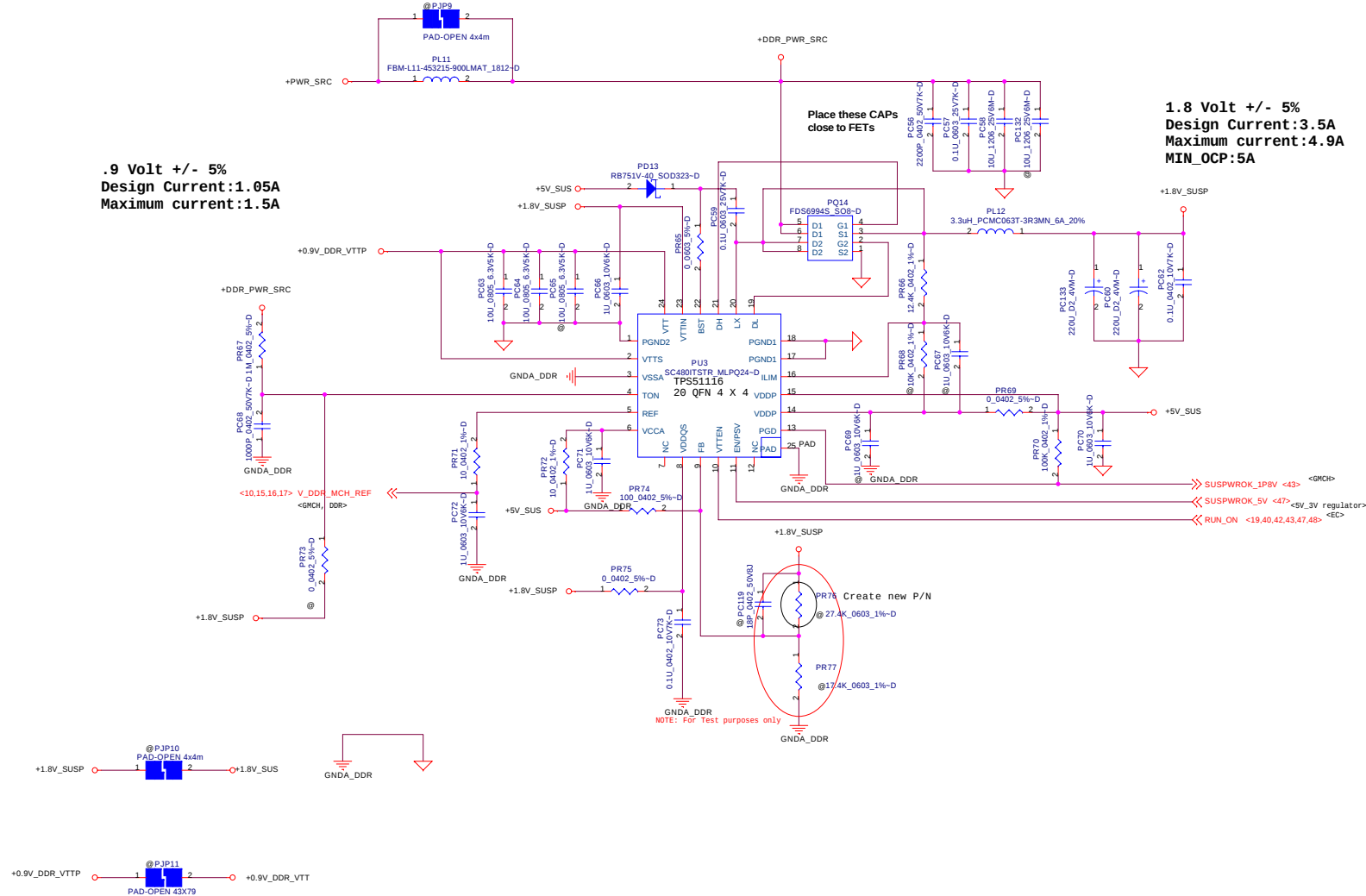
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+1.5VRUNP /+VCCP_1P05VP			
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NOTE: Component Values Shown for SEMTECH SC480 ONLY.
For Texas Instruments TPS51116, Please USE Reference BOM.



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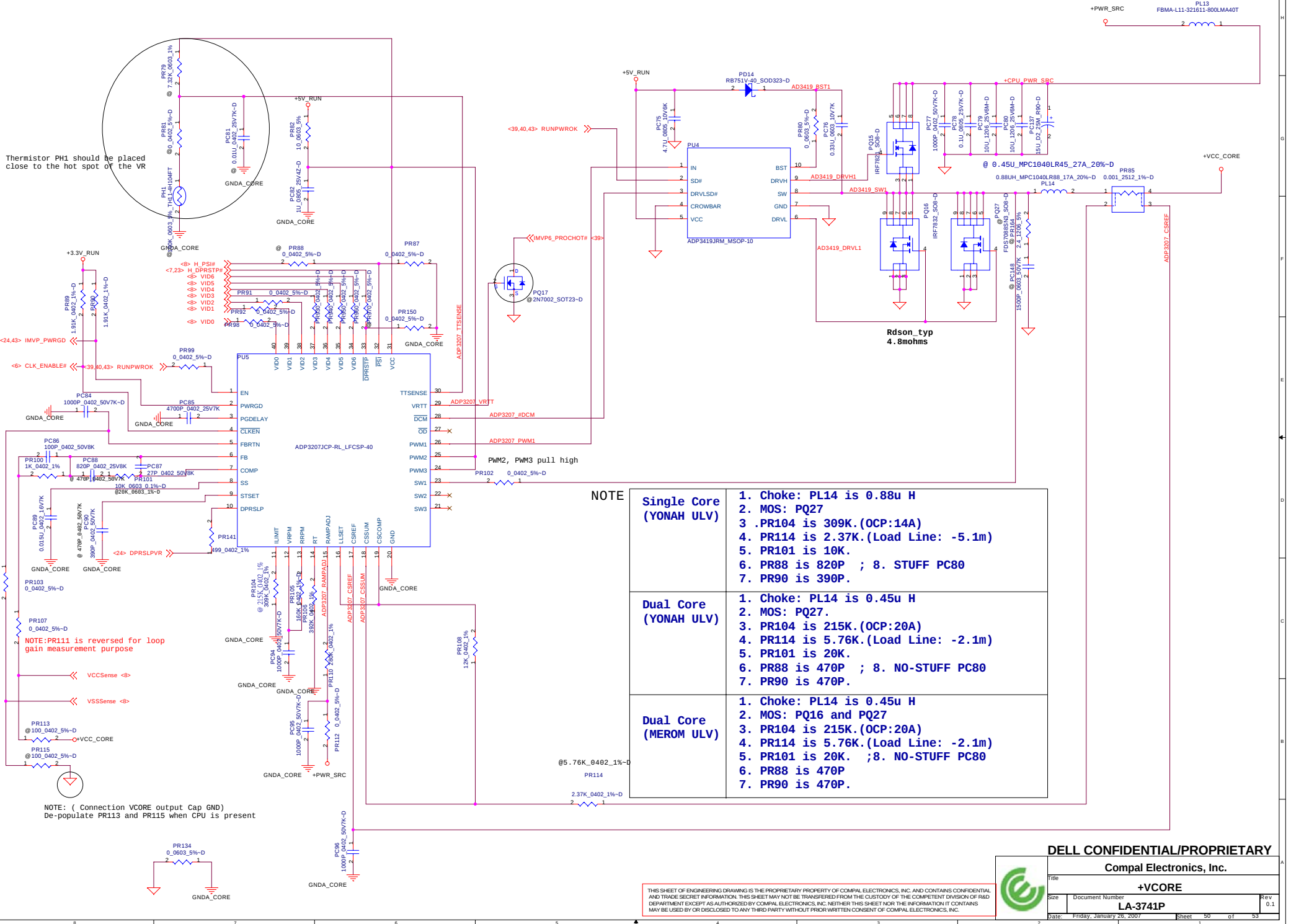
+1.8VSUSP/ +0.9V_DDR_VT

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IMVP-6 solution for Yonah ULV: 1-phase/9A

PL13
FBMA-L11-321611-800LMA40T

Thermistor PH1 should be placed close to the hot spot of the VR



NOTE

Single Core (YONAH ULV)

1. Choke: PL14 is 0.88u H
2. MOS: PQ27
3. PR104 is 309K.(OCP:14A)
4. PR114 is 2.37K.(Load Line: -5.1m)
5. PR101 is 10K.
6. PR88 is 820P ;
7. PR90 is 390P.
8. STUFF PC80

Dual Core (YONAH ULV)

1. Choke: PL14 is 0.45u H
2. MOS: PQ27.
3. PR104 is 215K.(OCP:20A)
4. PR114 is 5.76K.(Load Line: -2.1m)
5. PR101 is 20K.
6. PR88 is 470P ;
7. PR90 is 470P.
8. NO-STUFF PC80

Dual Core (MEROM ULV)

1. Choke: PL14 is 0.45u H
2. MOS: PQ16 and PQ27
3. PR104 is 215K.(OCP:20A)
4. PR114 is 5.76K.(Load Line: -2.1m)
5. PR101 is 20K. ;
6. PR88 is 470P
7. PR90 is 470P.
8. NO-STUFF PC80

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+VCORE



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Smart Charger

+DC_IN discharge path

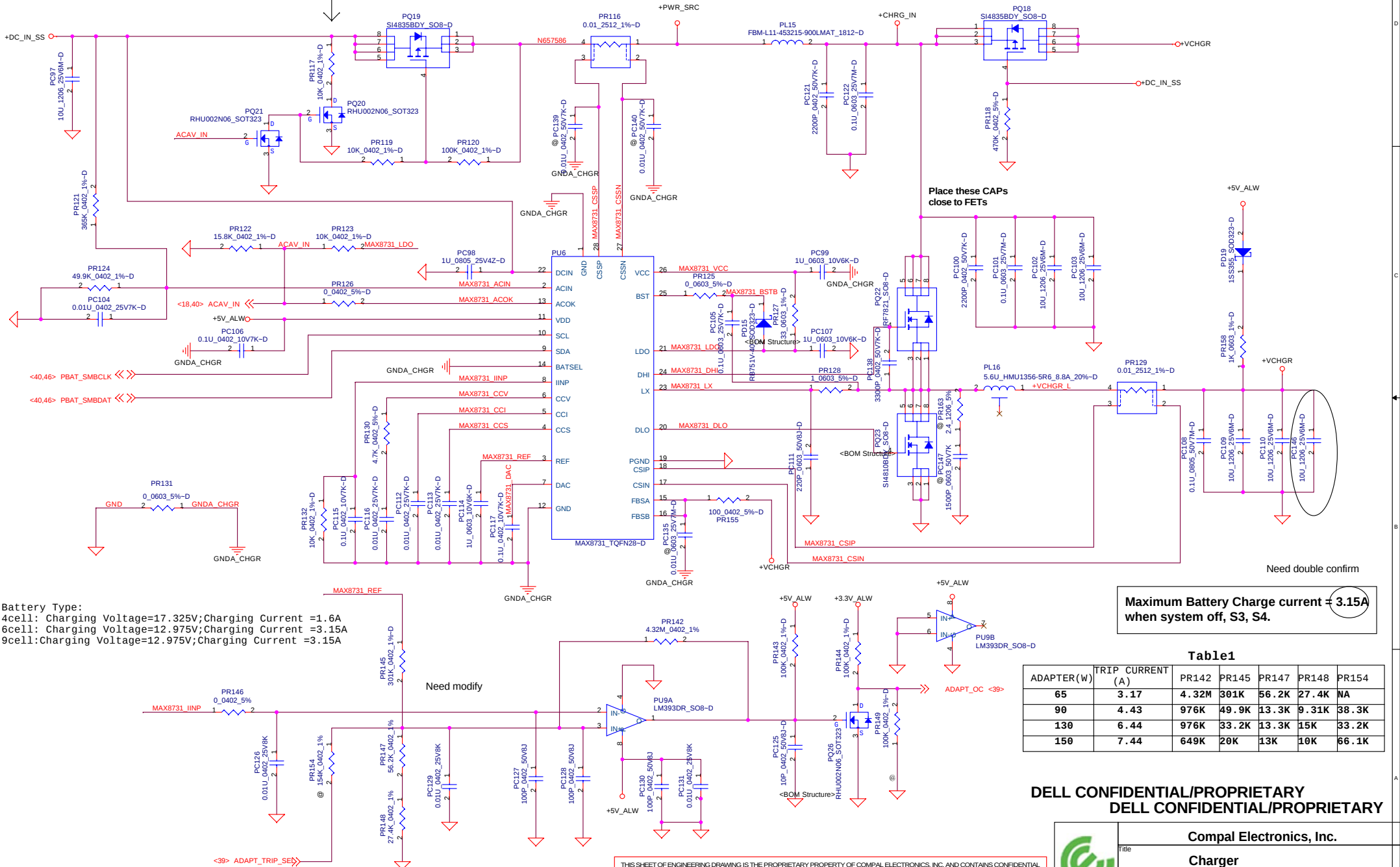


Table1

ADAPTER(W)	TRIP CURRENT (A)	PR142	PR145	PR147	PR148	PR154
65	3.17	4.32M	301K	56.2K	27.4K	NA
90	4.43	976K	49.9K	13.3K	9.31K	38.3K
130	6.44	976K	33.2K	13.3K	15K	33.2K
150	7.44	649K	20K	13K	10K	66.1K

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Charger

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Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	16, 17	DDR On board	01/09/07'	Compal	Update Memory symbol form 64M*8 to 128M*8	Update U4~U11 Symbol	0.1
2	17	DDR On board	01/09/07'	Compal	Add Termination resistors	Add R660 and R661(56_0402)	0.1
3	39	ECE5018	01/09/07'	Compal	Setting board ID from A00(Crockett) to X00(Carson)	Pop R475 and depop R471	0.1
4	29, 40	LOM	01/11/07'	Dell	Support ASF2.0	MEC5004 pin55(SGPI045) connect to BCM5752 ball H9 (GPI00), net name is LOM_SMB_ALERT# ; Add R662(4.7K ohm)pull up resistor to +3.3V_LAN	0.1
5	50	SPI ROM	01/23/07'	Dell	Add code to support different version of WLAN.	Change U50 from M25P16-VMW6TP(1M) to M25P16-VMW6TP(2M)	0.1
6	37	IrDA	01/23/07'	Dell	For cost saving	Depop U40, R433, R435, R436, C526~C528	0.1
7	28		01/23/07'	Dell	For best audio performance(Vista)	Change L28~L31 to BLM18BD601SN1D	0.1
8	33	USB	01/24/07'	Dell	For meet TUV's USB requirement	Reserved F2, F3(depop), JUMP1 and JUMP3. Short Jump for X00 build.	0.1

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Title

Changed-List History 1

Size

Document Number

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	page 51		1/09/07	Dell	M08 charger update for potential audible noise.	Add PC146 10uf/25V X5R Cap on charger output Cap.	X00
2	page 51		1/26/07	Dell	M08 charger update for EMC.	Add PC147 and PR163 from phase node to GND	X00
3	page 50		1/26/07	Dell	M08 Vcore update for EMC.	Add PC148 and PR164 from phase node to GND	X00

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Title **Changed-List History 2**

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	page 51		1/09/07	Dell	M08 charger update for potential audible noise.	Add PC146 10uf/25V X5R Cap on charger output Cap.	X00
2	page 51		1/26/07	Dell	M08 charger update for EMC.	Add PC147 and PR163 from phase node to GND	X00
3	page 50		1/26/07	Dell	M08 Vcore update for EMC.	Add PC148 and PR164 from phase node to GND	X00

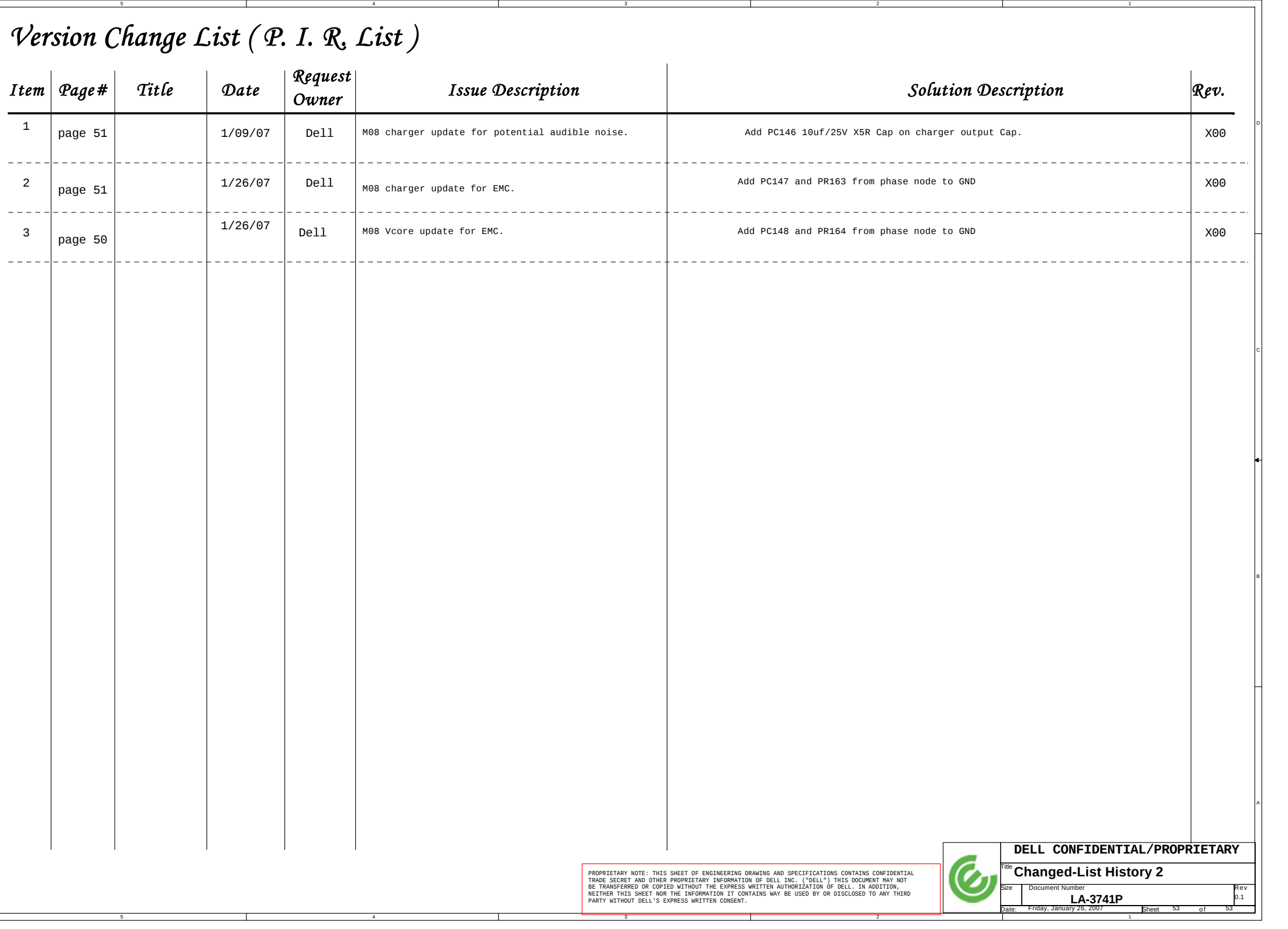
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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	page 51		1/09/07	Dell	M08 charger update for potential audible noise.	Add PC146 10uf/25V X5R Cap on charger output Cap.	X00
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3	page 50		1/26/07	Dell	M08 Vcore update for EMC.	Add PC148 and PR164 from phase node to GND	X00

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	page 51		1/09/07	Dell	M08 charger update for potential audible noise.	Add PC146 10uf/25V X5R Cap on charger output Cap.	X00
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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	page 51		1/09/07	Dell	M08 charger update for potential audible noise.	Add PC146 10uf/25V X5R Cap on charger output Cap.	X00
2	page 51		1/26/07	Dell	M08 charger update for EMC.	Add PC147 and PR163 from phase node to GND	X00
3	page 50		1/26/07	Dell	M08 Vcore update for EMC.	Add PC148 and PR164 from phase node to GND	X00

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<i>Item</i>	<i>Page#</i>	<i>Title</i>	<i>Date</i>	<i>Request Owner</i>	<i>Issue Description</i>	<i>Solution Description</i>	<i>Rev.</i>
1	page 51		1/09/07	Dell	M08 charger update for potential audible noise.	Add PC146 10uf/25V X5R Cap on charger output Cap.	X00
2	page 51		1/26/07	Dell	M08 charger update for EMC.	Add PC147 and PR163 from phase node to GND	X00
3	page 50		1/26/07	Dell	M08 Vcore update for EMC.	Add PC148 and PR164 from phase node to GND	X00

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