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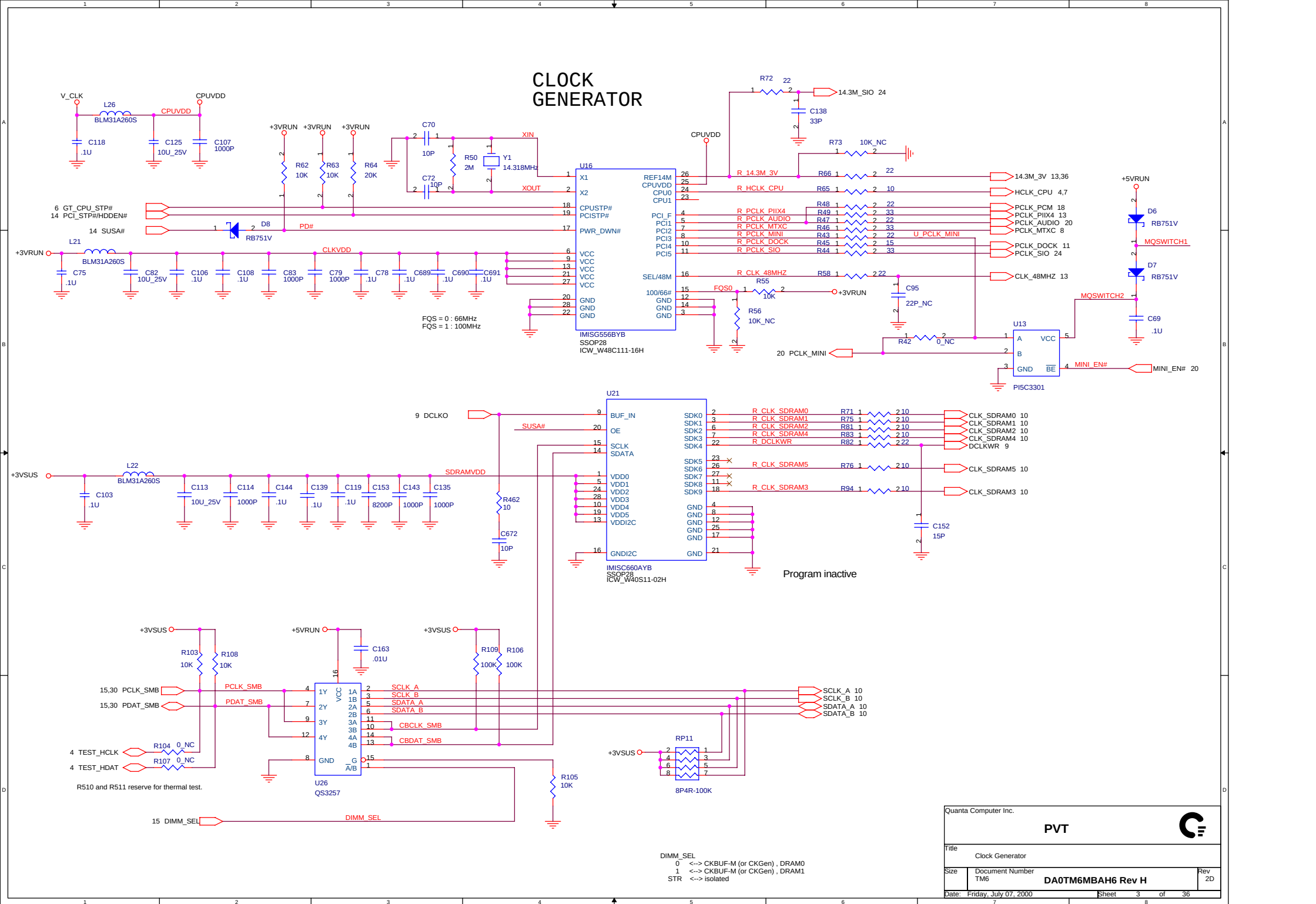
Power & Grounds

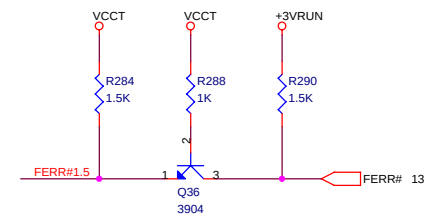
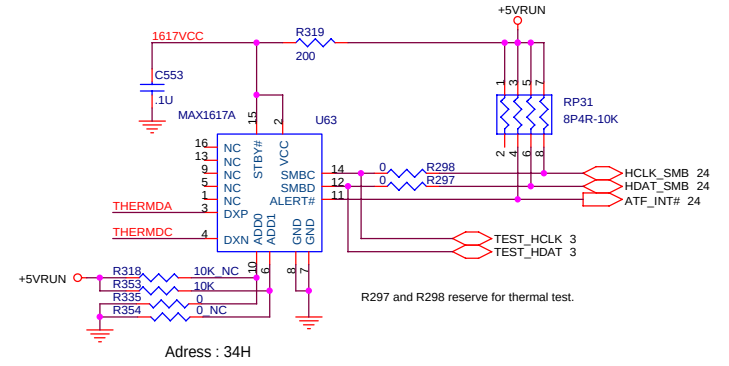
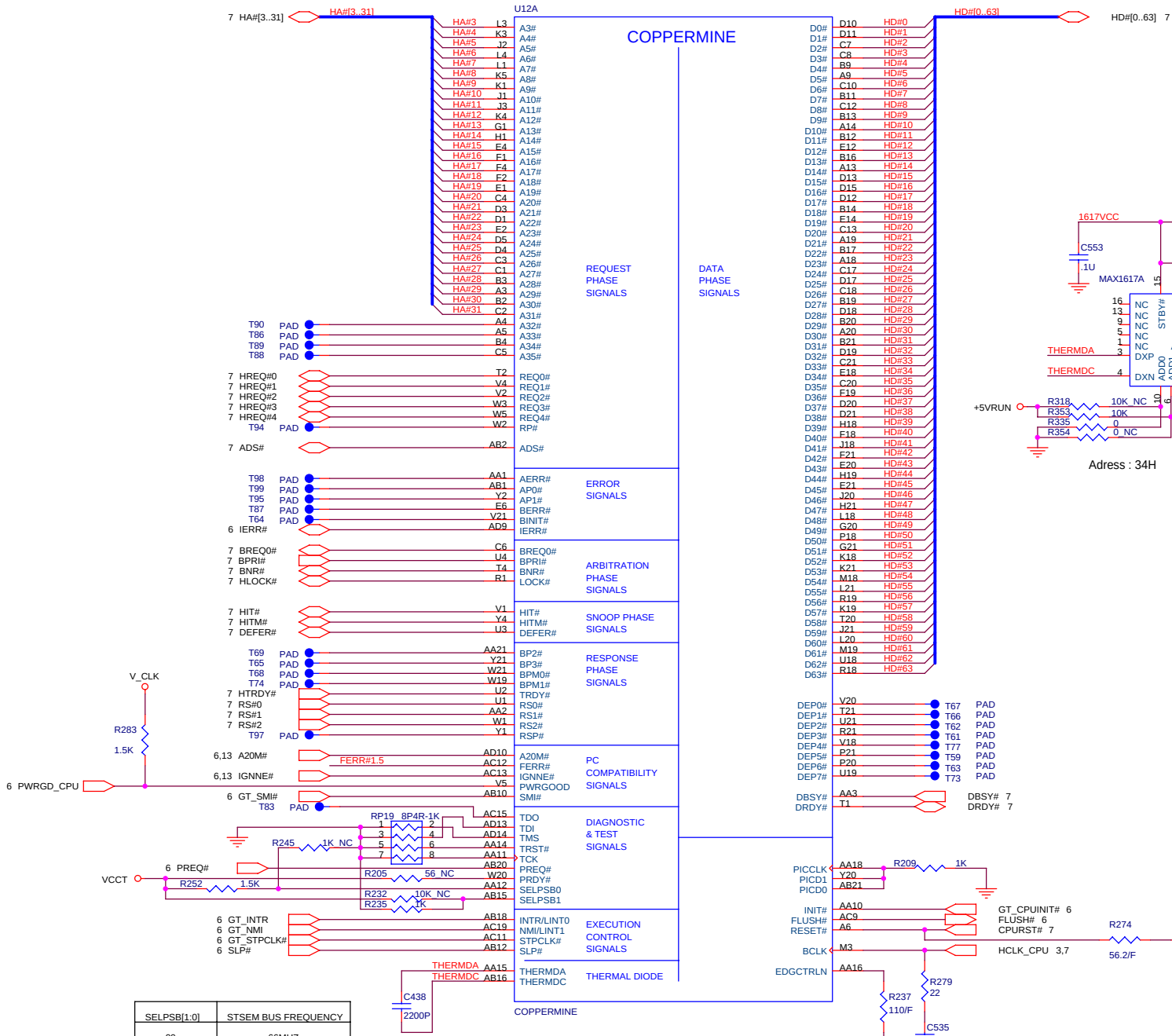
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DC_IN+	12, 35, 36, 37	AC ADAPTER (20V)	
BATT+	35, 36, 37	BATTERY CHARGE POWER	
ABATT+		MAIN BATTERY POWER	
BBATT+		SECOND BATTERY POWER	
PWR_SRC	12,30,31,32,36	MAIN POWER (10~20V)	
RTC_PWR	23, 32, 36	RTC & PCL POWER (3V)	
+12V	12,19,28,31,34,36	+12V	DRUNPWROK
VTT	5,6,30	1.6V	RUNPWROK
VCCT	4,5,6,7,30	1.5V	RUNPWROK
V_CLK	3,30	HOST I/O PLANE (2.5V)	RUNPWROK
+3VRUN	3,4,5,8,11,13,14,15,16,17,18,20,21,22,23,28,29,30,31,36	SUSB# CONTROLLED POWER	RUN_ON
+3VSUS	3,6,7,9,10,11,13,14,15,18,19,20,24,28,29,36	SUSC# CONTROLLED POWER	SUS_ON
+3VALW	7,16,20,23,24,25,31,36	8051 POWER (3V)	
+5VALW	12,25,29,30,32,33,36	SMBUS & OZ953 POWER	
+5V_BS	31,32	Battery selector Power	PWR_SRC
+5VRUN	3,4,11,15,17,19,20,26,27,28,29,31,34,36	SUSB# CONTROLLED POWER	RUN_ON
+5VSUS	7,12,19,21,27,28,29,30,34	SUSC# CONTROLLED POWER	SUS_ON
+5VHDD	16,28	HDD POWER (5V)	HDDC_EN#
+5VMOD	16,18	MODULE POWER (5V)	MODC_EN#
STRB#/5V	12,27,28	EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VFAN1,+5VFAN2	28	FAN POWER (5V)	FAN1_OFF/ON# FAN2_OFF/ON#
VDDA	21,22	AUDIO ANALOG POWER (5V)	RUN_ON
 GND	ALL PAGES	DIGITAL GROUND	
 AGND	21,22,35	AUDIO GND	
 GNDP	30	CPU POWER GND	
 CGNDP	33	CHARGER GND	
 TGND	24,26	STICK GND	

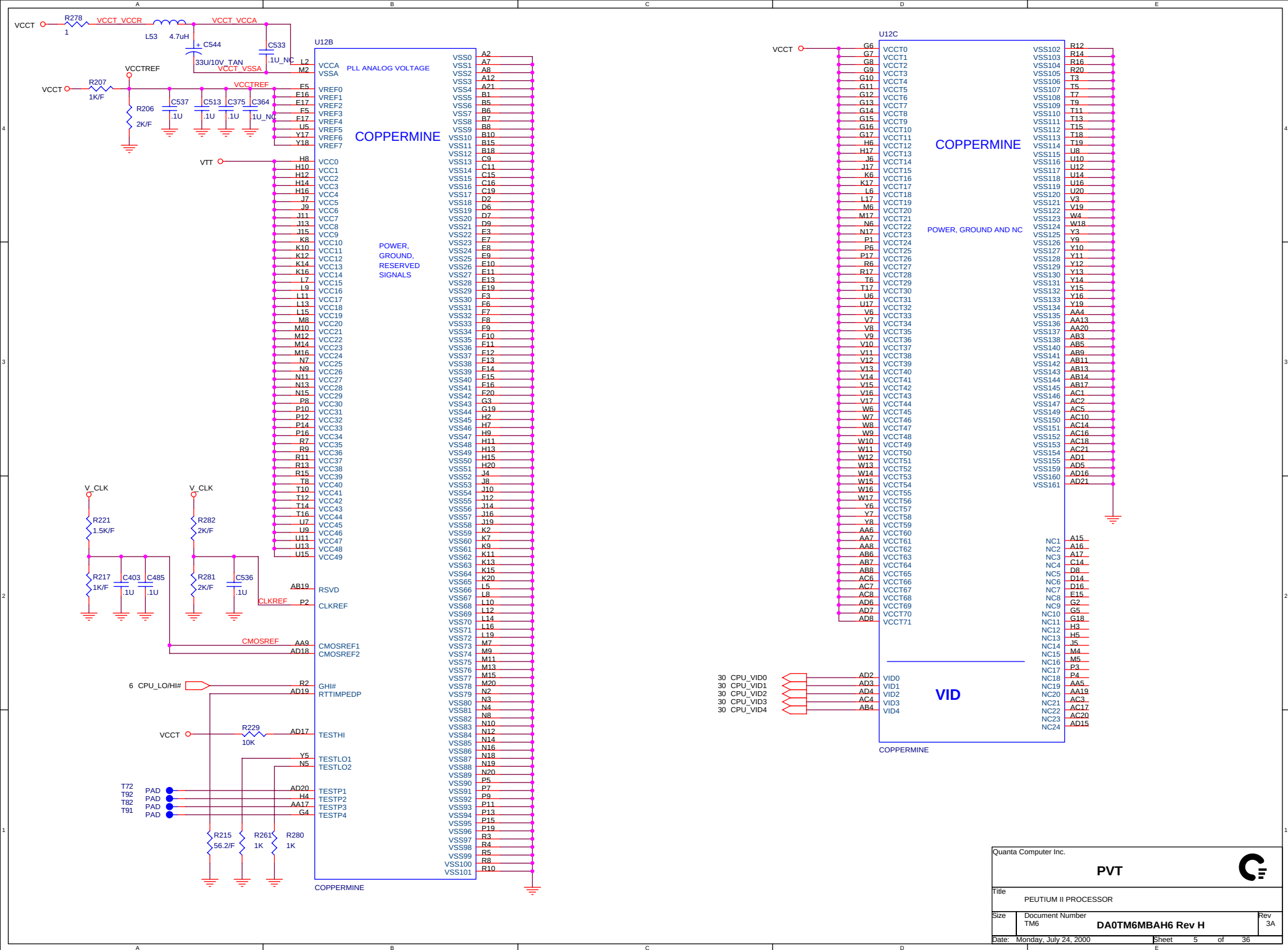
Quanta Computer Inc.

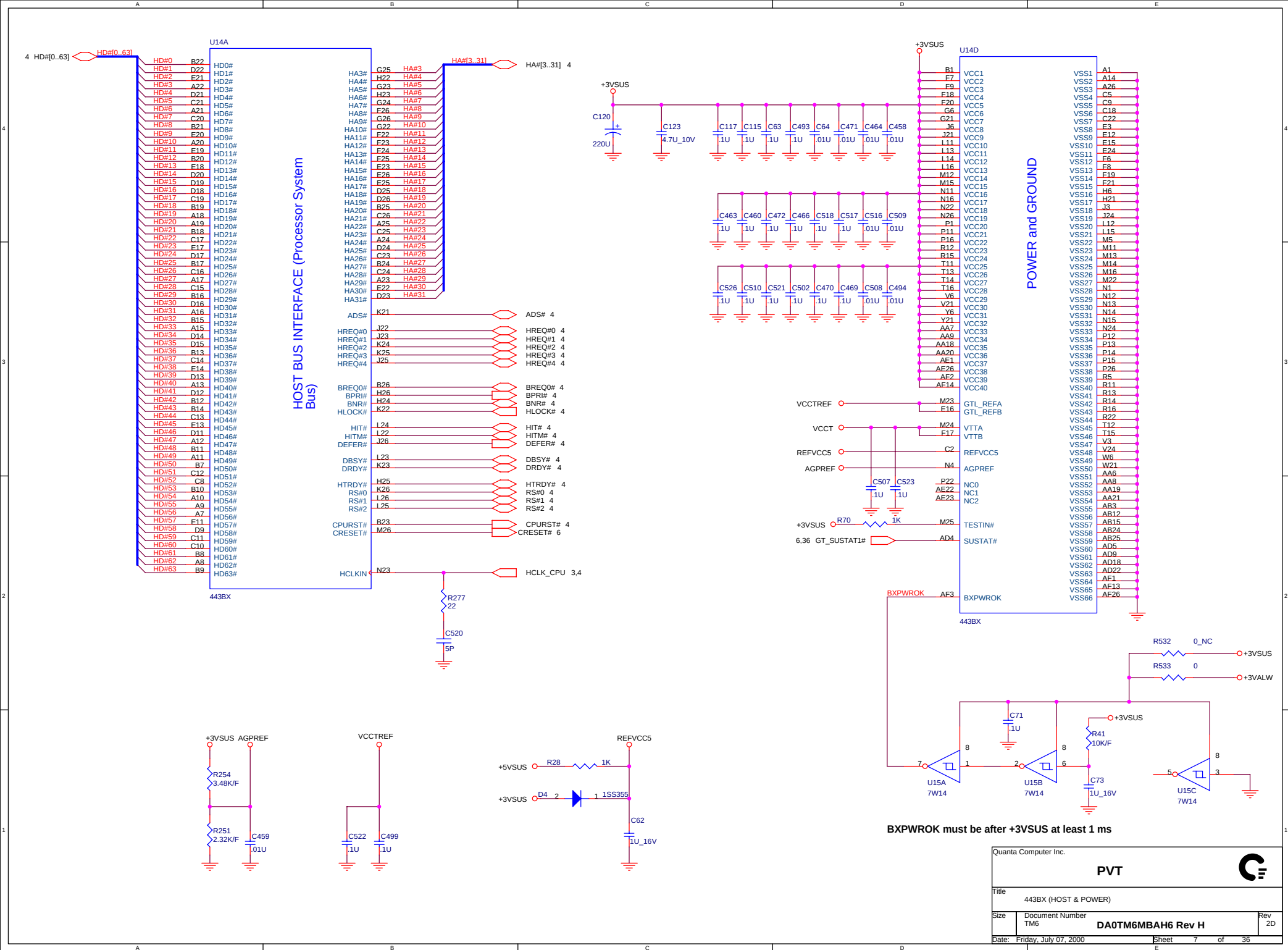
PVT

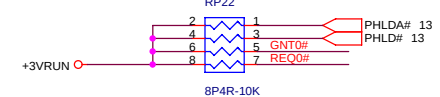
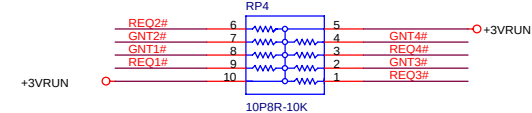
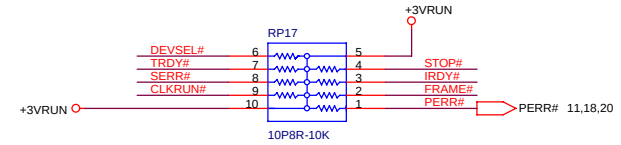
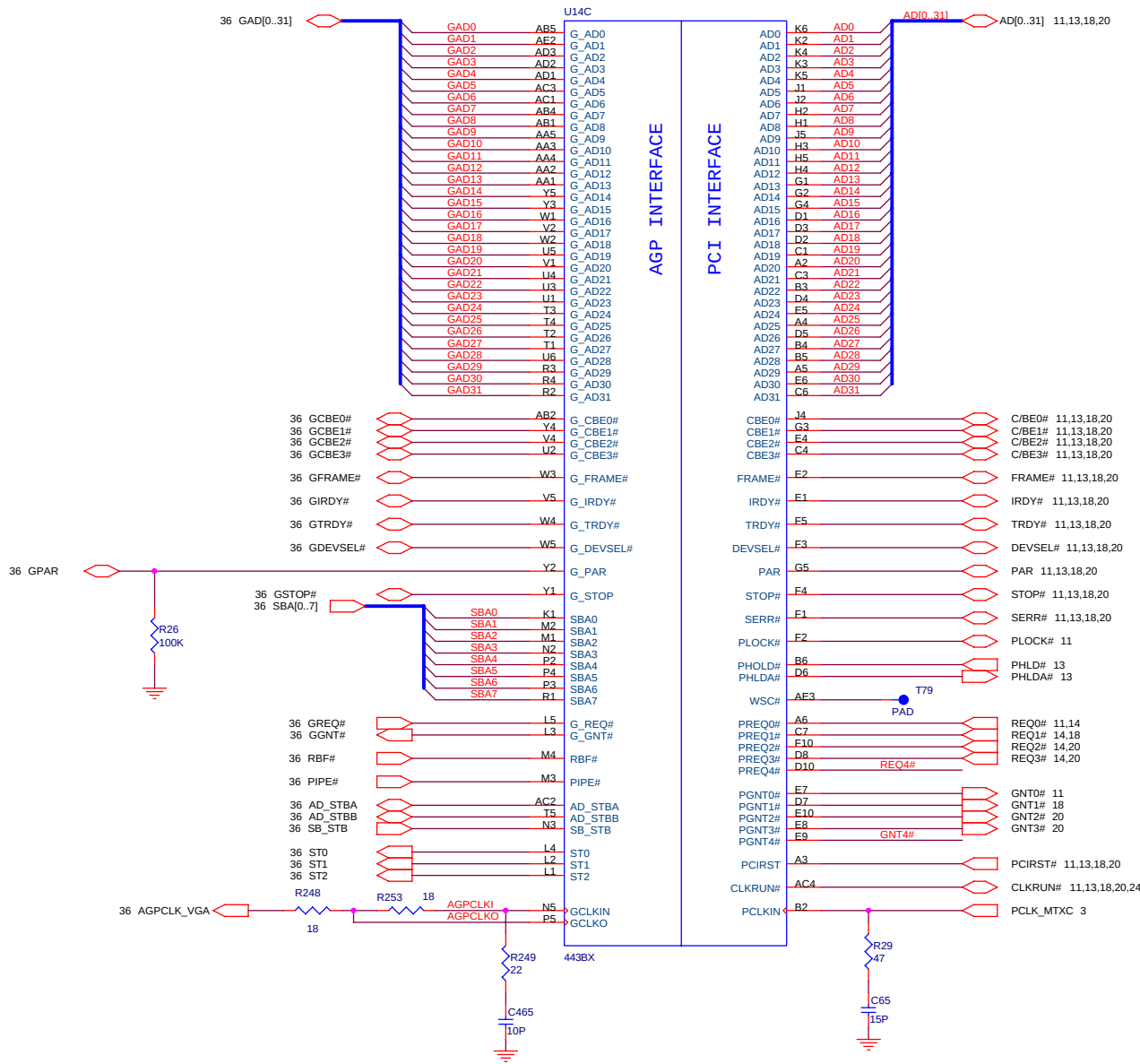
Title
Index,DNI,power & groundsSize
Document Number
TM6
DA0TM6MBAH6 Rev H
Rev
2GDate: Saturday, November 25, 2000
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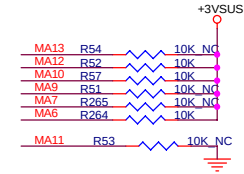
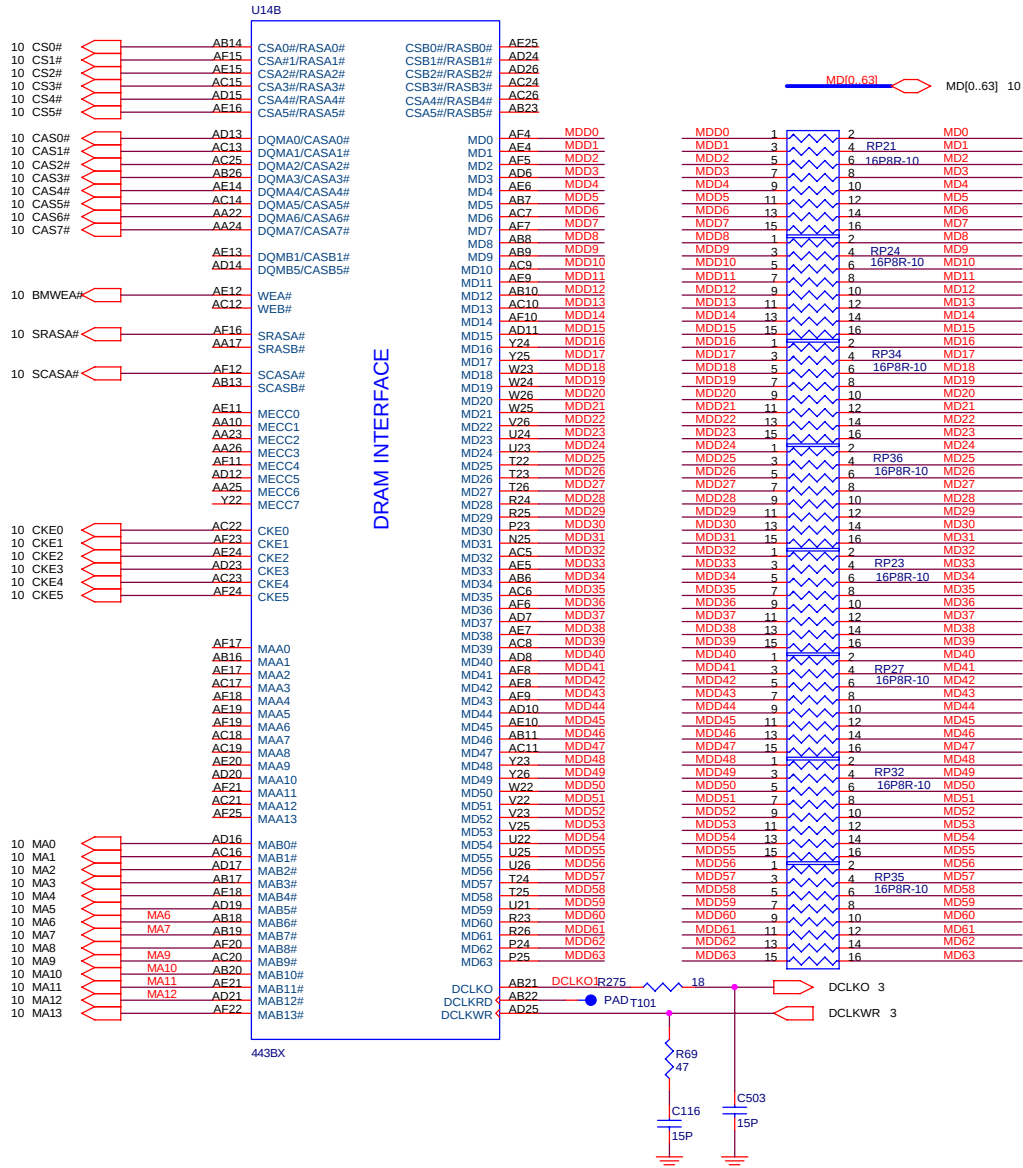




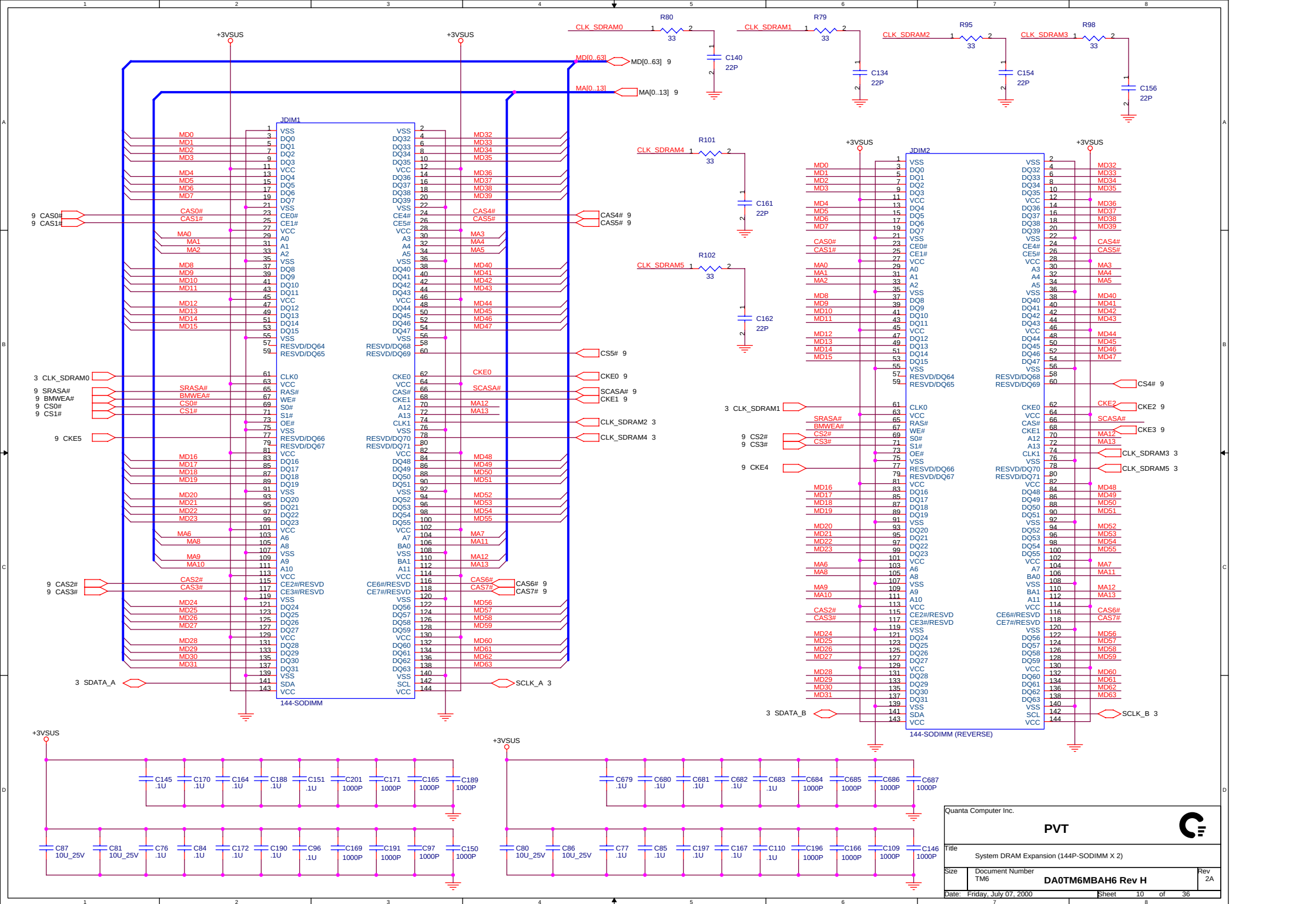


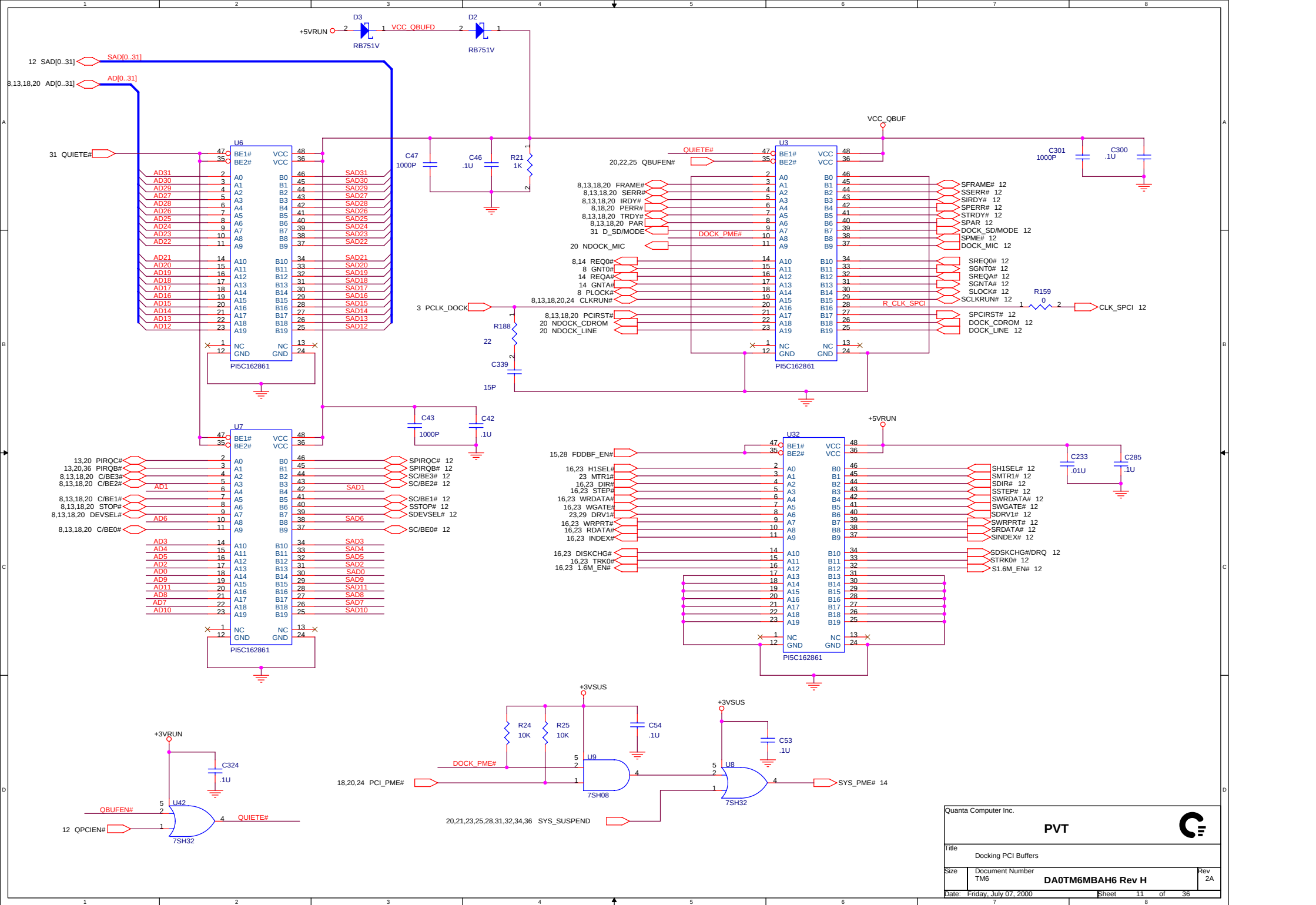




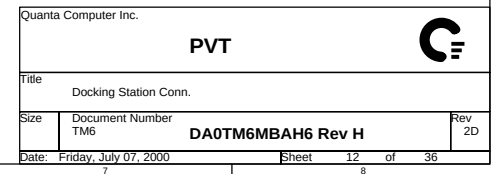


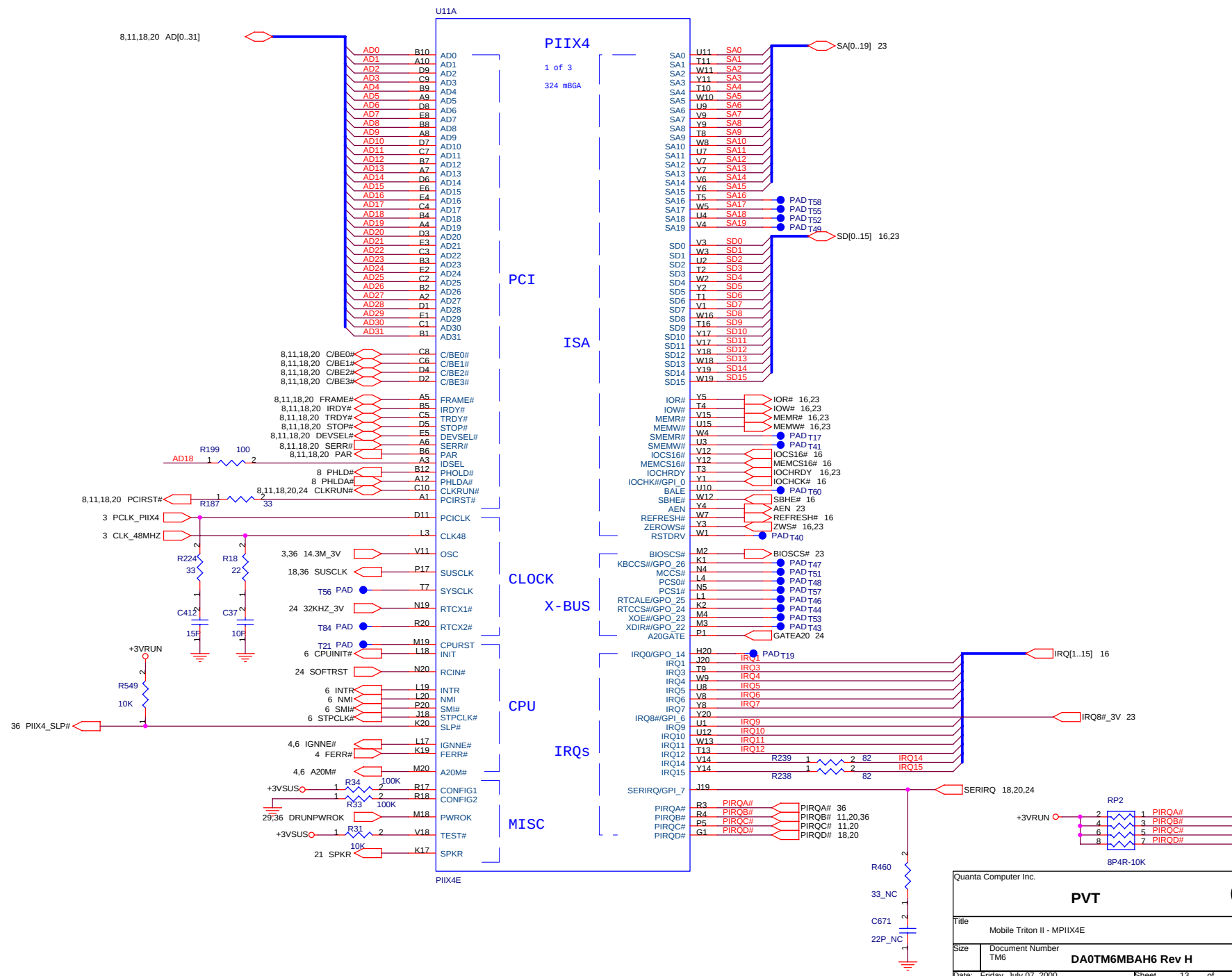
Pin Name	Function	Low	High	Internal Resistor	Status Register
MAB12#	Host Frequency Select	66MHz	100MHz	Pull-down	NBXCFC[13]
MAB11#	In-Order Queue Depth Enable	1 (no pipelining)	4 (max)	Pull-up	NBXCFC[2]
MAB10#	Quick Start Select	Stop Clock Mode	Quick Start Mode	Pull-down	PMCR[3]
MAB9#	AGP Disable	AGP Enabled	AGP Disabled	Pull-down	PMCR[1]
MAB7#	MM Configuration	Normal operation	Tri-states certain memory signals	Pull-down	DRAMC[5]
MAB6#	Host Bus Buffer Mode Select	Desktop GTL+	Mobile Low Power GTL+	Pull-down	none

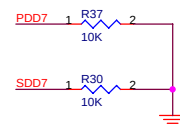
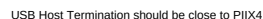


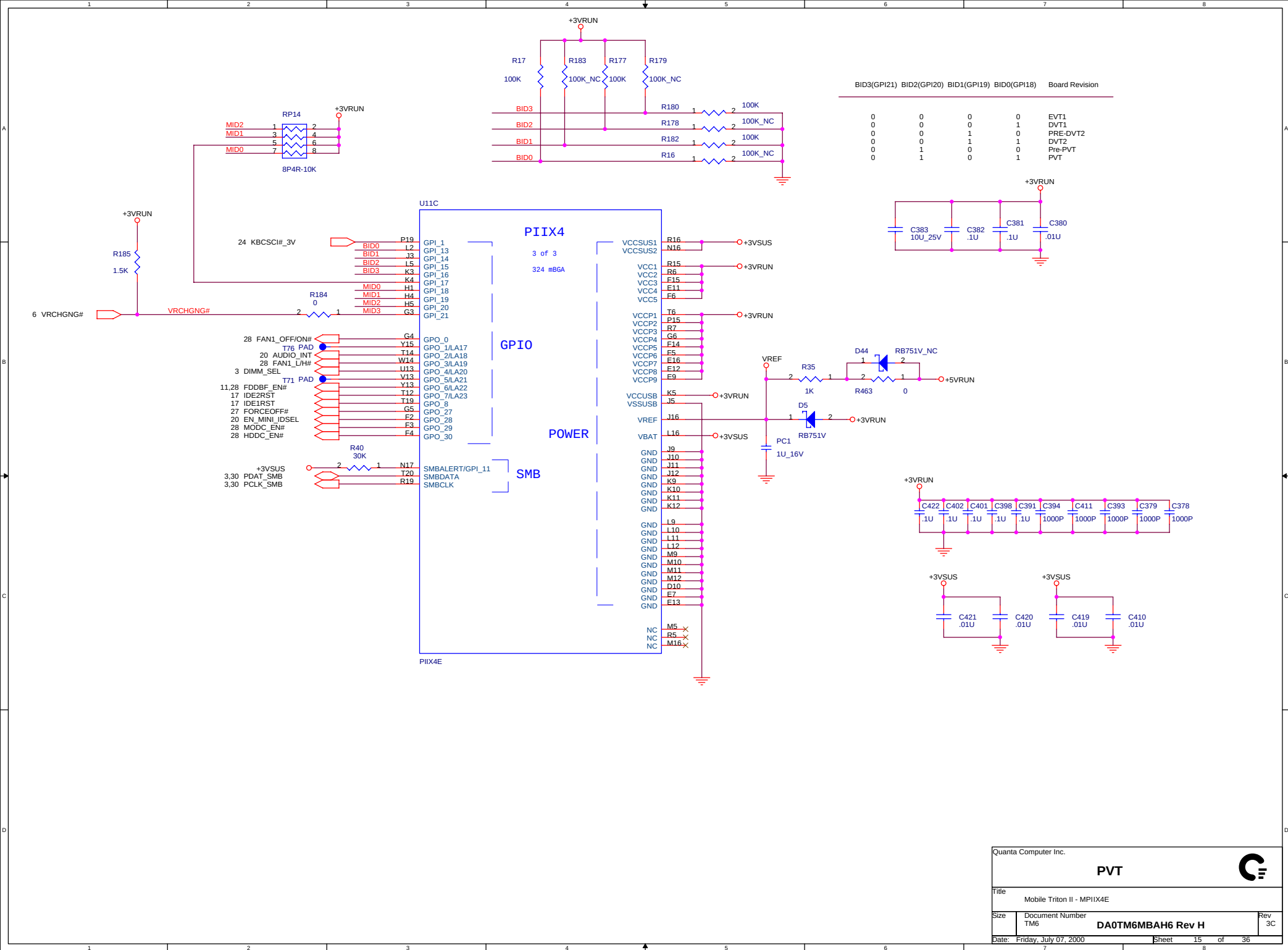


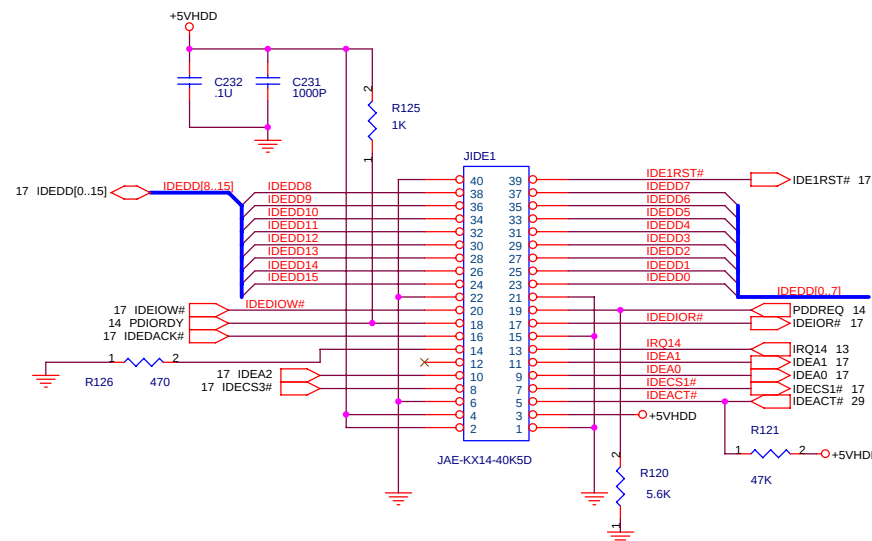
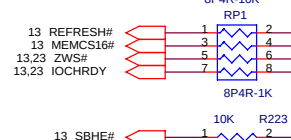
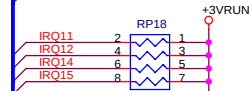
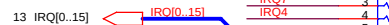
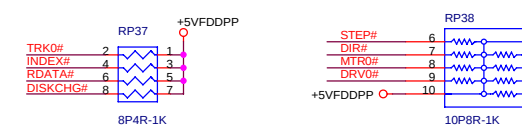
CLOSE TO AUDIO AREA











S1_WP_PCLK R258
22K
O S1_VCC

19 S1_VS2
19 S1_VS1
19 S1_WP_PCLK
19 S1_WE#
19 S1_WAIT#
19 S1_RST#
19 S1_REG#
19 S1_RDY#
19 S1_OE#
19 S1_IOWR#
19 S1_IORD#
19 S1_INPK# ILR
19 S1_CE2#
19 S1_CE1#
19 S1_CD2#
19 S1_CD1#
19 S1_B2/SPK_IDAT
19 S1_BVD1

S1_VS2
S1_VS1
S1_WP_PCLK
S1_WAIT#
S1_RST#
S1_REG#
S1_RDY#
S1_OE#
S1_IOWR#
S1_IORD#
S1_INPK# ILR
S1_CE2#
S1_CE1#
S1_CD2#
S1_CD1#
S1_B2/SPK_IDAT
S1_BVD1
S1_D15
S1_D14
S1_D13
S1_D12
S1_D11
S1_D10
S1_D9
S1_D8
S1_D7
S1_D6
S1_D5
S1_D4
S1_D3
S1_D2
S1_D1
S1_D0

FOR PCI1225 MOUNT THESE RPS

19 S1_D0[15]
19 S1_A0
19 S1_A1
19 S1_A2
19 S1_A3
19 S1_A4
19 S1_A5
19 S1_A6_IMCK
19 S1_A7_ISCK
19 S1_A8_Y2
19 S1_A9_Y0
19 S1_A10_HREF
19 S1_A11_VS
19 S1_A12_UV6
19 S1_A13_Y4
19 S1_A14_Y6
19 S1_A15_UV4
19 S1_A16_UV2
19 S1_A17_Y1
19 S1_A18_Y3
19 S1_A19_Y5
19 S1_A20_Y7
19 S1_A21_UV0
19 S1_A22_UV1
19 S1_A23_UV3
19 S1_A24_UV5
19 S1_A25_UV7

S1_D0[15]
S1_A0
S1_A1
S1_A2
S1_A3
S1_A4
S1_A5
S1_A6_IMCK
S1_A7_ISCK
S1_A8_Y2
S1_A9_Y0
S1_A10_HREF
S1_A11_VS
S1_A12_UV6
S1_A13_Y4
S1_A14_Y6
S1_A15_UV4
S1_A16_UV2
S1_A17_Y1
S1_A18_Y3
S1_A19_Y5
S1_A20_Y7
S1_A21_UV0
S1_A22_UV1
S1_A23_UV3
S1_A24_UV5
S1_A25_UV7

S1_VCC O
R260 0.0805
+3VVRUN
C481 1000P
R242 10K
C480 .1U
VCCA
13.20 PIRQD#
14.20 REQB#
13.20.24 SERIRQ#
14.20 GNTB#
8.11.13.20.24 CLKRUN#

PIRQD#
REQB#
SERIRQ#
GNTB#
CLKRUN#

SLOT A

SLOT B

PCI A/D ; INTERFACE

8.11.13.20 AD[0..31] ADIO_311

8.11.13.20 C/BE0#
8.11.13.20 C/BE1#
8.11.13.20 C/BE2#
8.11.13.20 C/BE3#
3 PCLK_PCM

R228

100

P10V

5P

C405

33

R222

100

P10V

5P

C405

33

R222

100

P10V

5P

C405

33

R222

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P10V

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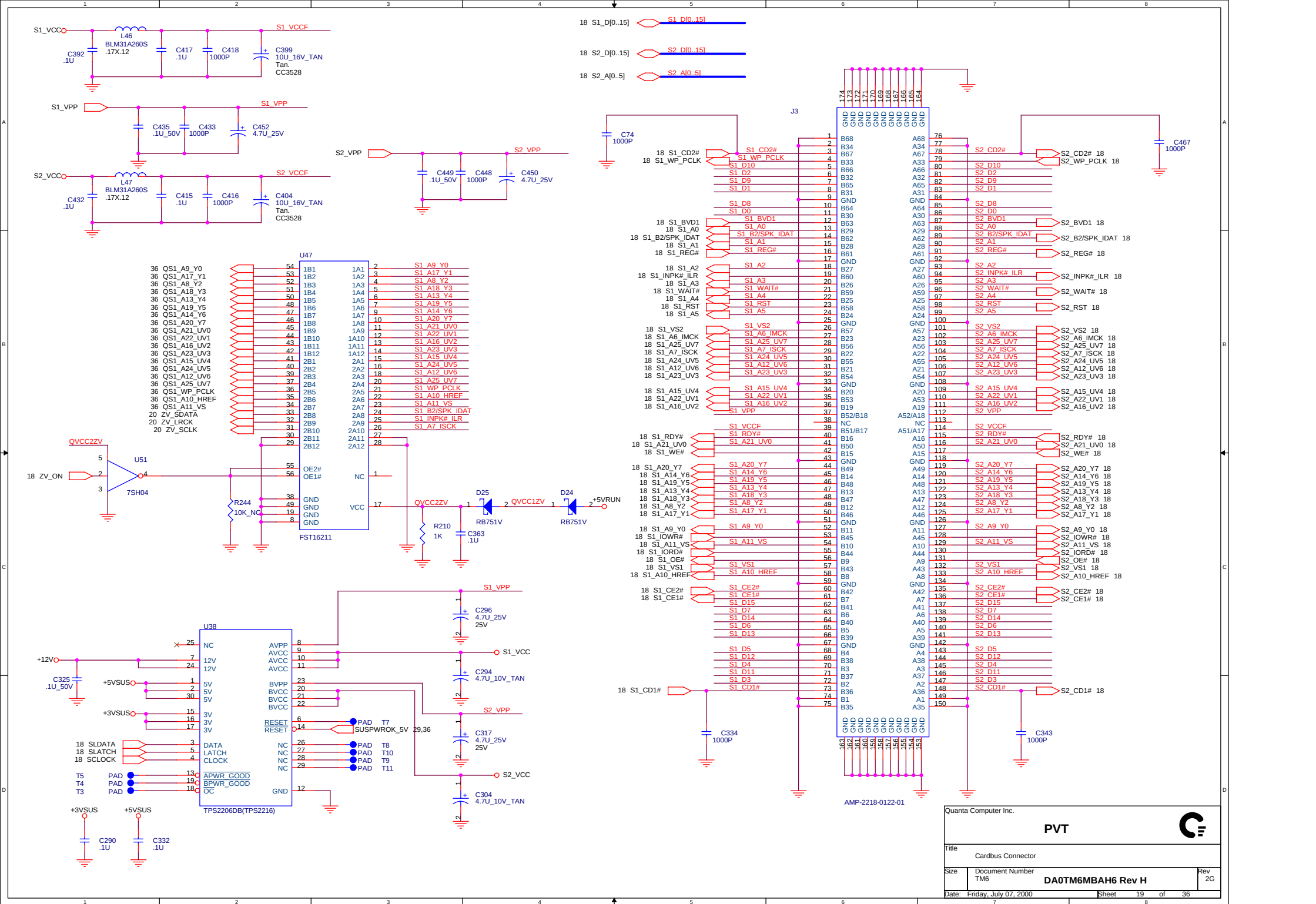
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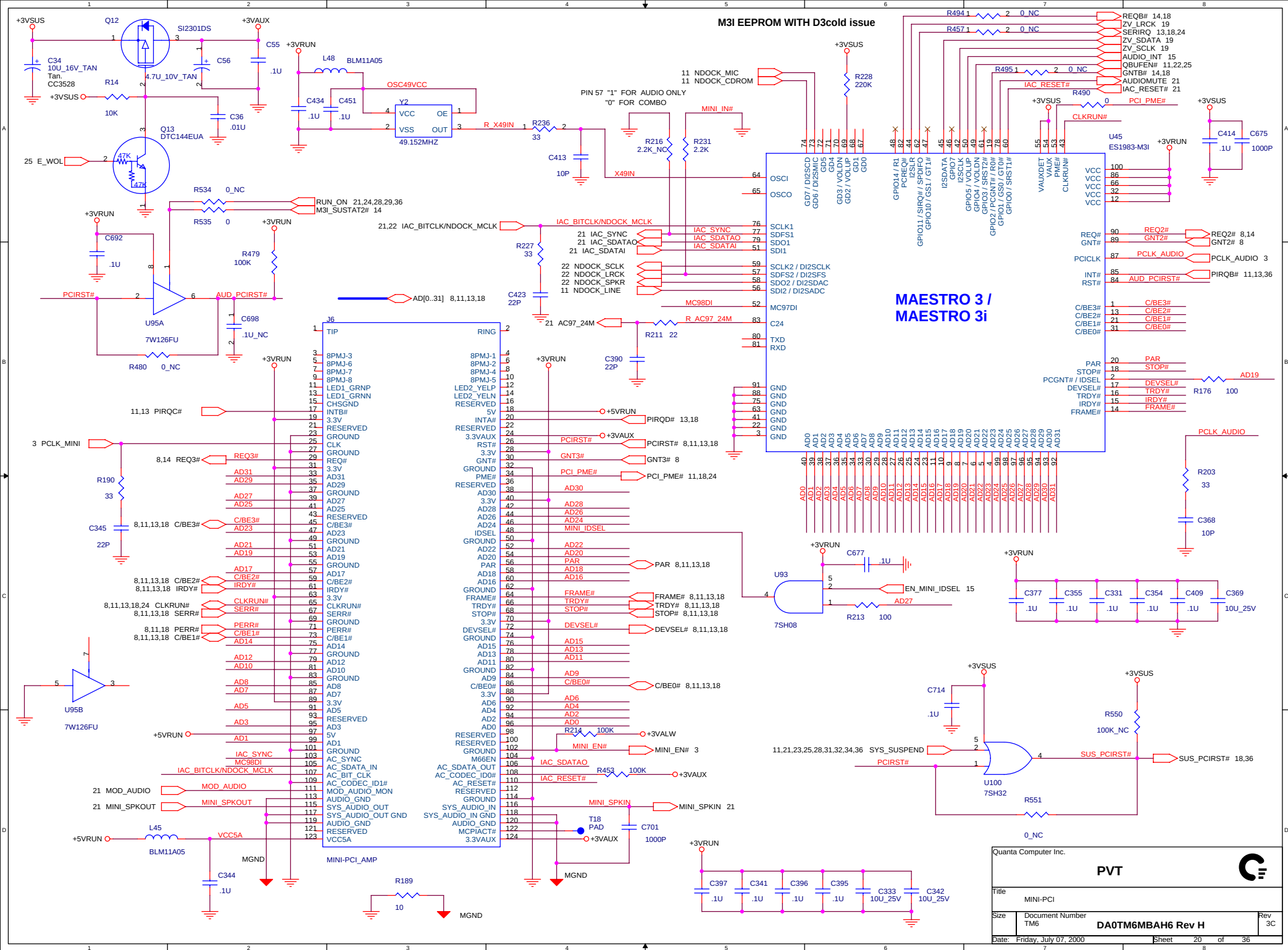
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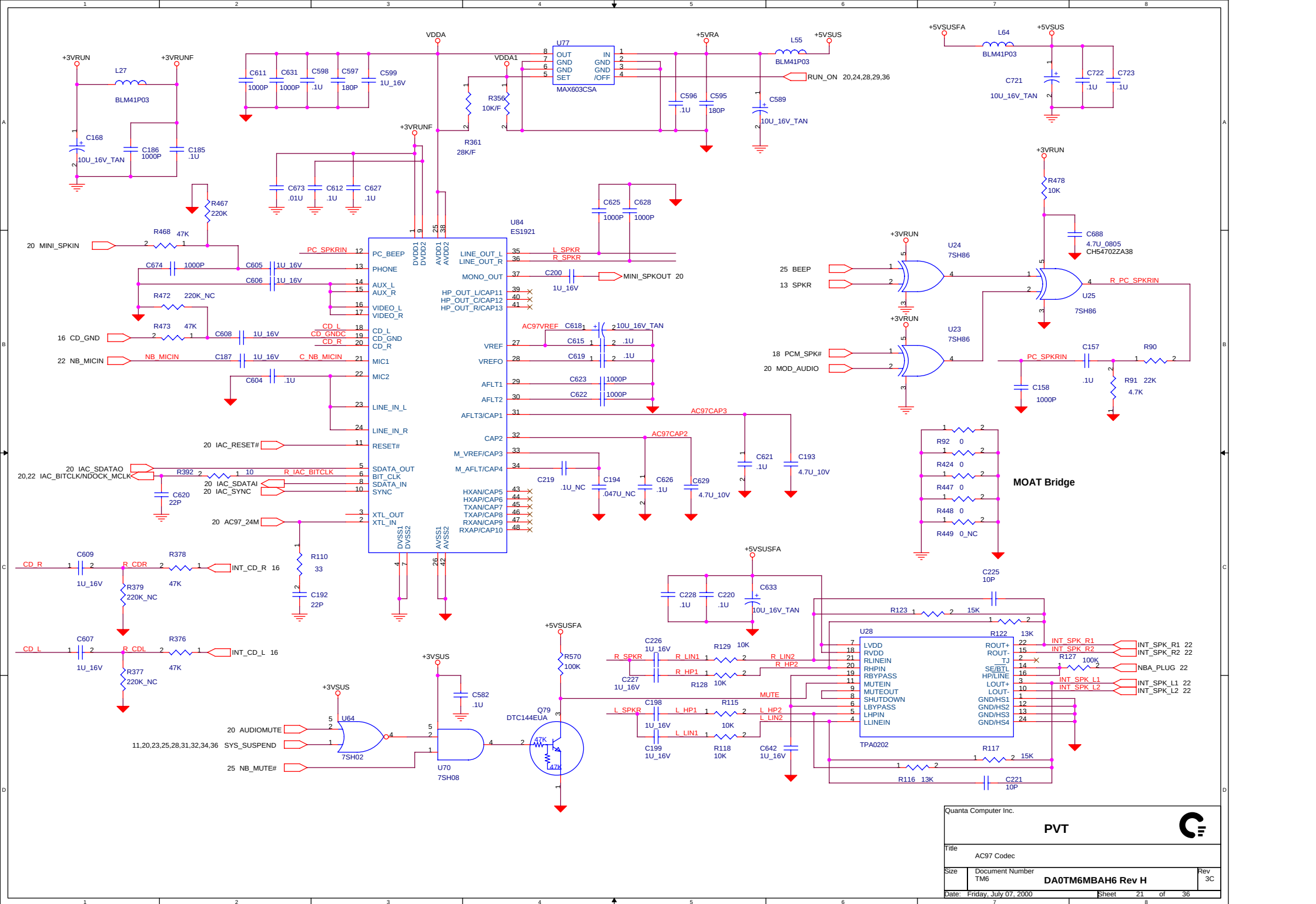
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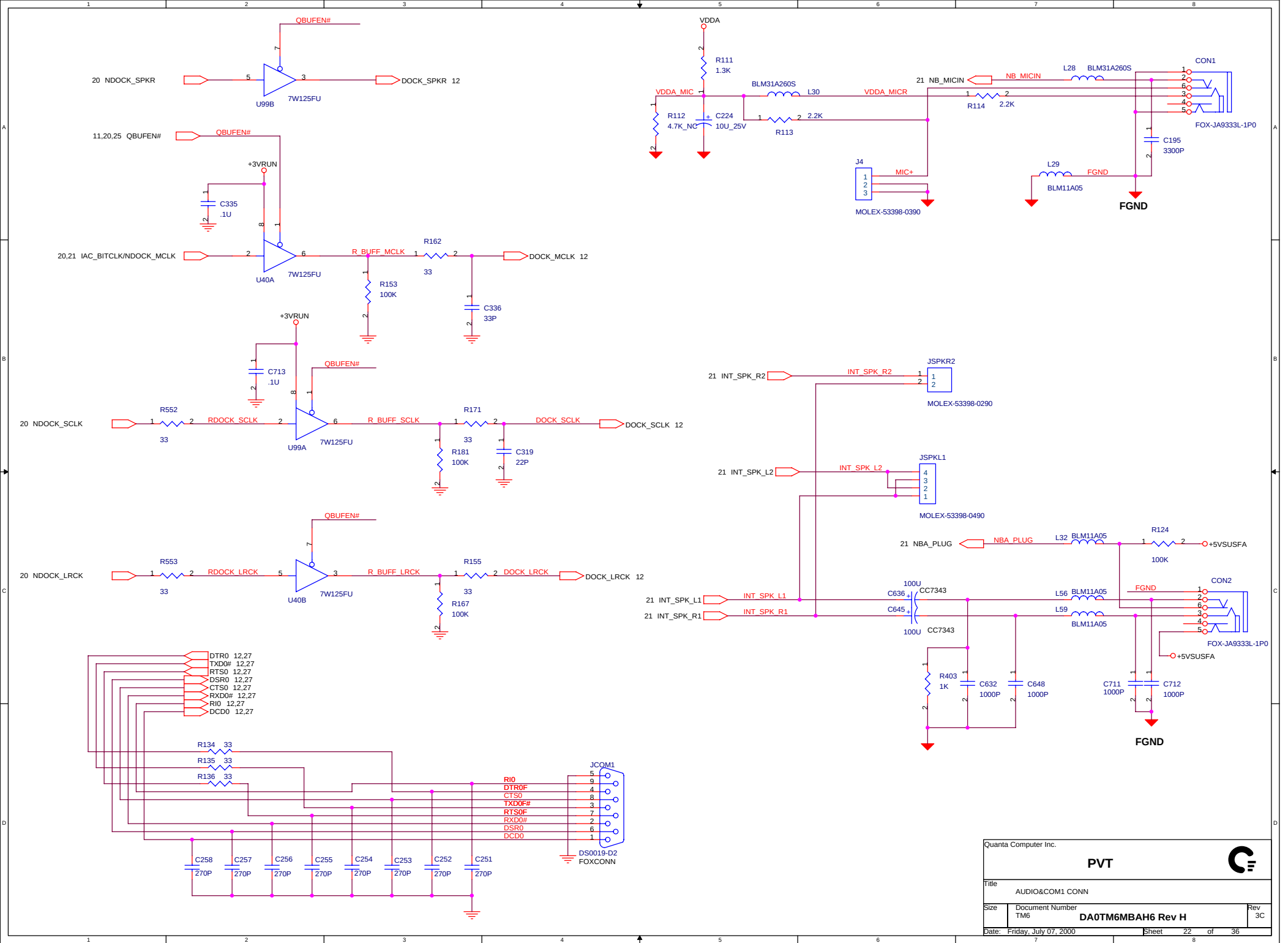
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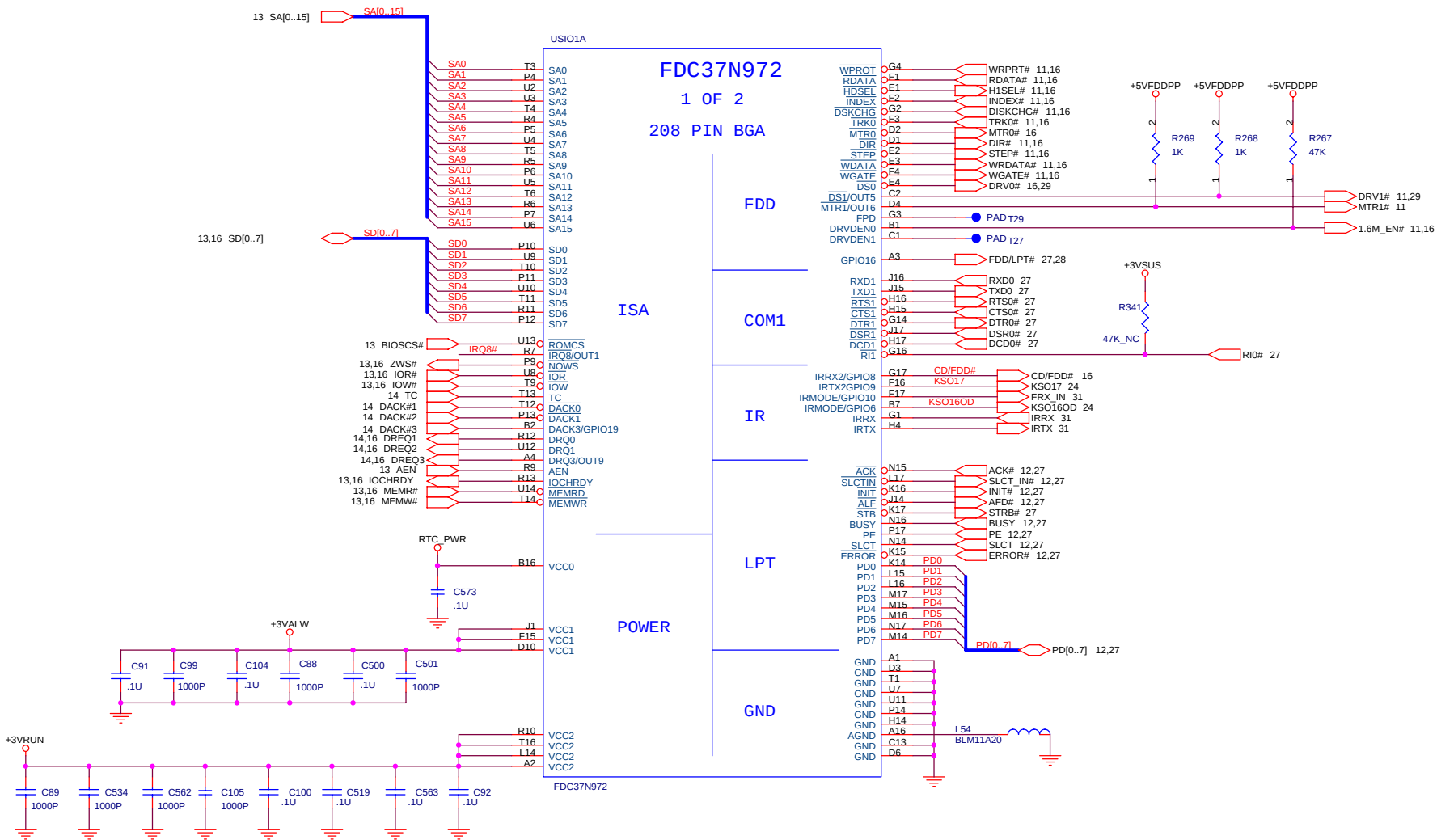
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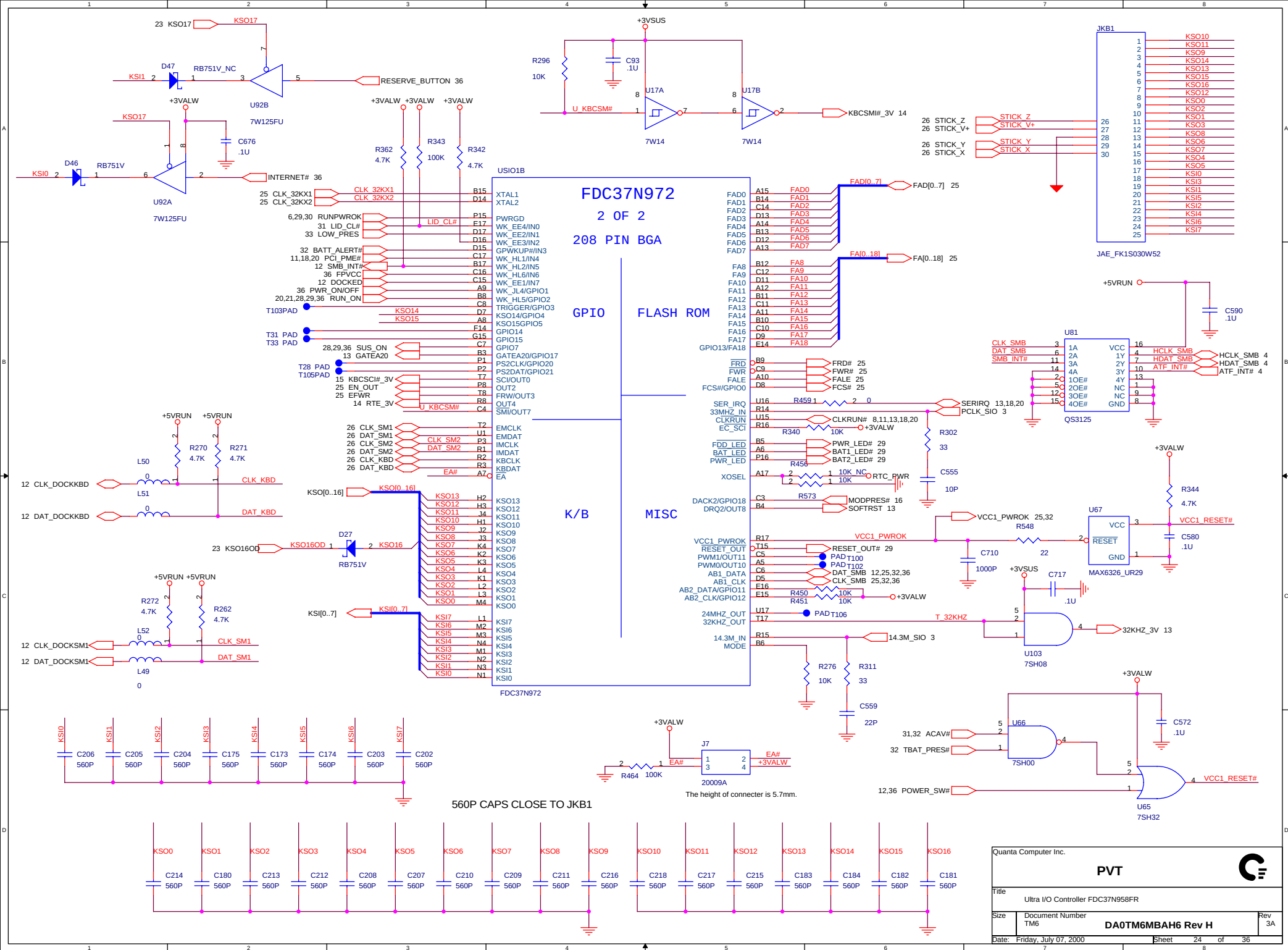


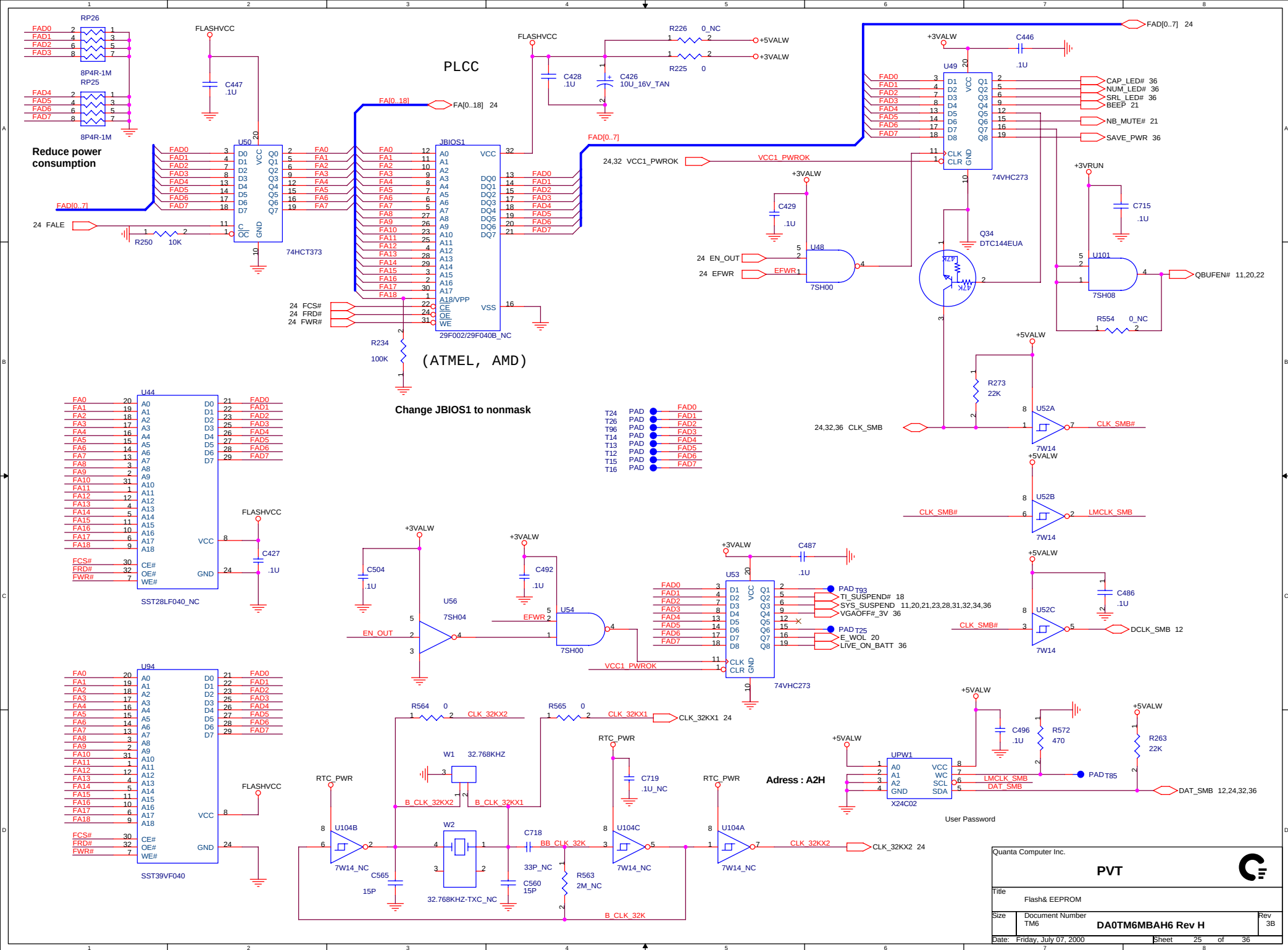




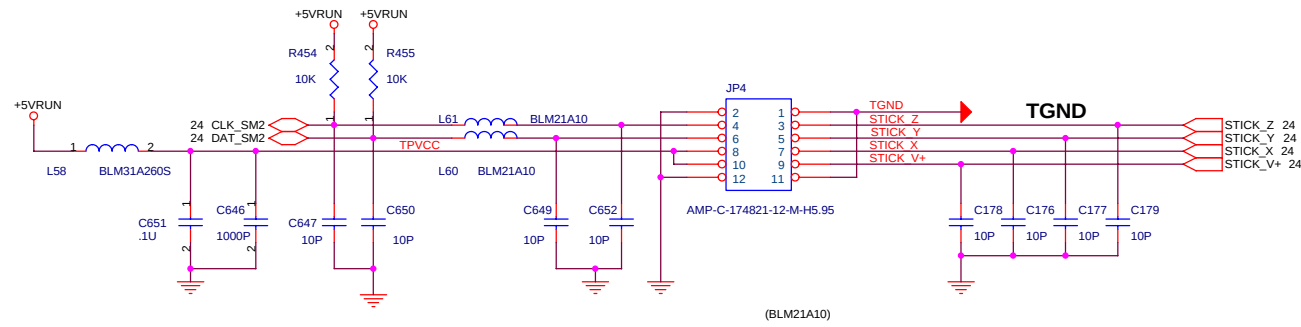




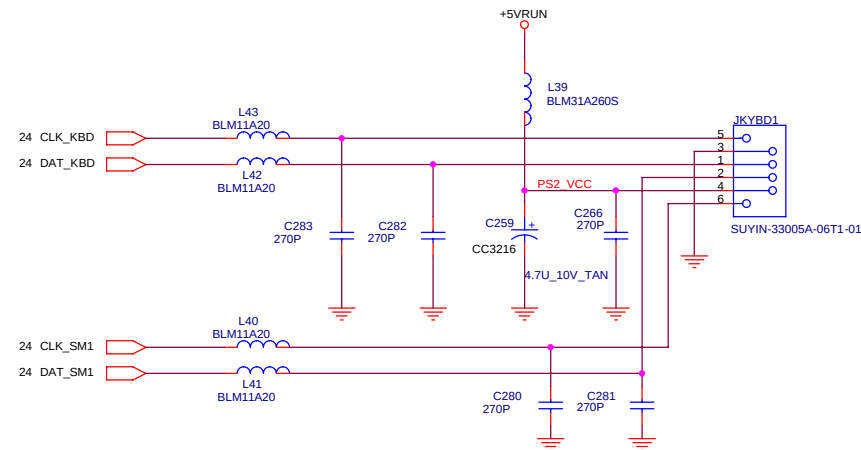




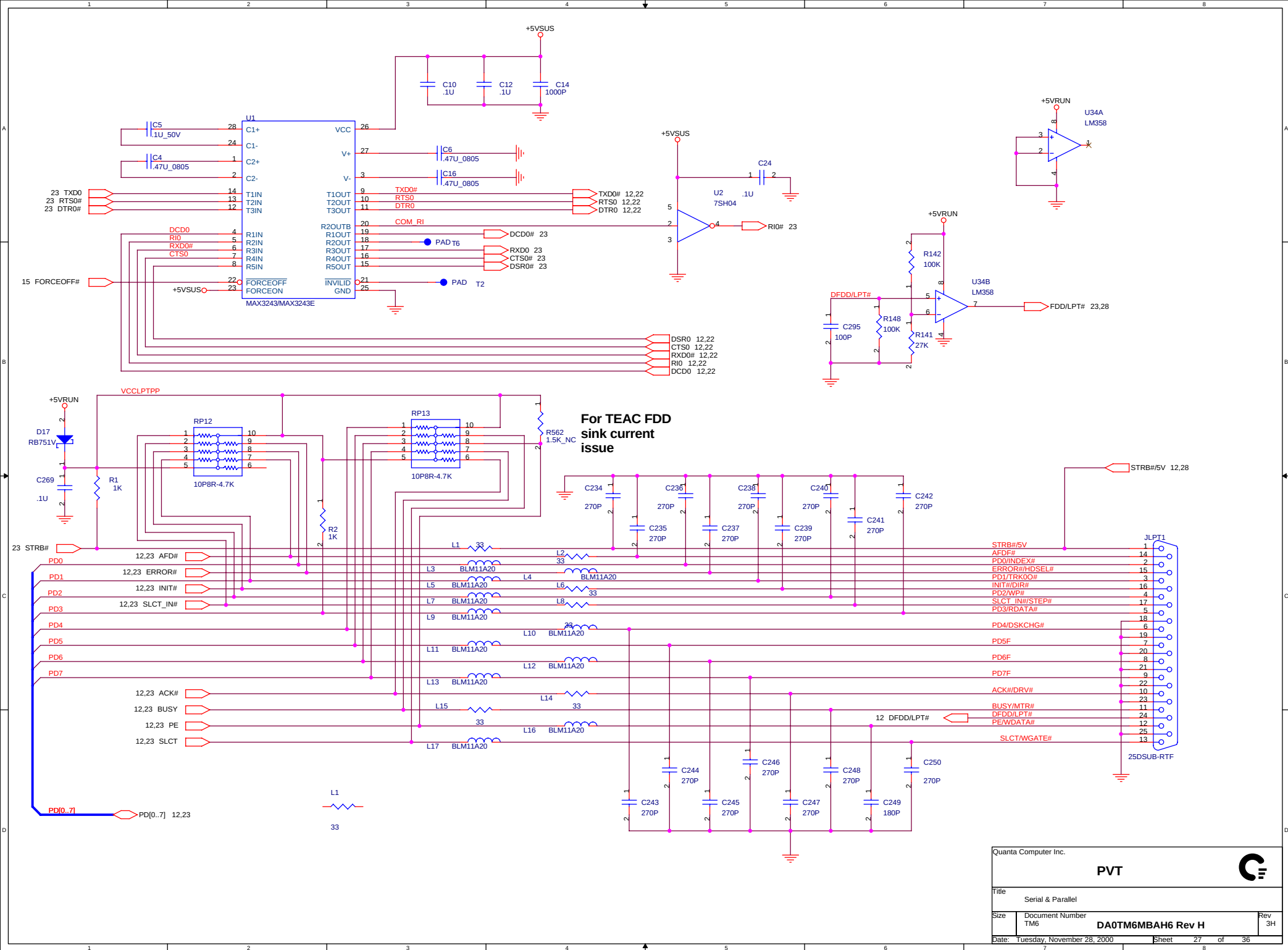
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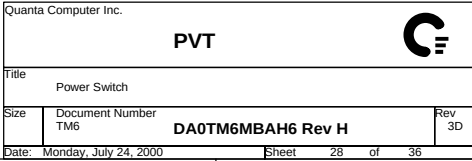


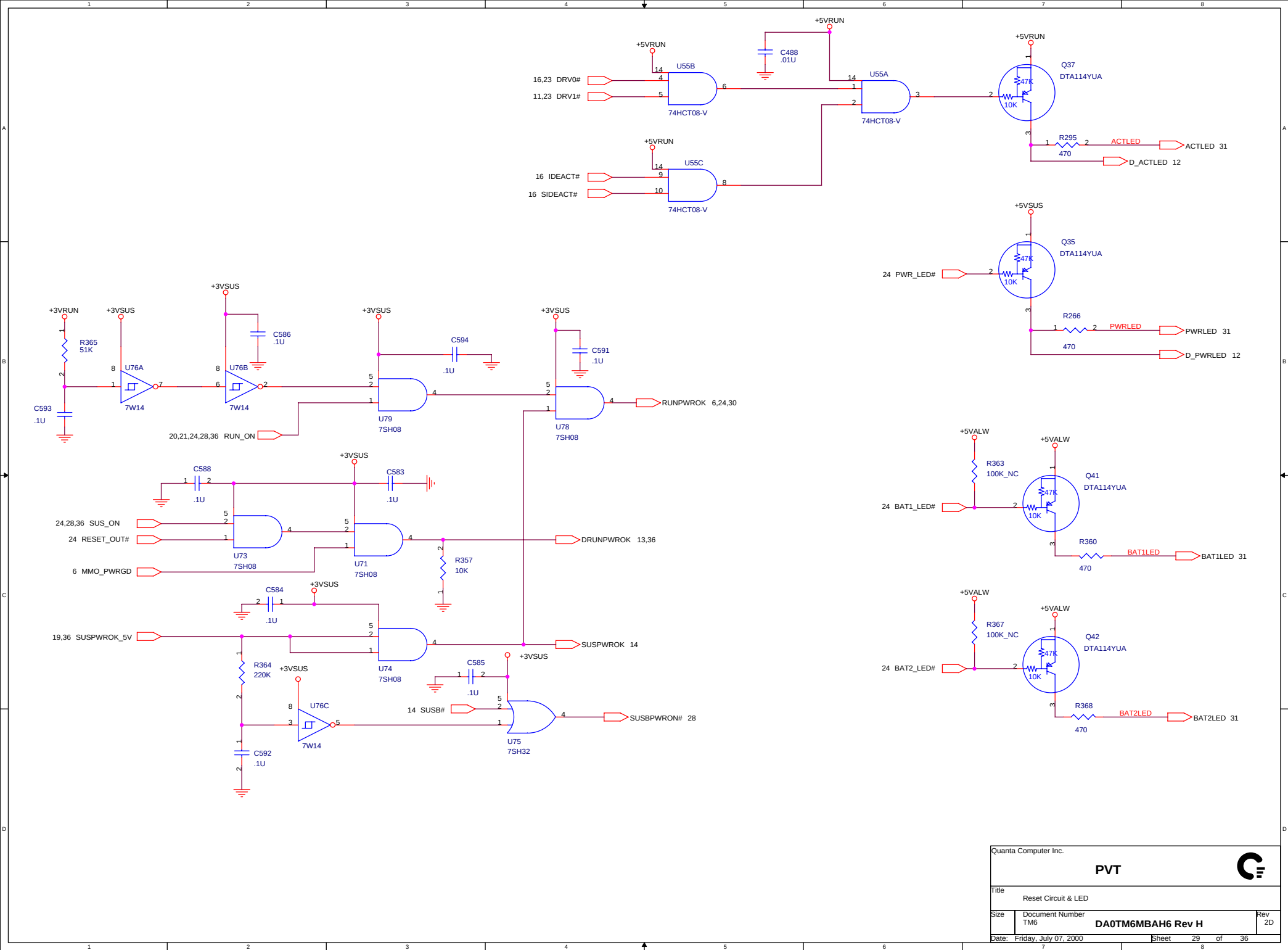
TIE GND TO H20

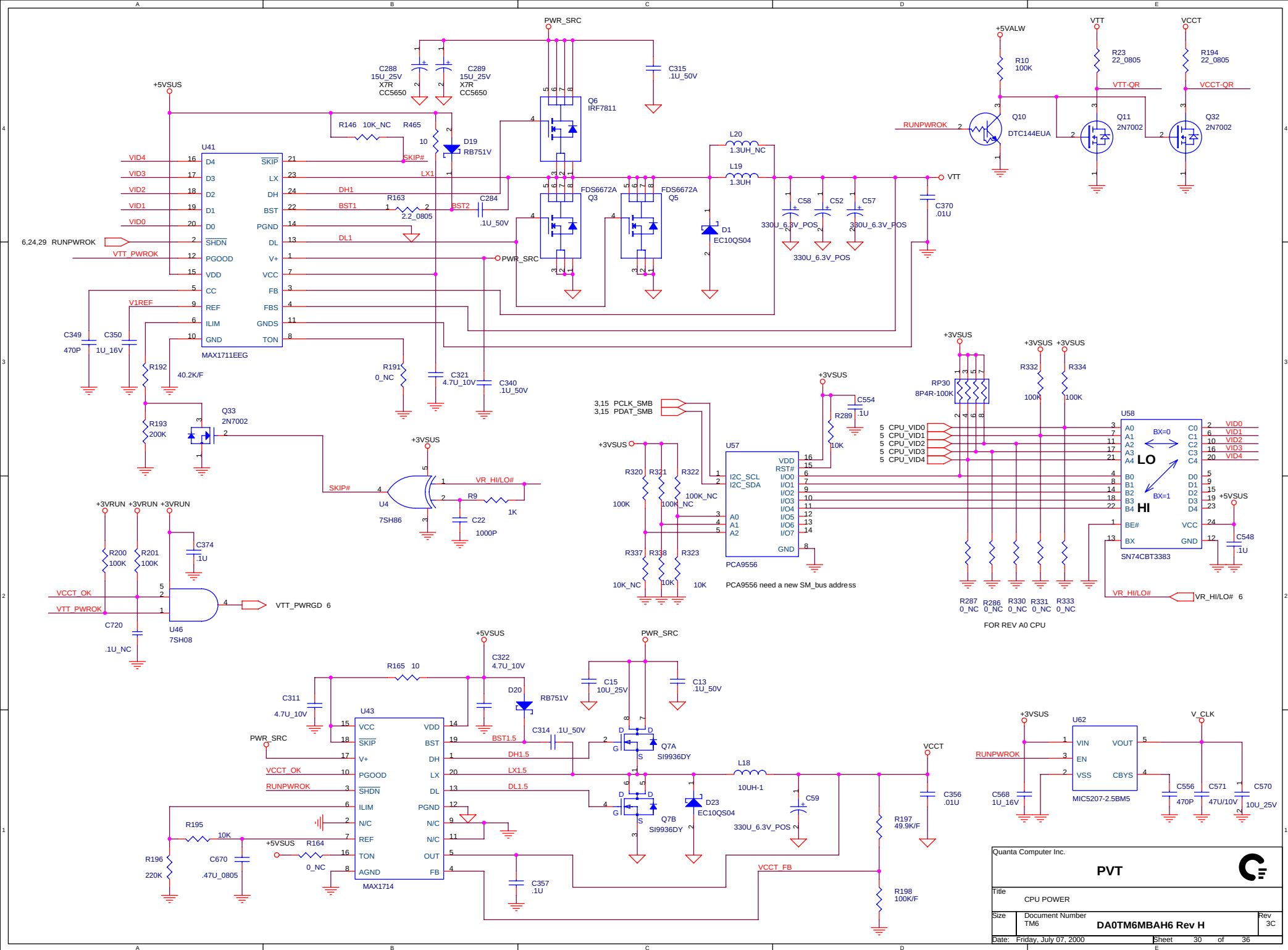


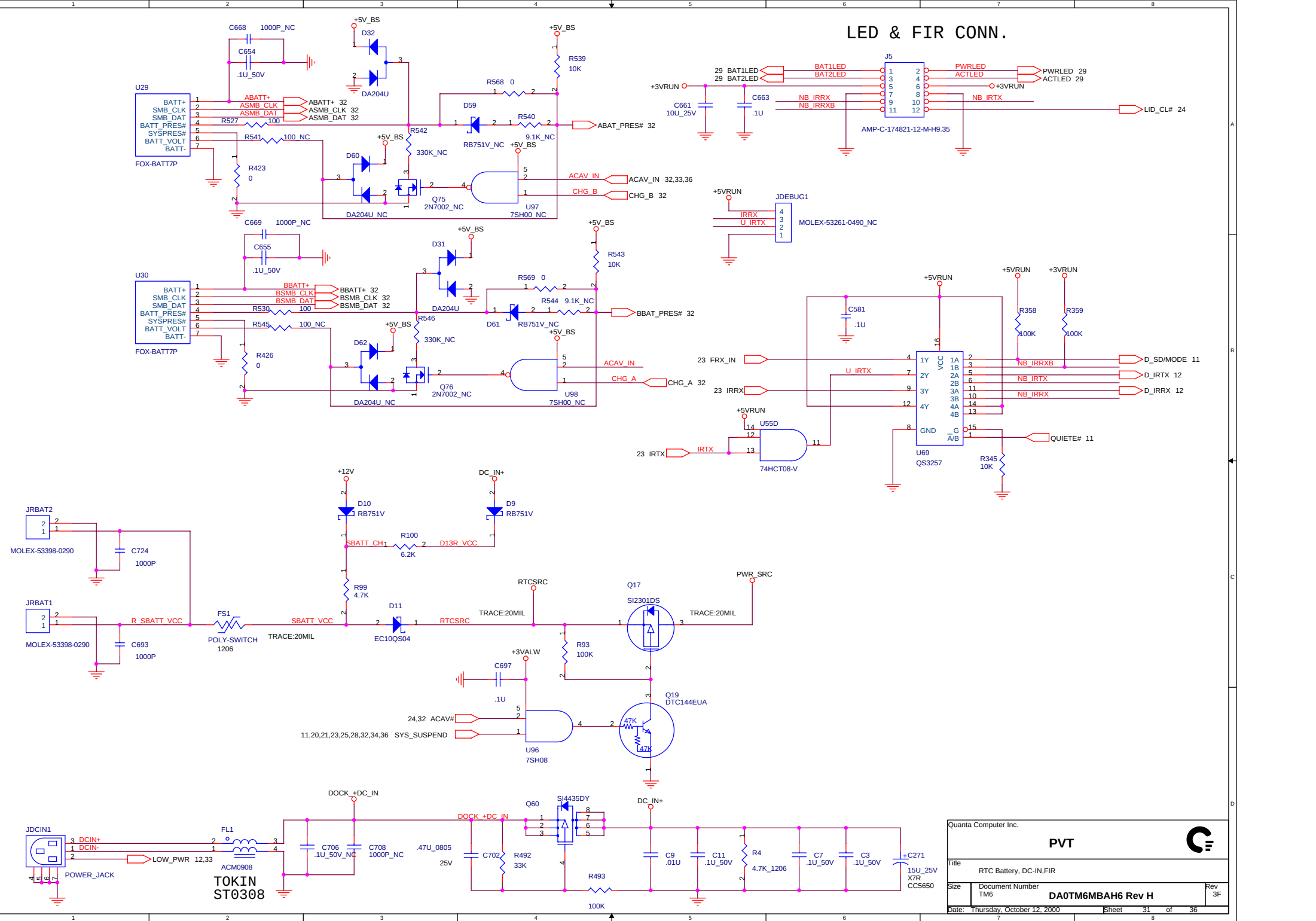
Quanta Computer Inc.			
PVT			
Title Stick&Touch Pad			
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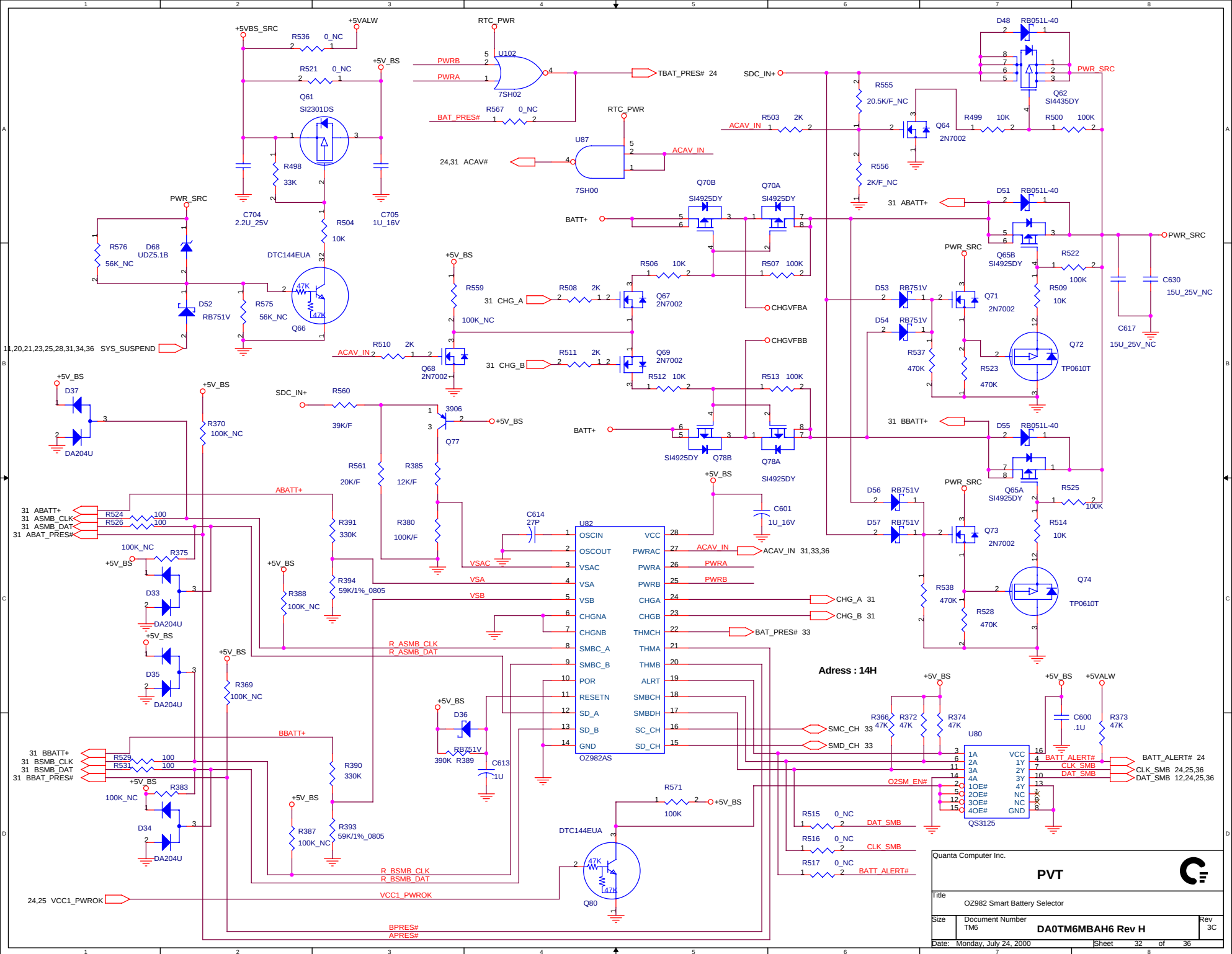


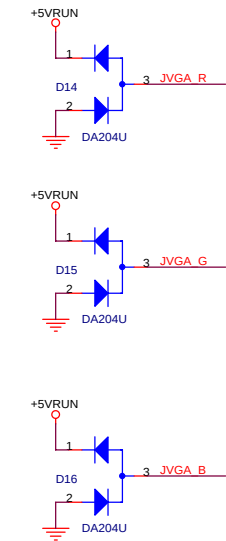
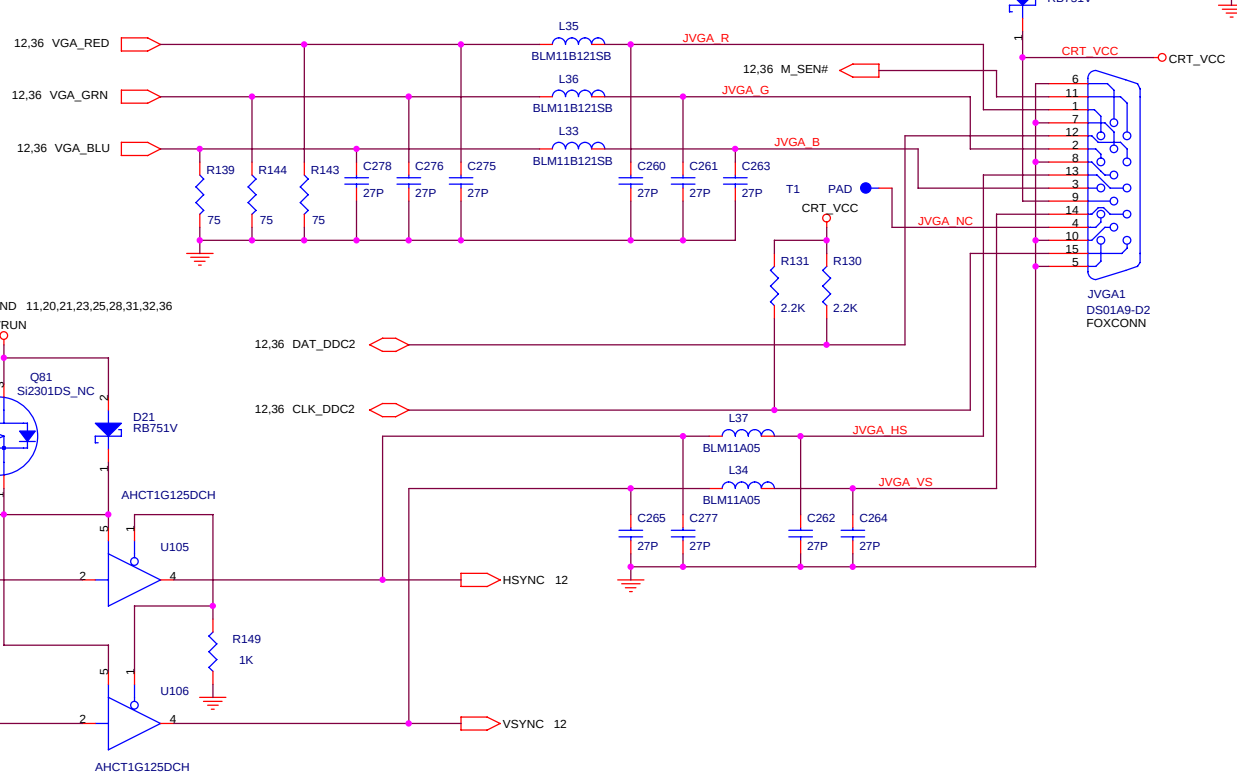
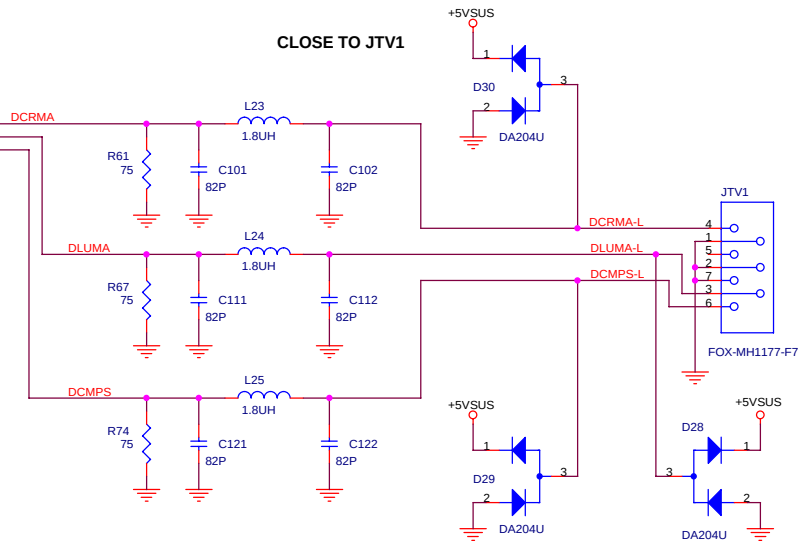




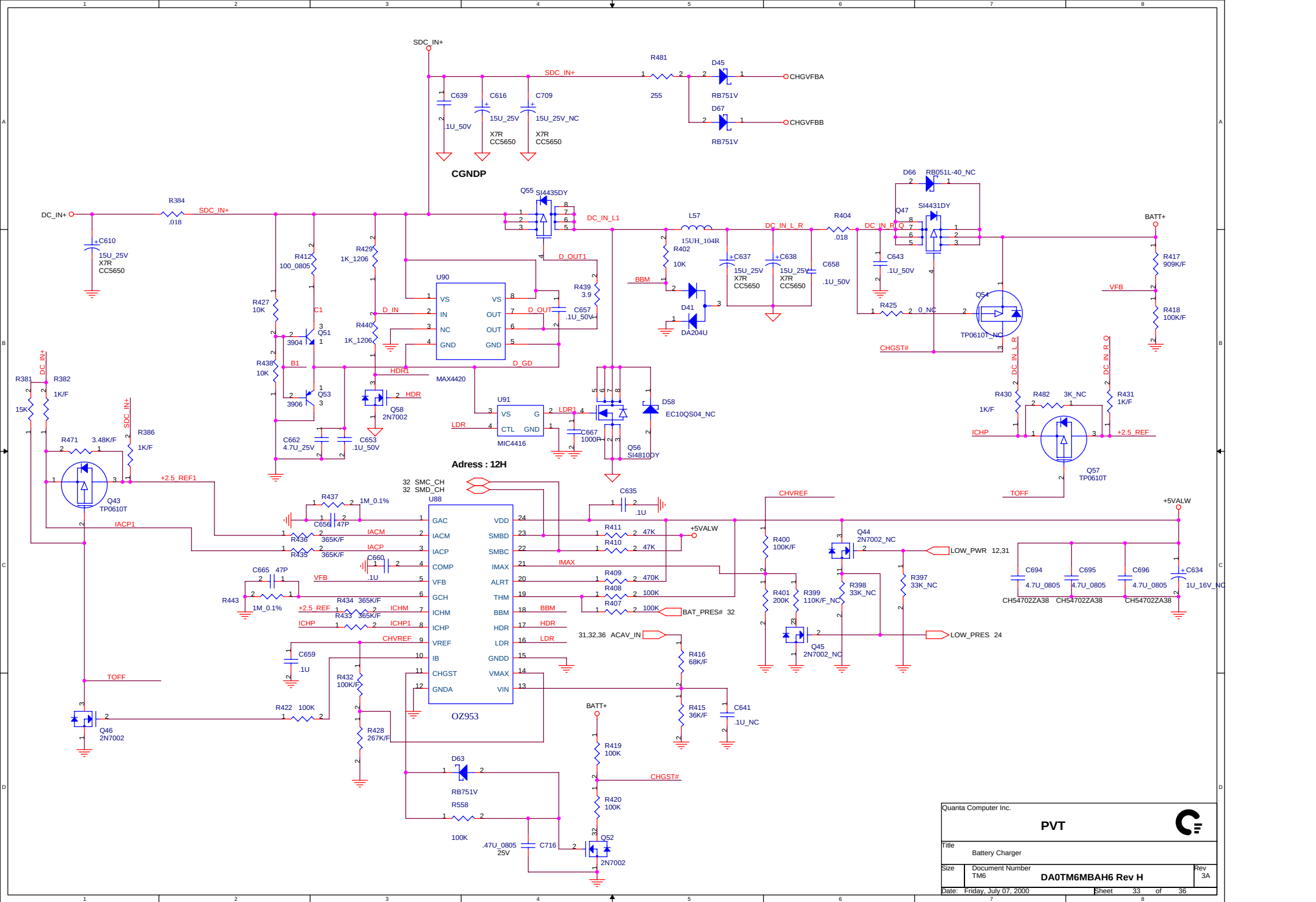


Quanta Computer Inc.			
PVT			
Title			
RTC Battery, DC-IN, FIR			
Size			
Document Number			
TM6			
Date: Thursday, October 12, 2000			
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Rev 3F			

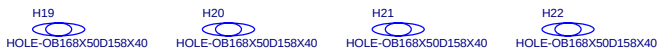
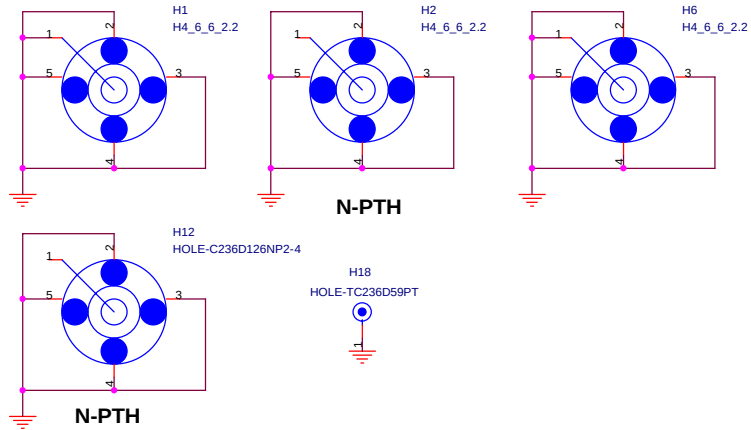
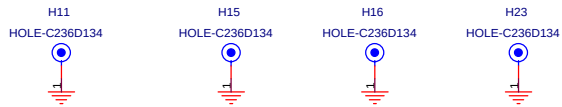




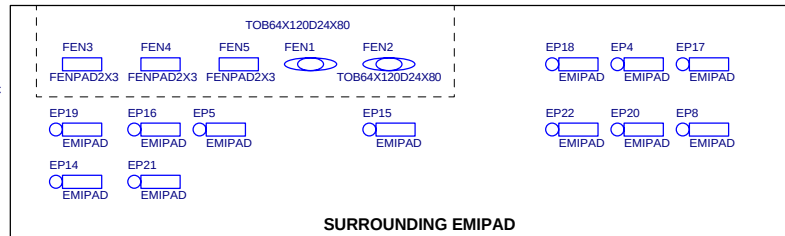
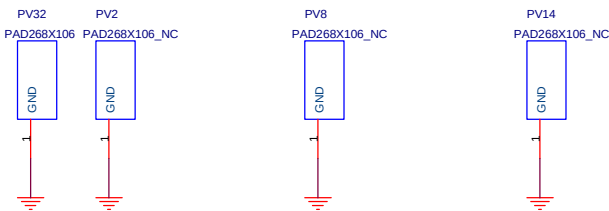
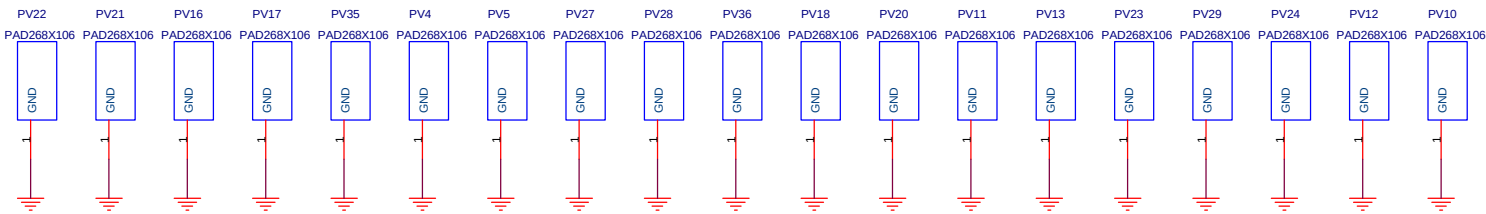
Quanta Computer Inc.				
PVT				
Title				
S-VIDEO & CRT				
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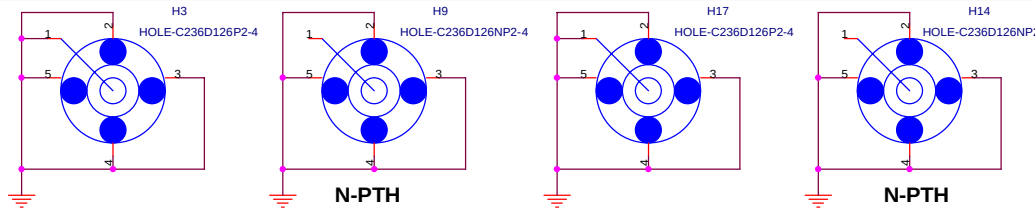
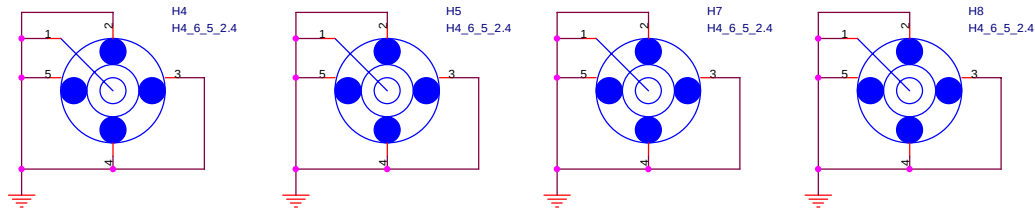
HDD



HDD TRAY GUIDE HOLE



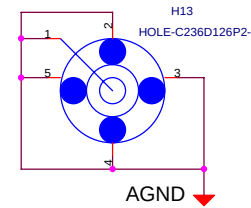
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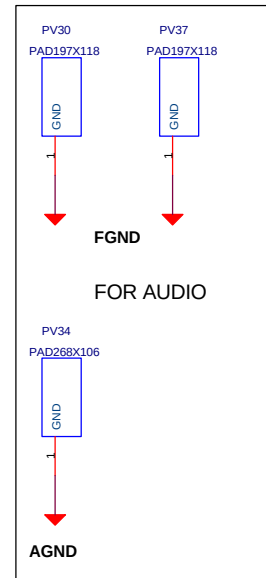
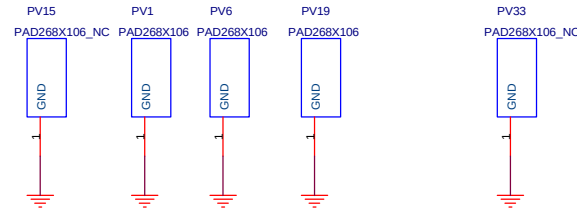
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N-PTH

KEYBOARD

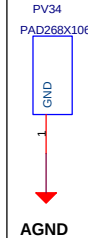


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