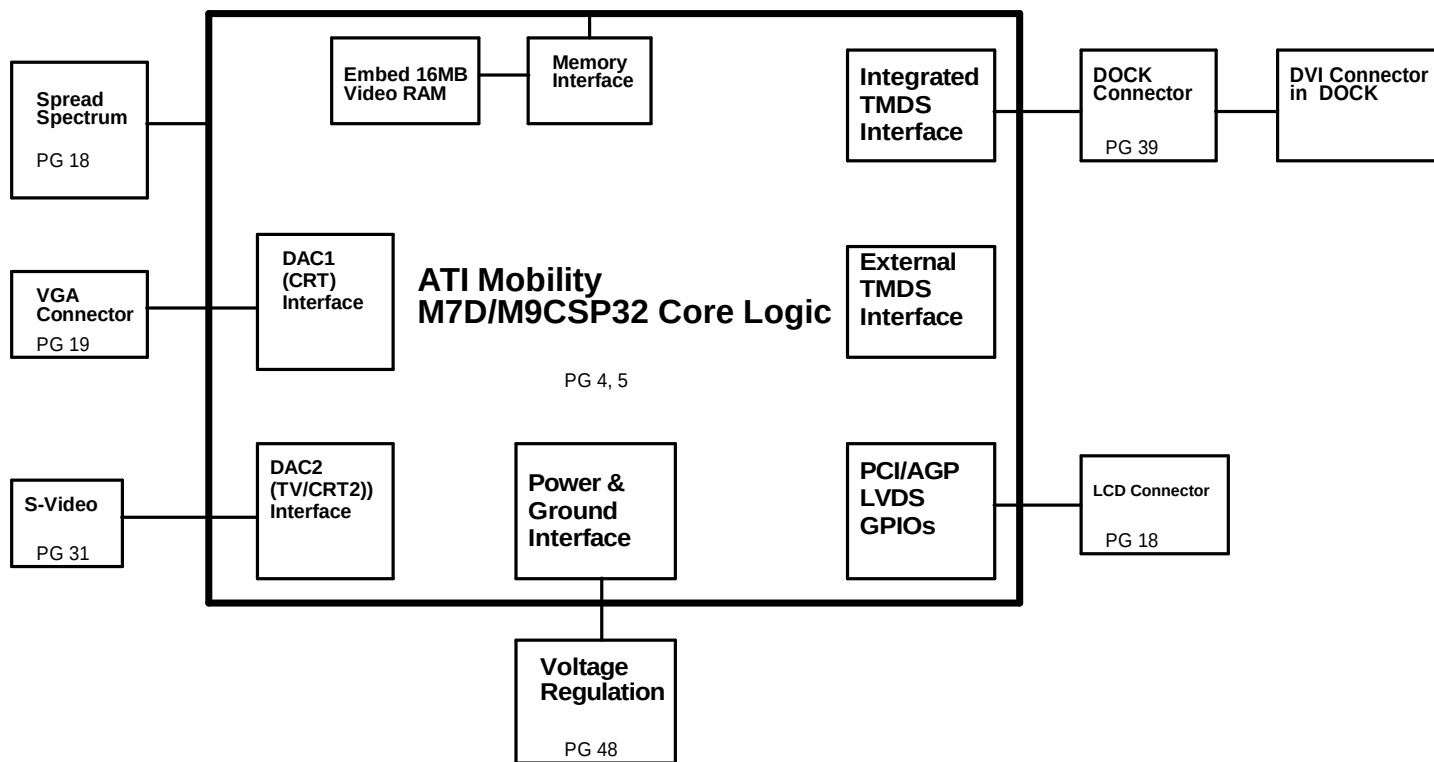


[illegible]



INDEX

Pg#	Description	DNI LIST
1	Schematic Block Diagram 1	
2	Schematic Block Diagram 2	
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6-8	Odem MCH-M	
9-11	ICH4	
12-13	DDR SO-DIMM(200P)	
14	CLOCK GENERATOR	
15-17	M7/M9 Graphics	
18-19	LCD CONN,CY25245 & CRT CONN	
20	IDE (HDD&CD_ROM)	
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22	O2_OZ711E1&CONN	
23-24	MINI-PCI & MDC CONN	
25-26	SIO	
27-28	SERIAL & PARALLEL	
29	FLASH	
30-31	TOUCH PAD & SWITCH&LED	
32	FAN & THERMAL	
33-34	AC97 CODEC & connector	
35-36	LAN(KENAI32-LAVON) & JACK	
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38-39	DOCKING Buffer and connector	
40	Reset circuit	
41	RTC BATTERY	
42-43	Battery Selector & Charger	
44	CPU Power	
45	1.2V,1.5V,1.8VS,2.5VS	
46	D/D Power	
47	RUN POWER SW	
48	VGA DC/DC	
49	DCIN,Batt	
50	CPU I/O Power	

Power & Ground

Label	Pg#	Description	Control Signal
DC_IN+		AC ADAPTER (20V)	
PBATT+		MAIN BATTERY + (10-17V)	
PWR_SRC		MAIN POWER (10-20V)	
RTC_PWR3_3V		RTC & PCL POWER (3.3V)	
+12V		+12V	DRUNPWROK
VHORE		CPU CORE POWER (1.25/1.15V)	RUNPWROK
V1_2RUN		AGTL+ POWER (1.2V)	RUNPWROK
+3VRUN		SLP_S3# CTRLD POWER	RUN_ON
+3VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VALW		8051 POWER (5V)	
+5VRUN		SLP_S3# CTRLD POWER	RUN_ON
+5VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VHDD		HDD POWER (5V)	HDDC_EN#
+5VMOD		MODULE POWER (5V)	MODC_EN#
STRB#5V		EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VFAN1, +5VFAN2		FAN POWER (5V)	FAN_OFF/ON#
VDDA		AUDIO ANALOG POWER (5V)	RUN_ON
1_8VSUS		RESUME WELL IN ICH	
1_8VRUN		SLP_S3# CTRLD POWER	
+3VALW		8051 POWER (3V)	
V1_5RUN		AGP I/O POWER	
 GND	ALL PAGES	DIGITAL GROUND	
 AGND		AUDIO GND	
 GNDP		CPU POWER GND	
 CGNDP		CHARGER GND	
 DGNDP		DC/DC POWER GND	
 LANGND		COMBO CONN GND	

QUANTA
COMPUTERTitle
Index, DNI, Power & GroundSize
Document Number

JMJ2

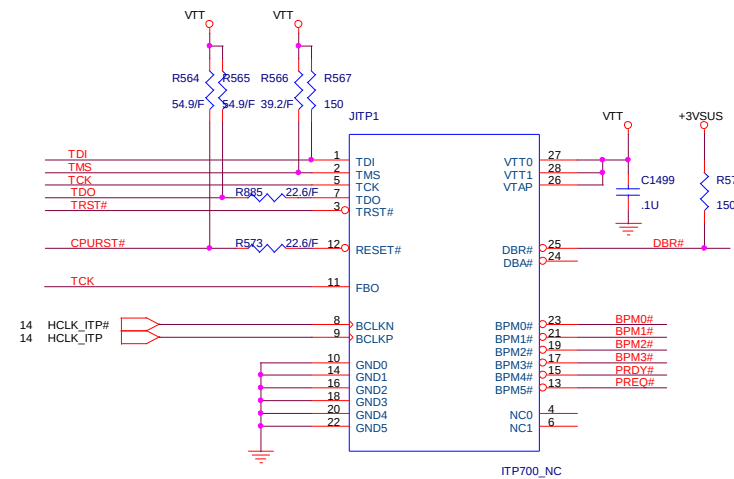
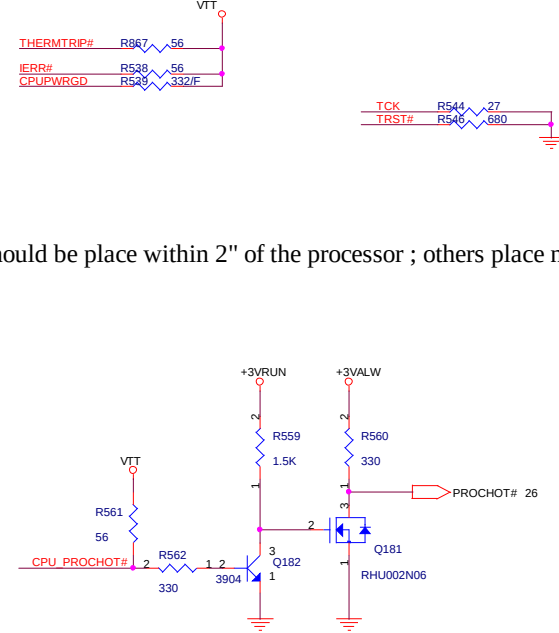
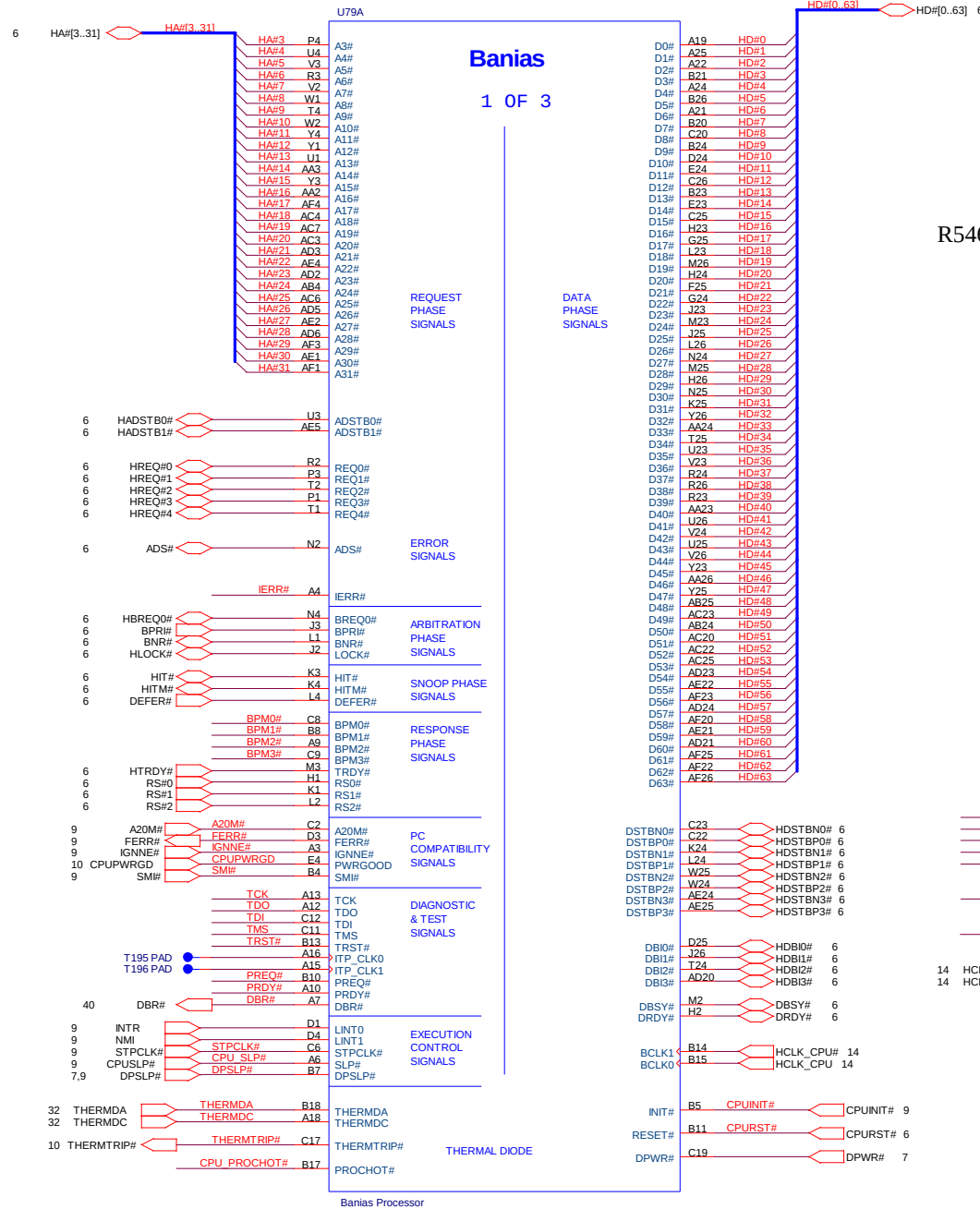
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1A

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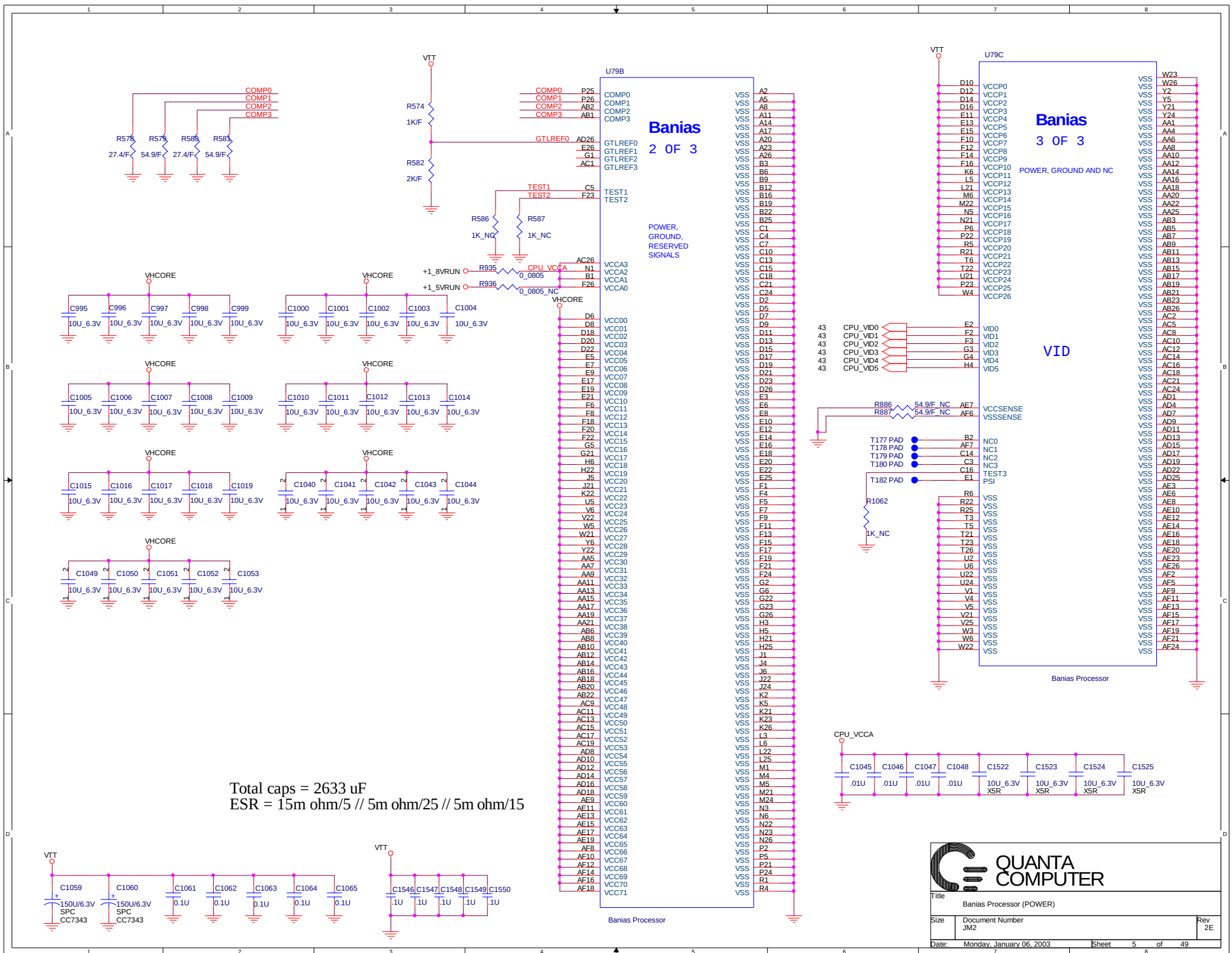
Sheet 3 of 49

R867 should be close to ICH4

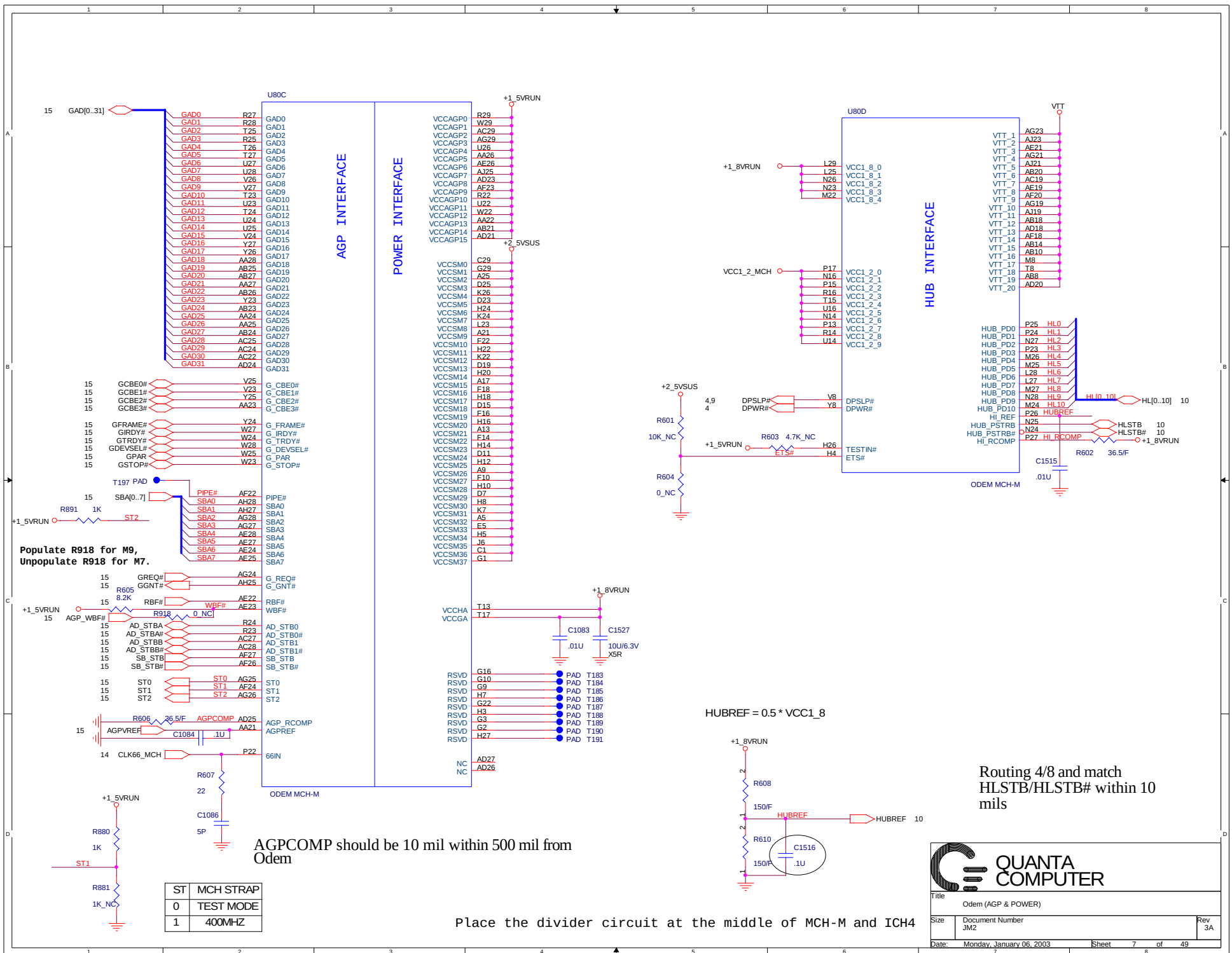
R546 should be place within 2" of the processor ; others place near ITP



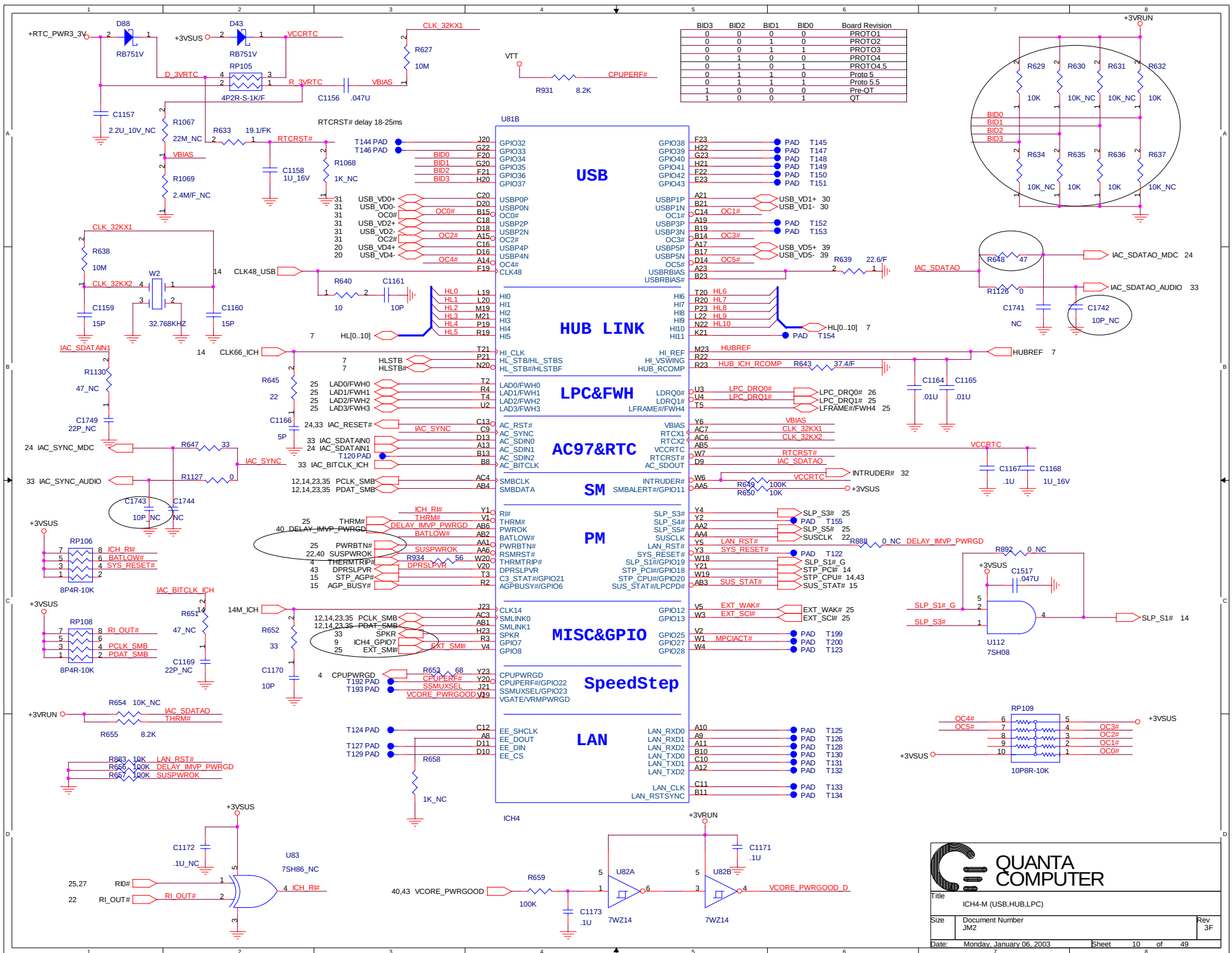
Title			
Banias Processor (HOST)			
Size	Document Number JM2		Rev 1A
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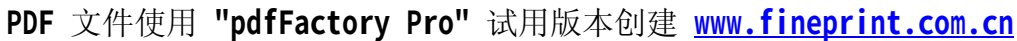


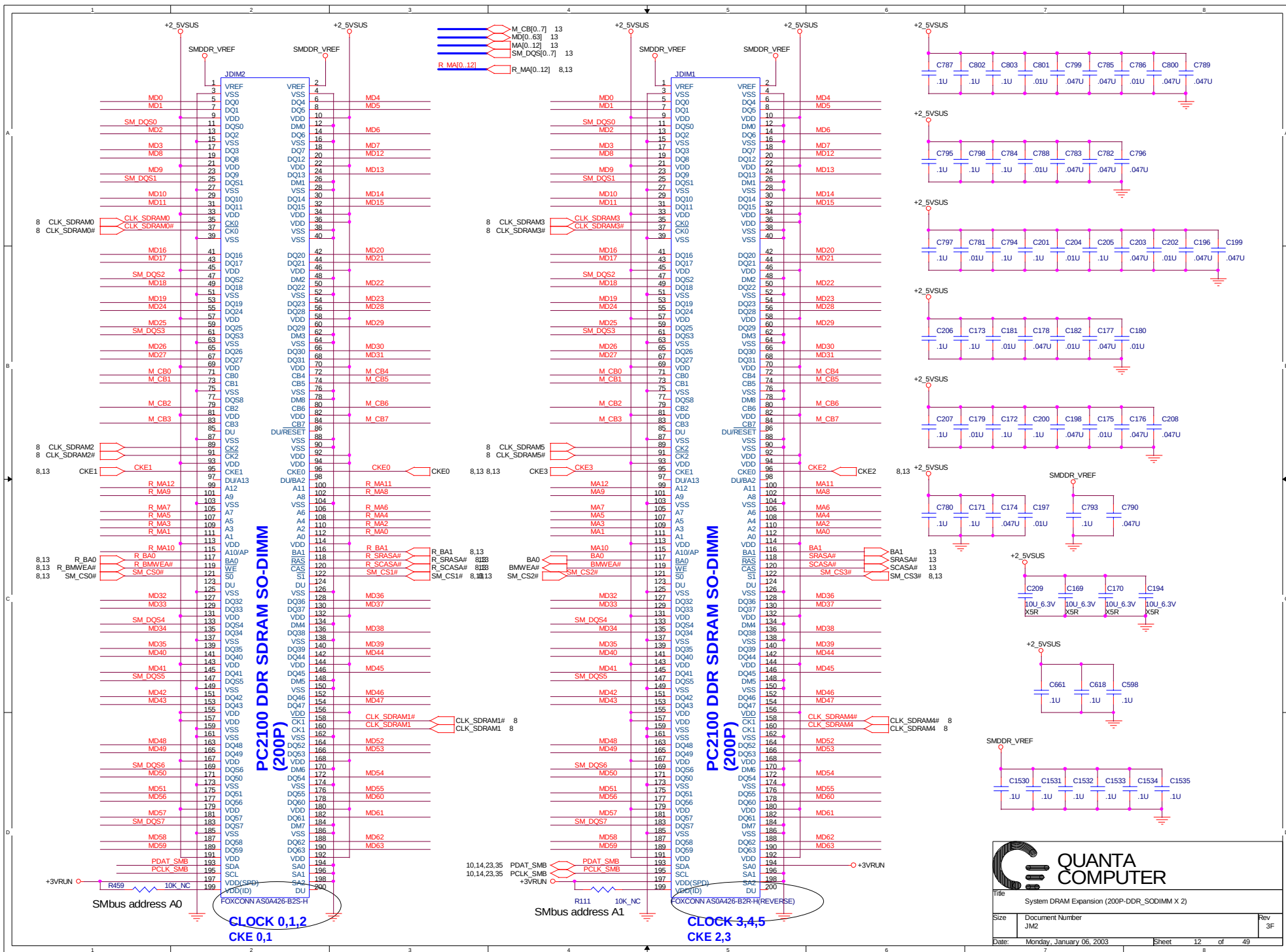


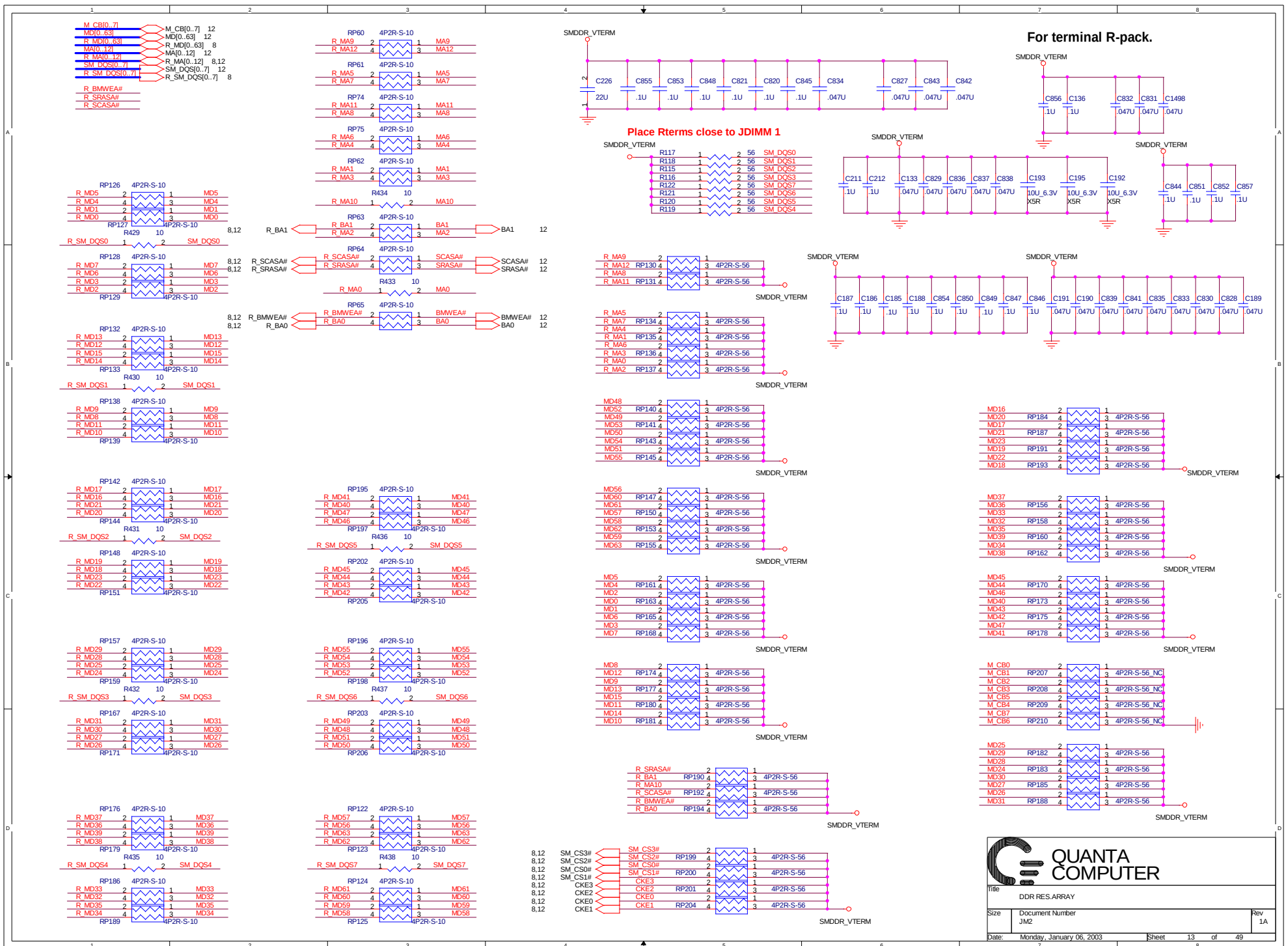






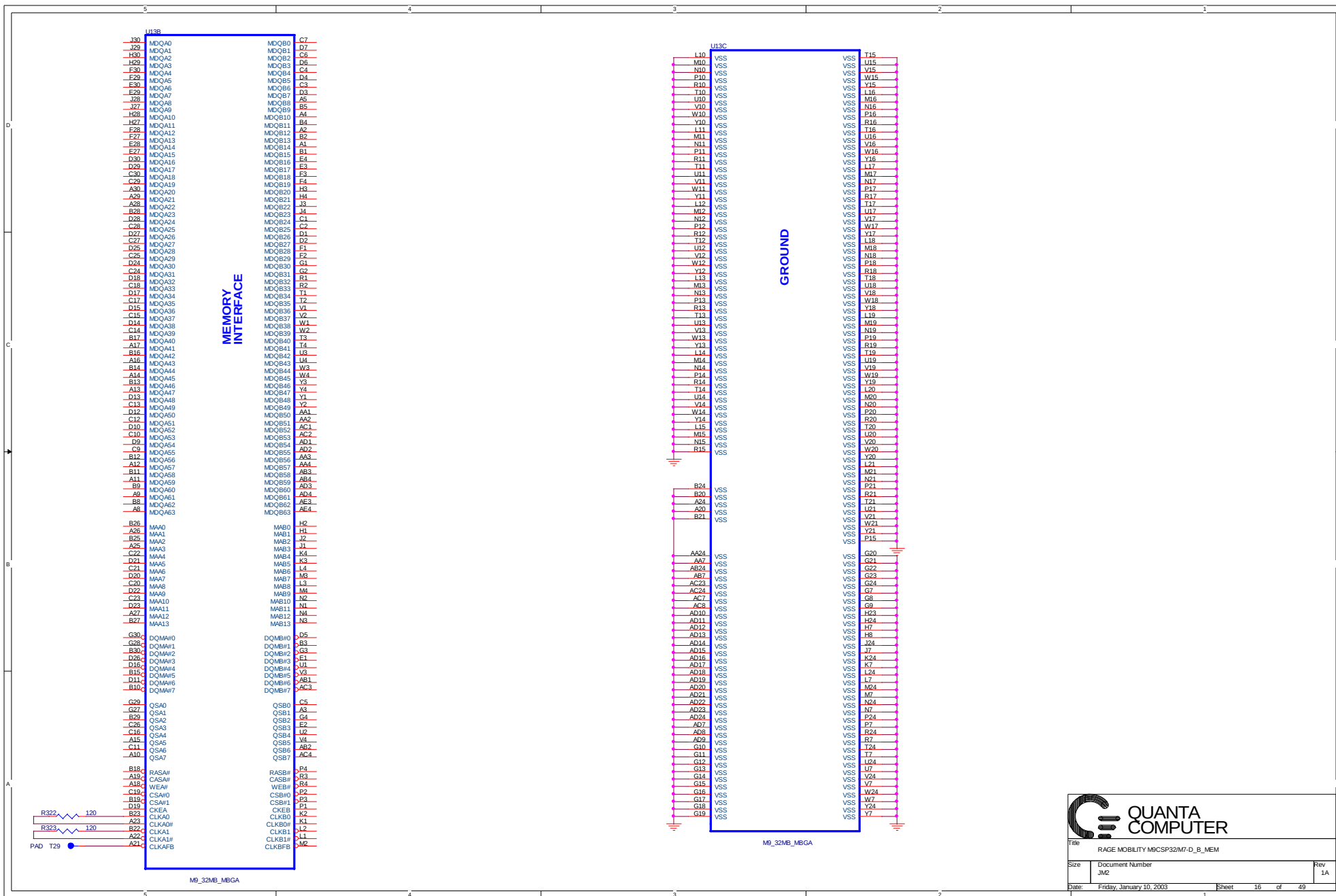


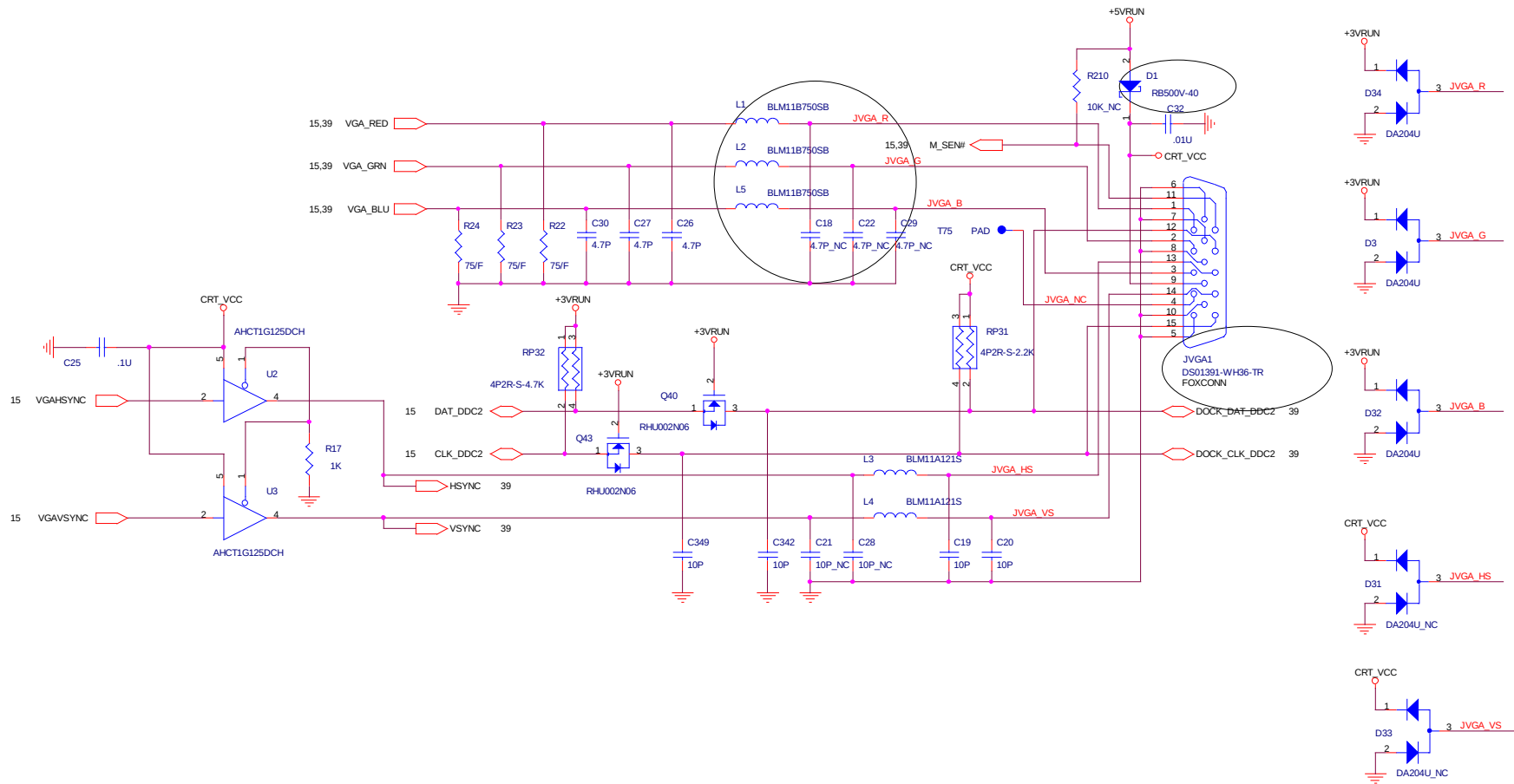




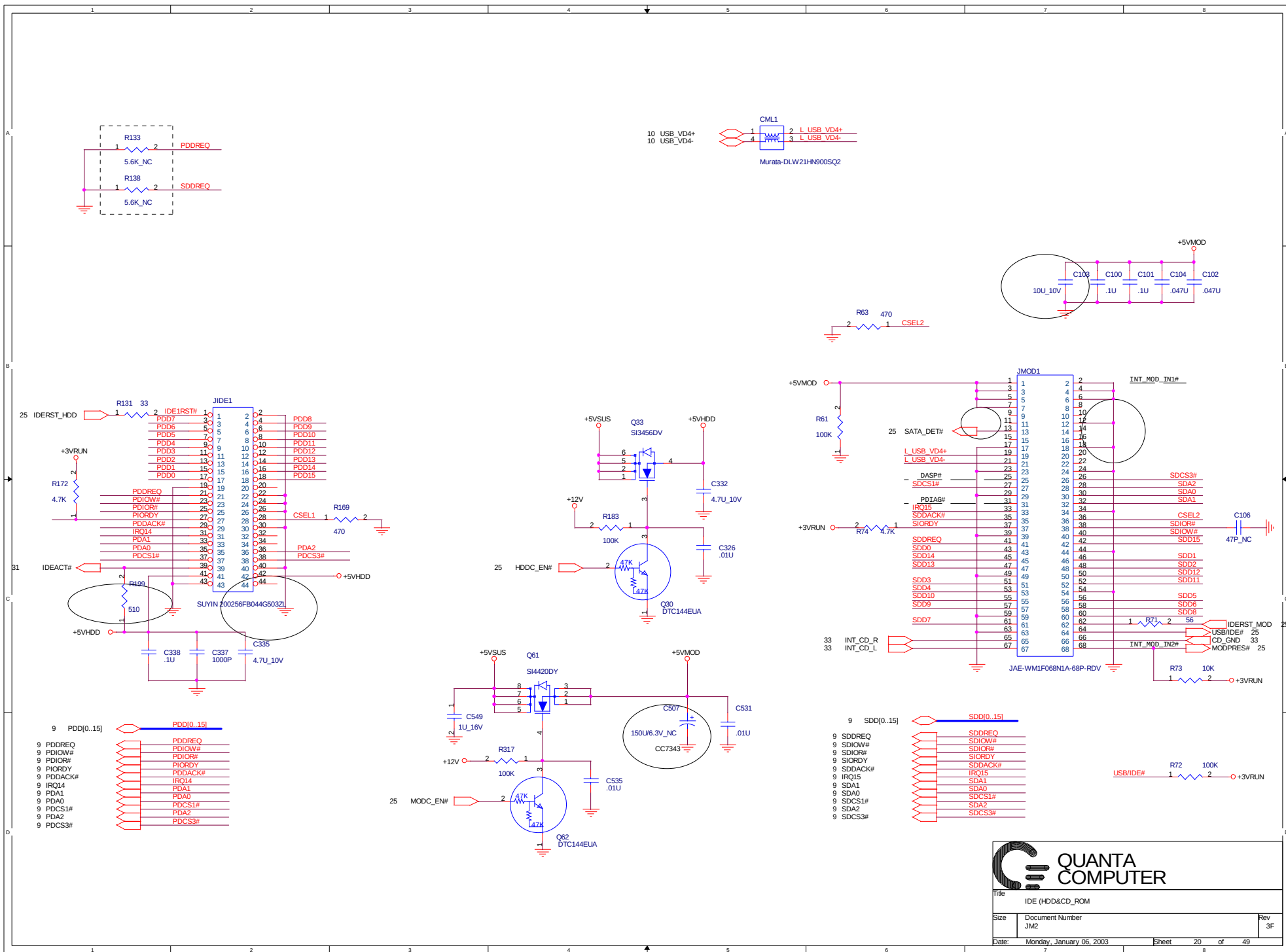
1

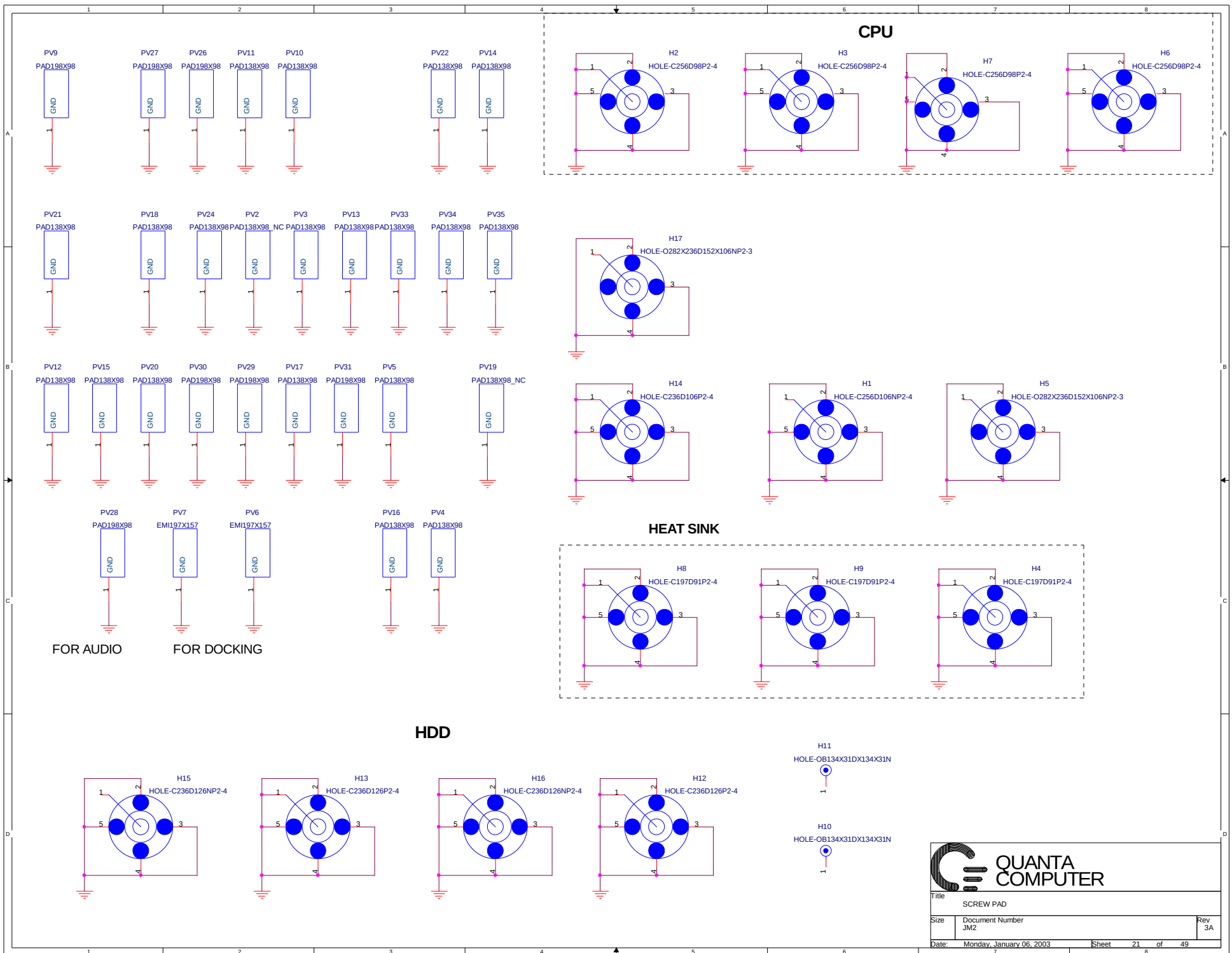
 QUANTA COMPUTER	
Title: CLOCK GENERATOR	
Size: JM2	Rev: 1A
Date: Monday, January 06, 2003	Sheet 14 of 49

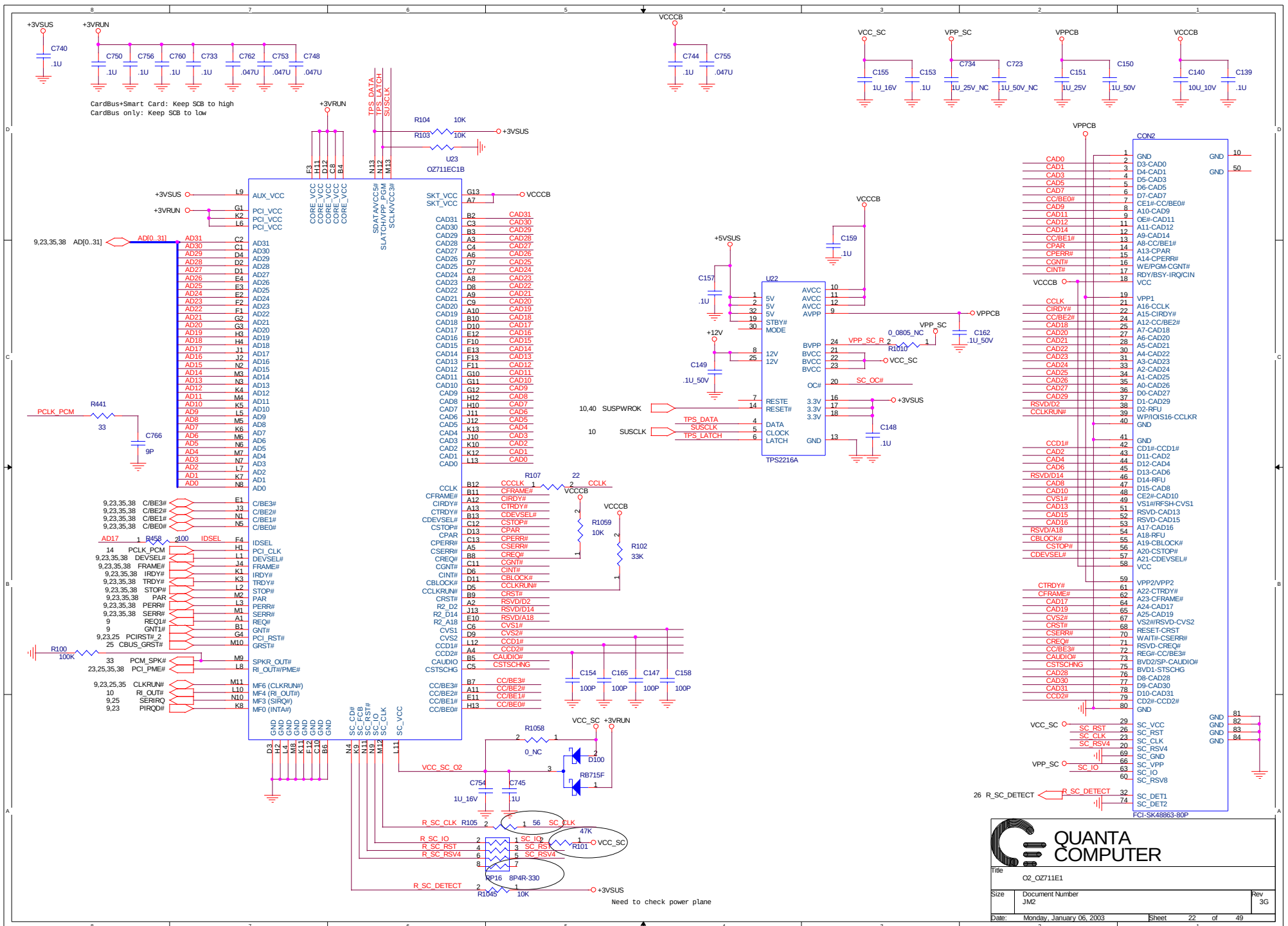




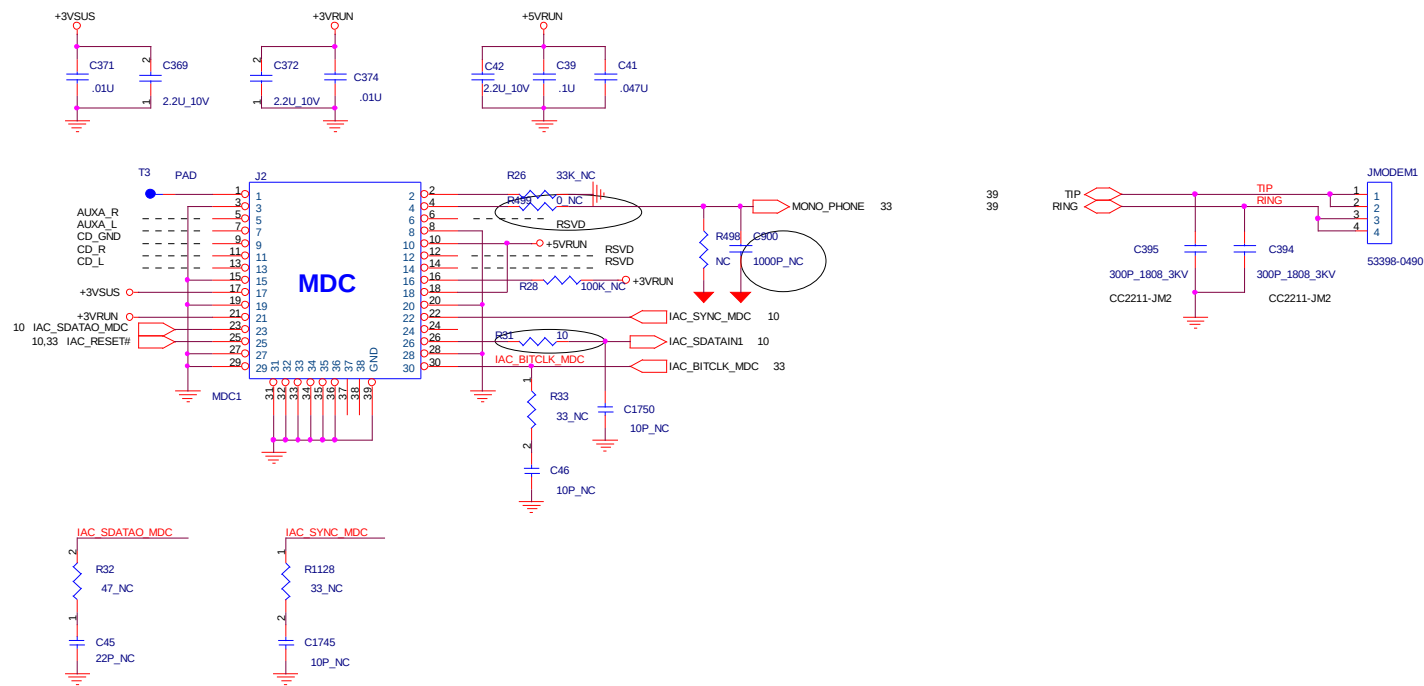
 QUANTA COMPUTER	
Title	CRT CONN
Size	Document Number JM2
Date	Monday, January 06, 2003
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Rev	3F

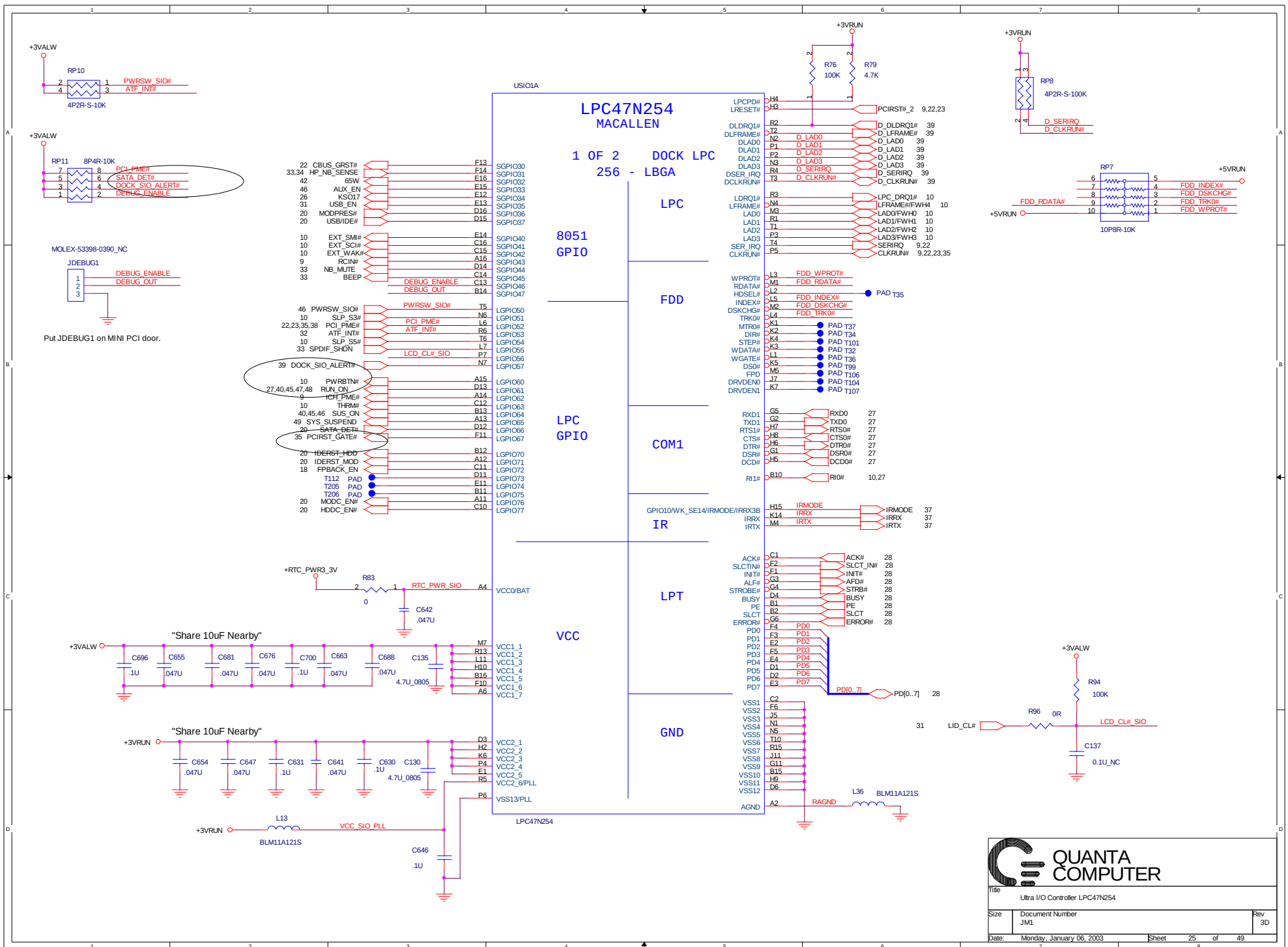


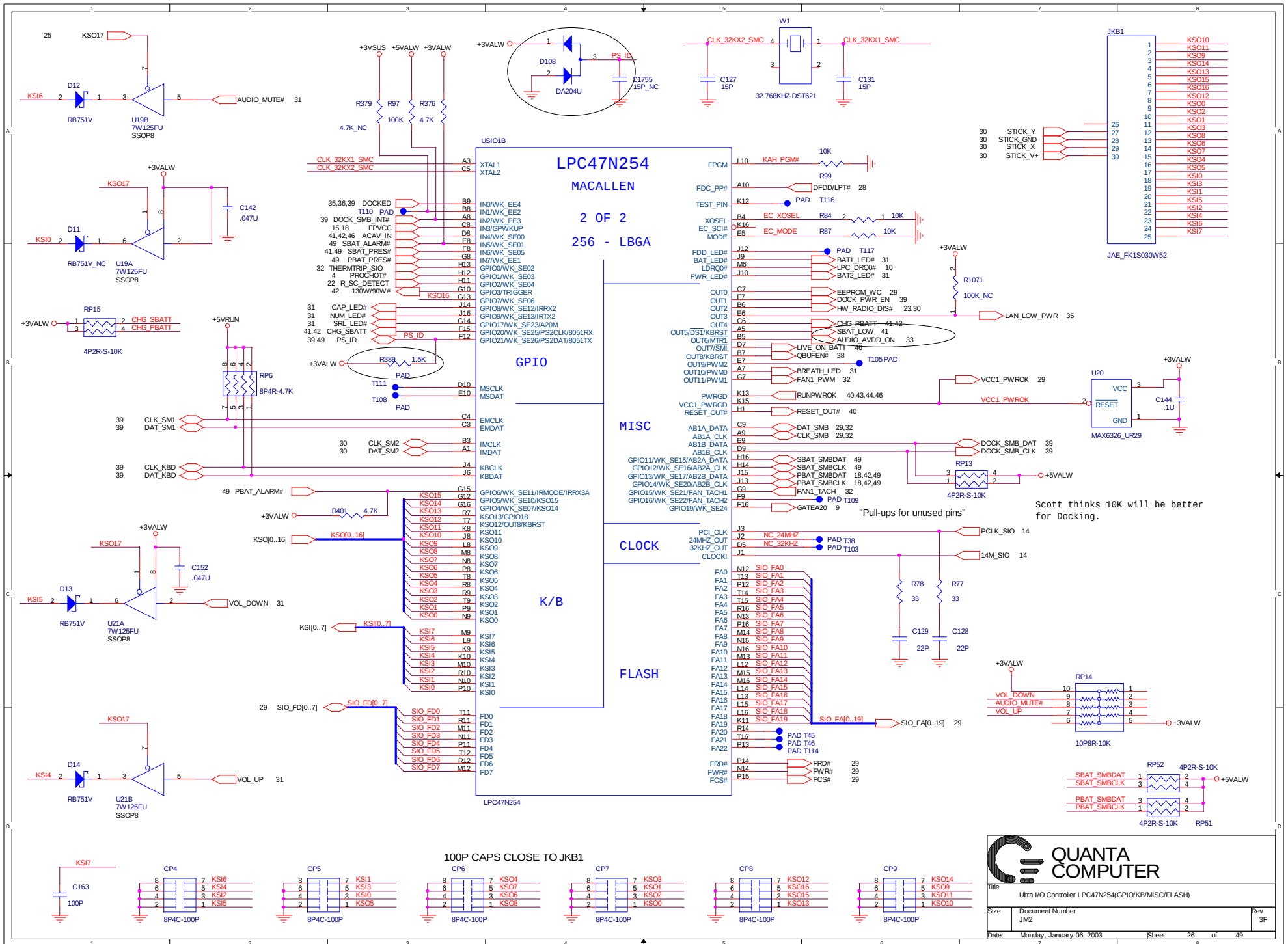


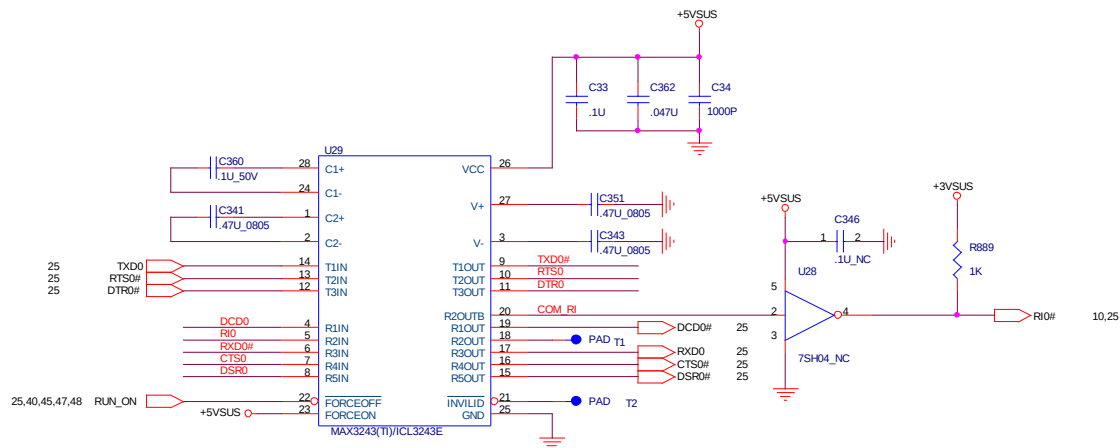




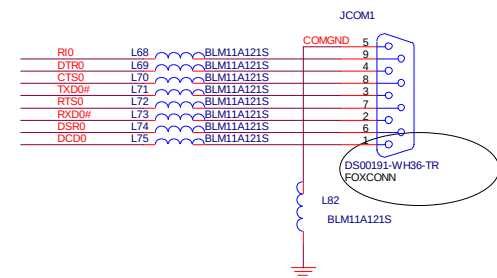








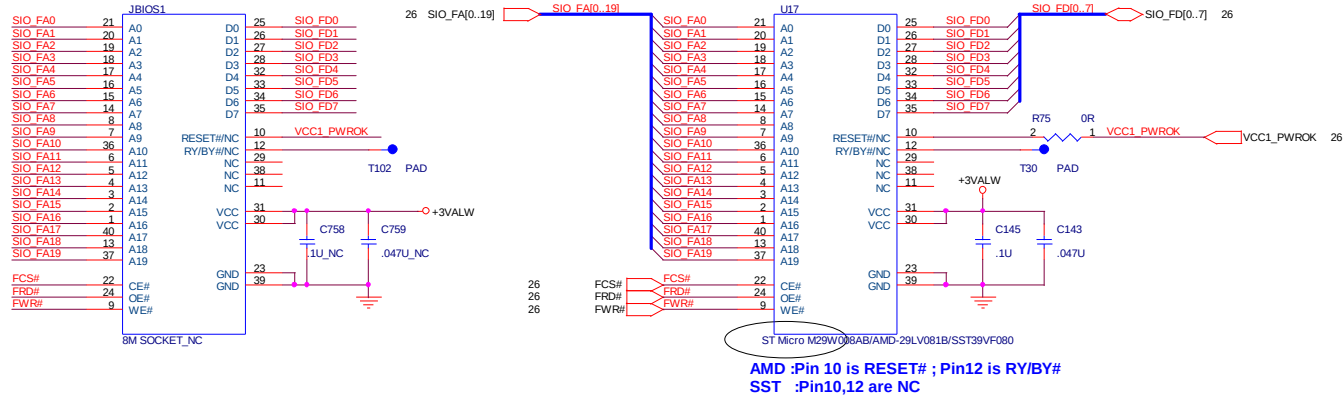
If MAX3243 pin 22 tied to RUN_ON, then it can not support Ring Out



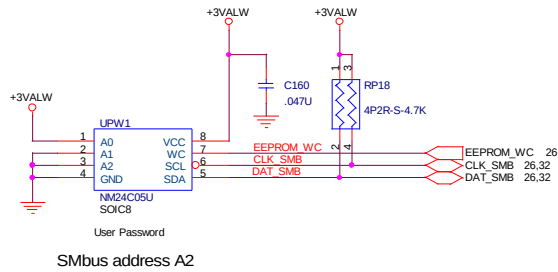
Place these beads close to JCOM1 as soon as possible

QUANTA COMPUTER	
Title SERIAL PORT	
Size Document Number JM2	Rev 3F
Date: Monday, January 06, 2003 Sheet 27 of 49	

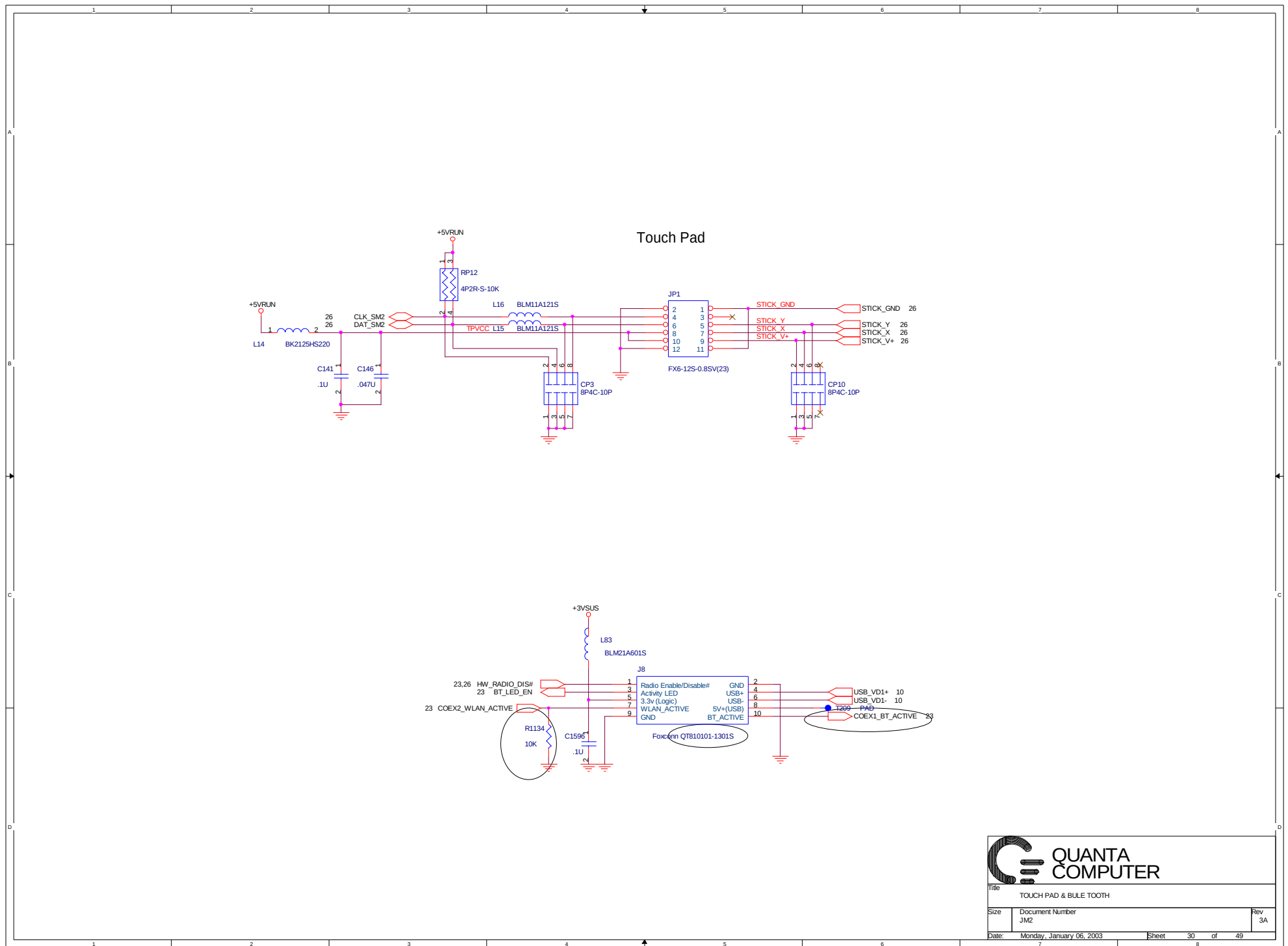
8Mbit (1M Byte),NO PLCC TYPE



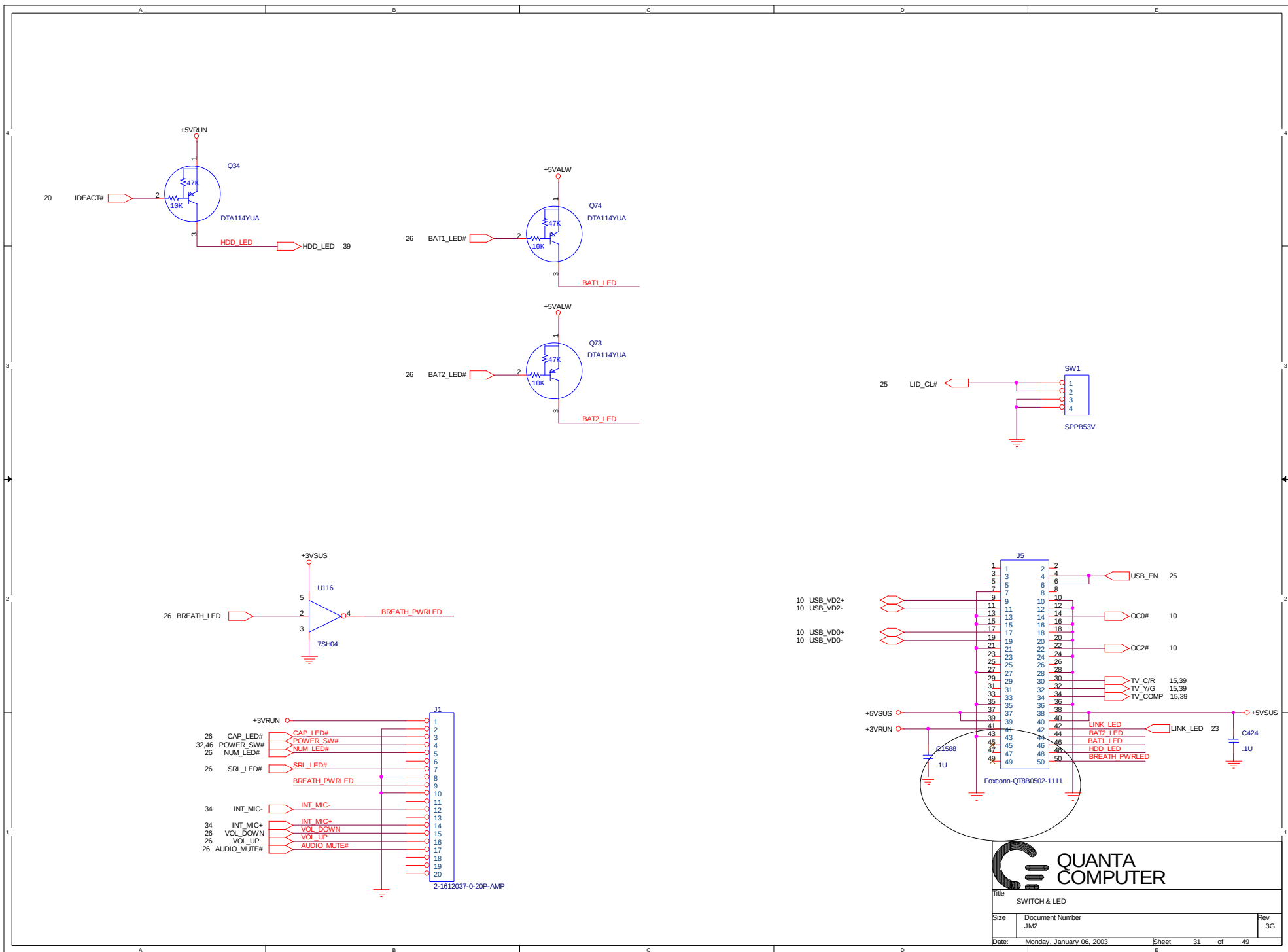
- 1.AMD-29LV081B require MAX 500nS Tready for it's hardware reset.And MAX6326 UR29 has >100ms reset timing.So we can tie it's reset# pin to +3VALW directly.
- 2.SIO has internal 20 mS delay of VCC1_PWROK



Title: FLASH	
Size: JMJ2	Document Number: Rev: 3A
Date: Monday, January 06, 2003	Sheet: 29 of 49

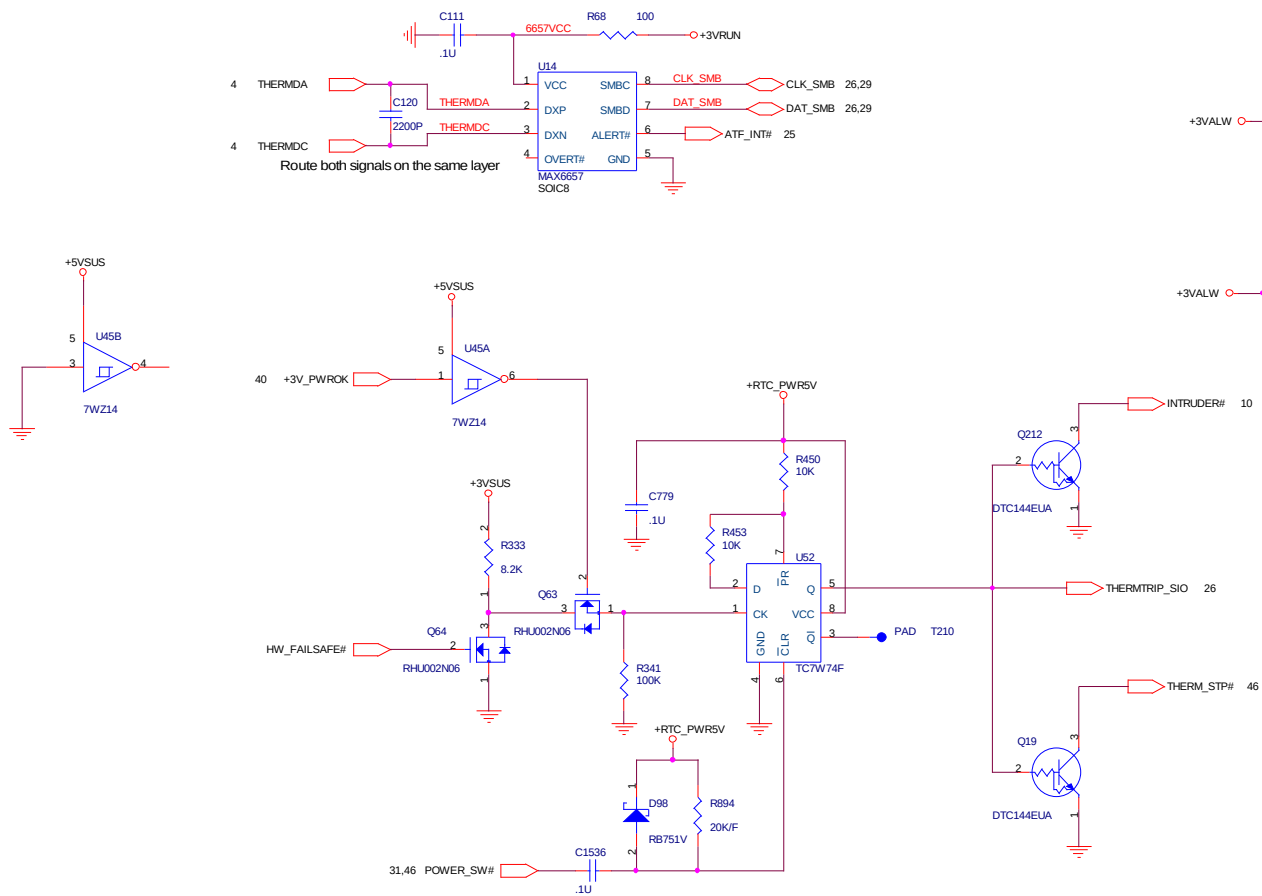
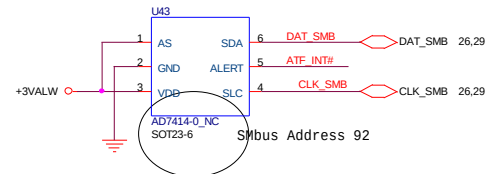


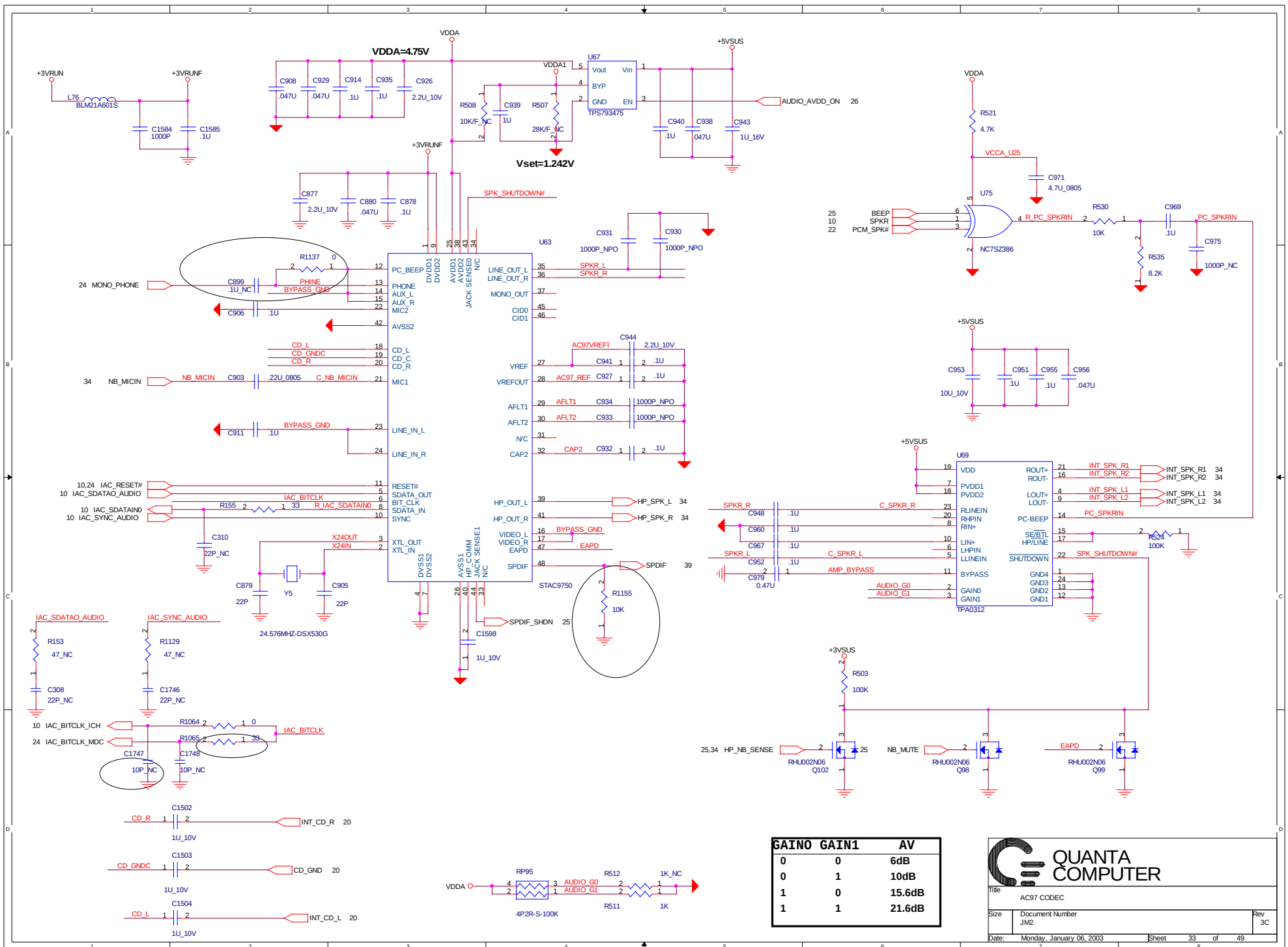
 QUANTA COMPUTER		Title	
		TOUCH PAD & BLUE TOOTH	
Size	Document Number	Rev	
	JM2	3A	
Date:	Monday, January 06, 2003	Sheet	30 of 49
	7		8

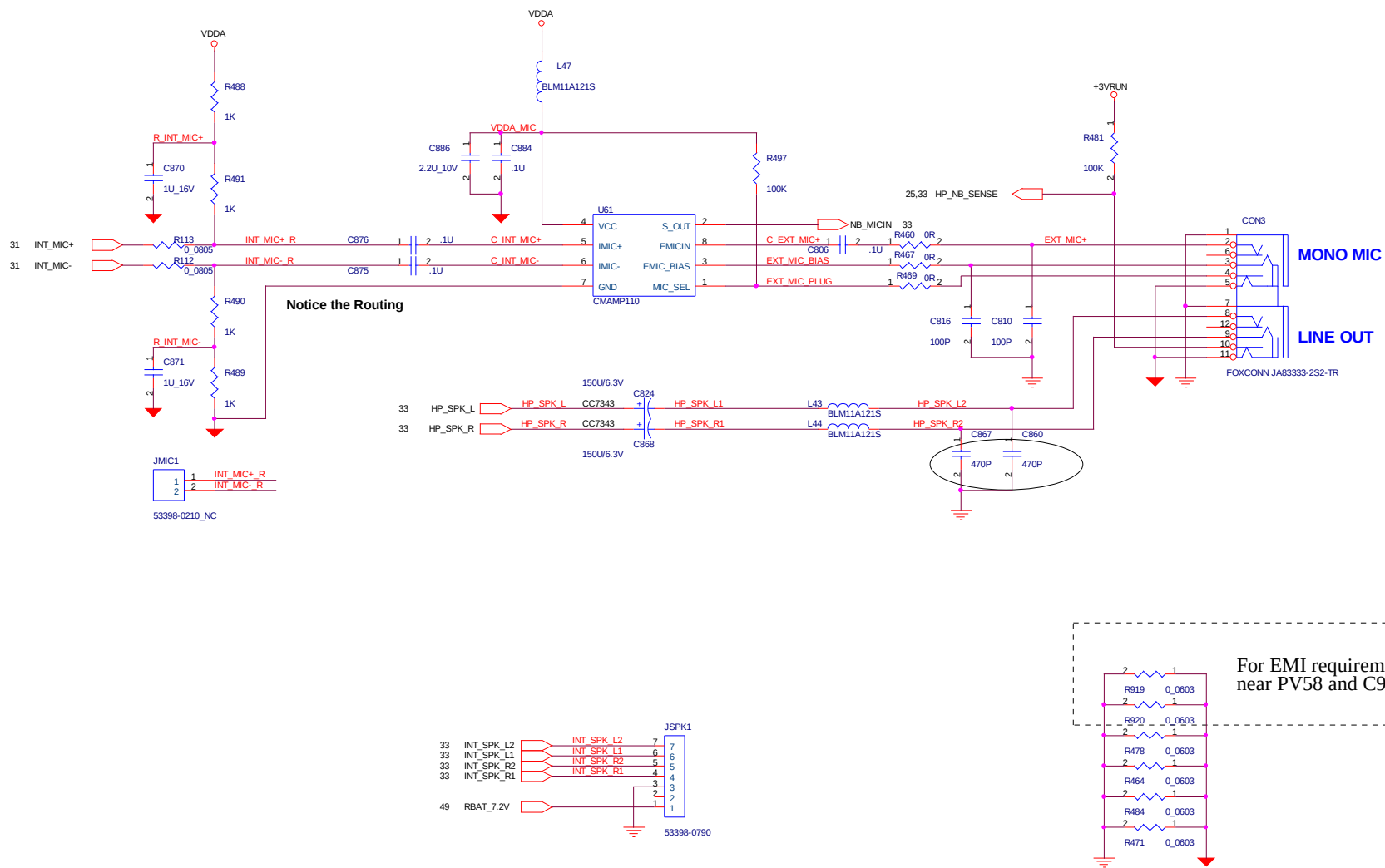


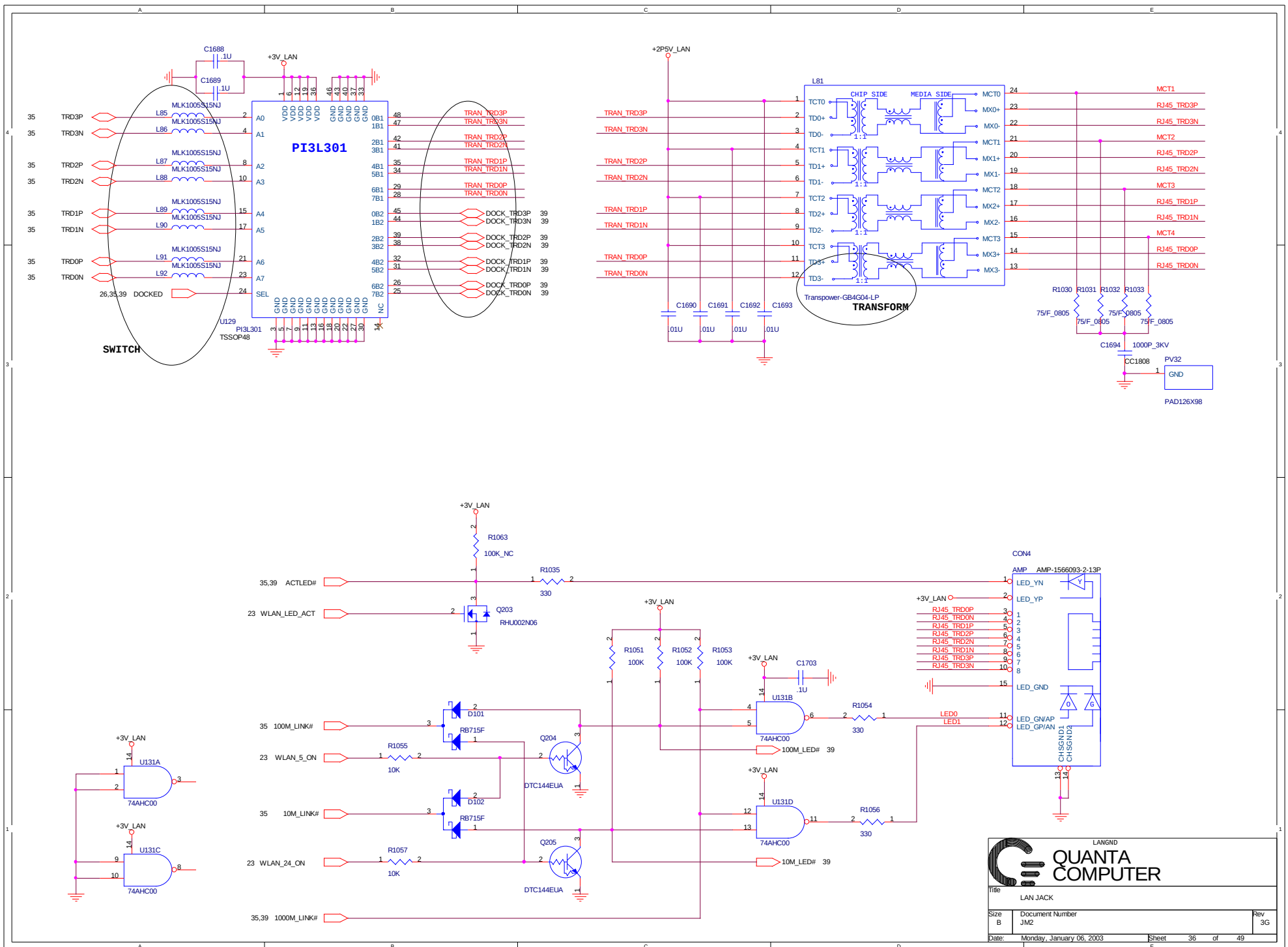
QUANTA COMPUTER

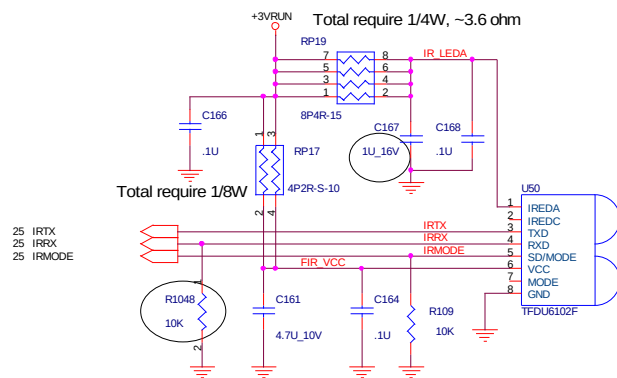
Title			SWITCH & LED	
Size	Document Number		Rev	
	JM2		3G	
Date:	Monday, January 06, 2003		Sheet	31 of 49



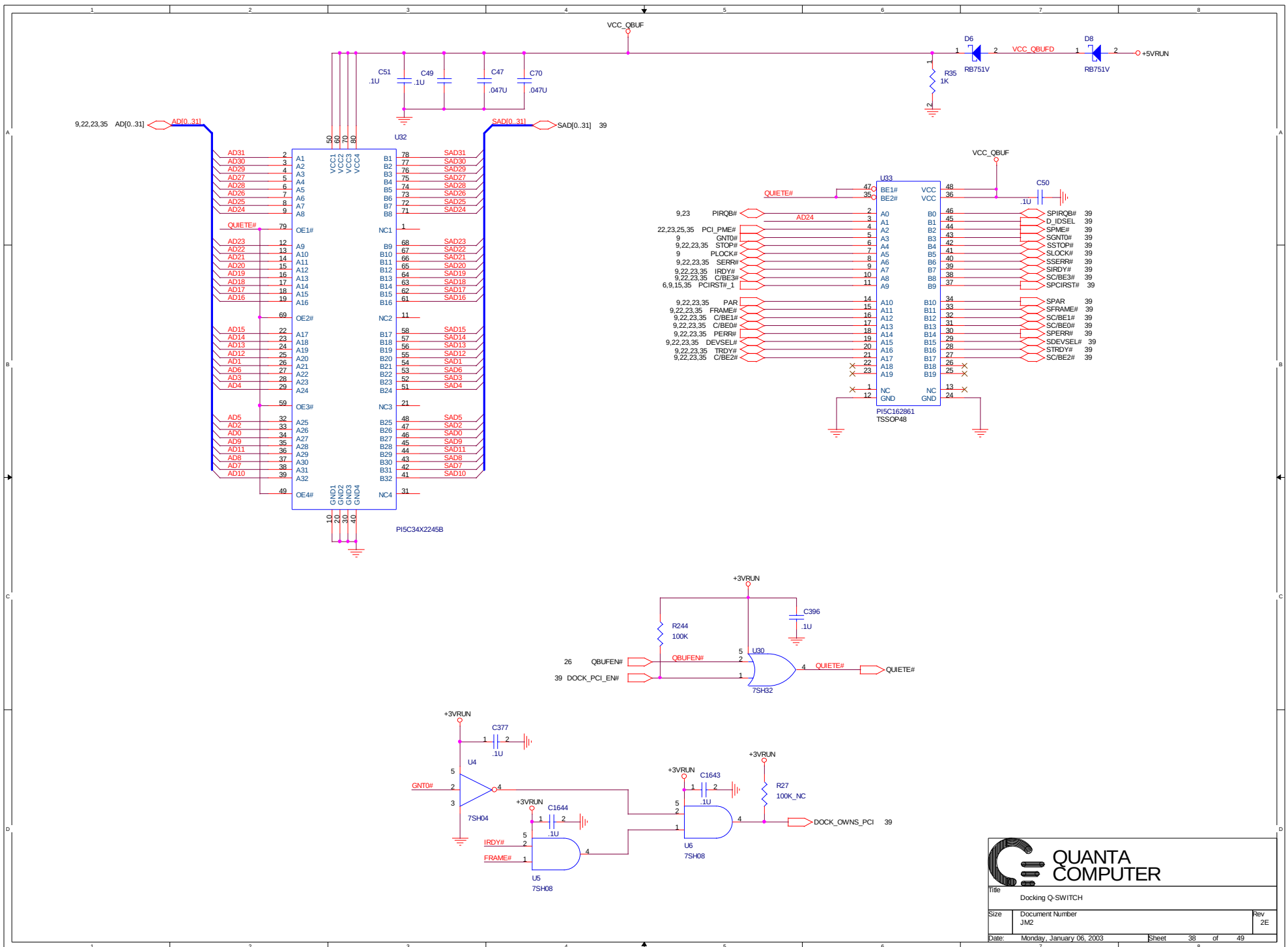




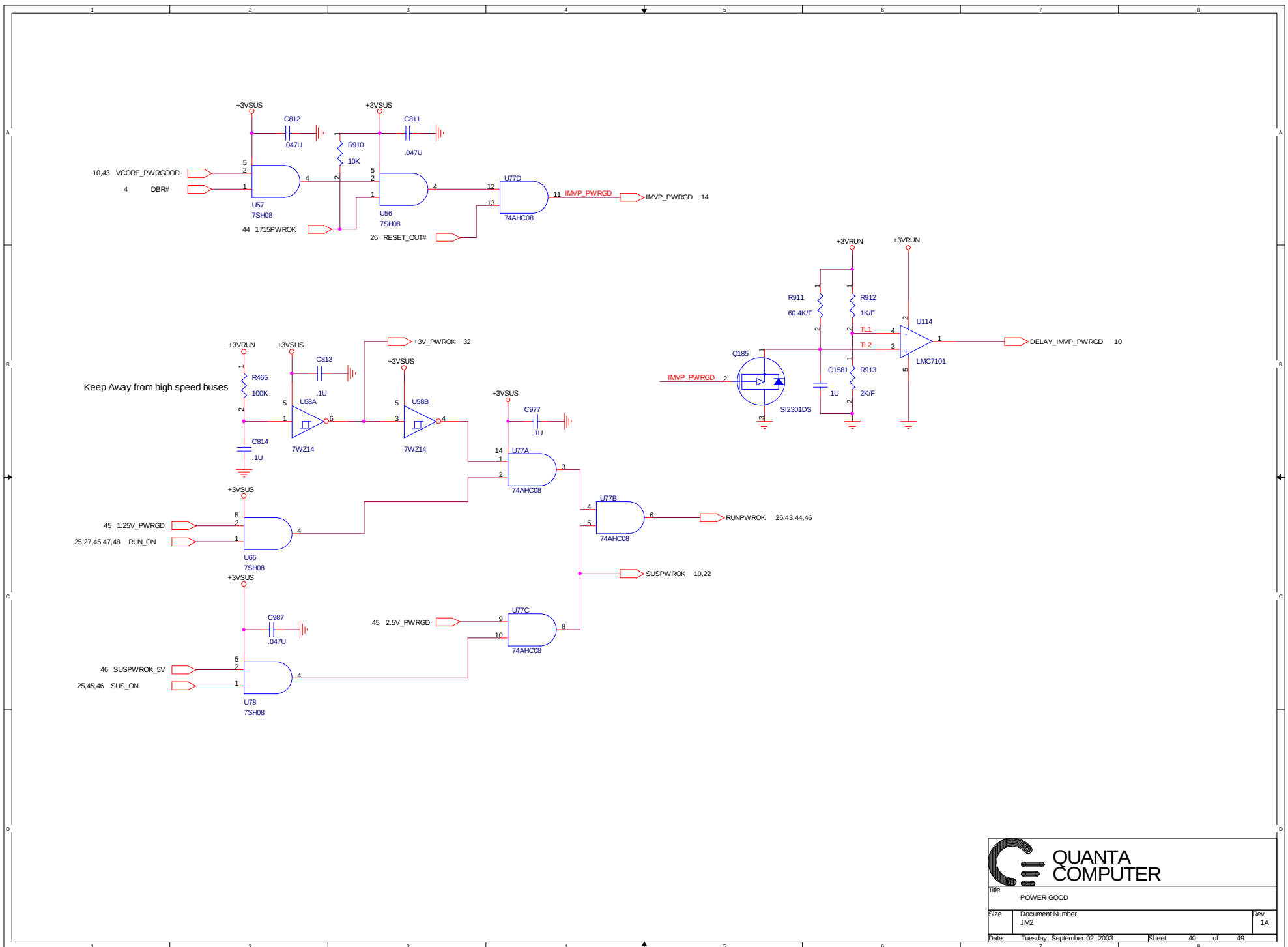


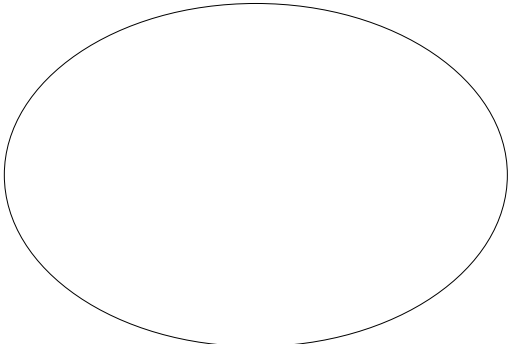


 QUANTA COMPUTER		Title	
		FIR	
Size	Document Number	Rev	
	JM2	3G	
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Title	Docking Q-SWITCH	
Size	Document Number JM2	Rev ZE
Date	Monday, January 06, 2003	Sheet 38 of 49





 QUANTA COMPUTER	
Title	Battery Charger
Size	Document Number JM2 Rev 3F
Date:	Monday, January 06, 2003 Sheet 42 of 49

value provided
2.67% offset;
old value was
for old 4.62%
offset.

BG waveforms
improved. may
delete from
future
revision.

Set up for
constant-ripple
mode. Was
constantfrequency
mode

20 mil Trace list for layout
The +5VRUN (PIN38
AND PIN20)
BST1_VCORE
BST2_VCORE

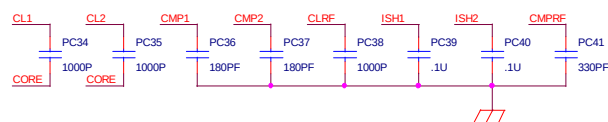
Added
filter for
PBOOT

V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V	
0	1	0	1	1	1	1.340	
0	1	1	0	0	0	1.324	
0	1	1	0	1	0	1.292	
0	1	1	1	0	0	1.260	
0	1	1	1	0	1	1.244	
0	1	1	1	1	1	1.212	
1	0	0	0	0	1	1.180	
1	0	0	0	1	1	1.148	
1	0	0	1	1	0	1.100	
1	0	1	0	0	1	1.052	
1	0	1	0	1	1	1.020	
1	0	1	1	1	0	0.972	
1	1	0	0	0	0	0.940	

100 mil Trace list for layout

DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

10 mil Trace list for layout
SC1476
pin 4 pin
5 pin 7
pin 25
pin 30



QUANTA
COMPUTER

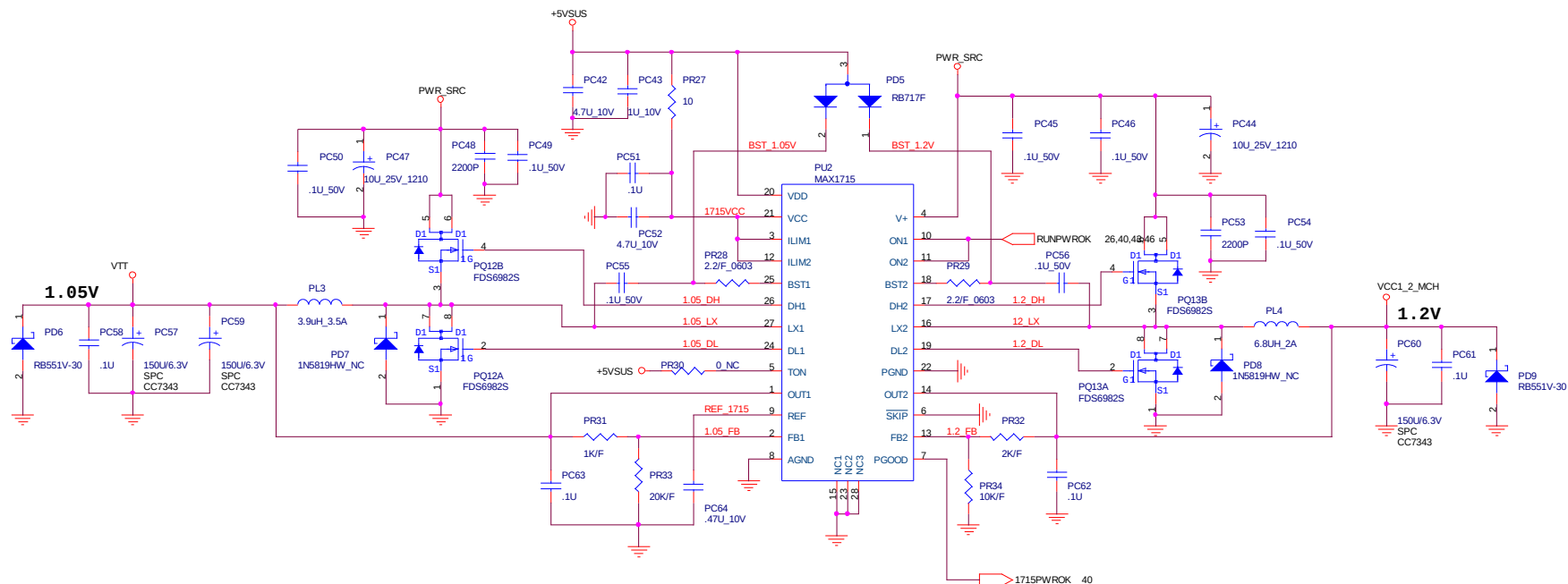
Title
CPU POWER

Size
Document Number
JM2

Date
Monday, January 06, 2003

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Rev
3C



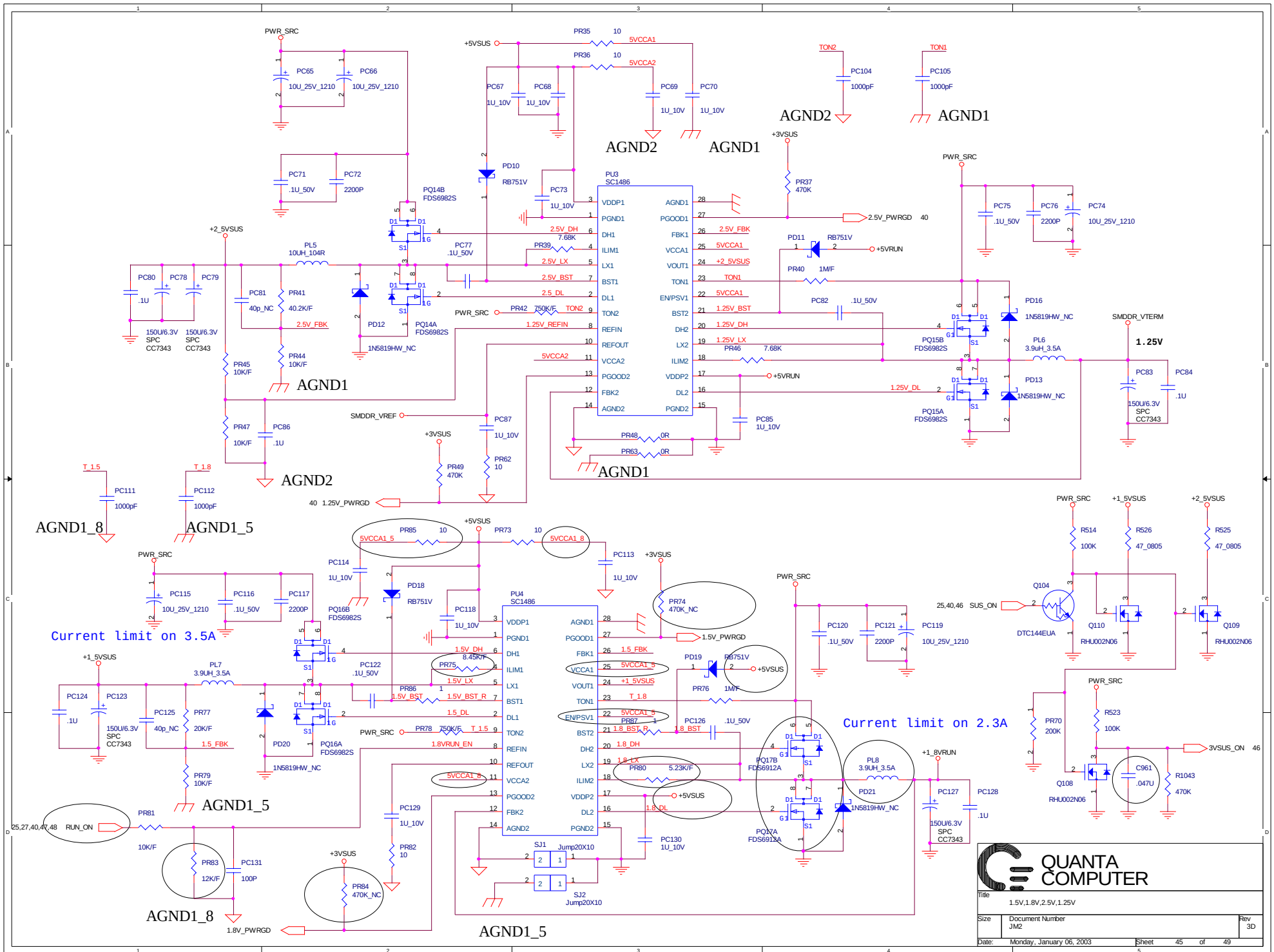
Title
VTT_1.05V,VCC1_2_MCH

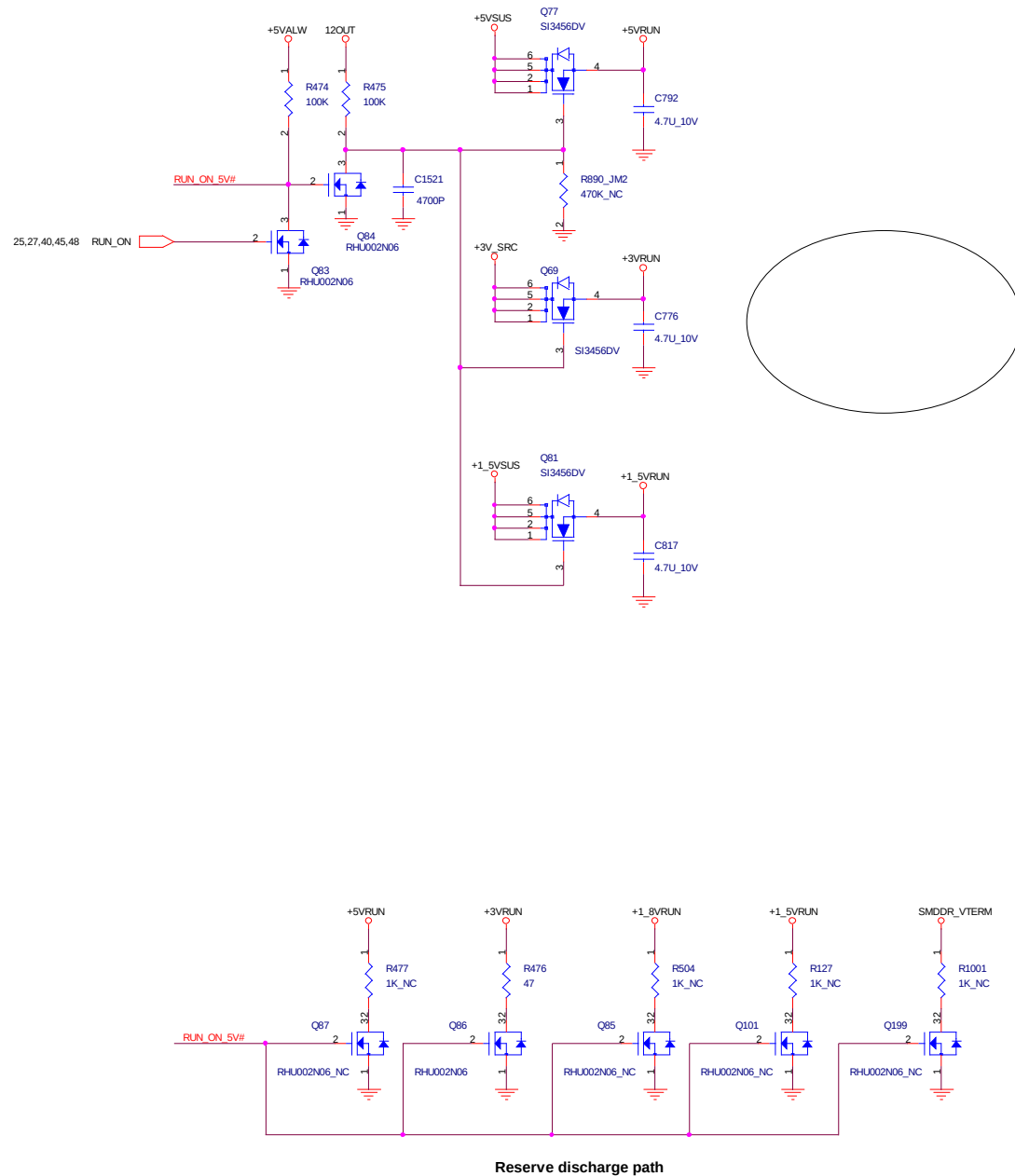
Size
JM2

Date: Monday, January 06, 2003

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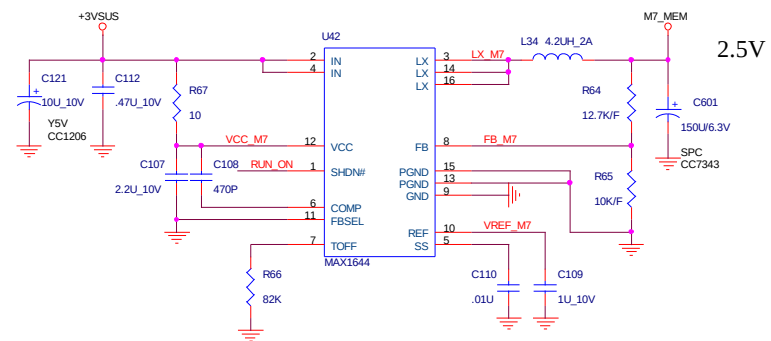
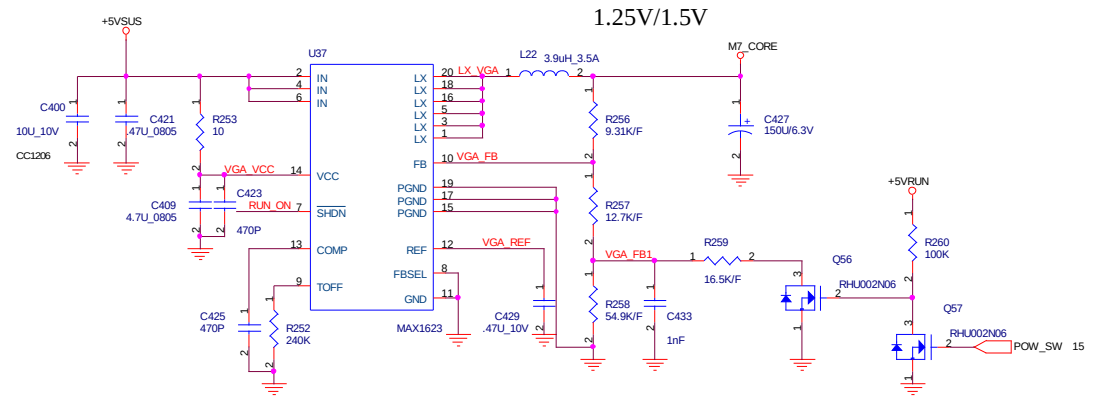
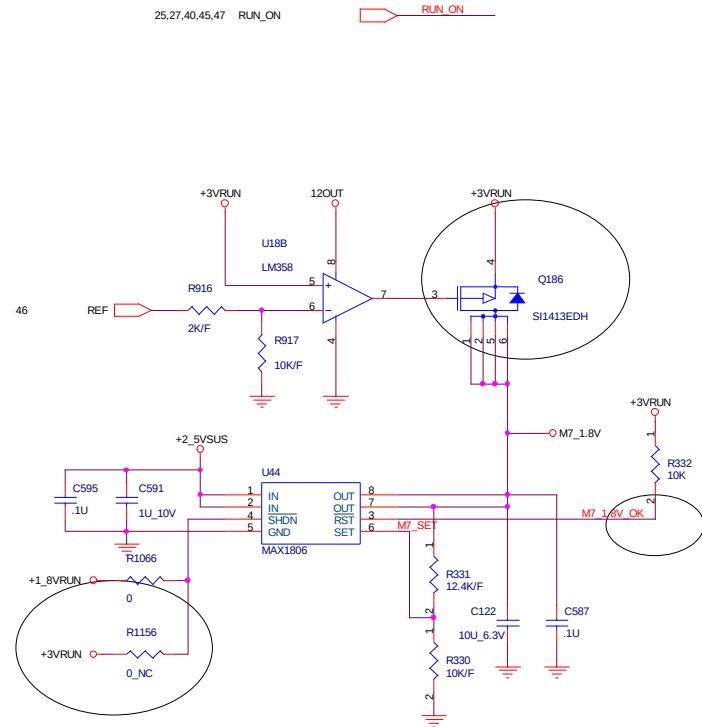
Rev
1A





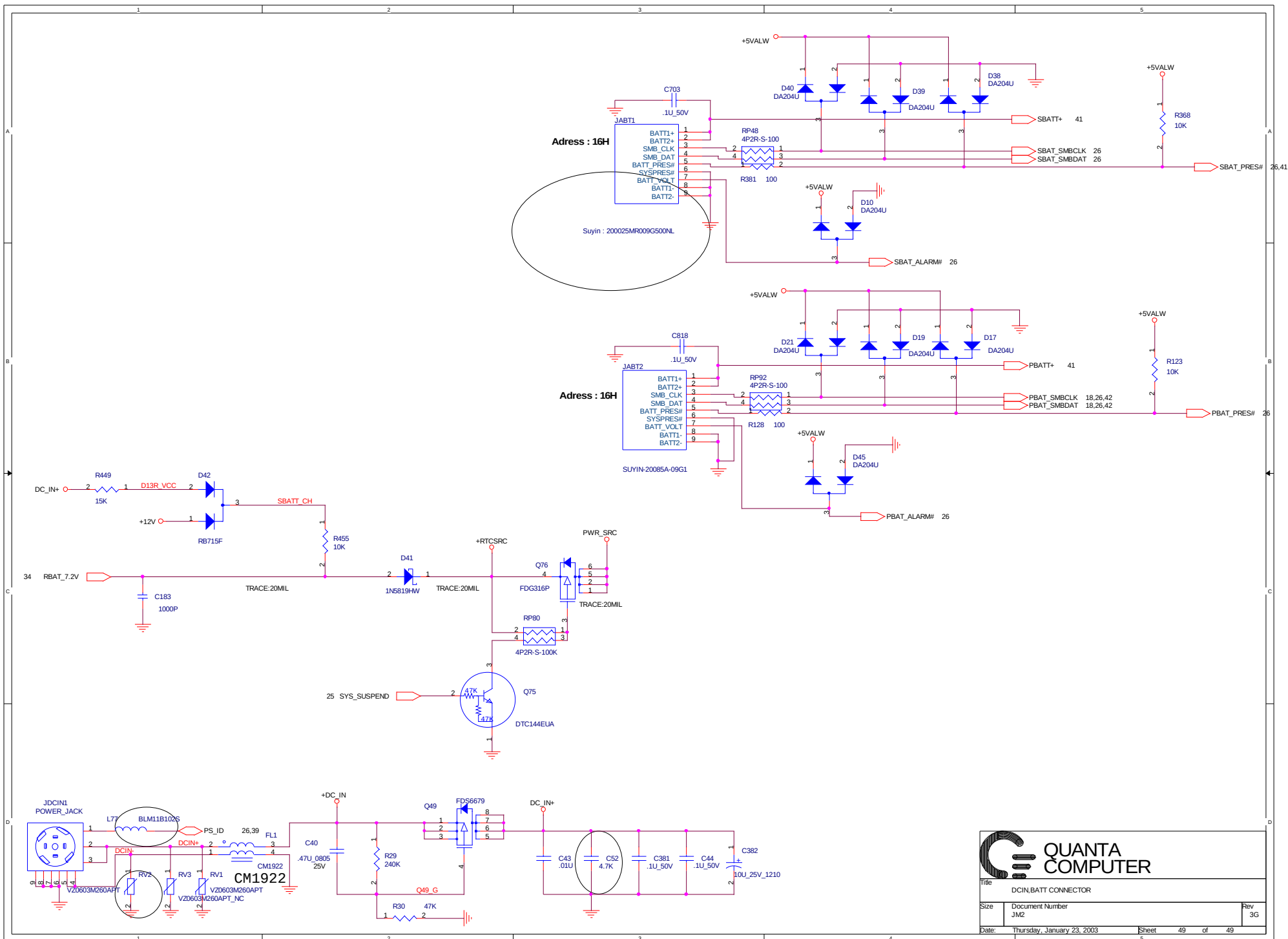
 QUANTA COMPUTER		
Title: RUN POWER SW		
Size: JM2	Document Number: JM2	Rev: 3C
Date: Monday, January 06, 2003	Sheet: 47	of 49

POW_SW	M7D/M9 CSP 16 32
0	1.5V
1	1.25V




**QUANTA
COMPUTER**

Title		VGA DC/DC
Size	Document Number	Rev
	JM2	3F
Date:	Friday, January 17, 2003	Sheet 48 of 49



QUANTA COMPUTER		
Title: DCIN.BATT CONNECTOR		
Size: JM2	Document Number: JM2	Rev: 3G
Date: Thursday, January 23, 2003	Sheet: 49	of 49